



(12) **EUROPEAN PATENT APPLICATION**
published in accordance with Art. 153(4) EPC

(43) Date of publication:
19.08.2009 Bulletin 2009/34

(51) Int Cl.:
H04R 19/00 (2006.01) A61B 8/00 (2006.01)

(21) Application number: **07831248.5**

(86) International application number:
PCT/JP2007/071516

(22) Date of filing: **06.11.2007**

(87) International publication number:
WO 2008/056643 (15.05.2008 Gazette 2008/20)

(84) Designated Contracting States:
AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IS IT LI LT LU LV MC MT NL PL PT RO SE SI SK TR

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(30) Priority: **08.11.2006 JP 2006302627**

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(54) **ULTRASONIC PROBE AND ULTRASONOGRAPHIC DEVICE USING THE SAME**

(57) Provided is an ultrasonic probe including: a cMUT chip having a plurality of vibration elements whose electromechanical coupling coefficient or sensitivity is changed according to a bias voltage and transmitting and receiving ultrasonic waves; an acoustic lens arranged above the cMUT chip; and a backing layer arranged below the cMUT chip.

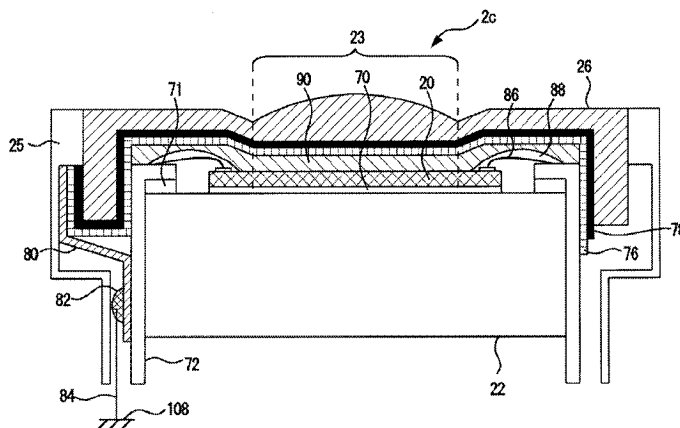
Electric leakage preventing means is provided at the ultrasonic wave transmission/reception surface side of

the acoustic lens or between the acoustic lens and the cMUT chip.

The electric leakage preventing means is, for example, an insulating layer such as a ground layer.

By using such a structure, it is possible to provide an ultrasonic probe capable of preventing electric leakage from the ultrasonic probe to an object to be examined so as to improve the electric safety and an ultrasonic diagnostic apparatus using the probe.

FIG. 9



Description

Technical Field

[0001] The present invention relates to an ultrasonic probe and ultrasonic diagnostic apparatus using the same, and more particularly to an ultrasonic probe and ultrasonic diagnostic apparatus using the same wherein the electric safety for an object to be examined is improved.

Background Art

[0002] An ultrasonic diagnostic apparatus is for imaging diagnostic images based on the echo signals and the reflected signals thereof outputted from an ultrasonic probe. In an ultrasonic probe, a plurality of ultrasonic transducers is disposed. The ultrasonic transducers convert driving signals into ultrasonic waves and transmit them to the object, as well as receive the reflected echo signals produced from the object and convert them into electric signals.

[0003] In recent years, ultrasonic probes using a cMUT (Capacitive Micromachined Ultrasonic Transducer) have been developed. The cMUT is an ultrafine capacitance ultrasonic transducer manufactured by semiconductor microfabrication process as disclosed, for example, in Patent document 1. In the cMUT, bias voltage is applied on two electrodes (the object side and the backing layer side) disposed in a plurality of pairs opposite each other in a direction parallel to the ultrasonic wave transmitting/receiving surface, driving signals are superimposed and applied, and ultrasonic waves are sent out.

[0004]

Patent Document 1: U.S. Patent No. 5894452

Patent Document 2: JP-A-2007-235795

Disclosure of the Invention

Problems to be Solved

[0005] However, the present inventors found out the problem described below as a result of reviewing the above-mentioned conventional technique.

That is, in the conventional ultrasonic probe using the conventional PZT, only one kind of driving signal is to be applied as voltage between the electrodes disposed opposite each other on the ultrasonic wave transmitting/receiving surface, whereby capable of reducing the current passing from the electrode toward the object by contacting the ground electrode to the electrode disposed on the object side as disclosed in, for example, Patent Document 2. However, in the ultrasonic probe using a cMUT, since the bias voltage is mainly applied to the electrode on the backing layer side at the same time as the driving signal formed by a high-frequency wave (drive voltage) is applied to the electrode on the object side,

the ground layer cannot be contacted to the electrode on the object side directly, thus the insulation becomes insufficient unless a ground layer is provided.

Also, while there are cases in the ultrasonic probe using a cMUT that an acoustic matching layer is not provided, which causes the problem of deterioration in insulation construction.

[0006] The objective of the present invention is to provide an ultrasonic probe using a cMUT and an ultrasonic diagnostic apparatus using the same capable of preventing electrical leakage on the object by providing adequate insulation configuration in an ultrasonic probe using a cMUT, improved in electrical safety.

15 Means to Solve the Problem

[0007] In accordance with the present invention, the ultrasonic probe comprises:

- 20 a cMUT chip having a plurality of vibration elements wherein electromechanical coupling coefficient or sensitivity varies in accordance with bias voltage, for transmitting/receiving ultrasonic waves;
- 25 an acoustic lens provided on the upper part of the cMUT chip; and
- a backing layer provided on the lower part of the cMUT chip,

characterized in further comprising electric leakage preventing means on the side of ultrasonic wave transmitting/receiving surface of the acoustic lens or between the acoustic lens and the cMUT chip.

Effect of the Invention

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[0008] In accordance with the present invention, it is possible to provide an ultrasonic probe using a cMUT and an ultrasonic diagnostic apparatus using the same improved in electrical safety capable of preventing electric leakage to an object by providing adequate insulation configuration.

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Brief Description of the Diagrams

45 [0009]

Fig. 1 is a configuration diagram showing an ultrasonic diagnostic apparatus related to the present invention.

Fig. 2 is a configuration diagram showing an ultrasonic probe related to the present invention.

Fig. 3 is a configuration diagram showing a transducer related to the present invention.

Fig. 4 is a configuration diagram of one vibration element in Fig. 3 viewed from the side.

Fig. 5 shows an ultrasonic probe related to embodiment 1.

Fig. 6 shows the wiring of the ultrasonic probe.

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Fig. 7 shows an ultrasonic probe related to embodiment 2.

Fig. 8 shows an ultrasonic probe related to embodiment 3.

Fig. 9 shows an ultrasonic probe related to embodiment 4.

Fig. 10 is a pattern diagram showing conductor configuration and insulation configuration.

Fig. 11 shows an ultrasonic probe related to embodiment 5.

Fig. 12 shows an ultrasonic probe related to embodiment 6.

Fig. 13 shows an ultrasonic probe related to embodiment 7.

Fig. 14 shows an ultrasonic probe related to embodiment 8.

Fig. 15 shows an ultrasonic probe related to embodiment 9.

Description of the Symbols

[0010] 2c... ultrasonic probe, 20... cMUT chip, 22... backing layer, 23... region from which ultrasonic waves are emitted, 25... ultrasonic probe cover, 26... acoustic lens, 70 & 71... adhesion layer, 72... flexible substrate, 76... conductive film, 78... insulating film, 80... conductive material, 82... connecting unit, 84... ground wire, 86... wire, 88... wire sealing resin, 90... adhesion layer, 108... ground

Best Mode for Carrying Out the Invention

[0011] The preferable embodiments of the ultrasonic probe and ultrasonic diagnostic apparatus using the same related to the present invention will be described below in detail referring to the attached diagrams. In the attached diagrams, the components having the same function are denoted by the same symbols, and the repeated explanation thereof will be omitted.

[0012] First, the configuration of ultrasonic diagnostic apparatus 1 will be described referring to Fig. 1.

Fig. 1 is a configuration diagram of the ultrasonic diagnostic apparatus 1.

[0013] The ultrasonic diagnostic apparatus 1 related to the present invention is configured by ultrasonic probe 2, transmitting means 3, bias means 4, receiving means 5, phasing and adding means 6, image processing means 7, display means 8, control means 9 and operation means 10.

[0014] The ultrasonic probe 2 is for transmitting/receiving ultrasonic waves to/from an object while being applied on the object. Ultrasonic waves are emitted from the ultrasonic probe 2 to the object, and the reflected echo signals produced from the object are received by the ultrasonic probe 2.

[0015] The transmitting means 3 and the bias means 4 are for applying bias voltage to the electrodes disposed in the ultrasonic probe 2 opposite each other, superim-

posing a drive signal and applying, and transmitting ultrasonic waves.

[0016] The receiving means 5 is for receiving the reflected echo signals to be transmitted to the ultrasonic probe 2. The receiving means 5 further performs the process such as analog/digital conversion on the received reflected echo signals.

[0017] The phasing and adding means 6 performs phasing and adding of the received reflected echo signals. The image processing means 7 constructs a diagnostic image (for example, a tomographic image or a blood flow image) based on the phased and added reflected echo signals.

[0018] The display means 8 displays the diagnostic image constructed in the image processing means 7.

[0019] The control means 9 controls the above-described respective components.

[0020] The operation means 10 gives commands such as to start diagnosis, to the control means 9. The operation means 10 is an input device such as a trackball, keyboard or mouse.

[0021] Next, the ultrasonic probe 2 will be described referring to Fig. 2 - Fig. 4.

[0022] Fig. 2 is a block diagram of the ultrasonic probe 2. Fig. 2 is a partly cutaway perspective view of the ultrasonic probe 2. The upper side of the diagram is the direction to be applied on the object and that ultrasonic waves are transmitted.

[0023] The ultrasonic probe 2 comprises a cMUT chip 20. The cMUT chip 20 is a one-dimensional array type of transducer group in which a plurality of transducers 21-1, 21-2,... are disposed in rectangles. Into the transducers 21-1, 21-2,..., a plurality of vibration elements 28 are disposed. While the probe illustrated in Fig. 2 is a linear-type probe, other types of transducer group such as a 2-dimensional array type or convex type may be used.

[0024] On the back surface of the cMUT chip 20 (lower side in the diagram), a backing layer 22 is provided. On the ultrasonic waves emitting side of the cMUT chip 20, an acoustic lens 26 is provided. The cMUT chip 20 and the backing layer 22, etc. are contained in an ultrasonic probe cover 25.

[0025] In the cMUT chip 20, on the basis of the application of bias voltage by bias means 4, the drive signals from the transmitting means 3 are converted into ultrasonic waves, and the converted ultrasonic waves are transmitted to the object.

[0026] The receiving means 5 converts the ultrasonic waves produced from the object into electric signals, and receives them as the reflected echo signals.

[0027] The backing layer 22 is a layer for absorbing the transmission of the ultrasonic waves emitted from the cMUT chip 20 to the back surface side so as to constrain the extra vibration.

[0028] The acoustic lens 26 is a lens for converging the ultrasonic beams transmitted from the cMUT chip 20. Curvature of the acoustic lens 26 is defined based on the

desired binocular vision.

[0029] A matching layer may be provided between the acoustic 26 and the cMUT chip 20. The matching layer is for matching the cMUT chip 20 and the acoustic impedance of the object so as to improve the transmitting efficiency of ultrasonic waves.

[0030] Fig. 3 is a configuration diagram of a transducer 21 in Fig. 2.

[0031] On the object side of the plurality of vibration elements 28 by which the transducers 21-1, 21-2,... are formed, upper electrodes 46-1, 46-2,... are disposed, divided to be pluralized in the long-axis direction X, and wire-connected for each transducer 21. In other words, the upper electrodes 46-1, 46-2,... are disposed in parallel in the long-axis direction X.

[0032] On the opposite side from the object side of the plurality of vibration elements by which the transducer 21 are formed, the lower electrodes (48-1 - 48-4) are disposed, divided to be pluralized (into four rows in Fig. 3) in the minor-axis direction Y to be wire-connected. In other words, the lower electrodes 48-1, 48-2, 48-3,... are disposed in parallel in the minor-axis direction Y.

[0033] Fig. 4 is a configuration diagram (cross-section view) of one vibration element 28 shown in Fig. 3, viewed from the side.

[0034] The vibration element 28 is formed by a basal plate 40, film body 44, film body 45, upper electrode 46, frame body 47 and lower electrode 48. The vibration element 28 is formed by semiconductor micro fabrication process. It is equivalent to one element of the cMUT.

[0035] The basal plate 40 is a semiconductor substrate such as silicon, and is disposed on the lower electrode side.

[0036] The film body 44 and the frame body 47 are formed by semiconducting compound such as silicon compound. The film body 44 is provided on the object side (ultrasonic waves emitting side) of the vibration elements 28, and the frame body 47 is disposed on the back surface of the film body 44 (opposite side from the object side). The upper electrode 46 is placed between the film body 44 and the frame body 47. The film body 45 is placed between the frame body 47 and the basal plate 40, and the lower electrode 48 is placed inside of the film body 45. An internal space 50 which is compartmented by the frame body 47 and the film body 45 is left as a vacuum or filled with predetermined gas.

[0037] The upper electrode 46 and the lower electrode 48 are respectively connected to the transmitting means 3 for providing alternating-current high-frequency voltage as drive signals and bias means 4 for applying direct-current voltage as bias voltage.

[0038] When ultrasonic waves are transmitted, direct-current bias voltage (V_a) is applied to the upper electrode 46 and the lower electrode 48 of the vibration elements 28, and an electric field is generated by bias voltage (V_a). Tensile force is generated in the film body 44 by the generated electric field, and reaches a predetermined electromechanical coupling coefficient (S_a). When the drive

signals are provided from the transmitting means 3 to the upper electrode 46, the ultrasonic waves having high intensity based on the electromechanical coupling coefficient (S_a) are emitted from the film body 44.

[0039] Also, when another direct-current bias voltage (V_b) is applied to the upper electrode 46 and the lower electrode 48 of the vibration elements 28, an electric field is generated by the bias voltage (V_b). Tensile force is generated in the film body 44 by the generated electric field and reaches a predetermined electromechanical coupling coefficient (S_b). When the drive signals are provided from the transmitting means 3 to the upper electrode 46, the intense ultrasonic waves based on the electromechanical coupling coefficient (S_b) are emitted from the film body 44.

Here, in the case that the bias voltage is " $V_a < V_b$ ", the electromechanical coupling coefficient is " $S_a < S_b$ ".

[0040] On the other hand, when ultrasonic waves are received, the film body 44 is excited by the reflected echo signals produced from the object, and the capacity of the internal space 50 is changed. This variation quantity of the internal space 50 is detected via the upper electrode 46 as electric signals.

[0041] The electromechanical coupling coefficient of the vibration elements 28 is determined by the tension applied on the film body 44. Therefore, if the level of bias voltage to be applied to the vibration elements 28 is changed and the tension of the film body 44 is controlled, even in the case that the drive signals having the same amplitude are inputted, the intensity (or acoustic pressure, amplitude) of the ultrasonic waves to be emitted from the vibration elements 28 can be changed.

Embodiment 1

[0042] Next, embodiment 1 of the present invention will be described referring to Fig. 5 and Fig. 6.

[0043] Fig. 5 shows the ultrasonic probe 2 related to embodiment 1. Fig. 5 is a cross-sectional view of the flat plane "A" of the ultrasonic probe 2 shown in Fig. 2.

In accordance with Fig. 5, an insulating film 78 which is an insulation layer is formed on the back surface of the acoustic lens 26. The insulating film 78 is, for example, a silicon oxide film or a paraxylene film.

[0044] The cMUT chip 20 is glued to the upper surface of the backing layer 22 via the adhesion layer 70. A flexible substrate 72 (Flexible printed circuits: FPC) is provided from the peripheral border of the upper surface of the backing layer 22 over to the side surfaces in four directions. The flexible substrate 72 is glued onto the peripheral border of the upper surface of the backing layer 22 via the adhesion layer 71.

[0045] The adhesion layer 70 and the adhesion layer 71 are an adhesive agent formed by, for example, epoxide resin. The height, direction and position of the cMUT chip 20 and the flexible substrate 72 can be adjusted by arbitrarily adjusting the layer thickness of the adhesion layer 70 and the adhesion layer 71.

[0046] The flexible substrate 72 and the cMUT chip 20 are electrically connected via a wire 86. The wire 86 is connected by the wire bonding method. As for the wire 86, Au wire, etc. can be used. A wire-sealing resin 88 is filled around the wire 86.

[0047] The acoustic lens 26 is glued onto the upper surface of the cMUT chip 20 via an adhesion layer 90. As for the material of the acoustic lens 26, for example, silicon rubber is used. In regard to the material of the adhesion layer 90, it is desirable to use a material similar to that used in the acoustic lens 26 (for example, silicon).

[0048] The upper surface of the acoustic lens 26 has a convex shape in the ultrasonic waves emitting direction in the range of at least region 23 within the region from which the ultrasonic waves are emitted. In the cMUT chip 20, the vibration elements 28 are disposed within the region corresponding at least to the region 23. Ultrasonic waves are to be irradiated from the convex portion which is on the ultrasonic waves irradiating side (toward the object side) of the acoustic lens 26.

[0049] The back surface of the acoustic lens 26 (the opposite side from the object side, which is the backing layer side) has a concave portion so that the cMUT chip 20 can be disposed therein. In this concave portion, the cMUT chip 20 and the flexible substrate 72 are fitted by the connecting portion (wire preventing resin 88).

[0050] The ultrasonic probe cover 25 is provided on the surface in four sides of the ultrasonic probe 2, and is fixed on the surface in four sides of the acoustic lens 26. An examiner is to hold the ultrasonic probe cover 25 by his/her hand to operate the ultrasonic probe 2.

[0051] Fig. 6 shows the wiring of the ultrasonic probe 2. The basal plate 40 of the cMUT chip 20 is fixed on the upper surface of the backing layer 22. The flexible substrate 72 is fixed on the peripheral border of the upper surface of the backing layer 22.

[0052] On the flexible substrate 72, signal pattern 38-1 ~ signal pattern 38-n are disposed in pairs vertically on the diagram, and signal pattern 41-1 - signal pattern 41-4 are disposed in pairs horizontally on the diagram.

[0053] The upper electrode 46-1 - upper electrode 46-n are connected to the signal pattern 38-1 - signal pattern 38-n. The lower electrode 48-1 - lower electrode 48-4 are connected to the signal pattern 41-1 - signal pattern 41-4. The spacings between the adjacent lower electrode 48-1 - lower electrode 48-4 are mutually insulated.

[0054] The upper electrode 46 and the lower electrode 48 are connected to the flexible substrate 72 via the wire 86 respectively by the wire bonding method.

[0055] As for the shape of the lower electrode 48-1 - lower electrode 48-4, it is desirable that it corresponds to the shape of the vibration elements 28 (for example, a hexagon) such as a waveform. In this manner, the respective vibration elements 28 can be disposed corresponding to only one of the lower common electrode 48-1 - lower common electrode 48-4.

[0056] While four electrodes are disposed in the lower electrode 48-1 - lower electrode 48-4, the number does

not have to be limited to four.

[0057] Also, while the signal pattern 38-1 - signal pattern 38-n are disposed in pairs vertically on the diagram and the signal pattern 48-1 - signal pattern 48-4 are disposed in pairs horizontally on the diagram, the pattern does not have to be limited in pairs and they may be disposed only on one side.

[0058] Also, while it is described above that the wire bonding method is used to connect the signal pattern and the upper electrode or the lower electrode, the method does not have to be limited thereto and the flip chip bonding method which connects pads to pads may be used.

[0059] As described above in detail, in the ultrasonic probe 2 of embodiment 1, the insulation layer as the electric leakage preventing means for preventing the leakage of electricity from the electrode in the cMUT chip to the object is formed as the insulating film 78 between the acoustic lens 26 and the cMUT chip. The spacing between the ultrasonic wave transmitting/receiving surface and the cMUT chip is performed with double insulation by the acoustic lens and the insulation layer. In this manner, even when friction or damage is caused in the ultrasonic wave transmitting/receiving surface, electric leakage from the ultrasonic wave transmitting/receiving surface to the object or electric shock due to the leakage can be prevented whereby improving the safety of the ultrasonic probe.

Embodiment 2

[0060] Next, embodiment 2 will be described referring to Fig. 7.

Fig. 7 shows an ultrasonic probe 2a related to embodiment 2. Fig. 7 is equivalent to the cross-sectional view of the flat plane "A" shown in Fig. 2.

[0061] While the insulation film 78 is illustrated as being disposed in the lower surface of the acoustic lens in embodiment 1, the insulation layer which is electric leakage preventing means for preventing the leakage of electricity from the electrode in the cMUT chip to the object is disposed in the upper surface (object side) of the acoustic lens 26 as an insulation film 78a in embodiment 2.

[0062] In this manner, in the ultrasonic probe 2a of embodiment 2, the insulation layer is formed in the upper surface of the acoustic lens. The spacing between the ultrasonic wave transmitting/receiving surface and the cMUT chip is performed with double insulation by the insulation layer (insulation film) and the acoustic lens. Therefore, even when friction or damage is caused in the ultrasonic wave transmitting/receiving surface, electric leakage from the ultrasonic wave transmitting/receiving surface to the object or electric shock due to the leakage can be prevented whereby improving the safety of the ultrasonic probe, which provides the same effect as embodiment 1.

[0063] In addition, in embodiment 2, the manufacturing process of the probe is easier compared to embodiment

1 since the insulation layer is provided on the upper surface of the acoustic lens, not on the lower surface.

Embodiment 3

[0064] Next, embodiment 3 will be described referring to Fig. 8.

Fig. 8 shows an ultrasonic probe 2b related to embodiment 3. Fig. 8 is equivalent to the cross-sectional view of the flat plane "A" shown in Fig. 2.

[0065] While the insulation film 78 is described as being disposed on the lower surface of the acoustic lens 26 in embodiment 1, the insulation layer which is electric leakage preventing means for preventing the leakage of electricity from the electrode in the cMUT chip to the object is disposed on the upper surface of the cMUT chip 20 as an insulation film 78b in embodiment 3.

[0066] In this manner, in the ultrasonic probe 2b of embodiment 3, the insulation layer is formed on the upper surface of the cMUT chip. In the spacing between the ultrasonic wave transmitting/receiving surface and the cMUT chip, double insulation is performed by the insulation layer and the acoustic lens. Thus the same effect can be performed as embodiment 1.

Embodiment 4

[0067] Next, embodiment 4 will be described referring to Fig. 9 and Fig. 10.

Fig. 9 shows an ultrasonic probe 2c related to embodiment 4. Fig. 9 is equivalent to the cross-sectional view of the flat plane "A" shown in Fig. 2.

[0068] While embodiment 1 is described without the provision of a ground layer, in embodiment 4, a conductive film 76 which is the ground layer is further provided as electric leakage preventing means for preventing the leakage of electricity from the electrode in the cMUT chip to the object is provided on the back surface (the opposite side from the object side) of the insulation film 78 as an insulation layer.

[0069] The conductive film 76 is connected to a ground 108 which is the ground potential. The conductive film 76 is, for example, a Cu film. The insulation film 78 is placed on the back surface of the acoustic lens 26, and the Cu film is deposited on the back surface of the insulating film 78 to form the conductive film 76.

[0070] The conductive film 76 is formed from the internal lower surface of the acoustic lens 26 over to the external side surfaces. The conductive film 76 is connected to a ground 108 on the main apparatus side via a conductive material 80 and a ground wire 84.

[0071] The conductive material 80 is a material which has electrical conductivity. The conductive material 80 can be manufactured using a highly reliable material which is more heavy-duty compared to the conductive film 76. The conductive material 80 is, for example, a Cu tape which has higher rigidity compared to the conductive film 76. The conductive material 80 is fixed on the external

side surface of the flexible substrate 72.

The ground wire 84 is connected to the conductive material 80 via the connecting portion 82 using a method such as soldering.

[0072] Fig. 10 is a pattern diagram showing the conducting structure and insulating structure.

The upper electrode 46 is connected to a reception amplifier 100 and the transmitting means 3 via a cable 96 and a transmission/reception separating circuit 98. The lower electrode 48 is connected to the bias means 4 via a cable 102.

A resistor 110 is a resistor element for stabilizing the electric potential of bias means 4 to the ground potential. A capacitor 112 is a capacitive element for bypassing signal current.

The conductive film 76 is connected to the ground wire 84, and further connected to the ground 108 via a chassis ground of the main apparatus.

[0073] In this manner, in the ultrasonic probe 2c in embodiment 4, the conductive film 76 as the ground layer is provided on the lower part of the insulating film 78 as an insulation layer. By such configuration, even when the acoustic lens 26 and the insulating film 78 are damaged, the conductive film 76 can prevent electric shock because of the ground potential, whereby improving electrical safety of the ultrasonic probe with respect to an object.

[0074] Also, by the conductive film 76, the ground wire 84 and the chassis ground of the main apparatus, a closed space of electric potential is formed. In other words, since the main components of the ultrasonic probe 2c or the main circuits of the ultrasonic diagnostic apparatus are contained in the closed space of the ground potential, it is possible to prevent the influence of unwanted electric waves from outside or exerting a bad influence upon external apparatuses due to electromagnetic waves produced from the ultrasonic probe 2c itself.

[0075] Also, in the ultrasonic probe 2c of embodiment 4, the conductive film 76 is formed from the internal lower surface of the acoustic lens 26 over to its external surfaces, and is connected to the ground 108 via the conductive material 80 and the ground wire 84 which are highly reliable.

[0076] By such configuration, the conductive film 76 can be easily and securely connected to the ground wire via the conductive material from the conductive film formed from the internal lower surface of the acoustic lens over to its external surfaces, not from the sheet-like conductive film to be pulled out by the in mold forming, whereby it is possible to improve assuredness and workability in the implementation of the apparatus.

[0077] Also, by using highly reliable conductive material, damage of conductive material upon being fixed to the flexible substrate can be prevented.

[0078] Also, while the conductive material 80 and the ground wire 84 are illustrated only on the left side surface of the flexible substrate 72 in the diagram of Fig. 9, they may be placed on any of the four side surfaces of the

flexible substrate 72.

[0079] The conductive film 76 and the ground wire 84 can be connected directly. In this case, the mounting operation needs to be conducted cautiously since the conductive film 76 is thin in its thickness.

[0080] In the present embodiment, while the ground layer is provided on the back surface of the insulating film 78 (the opposite side from the object side), an intervening layer which does not conduct electricity is disposed as a film body 44 shown in Fig. 4 in a part of the cMUT chip between the ground layer and the electrode in the cMUT chip (for example, the electrode 46 in Fig. 4). Therefore, since the electrode in the cMUT chip (for example, the electrode 46 shown in Fig. 4) and the ground layer are not in contact as disclosed in Patent Document 2, there is an advantage also that the drive voltage for transmitting/receiving ultrasonic waves can be applied to the electrode on the ultrasonic wave transmitting/receiving side (object side) in the cMUT chip.

Embodiment 5

[0081] Next, embodiment 5 will be described referring to Fig. 11. Fig. 11 shows an ultrasonic probe 2d related to embodiment 5. Fig. 11 is equivalent to the cross-sectional view of the flat plane "A" shown in Fig. 2.

[0082] While the conductive material 80 is described to be fixed on the external side surfaces of the flexible substrate 72 in embodiment 4, it can be fixed on any place formed by a material capable of fixing the conductive material 80. In embodiment 5, a conductive material 80d is fixed on the internal side surfaces of the ultrasonic probe cover 25.

[0083] The conductive film 76 is connected to the ground 108 on the main apparatus side via a conductive material 80d and a ground wire 84d.

The conductive material 80d is fixed on the internal side surfaces of the ultrasonic probe cover 25. The ground wire 84d is connected to the conductive material 80d via a connecting portion 82d by a method such as soldering. In this manner, in embodiment 5, it is possible to connect the conductive film and the ground wire easily and unfailingly as in embodiment 4 while securing high reliability.

[0084] While the conductive material 80d and the ground wire 84d are shown only on the left side of the internal side surfaces in the ultrasonic probe cover 25 of in Fig. 11, they can be placed on the internal side surface of any four sides in the ultrasonic probe cover 25.

Embodiment 6

[0085] Next, embodiment 6 will be described referring to Fig. 12.

Fig. 12 shows an ultrasonic probe 2e related to embodiment 6. Fig. 12 is equivalent to the cross-sectional view of the flat plane "A" shown in Fig. 2.

[0086] While it is described that one layer of insulation

is provided in embodiment 1 - embodiment 5, two layers of insulation is provided in embodiment 6 having the ground layer therebetween.

[0087] Between the acoustic lens 26 and the adhesion layer 90 on the cMUT chip 20, the insulating film 78 as an upper insulation layer, the conductive film 76 as a ground layer and the insulating film 74 as a lower insulating layer are formed. The conductive film 76 is formed between the insulating film 78 and the insulating film 74.

[0088] In other words, from the direction that the ultrasonic probe 2 is applied to an object, the acoustic 26, insulating film 78, conductive film 76 and insulating film 74 are layered in order. For details, the insulating film 78 is formed on the lower surface of the acoustic lens 26, the conductive film 76 is formed by depositing the Cu film on the lower surface of the insulating film 78, and the insulating film 74 is formed on the lower surface of the conductive film 76.

[0089] In this manner, in embodiment 6, between the ultrasonic wave transmitting/receiving surface and the cMUT chip, two layers of insulation layers are comprised having a conductive film therebetween as electric leakage preventing means for preventing the leakage of electricity from the electrode in the cMUT chip to the object. By such configuration, it is possible to improve safety by increasing insulation of the ultrasonic probe.

[0090] Also, while the ground layer is provided between the two layers of insulating films 74 and 78 in the present embodiment, the insulating film 74 is further disposed between the ground layer and the electrode in the cMUT chip as an intervening layer which does not conduct electricity (for example, the electrode 46 in Fig. 4). Therefore, since the electrode in the cMUT chip (for example, the electrode 46 in Fig. 4) and the ground layer are not in contact as disclosed in Patent Document 2, there is an advantage that the drive voltage for transmitting/receiving ultrasonic waves can be applied also to the electrode on the ultrasonic wave transmitting/receiving surface side (object side) in the cMUT chip.

Embodiment 7

[0091] Next, embodiment 7 will be described referring to Fig. 13.

Fig. 13 shows an ultrasonic probe 2f related to embodiment 7. Fig. 13 is equivalent to the cross-sectional view of the flat plane "A" shown in Fig. 2.

[0092] While the insulating film 74 is described as being provided in the region indicated as region 23 in embodiment 6, an insulating film 74f in embodiment 7 is not provided in the region 23.

[0093] In this manner, in embodiment 7, the insulating film 74f is not placed on the cMUT chip 20, thus there is no influence of the insulating film 74f on the ultrasonic waves transmitted/received in the cMUT chip 20, whereby improving the acoustic characteristics.

[0094] In the above-described embodiment, it is desirable to make the film thickness of the conductive layer

in the range of 0.1 μm , and the film thickness of the insulation layer in the range of 1 μm . By making the respective film thickness of the insulation layer and the conductive layer, the influence to the ultrasonic waves transmitted/received in the cMUT chip (influence or attenuation to pulse and frequency characteristic) can be restrained.

[0095] As for the method to form the films, the method to perform the in mold forming on the insulating sheet attached with the conductive film at the same time as forming the acoustic lens, or the method for forming the insulating film and the conductive film by chemical deposition can be used. While the film can be formed at low cost in the in-mold forming method, the limit of the film thickness is in the range of 10 μm . On the other hand, the film thickness can be in the range of 1 μm in the film forming by deposition method.

[0096] While the region where the insulating film is not provided within the region 23 is "74f" in the present embodiment, it may be "78" or both.

[0097] The ultrasonic probe and ultrasonic diagnostic apparatus may be configured by properly combining the above-described embodiments.

Embodiment 8

[0098] Fig. 14 shows an ultrasonic probe 2g related to embodiment 8.

In the ultrasonic probe 2g, a ground layer 76g is placed on the lower surface of the acoustic lens 26, an insulation layer 78g is further placed on the upper surface (the object side) of the acoustic lens 26, and an insulating film 74g is placed on the back surface of the ground layer 76g.

Embodiment 9

[0099] Fig. 15 shows an ultrasonic probe 2h related to embodiment 9.

The ultrasonic probe 2h is the case that the two layers of insulation layers having a conductive film therebetween is comprises as electric leakage preventing means for preventing the leakage of electricity from the electrode in the cMUT chip to an object, and an example that the conductive material 80 for connecting a conductive film 76h and the ground wire 84 is fixed on the flexible substrate 72.

[0100] While preferable embodiments of the ultrasonic probe and ultrasonic diagnostic apparatus related to the present invention have been described above referring to the attached diagrams, the description herein of specific embodiments is not intended to limit the invention to the particular forms described. On the contrary, it is apparent to those skilled in the art that the intension is to cover all modifications, equivalents and alternations falling within the spirit and scope of invention as defined by the appended claims.

Claims

1. An ultrasonic probe comprising:

a plurality of vibration elements wherein the electromechanical coupling coefficient or sensitivity varies in accordance with bias voltage;
a cMUT chip for transmitting/receiving ultrasonic waves;
an acoustic lens disposed on the upper part of the cMUT chip; and
a backing layer placed on the lower part of the cMUT chip,

characterized in further comprising electric leakage preventing means for preventing the leakage of electricity from the electrode in the cMUT chip to an object to be examined, on the ultrasonic wave transmitting/receiving surface side of the acoustic lens or between the acoustic lens and the cMUT chip.

2. The ultrasonic probe according to claim 1, wherein the electric leakage preventing means is at least one layer of insulation layer.

3. The ultrasonic probe according to claim 1, wherein the electric leakage preventing means is at least one layer of ground layer which is disposed between the acoustic lens and the cMUT chip.

4. The ultrasonic probe according to claim 3, **characterized in** comprising an intervening layer between the ground layer and the electrode comprised in the cMUT chip.

5. The ultrasonic probe according to claim 4, wherein the intervening layer is an insulation layer formed by the material which does not conduct electricity, and is comprised between the ground layer and the cMUT chip.

6. The ultrasonic probe according to claim 4, wherein the intervening layer is a part of the cMTU chip, and is comprised on the ultrasonic wave transmitting/receiving surface side of the electrode.

7. The ultrasonic probe according to claim 6, wherein the intervening layer is a film body.

8. The ultrasonic probe according to claim 2, wherein the insulation layer is comprised on the ultrasonic wave transmitting/receiving surface side of the acoustic lens.

9. The ultrasonic probe according to claim 2, wherein the insulation layer is comprised along the opposite surface from the object side of the acoustic lens.

10. The ultrasonic probe according to claim 2, wherein the insulation layer is comprised on the surface of the object side of the cMUT chip.
11. The ultrasonic probe according to claim 2, **characterized in that** at least one layer of ground layers is comprised on the opposite side from the object side of the insulation layer. 5
12. The ultrasonic probe according to claim 3, wherein: 10
- a flexible substrate is placed from peripheral border of the upper surface the backing layer over to the surfaces in four sides; and
- the ground layer is connected to the ground via a conductive material and a ground wire fixed on the external side surface of the flexible substrate. 15
13. The ultrasonic probe according to claim 3 comprising: 20
- a flexible substrate placed from peripheral border of the upper surface of the backing layer over to the side surfaces in four directions; and 25
- an ultrasonic probe cover placed over the side surfaces of the ultrasonic probe in four directions,
- wherein the ground layer is connected to a ground via the conductive material and the ground wire fixed on the internal side surfaces of the ultrasonic probe cover. 30
14. The ultrasonic probe according to claim 1, wherein the electric leakage preventing means is placed between the acoustic lens and the cMUT chip, and is configured by a layer of ground layer and two layers of insulation layers having the ground layer therebetween. 35 40
15. The ultrasonic probe according to claim 14, **characterized in that** a part of at least one layer of the two layers of insulation layers is not disposed in at least a part of the portion corresponding to the object side of the plurality of vibration elements. 45
16. The ultrasonic probe according to claim 2, wherein the insulation layer is a silicon oxide or a paraxylene film. 50
17. The ultrasonic probe according to claim 3, wherein the ground layer is a Cu film. 55

FIG. 1

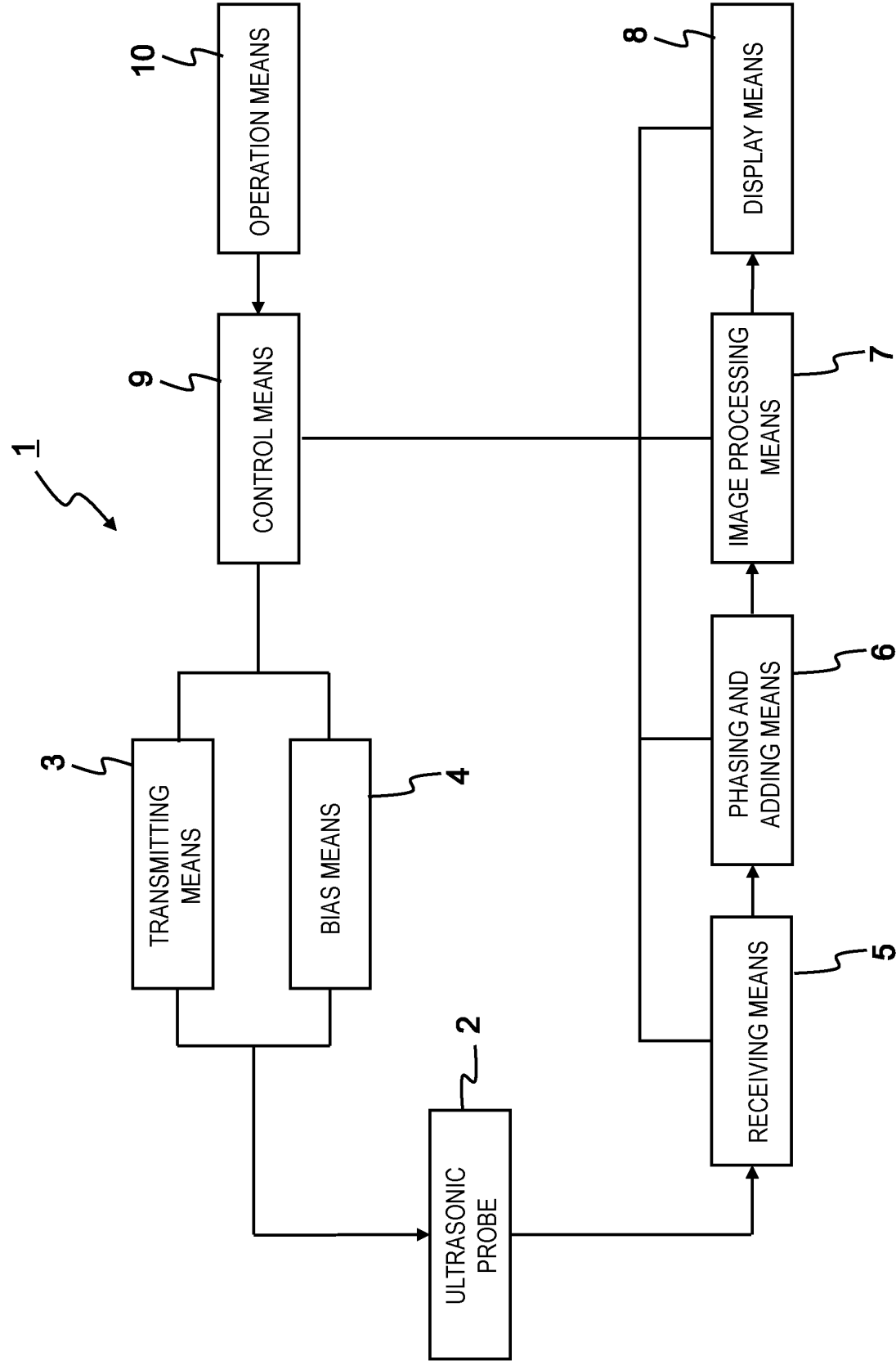


FIG. 2

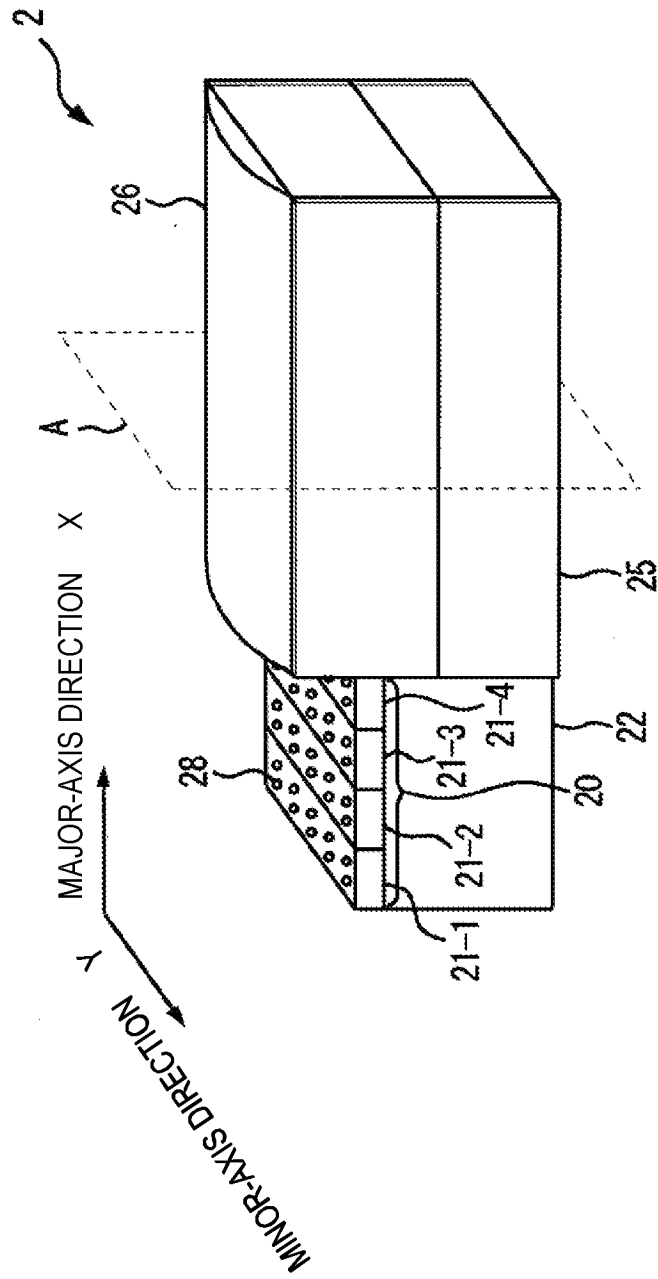


FIG. 3

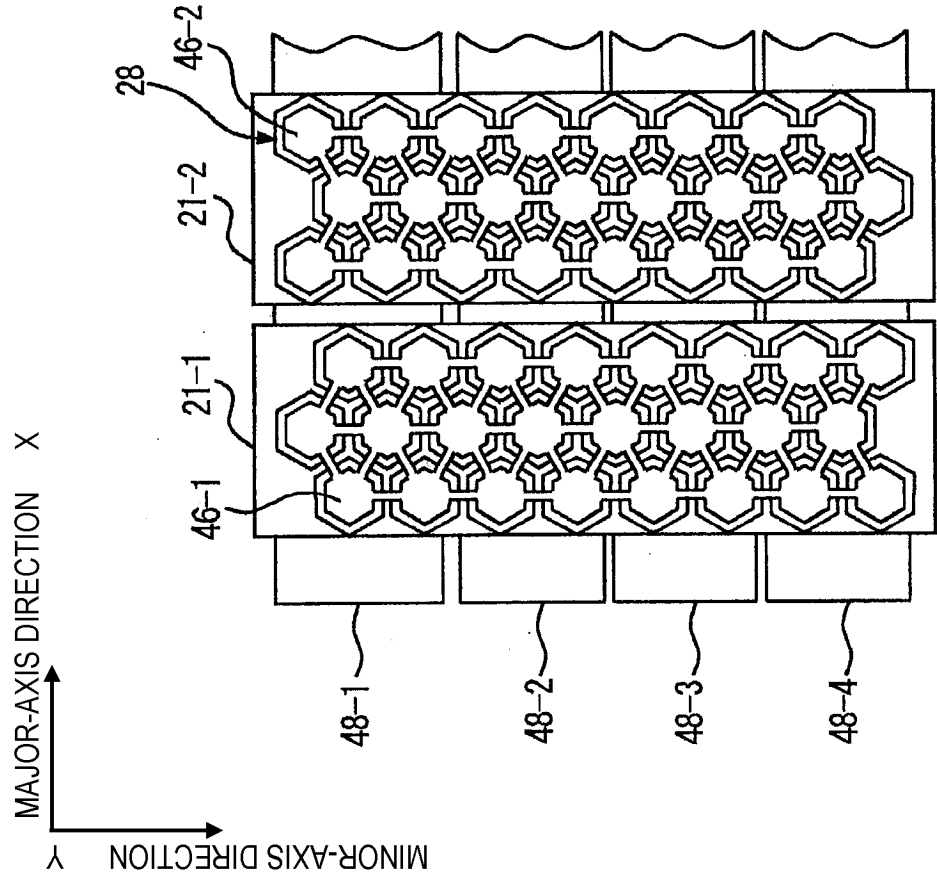


FIG. 4

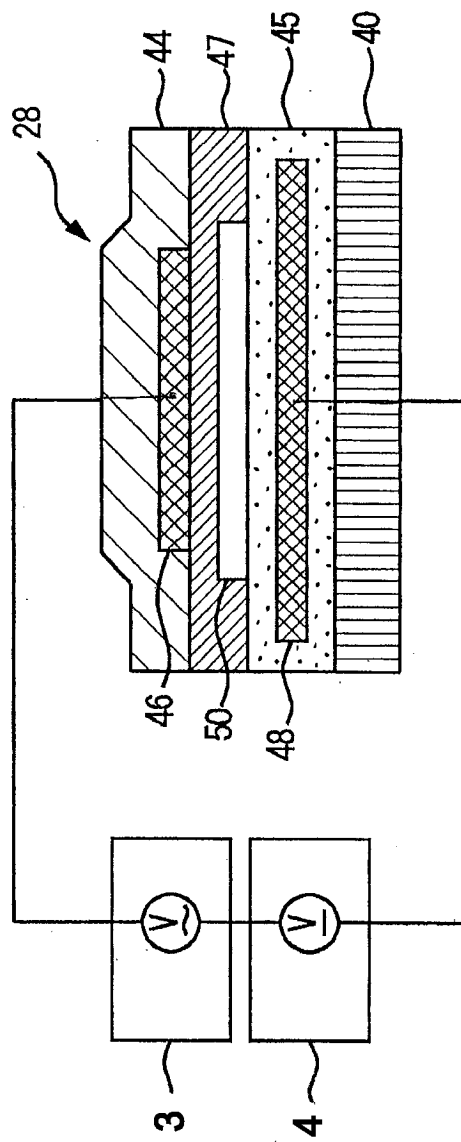


FIG. 5

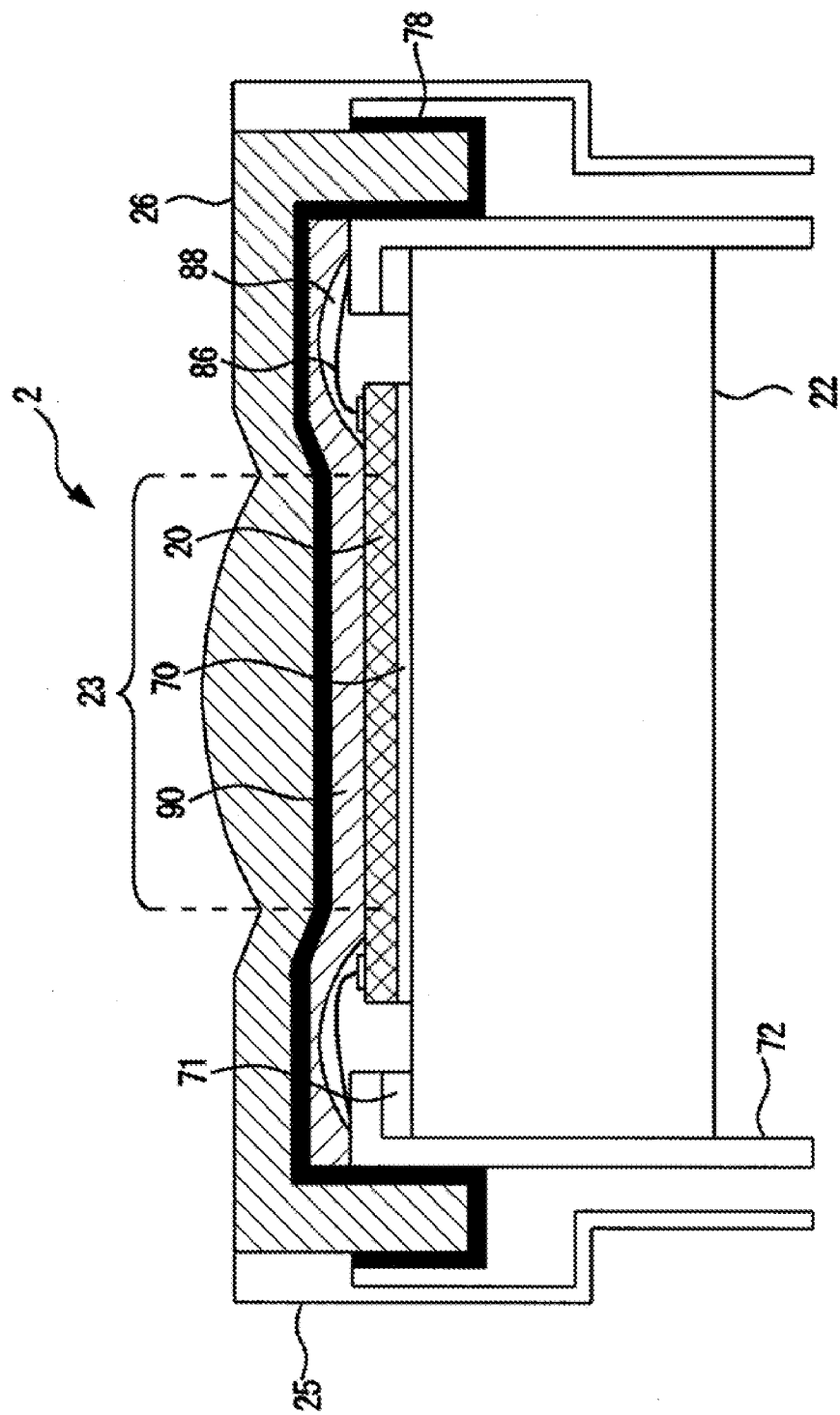


FIG. 6

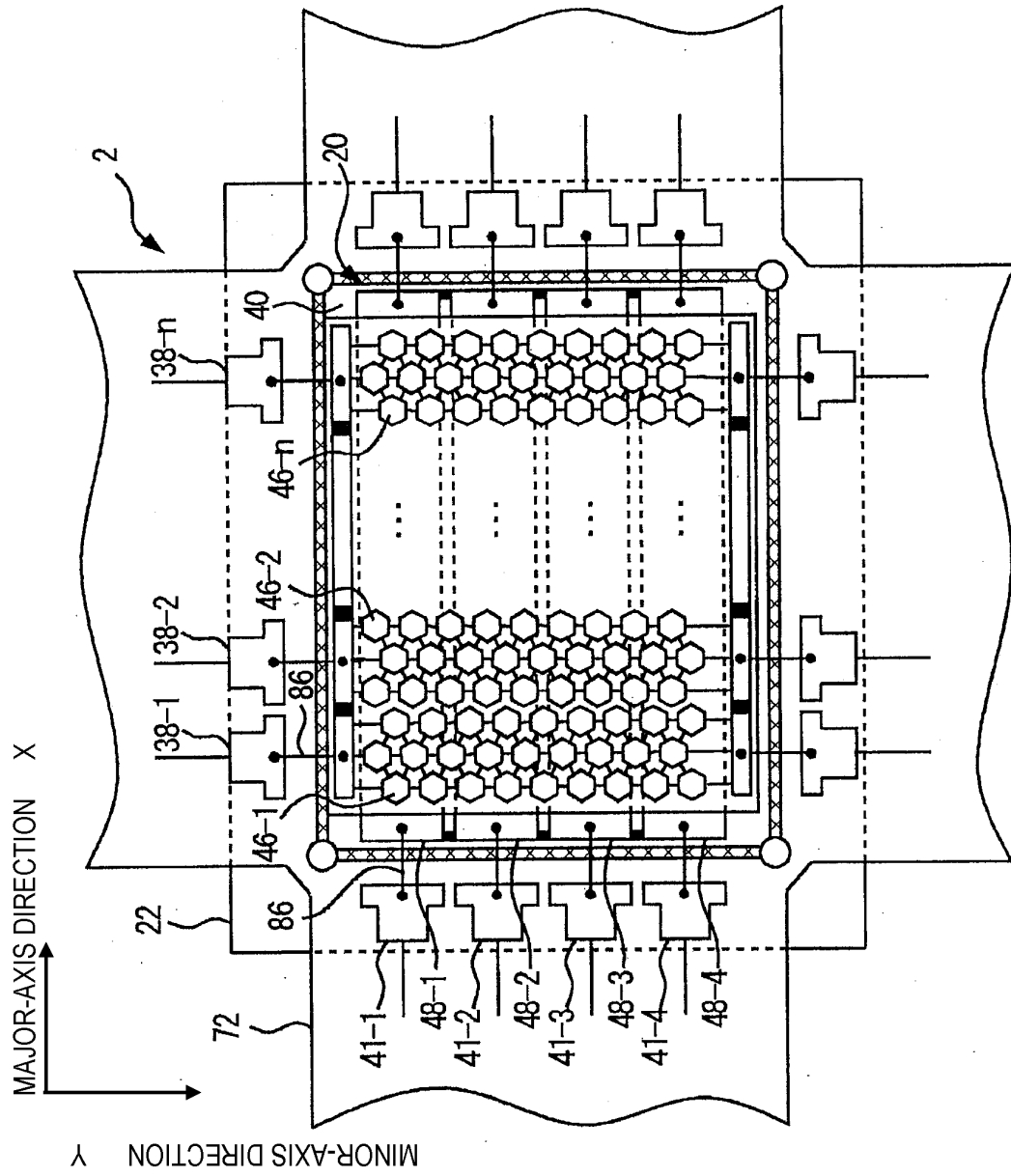


FIG. 7

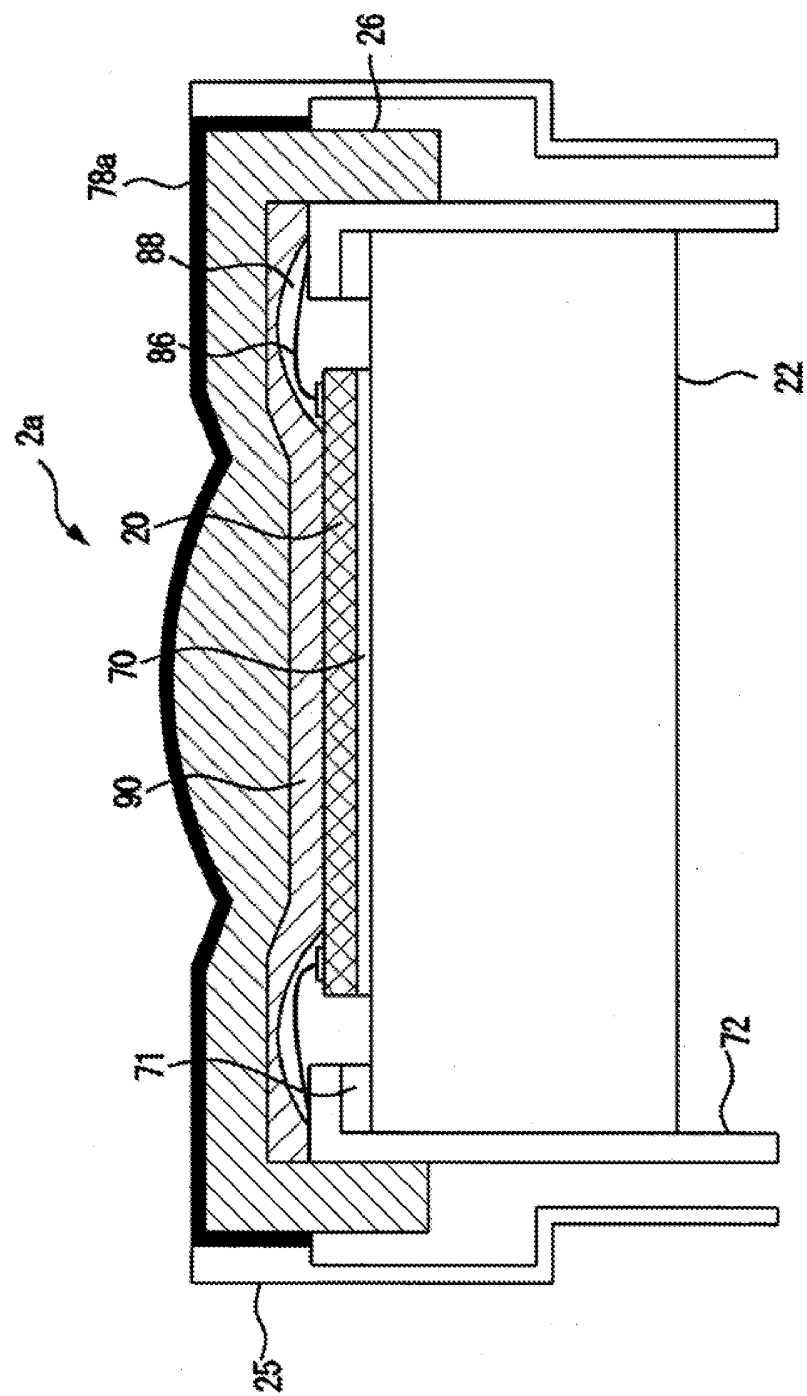


FIG. 10

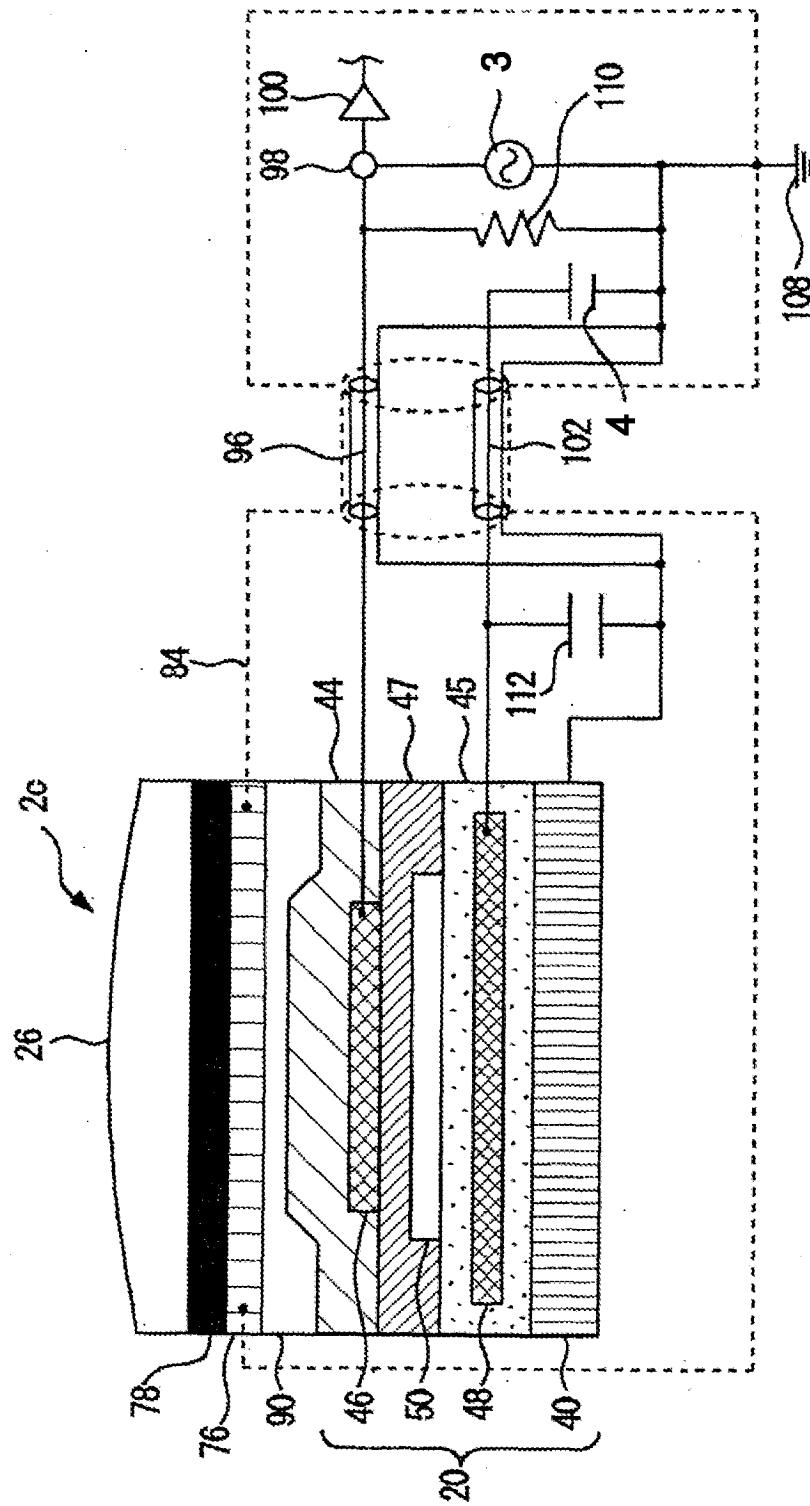


FIG. 11

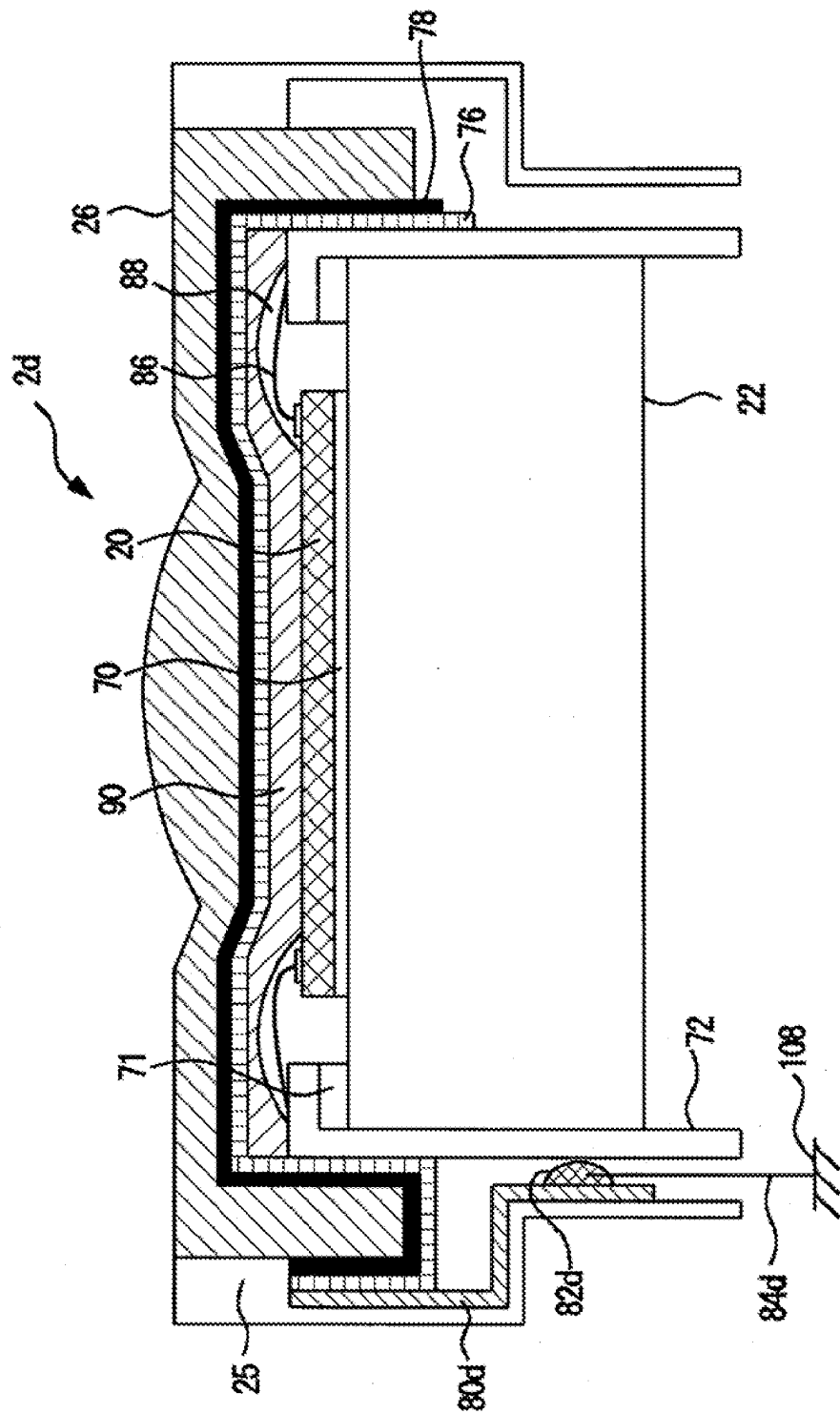


FIG. 12

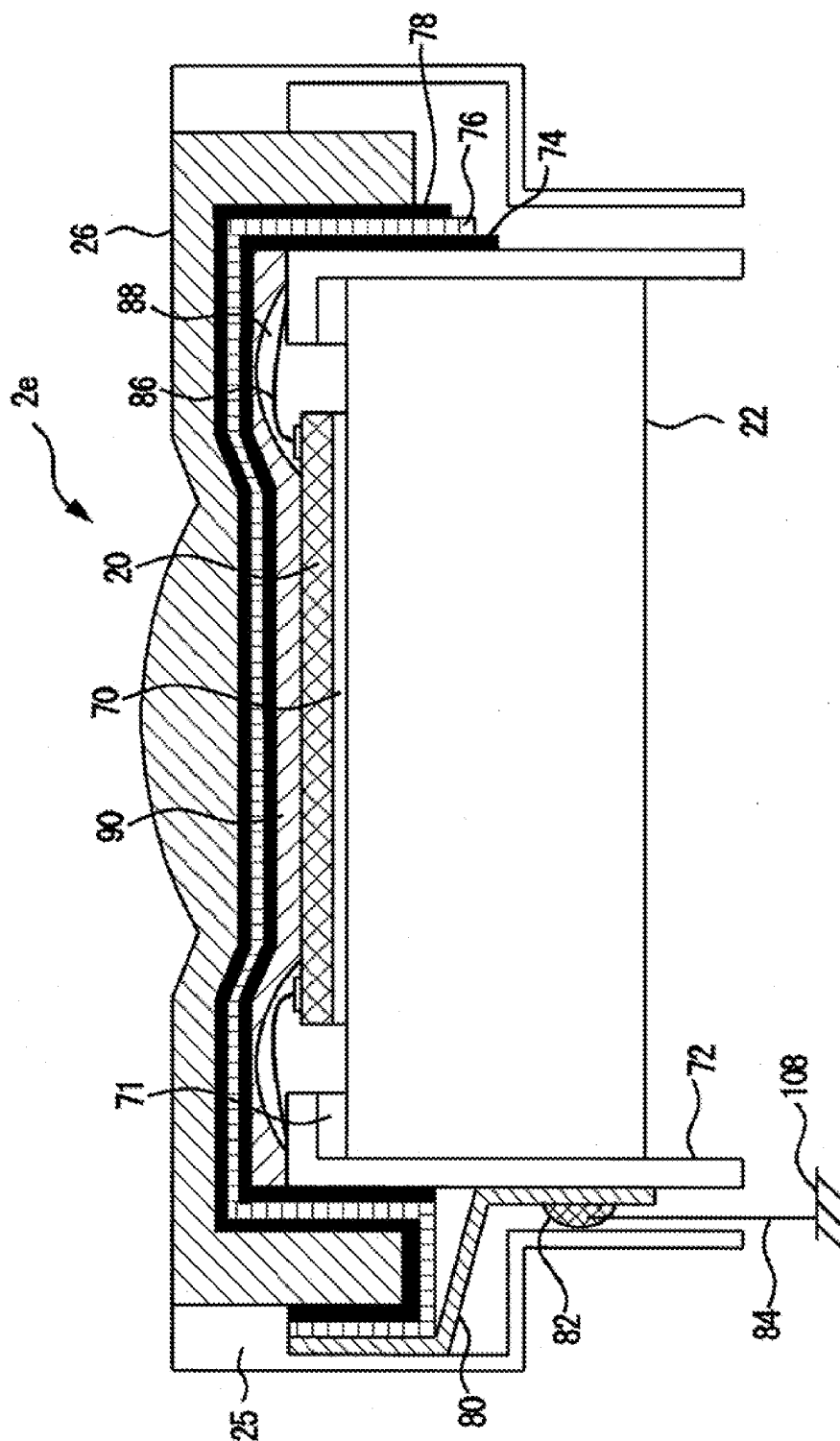


FIG. 13

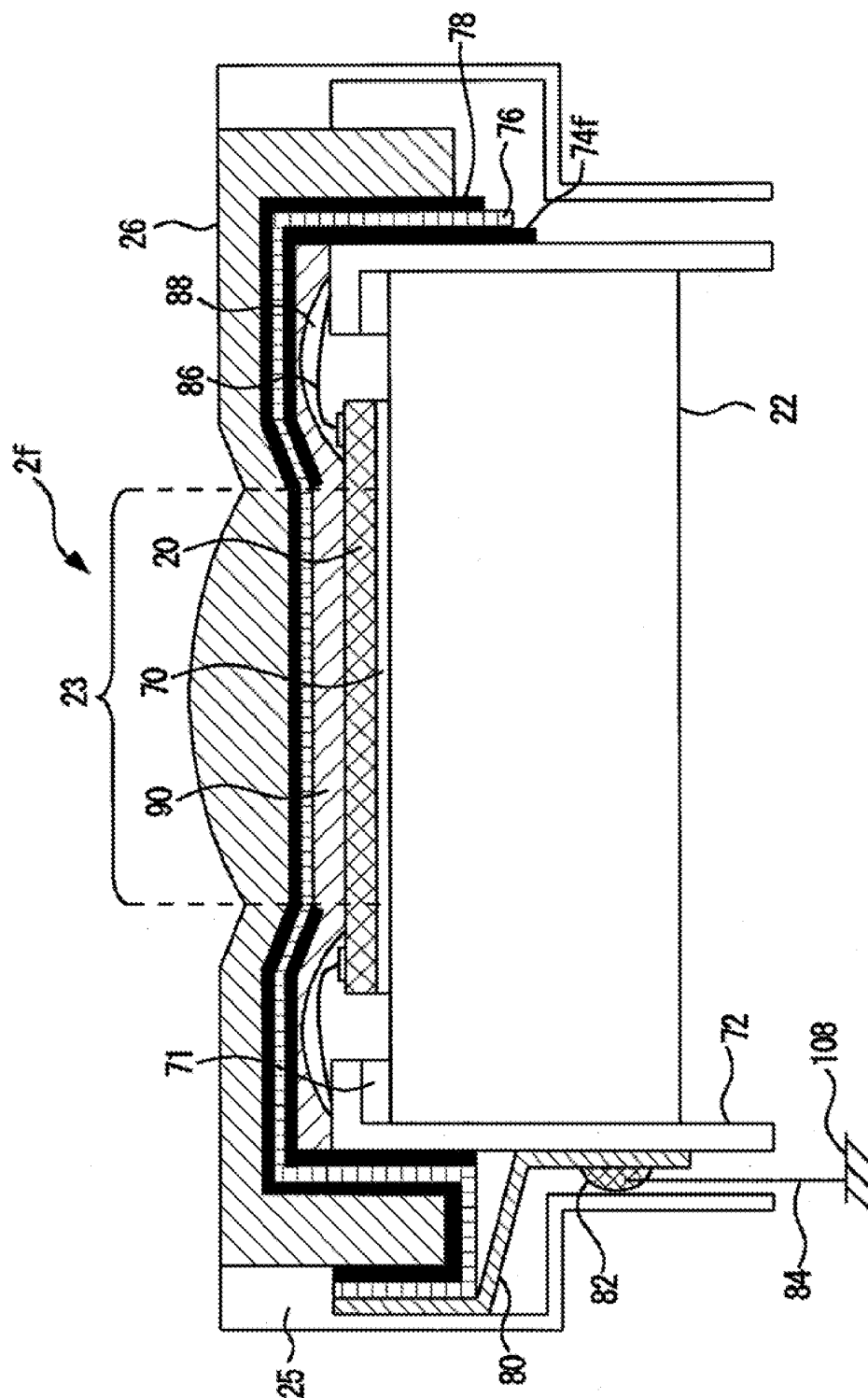


FIG. 14

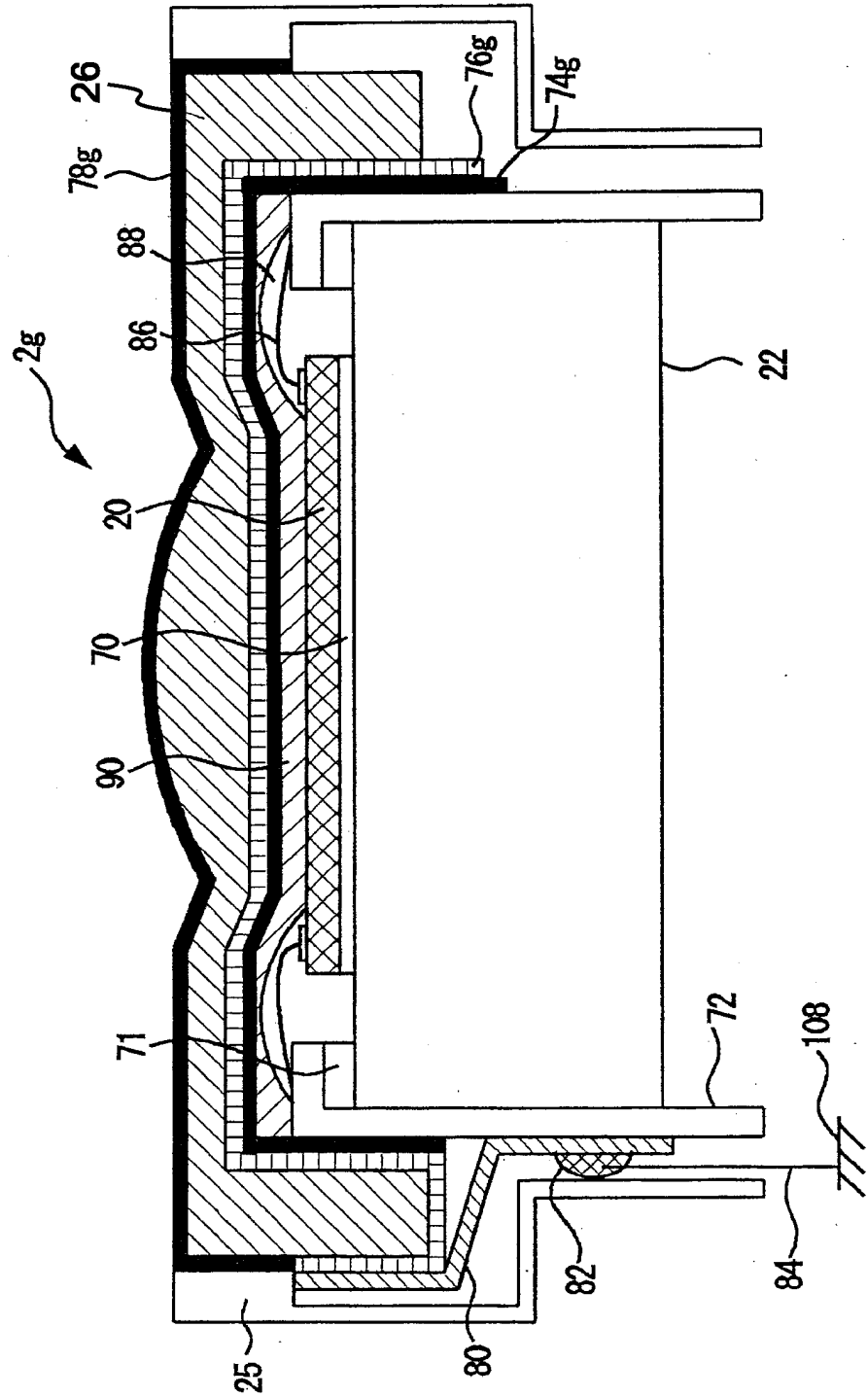
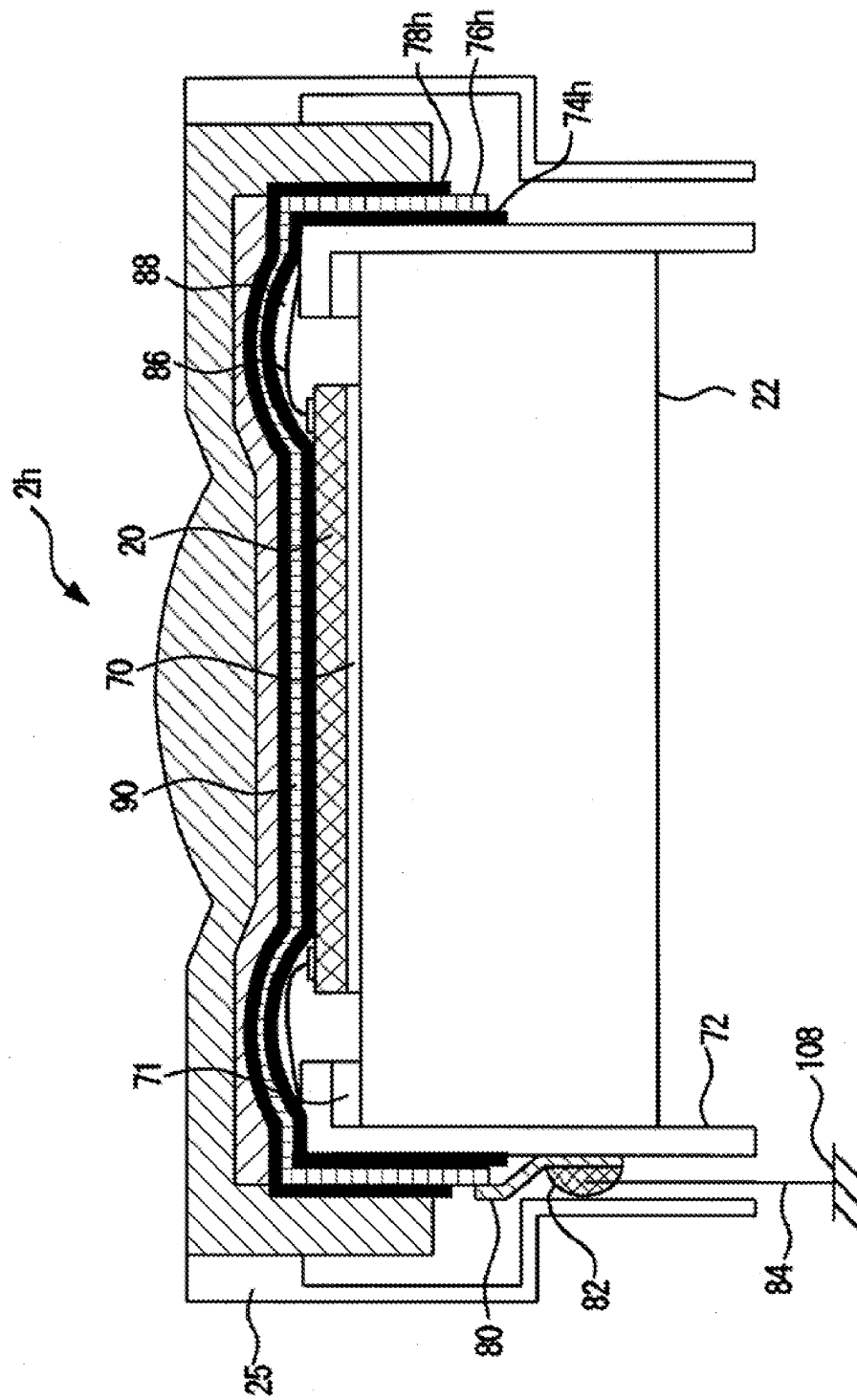


FIG. 15



INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2007/071516

A. CLASSIFICATION OF SUBJECT MATTER

H04R19/00(2006.01) i, A61B8/00(2006.01) i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H04R19/00, A61B8/00

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Jitsuyo Shinan Koho	1922-1996	Jitsuyo Shinan Toroku Koho	1996-2008
Kokai Jitsuyo Shinan Koho	1971-2008	Toroku Jitsuyo Shinan Koho	1994-2008

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X Y	JP 2006-166985 A (Fuji Photo Film Co., Ltd.), 29 June, 2006 (29.06.06), Par. Nos. [0052] to [0055]; Figs. 11 to 14 & US 2006/0184035 A1	1, 2, 9, 10, 16 8
Y	JP 06-205772 A (Toshiba Corp.), 25 July, 1994 (25.07.94), Par. Nos. [0027] to [0036]; Fig. 3 (Family: none)	8

☐

Further documents are listed in the continuation of Box C.

☐

See patent family annex.

* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

19 February, 2008 (19.02.08)

Date of mailing of the international search report

04 March, 2008 (04.03.08)

Name and mailing address of the ISA/

Japanese Patent Office

Authorized officer

Facsimile No.

Telephone No.

Form PCT/ISA/210 (second sheet) (April 2007)

INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2007/071516

Box No. II Observations where certain claims were found unsearchable (Continuation of item 2 of first sheet)

This international search report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. ☐ Claims Nos.:
because they relate to subject matter not required to be searched by this Authority, namely:

2. ☐ Claims Nos.:
because they relate to parts of the international application that do not comply with the prescribed requirements to such an extent that no meaningful international search can be carried out, specifically:

3. ☐ Claims Nos.:
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

Box No. III Observations where unity of invention is lacking (Continuation of item 3 of first sheet)

This International Searching Authority found multiple inventions in this international application, as follows:

Because of the reason given below, the international application includes two groups of inventions which do not satisfy the requirement of unity of invention.

First group of inventions: claims 1, 2, 8-10, and 16

Second group of inventions: claims 3-7, 11-15, and 17

(Continued to extra sheet)

1. ☐ As all required additional search fees were timely paid by the applicant, this international search report covers all searchable claims.
2. ☐ As all searchable claims could be searched without effort justifying additional fees, this Authority did not invite payment of additional fees.
3. ☐ As only some of the required additional search fees were timely paid by the applicant, this international search report covers only those claims for which fees were paid, specifically claims Nos.:

4. ☒ No required additional search fees were timely paid by the applicant. Consequently, this international search report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.: 1, 2, 8-10, and 16

Remark on Protest
the

- ☐ The additional search fees were accompanied by the applicant's protest and, where applicable, payment of a protest fee.
- ☐ The additional search fees were accompanied by the applicant's protest but the applicable protest fee was not paid within the time limit specified in the invitation.
- ☐ No protest accompanied the payment of additional search fees.

INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2007/071516

Continuation of Box No. III of continuation of first sheet (2)

The search was made on claims 1, 2, 8 as "the first (main) group of inventions". However, the search has revealed that the technical feature of claims 1 and 2 is not novel since it is disclosed as a prior art in Document 1: JP 2006-166985 A (Fuji Photo Film Co., Ltd.), 29 June, 2006 (29.06.06), [0052]-[0055], Figs. 11-14.

The "cMUT chip" in claim 1 is disclosed as the "electrostatic oscillator" produced in the semiconductor manufacturing process in Document 1. The "acoustic lens" in claim 1 is disclosed as the "acoustic lens" in Document 1. The "backing layer" in claim 1 is disclosed as the "backing member" of Document 1. The "insulating layer" in claim 2 is disclosed as "the protection insulating layer" in Document 1.

Accordingly, the technical feature of claims 1 and 2 cannot be "a special technical feature" within the meaning of PCT Rule 13.2, second sentence.

When claim 8 is compared to the aforementioned prior art, the "(temporary) special technical feature" of the first group of inventions is that "the insulating layer is arranged on the ultrasonic transmission/reception surface of the acoustic lens".

Moreover, when claim 11 (second group of invention) is compared to the aforementioned prior art technique, the (temporary) special technical feature" of the second group of inventions is that "at least one ground layer is provided at the side of the insulating layer opposite to the examinee side".

Consequently, there is no technical relationship between the first and the second group of inventions involving one or more of the same or corresponding special technical features.

It should be noted that the inventions of claims 9, 10, 16 are equivalent to the invention of claim 2 added by a conventional technique and accordingly, they are classified into the same group as claim 2. Moreover, the inventions of claims 3-7, 12-15, 17 have the same special technical feature as claim 11 and accordingly, they are classified into the same group as claim 11.

REFERENCES CITED IN THE DESCRIPTION

This list of references cited by the applicant is for the reader's convenience only. It does not form part of the European patent document. Even though great care has been taken in compiling the references, errors or omissions cannot be excluded and the EPO disclaims all liability in this regard.

Patent documents cited in the description

- US 5894452 A [0004]
- JP 2007235795 A [0004]

专利名称(译)	超声波探头和使用该超声波探头的超声波装置		
公开(公告)号	EP2091265A4	公开(公告)日	2010-12-15
申请号	EP2007831248	申请日	2007-11-06
[标]申请(专利权)人(译)	株式会社日立医药		
申请(专利权)人(译)	日立医疗器械股份有限公司		
当前申请(专利权)人(译)	日立医疗器械股份有限公司		
[标]发明人	SANO SHUZO SAKO AKIFUMI KOBAYASHI TAKASHI IZUMI MIKIO		
发明人	SANO, SHUZO SAKO, AKIFUMI KOBAYASHI, TAKASHI IZUMI, MIKIO		
IPC分类号	H04R19/00 A61B8/00 B06B1/02		
CPC分类号	A61B8/4281 A61B8/4455 A61B8/4483 B06B1/0292 H04R19/005		
代理机构(译)	厄尼、托拜厄斯		
优先权	2006302627 2006-11-08 JP		
其他公开文献	EP2091265A1		
外部链接	Espacenet		

摘要(译)

提供一种超声波探头，包括：cMUT芯片，具有多个振动元件，其机电耦合系数或灵敏度根据偏置电压而改变，并且发送和接收超声波；布置在cMUT芯片上方的声透镜；和布置在cMUT芯片下方的背衬层。防漏装置设置在声透镜的超声波发射/接收表面侧或声透镜和cMUT芯片之间。防漏电装置例如是诸如接地层的绝缘层。通过使用这种结构，可以提供一种能够防止从超声波探头漏电到待检查对象的超声波探头，从而提高电气安全性和使用该探头的超声波诊断装置。