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(72) Inventors:
• **FRANCK, Christoph**
5656 AE Eindhoven (NL)
• **MUELLER, Erwin**
5656 AE Eindhoven (NL)

(74) Representative: **de Haan, Poul Erik et al**
Philips International B.V.
Philips Intellectual Property & Standards
High Tech Campus 5
5656 AE Eindhoven (NL)

(71) Applicant: **Koninklijke Philips N.V.**
5656 AE Eindhoven (NL)

(54) **AN APPARATUS AND METHOD FOR MEASURING ELECTRODE IMPEDANCE DURING ELECTROPHYSIOLOGICAL MEASUREMENTS**

(57) The present disclosure is related to methods, systems and apparatus for performing electrophysiological measurements utilizing three or more electrodes attached to a patient. The system in various embodiments may include three or more electrodes attached to the patient and at least one analog-to-digital converter with external circuitry electrically coupled to the electrodes.

The system may further include a microprocessor for driving the analog-to-digital conversion process, various inputs and variable frequency current outputs electrically coupled to the microprocessor for receiving signals from the electrodes and sending driven current signals to the electrodes.

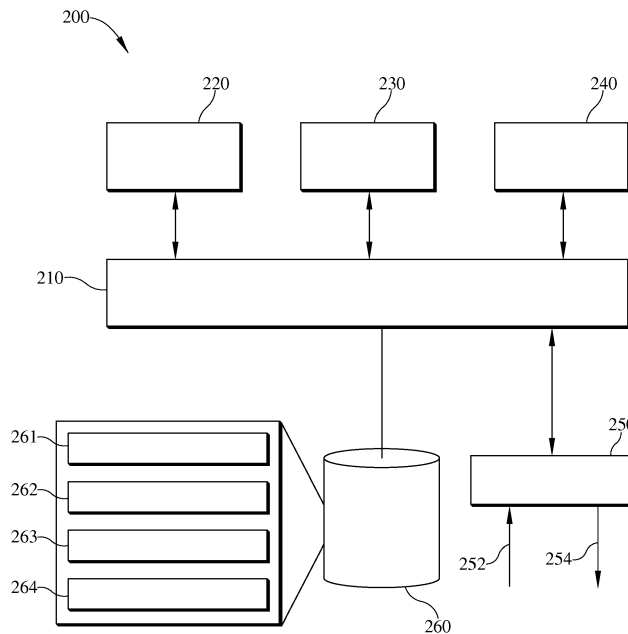


FIG. 1

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Description

TECHNICAL FIELD

[0001] The present disclosure is directed generally to a system for measuring impedance in an electrode. More particularly, but not exclusively, various methods and apparatus disclosed herein relate to systems and methods for measuring the impedances in an array of electrodes used in electrophysiological measurements on human patients, such as those electrodes used in electrocardiography.

BACKGROUND

[0002] The measurement of electrical signals transmitted in biological tissue is commonly practiced in many branches of science, and particularly medicine. Electrophysiological measurements are often taken in studying and diagnosing the human heart. Most systems used to conduct electrophysiological measurements rely on physiological electrodes that require an electrode to skin contact to operate. Commonly used examples of these electrode sensors include electroencephalography (EEG), electrocardiography (EKG), electromyography (EMG) and electrooculography (EOG), to name a few. Each of these systems requires placement of physiological electrodes in contact with the skin to conduct the corresponding electrical signals to signal processing systems. The quality of these electrical signals is highly dependent upon the quality of the connection between the electrode and the patient's body, as well as the quality of the connection between the electrode and the signal processing system itself.

[0003] Poor signal quality is often caused by high impedance electrode-skin contact, which naturally results in a poor physiological signal. In turn the poor signal is often difficult to separate from electrical noise or distortion in the system. These signal deficiencies may result in incorrect diagnoses and all the attendant dangers that follow. Furthermore, the overall impact of poor physiological signal quality is indeterminate test results, increased medical technician time, hardware changes, and of course the expense associated with all these difficulties. Thus measuring the impedance at each electrode can help correct and rectify many signal errors in an electrophysiological measurement system and is therefore highly desirable.

[0004] The measurements of electrical activity in the heart typically involve measuring the voltage difference between a weighted sum of one set of input electrodes and a weighted sum of another set of input electrodes positioned at various points on the body. Typically, one electrode which is excluded from the group of electrodes that make up the weighted sum is an output electrode for a feedback loop which reduces common mode interference. The quality of the output electrode signal measurement is directly affected by the impedance of the skin-

electrode interfaces. For example, when high impedance skin-electrode interfaces are present the signals measured usually suffer from high or increased noise and/or distortion. High impedance can be caused by improperly attached electrodes, insufficient skin preparation or aging of the electrode, for example.

[0005] Some prior art systems for measuring electrode impedance require a system to sequentially drive variable currents through each electrode in a multi-electrode system. The electrode impedance can be measured by driving an alternating current through each electrode and measuring the corresponding voltage between this electrode and another input electrode which does not have an input current of the same frequency and phase injected. This impedance measurement method is made more complex if the attached/detached and the input/output status of the electrodes can or do change during the measurement and the impedance measurement is required to function with certain electrodes detached. While many of these prior art techniques are operable, the hardware required to perform this impedance testing is typically expensive and difficult to operate.

[0006] Systems that measure electrode impedance using injected currents with one or two frequencies face several problems. Initially, not all subsets of attached electrodes may be capable of measuring impedance in these systems due to all the attached electrodes having AC current of the same frequency injected into them. In this case, the system can either not display an impedance value, or switch the frequency injected into one or more attached electrodes to a different value.

[0007] The above-described frequency switching technique has disadvantages. These systems may require additional circuitry to measure impedances accurately thereby necessitating complex internal state machines. Furthermore, these systems require calibration for each frequency that can be injected in a certain electrode, or introduce measurement error due to the circuitry having different gains at different AC voltage frequencies if only one calibration point is used.

[0008] Thus there is a need in the art for a system and method of reliably and efficiently monitoring electrode impedance in an electrophysiological measurement system while minimizing system down time and data loss.

SUMMARY

[0009] The present disclosure is related to methods, systems and apparatus for performing electrophysiological measurements utilizing three or more electrodes attached to, for example, a patient. The system in various embodiments may include three or more electrodes attached to the patient and at least one analog-to-digital converter with external circuitry electrically coupled to the electrodes. The system may further include a microprocessor for driving the analog-to-digital conversion process, various inputs and outputs electrically coupled to the microprocessor for receiving signals from the elec-

trodes and sending driven current signals to the electrodes. The microprocessor or controller may include integral digital signal processing, and further a user interface may be employed for displaying or communicating the measured electrophysiological voltage at each electrode. In some aspects of the invention the system will include circuitry or hardware for generating at least three alternating current (AC) signals of different frequencies and injecting an AC current of one assigned frequency into each of the electrically coupled electrodes. The system of the invention may further comprise peripherals integrated into the microcontroller, such as timers, pulse width modulation outputs, or digital to analog converters for generating AC signals. Alternatively, these functions may be performed by external circuits electrically coupled to the microcontroller.

[0010] In some aspects and embodiments one input signal frequency may be assigned to each electrode, and the assignment of frequencies to the extant electrodes is chosen in a way that allows both the electrophysiological measurement and the skin-electrode impedance measurement to function if specified subsets of the electrodes are attached to the patient. In one embodiment of the invention voltages at each electrode are sampled using continuous analog-to-digital conversion of the signal measured. For the measurement of each skin-electrode impedance of an input electrode, three electrodes are utilized: 1) the input electrode (E1) whose impedance is to be measured; 2) an attached (in contact with the patient) input electrode (E2) that does not have the same frequency injected as the E1 electrode and acts as a reference, and 3) the output electrode which must be connected so it can act as a sink for the currents injected at E1 and E2.

[0011] In accordance with some aspects of the invention the microprocessor or control constructs a vector by subtracting the voltage at E2 from E1; thus reducing common mode interference. The amplitude of the voltage caused by the injected current is then extracted from the signal using standard digital signal processing methods, including, but not limited to, digital filtering, discrete/fast Fourier transform, and/or the Goertzel algorithm. The impedance of a particular skin-electrode interface may, in some embodiments, be calculated from the voltage, for example by using gain/offset calibration coefficients that take into account the tolerance of the value of the injected current, the amplitude response of the external circuitry connected to the analog to digital converter (ADC) input, and the impedance of any external circuitry which appears in series with the skin-electrode impedance, which is subtracted from the measured impedance.

[0012] In various embodiments of the invention the user interface may include a low power output display such as an LED light display or an e-ink screen for displaying patient data. In some aspects of the invention an operator interface may further include a touchpad or keypad powered by the energy storage system or electrical generator to facilitate entry of patient information and system infor-

mation.

[0013] In further alternative embodiments of the invention an instruction set including a method for measurement and calculation of the impedance of individual electrodes in the system is provided in storage memory.

[0014] As used herein for purposes of the present disclosure, the term "electrode" is generally used to describe various devices that establish electrical connections between a biological tissue or a patient and monitoring circuitry for the purposes of measuring electrical voltage or current changes in various biological tissues. Electrodes can be utilized in various measurement systems for performing physiological measurements including, but not limited to electroencephalography (EEG), electrocardiography (EKG), electromyography (EMG) and electrooculography (EOG).

[0015] The term "processor" or alternatively "controller" is used herein generally to describe various apparatus relating to the operation of one or more light sources. A controller can be implemented in numerous ways (e.g., such as with dedicated hardware) to perform various functions discussed herein. A "processor" is one example of a controller which employs one or more microprocessors that maybe programmed using software (e.g., microcode) to perform various functions discussed herein. A controller may be implemented with or without employing a processor, and also may be implemented as a combination of dedicated hardware to perform some functions and a processor (e.g., one or more programmed microprocessors and associated circuitry) to perform other functions. Examples of controller components that may be employed in various embodiments of the present disclosure include, but are not limited to, conventional microprocessors, application specific integrated circuits (ASICs), programmable logic controllers and field-programmable gate arrays (FPGAs).

[0016] In various implementations, a processor or controller may be associated with one or more storage media (generically referred to herein as "memory," e.g., volatile and non-volatile computer memory such as RAM, PROM, EPROM, and EEPROM, floppy disks, compact disks, optical disks, magnetic tape, etc.). In some implementations, the storage media may be encoded with one or more programs that, when executed on one or more processors and/or controllers, perform at least some of the functions discussed herein. Various storage media maybe fixed within a processor or controller or maybe transportable, such that the one or more programs stored thereon can be loaded into a processor or controller so as to implement various aspects of the present disclosure discussed herein. The terms "program" or "computer program" are used herein in a generic sense to refer to any type of computer code (e.g., software or microcode) that can be employed to program one or more processors or controllers.

[0017] The term "user interface" as used herein refers to an interface between a user or an operator and one or more devices that enables interaction between the us-

er and the device(s). Examples of user interfaces that may be employed in various implementations of the present disclosure include, but are not limited to, switches, potentiometers, buttons, dials, sliders, a mouse, keyboard, keypad, various types of game controllers (e.g., joysticks), track balls, display screens, various types of graphical user interfaces (GUIs), touch screens, microphones and other types of sensors that may receive some form of human-generated stimulus and generate a signal in response thereto.

[0018] It should be appreciated that all combinations of the foregoing concepts and additional concepts discussed in greater detail below (provided such concepts are not mutually inconsistent) are contemplated as being part of the inventive subject matter disclosed herein. In particular, all combinations of claimed subject matter appearing at the end of this disclosure are contemplated as being part of the inventive subject matter disclosed herein. It should also be appreciated that terminology explicitly employed herein that also may appear in any disclosure incorporated by reference should be accorded a meaning most consistent with the particular concepts disclosed herein.

BRIEF DESCRIPTION OF THE DRAWINGS

[0019] In the drawings, like reference characters generally refer to the same parts throughout the different views. Also, the drawings are not necessarily to scale, emphasis instead generally being placed upon illustrating the principles of the disclosure, wherein:

Fig. 1 graphically illustrates an exemplary hardware diagram for implementing aspects of the invention, in accordance with various embodiments;

Fig. 2 depicts an exemplary electrical diagram in accordance with various embodiments of the invention;

Fig. 3 depicts an exemplary electrical diagram in accordance with various embodiments of the invention;

DETAILED DESCRIPTION

[0020] FIG. 1 illustrates an exemplary hardware 200 diagram for implementing a system for measuring impedance of electrodes. The device 200 includes a processor 220, memory 230, user interface 240, communication interface 250, and storage 260 interconnected via one or more system buses 210. It will be understood that FIG. 1 constitutes, in some respects, an abstraction and that the actual organization of the components of the hardware 200 may be more complex than illustrated.

[0021] The processor 220 may be any hardware device capable of executing instructions stored in memory 230 or storage 260 or otherwise processing data. As such, the processor may include a microprocessor, field programmable gate array (FPGA), application-specific integrated circuit (ASIC), or other similar devices.

[0022] The memory 230 may include various memo-

ries such as, for example L1, L2, or L3 cache or system memory. As such, the memory 230 may include static random access memory (SRAM), dynamic RAM (DRAM), flash memory, read only memory (ROM), or other similar memory devices. It will be apparent that, in embodiments where the processor includes one or more ASICs (or other processing devices) that implement one or more of the functions described herein in hardware, the software described as corresponding to such functionality in other embodiments may be omitted.

[0023] The user interface 240 may include one or more devices for enabling communication with a user such as an administrator. For example, the user interface 240 may include a display, a mouse, and a keyboard for receiving user commands. In some embodiments, the user interface 240 may include a command line interface or graphical user interface that may be presented to a remote terminal via the communication interface 250.

[0024] The communication interface 250 may include one or more devices for enabling communication with other hardware devices. For example, the communication interface 250 may include a network interface card (NIC) configured to communicate according to the Ethernet protocol. Additionally, the communication interface 250 may implement a TCP/IP stack for communication according to the TCP/IP protocols. Furthermore, communication interface 250 may include a plurality of inputs 252 and outputs 254, either analog, digital, or both, of varying signal types, to enable processor 220 to receive electrical signals from a variety of sensors, for example electrodes in this instance, as well as output electrical signals, for example electrical signals at predetermined amplitudes and current levels. Inputs 252 and outputs 254 may be provided to communicate directly to processor 220 or maybe send and receive digital signals to and from processor 220 respectively. Various alternative or additional hardware or configurations for the communication interface 250 will be apparent.

[0025] The storage 260 may include one or more machine-readable storage media such as read-only memory (ROM), random-access memory (RAM), magnetic disk storage media, optical storage media, flash-memory devices, or similar storage media. In various embodiments, the storage 260 may store instructions for execution by the processor 220 or data upon which the processor 220 may operate. For example, the storage 260 may store a base operating system 261 for controlling various basic operations of the hardware 200. Storage 260 may further store a plurality of instruction sets 262, 263, 264 that operate to perform necessary processor functions required to operate system 10.

[0026] It will be apparent that various information described as stored in the storage 260 may be additionally or alternatively stored in the memory 230. In this respect, the memory 230 may also be considered to constitute a "storage device" and the storage 260 may be considered a "memory." Various other arrangements will be apparent. Further, the memory 230 and storage 260 may both

be considered to be "non-transitory machine-readable media." As used herein, the term "non-transitory" will be understood to exclude transitory signals but to include all forms of storage, including both volatile and non-volatile memories.

[0027] While the hardware 200 is shown as including one of each described component, the various components may be duplicated in various embodiments. For example, the processor 220 may include multiple microprocessors that are configured to independently execute the methods described herein or are configured to perform steps or subroutines of the methods described herein such that the multiple processors cooperate to achieve the functionality described herein. Further, where the device 200 is implemented in a cloud computing system, the various hardware components may belong to separate physical systems. For example, the processor 220 may include a first processor in a first server and a second processor in a second server.

[0028] Referring now to Figs. 2 and 3, and in accordance with various embodiments of the invention, the system 10 for measuring skin to electrode impedance of a patient 1 may include three or more electrodes E1, E2, E3, ... En, attached to a patient, each of said electrodes En having an electrode to skin impedance, denoted in Fig. 2 as R1-Rn. Each electrode En is electrically coupled to an analog-to-digital converter 310 that provides an input 252 to processor 220, thereby providing processor 220 with a digital representation of a voltage at En. In addition to driving the analog-to-digital conversion and digital signal processing functions of the invention, controller 220 provides outputs 254 to user interface 240, thereby displaying or communicating the measured electrophysiological voltage at any electrode En.

[0029] In accordance with some embodiments of the invention controller 220 further comprises a plurality of outputs 254, which can comprise many different known circuits or hardware, for generating at least three AC current signals, denoted in Fig. 2 as I(f1)-I(fn) of different frequencies. These current signals I(fn) are then electrically coupled or injected into each of the electrodes, by supplying one assigned frequency I(fn) to each electrode. In accordance with some embodiments various peripheral circuitry may be integrated into the processor 220, for example timers, pulse width modulation outputs 254, or digital to analog converters that may be used to generate the AC current signals. Alternatively, in some embodiments these signal generating functions may be produced or provided by external circuitry and driven by instructions or outputs 252 from processor 220.

[0030] In some embodiments the impedance measurement Rn of one electrode-skin interface requires three electrodes to operate. First electrode E1, whose impedance is being measured, is provided an AC current I(f1) at a predetermined frequency f1. A second electrode E2 is provided an AC current I(f2) at a predetermined second frequency f2 in which $f2 \neq f1$, that is to say E2 is not supplied with an AC current of the same frequency as E1. A

third electrode Eout acts as a sink for all the currents I(fn) injected into the other electrodes, and thus serves as the output electrode for the physiological measurement being conducted.

5 **[0031]** In this embodiment of the invention the voltages at electrode E1 which is caused by the injected currents I(f1) in conjunction with the skin-electrode impedance R1, is measured in system 10 by continuous sampling and analog-to-digital conversion of the voltages at E1 and E2 by AD converters 310. Processor 220 then subtracts the two voltages measured at E1 and E2 to reduce common mode interference. Processor 220 then subsequently extracts the amplitude of the voltage at E1 caused by the injected AC current output with known methods of digital signal processing. In some aspects and embodiments of the invention processor 220 processes the measured voltage signals by techniques including, but not limited to, a combination of digital filtering, discrete Fourier transform, fast Fourier transform, and/or the Goertzel algorithm. Once the amplitude of the voltage at electrode E1 is known, the impedance R1 of the skin-electrode interface is then derived from the voltage at E1, for example by using gain/offset calibration coefficients in accordance with known methods for calculating impedances.

20 **[0032]** In some embodiments of the invention as used in an exemplary ECG (electrocardiogram), system 10 maybe employed by utilizing three electrodes typically positioned on the right arm (RA), left arm (LA), and left leg (LL) of a patient. As is known in the art, the ECG electrode E positions RA, LA, RL, and LL are not necessarily on a patient's limbs per se, but are conventionally located on the right shoulder (RA), left shoulder (LA), right lower abdomen (RL) and left lower abdomen (LL). System 10, and specifically processor 220 is then capable of measuring capable of measuring one of three signal vectors: either vector I between RA-LA electrodes, vector II between RA-LL electrodes or vector III between LA-LL electrodes. Furthermore, processor 220 then selects whichever electrode is not used in the vector measurement, (the unused electrode) as an output electrode. By assigning one of three different frequencies f1, f2, f3 to each of the three electrodes (e.g. F1/RA, F2/LA, F3/LL), it is assured that for each of the two input electrodes selected there is another input electrode which does not have the same frequency injected, and the electrode-skin impedance measurement described in detail herein above utilizing three electrodes per skin-electrode interface can be accomplished for each of the two input electrodes.

35 **[0033]** In some embodiments and aspects of the invention, system 10 can be employed as an ECG system utilizing four electrodes (RA, LA, LL and Right Leg-RL), in which RL is always selected to be used as the output electrode when it is connected to the patient 1. In this embodiment the system 10 is required to measure at least one ECG vector between two electrodes RA, LA, LL if any three out of the four electrodes are connected. Furthermore, additional ECG vectors become available

to be used if all four electrodes are connected. By assigning one of the three frequencies f_1 , f_2 , f_3 to RA, LA, LL and assigning either f_1 , f_2 or f_3 to RL (e.g. $F1/RA$, $F2/LA$, $F3/LL$, $F1/RL$), the system 10 can then measure the ECG wave as required for patient diagnosis, and further measure the impedance R_n of each connected input electrode RA, LA, LL if any subset of three electrodes is electrically connected to the patient.

[0034] Referring again to Figs. 2 and 3, in a further embodiment system 10 can be employed as an ECG measurement system utilizing five or more electrodes (RA, LA, LL, RL, and, for example, chest electrodes V_1 , V_2 , ... V_n). Again, in multiple electrode ECG systems such as this one the RL electrode is always used as an output electrode if it is connected to the patient 1, and the system 10 is required to measure at least one ECG vector if any three out of the four electrodes RA, LA, LL, RL are connected to the patient 1. In this embodiment, the invention assigns one of the three frequencies f_1 , f_2 , f_3 to the RA, LA, LL electrodes respectively, and assigns one of f_1 , f_2 or f_3 to the RL, V_1 , V_2 ... V_n electrodes. (e.g. f_1/RA , f_2/LA , f_3/LL , f_1/RL , f_2/V_1 , f_1/V_2 ...). By using this frequency assignment, it is possible for system 10 to measure the ECG wave as required for patient 1 diagnosis and further measure the impedance of each connected input electrode if any subset of three electrodes out of RA/LA/LL/RL is connected.

[0035] In yet other aspects and embodiments, system 10 consists of an EEG system utilizing four or more electrodes E_n , in which the output electrode E_{out} does not change and the impedance measurement is required to work if at least two input electrodes are connected. In this embodiment of the instant invention, the impedance R_n measurement and determination technique uses as many frequencies as there are possible input electrodes E_n , whereby a unique individual signal frequency $I(f_n)$ is assigned to each input electrode E_n . The assignment of a unique frequency $I(f_n)$ assures that if at least two input electrodes E_1 , E_2 and the output electrode E_{out} or E_3 are connected to the patient 1, the skin-electrode interface impedance can be measured using the three-electrode method described herein above.

[0036] In various aspects of the invention it should be noted that the techniques and systems 10 disclosed herein, while discussing exemplary systems for ECG monitoring and diagnoses, are equally applicable to other electrophysiological measurements, including but not limited to electroencephalography (EEG), electrocardiography (EKG), electromyography (EMG) and electrooculography (EOG). In operation, the invention can be employed to detect electrode to skin impedance R_n by measuring the current(s) that an electrophysiological measurement device injects into the electrode E_n connections. If there are more than two different frequencies of injected currents, and the assignment of these frequencies to electrodes E_n is chosen in a way that respects specified subsets of electrodes and does not change during the measurement, it is possible to detect

all the required impedances R_n in the system 10 without the necessity of frequency switching and all the attendant difficulties and expense that known technique entails.

[0037] In some aspects of the invention system 10 may employ the following method for measuring the impedance R_1 of an electrode E_1 -skin interface of a patient 1. Initially system 10 processor 220 provides a plurality of signal outputs $I(f_1)$ - $I(f_n)$ (254) to a plurality of electrodes E_n that are placed in contact with the skin of a patient 1. In an exemplary but non-limiting three electrode system the frequency of the alternating current output injected into a first electrode E_1 is different from the alternating current output injected into a second electrode E_2 . In such a system a third electrode E_3 is selected as a current sink and output electrode. Each attached electrode E_n is electrically coupled to an analog to digital converter ADC, 310, which in turn supplies a digital output to an input 252 of processor 220 that is indicative of a voltage measured at the skin-electrode interface. This resultant voltage at E_1 and E_2 may then be subtracted to reduce common mode interference, and then the impedance at E_1 may be calculated by applying selected signal processing to the voltage at E_1 , to derive the amplitude of the E_1 voltage. Non-limiting examples of signal processing techniques that may be employed include digital filtering, discrete Fourier Transforms (DFT), Fast Fourier Transforms (FFT) and Goertzel algorithms. Once the E_1 voltage amplitude is derived, impedance at the E_1 skin-electrode interface is then calculated by applying the system offset calibration and gain coefficients to the derived voltage.

[0038] While various inventive embodiments have been described and illustrated in this specification, those of ordinary skill in the art may discern a variety of other systems or structures for performing the functions and/or obtaining the results and advantages described herein. Each of these variations and modifications is deemed to be within the scope of the inventive embodiments described herein. One of ordinary skill in the art will readily appreciate that all parameters, dimensions, materials, and configurations described herein are meant to be exemplary of the invention, and that the actual parameters, dimensions, materials, and configurations will depend upon the specific application or applications of the invention. Furthermore, those skilled in the art will recognize, or be able to ascertain using no more than routine experimentation, many equivalents to the specific inventive embodiments described herein. Accordingly, it is understood that the foregoing embodiments are presented by way of example only and that, within the scope of the appended claims and equivalents thereto, inventive embodiments may be practiced otherwise than as specifically described and claimed. Inventive embodiments of the present disclosure are directed to each individual feature, system, article, material, kit, and/or method described in this specification. Any combination of two or more such features, systems, articles, materials, kits, and/or methods, if such features, systems, articles, ma-

terials, kits, and/or methods are not mutually inconsistent, is included within the inventive scope of the present disclosure.

[0039] All definitions, as defined and used herein, should be understood to control over dictionary definitions, definitions in documents incorporated by reference, and/or ordinary meanings of the defined terms.

[0040] The indefinite articles "a" and "an," as used herein in the specification and in the claims, unless clearly indicated to the contrary, should be understood to mean "at least one."

[0041] The phrase "and/or," as used herein in the specification and in the claims, should be understood to mean "either or both" of the elements so conjoined, i.e., elements that are conjunctively present in some cases and disjunctively present in other cases. Multiple elements listed with "and/or" should be construed in the same fashion, i.e., "one or more" of the elements so conjoined. Other elements may optionally be present other than the elements specifically identified by the "and/or" clause, whether related or unrelated to those elements specifically identified. Thus, as a non-limiting example, a reference to "A and/or B", when used in conjunction with open-ended language such as "comprising" can refer, in one embodiment, to A only (optionally including elements other than B); in another embodiment, to B only (optionally including elements other than A); in yet another embodiment, to both A and B (optionally including other elements); etc.

[0042] As used herein in the specification and in the claims, "or" should be understood to have the same meaning as "and/or" as defined above. For example, when separating items in a list, "or" or "and/or" shall be interpreted as being inclusive, i.e., the inclusion of at least one, but also including more than one, of a number or list of elements, and, optionally, additional unlisted items. Only terms clearly indicated to the contrary, such as "only one of" or "exactly one of," or, when used in the claims, "consisting of," will refer to the inclusion of exactly one element of a number or list of elements. In general, the term "or" as used herein shall only be interpreted as indicating exclusive alternatives (i.e. "one or the other but not both") when preceded by terms of exclusivity, such as "either," "one of," "only one of," or "exactly one of." "Consisting essentially of," when used in the claims, shall have its ordinary meaning as used in the field of patent law.

[0043] As used herein in the specification and in the claims, the phrase "at least one," in reference to a list of one or more elements, should be understood to mean at least one element selected from any one or more of the elements in the list of elements, but not necessarily including at least one of each and every element specifically listed within the list of elements and not excluding any combinations of elements in the list of elements. This definition also allows that elements may optionally be present other than the elements specifically identified within the list of elements to which the phrase "at least

one" refers, whether related or unrelated to those elements specifically identified. Thus, as a non-limiting example, "at least one of A and B" (or, equivalently, "at least one of A or B," or, equivalently "at least one of A and/or B") can refer, in one embodiment, to at least one, optionally including more than one, A, with no B present (and optionally including elements other than B); in another embodiment, to at least one, optionally including more than one, B, with no A present (and optionally including elements other than A); in yet another embodiment, to at least one, optionally including more than one, A, and at least one, optionally including more than one, B (and optionally including other elements); etc.

[0044] It should also be understood that, unless clearly indicated to the contrary, in any methods claimed herein that include more than one step or act, the order of the steps or acts of the method is not necessarily limited to the order in which the steps or acts of the method are recited.

[0045] In the claims, as well as in the specification above, all transitional phrases such as "comprising," "including," "carrying," "having," "containing," "involving," "holding," "composed of," and the like are to be understood to be open-ended, i.e., to mean including but not limited to. Only the transitional phrases "consisting of" and "consisting essentially of" shall be closed or semi-closed transitional phrases, respectively, as set forth in the United States Patent Office Manual of Patent Examining Procedures, Section 2111.03. It should be understood that certain expressions and reference signs used in the claims pursuant to Rule 6.2(b) of the Patent Cooperation Treaty ("PCT") do not limit the scope.

35 Claims

1. A system for measuring the impedance if an electrode-skin interface of a patient, said system comprising:

a plurality of electrodes electrically coupled to said patient at a plurality of points, each of said plurality of electrodes electrically coupled to an analog to digital converter;

a processor having a plurality of inputs electrically coupled to said analog to digital converter of each electrode, for measuring a signal representative of voltage at each of said electrodes and;

a plurality of alternating current outputs arranged to supply currents at different frequencies to said plurality of electrodes.

2. The system of claim 1 comprising:

a digital signal processing system for calculating a voltage at an electrode.

- 3. The system of claim 2 wherein said digital signal processing system is arranged to perform digital filtering.
- 4. The system of claim 2 wherein said digital signal processing system is arranged to apply a discrete Fourier transform.
- 5. The system of claim 2 wherein said digital signal processing system is arranged to apply a fast Fourier transform.
- 6. The system of claim 2 wherein said digital signal processing system is arranged to apply a Goertzel algorithm.
- 7. The system of claim 1 wherein each of a subset of said plurality of electrodes is supplied with a unique frequency current input, and an output electrode is assigned to sink the currents from said plurality of electrodes.
- 8. The system of claim 1 wherein said plurality of electrodes comprises three electrodes for electrocardiography, and wherein each of said electrodes is supplied with a unique frequency current input.
- 9. The system of claim 8 wherein said third electrode is assigned as an output electrode and is arranged to be secured to the skin at the RL position of said patient.
- 10. A system for measuring the impedance of an electrode-skin interface of a patient, said system comprising:
 - at least three electrodes arranged to be electrically coupled to said patient at a plurality of points, each of said three of electrodes electrically coupled to an analog to digital converter;
 - a processor having a plurality of inputs electrically coupled to said analog to digital converters for measuring a signal representative of voltage at each of said electrodes and;
 - a plurality of alternating current signals arranged to carry currents at different frequencies to two of said three electrodes, and wherein the third of said electrode is designated as an output electrode.
- 11. The system of claim 10 wherein said processor includes a digital signal processor for determining the amplitude of the voltage at a one of said electrodes, and determining a skin-electrode impedance based upon said voltage.
- 12. The system of claim 11 wherein each of a subset of said three electrodes is supplied with a unique fre-

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quency current signal.

13. The system of claim 12 comprising:

a fourth electrode acting as an input electrode for an ECG system.

14. A method of determining the skin-electrode impedance in an electrophysiological measurement system having a plurality of electrodes secured to the skin of a patient, said method comprising the steps of:

- electrically coupling said plurality of electrodes to said patient at a plurality of points;
- electrically coupling said plurality of electrodes to an analog to digital converter;
- electrically coupling said analog to digital converters to a processor having a plurality of inputs for measuring a signal representative of voltage at each of said electrodes;
- supplying a plurality of alternating current output signals to at different frequencies to said plurality of electrodes;
- assigning one electrode as an output electrode; and

determining the electrode-skin impedance at a one of said plurality of electrodes by measuring a voltage at that electrode for a predetermined current signal.

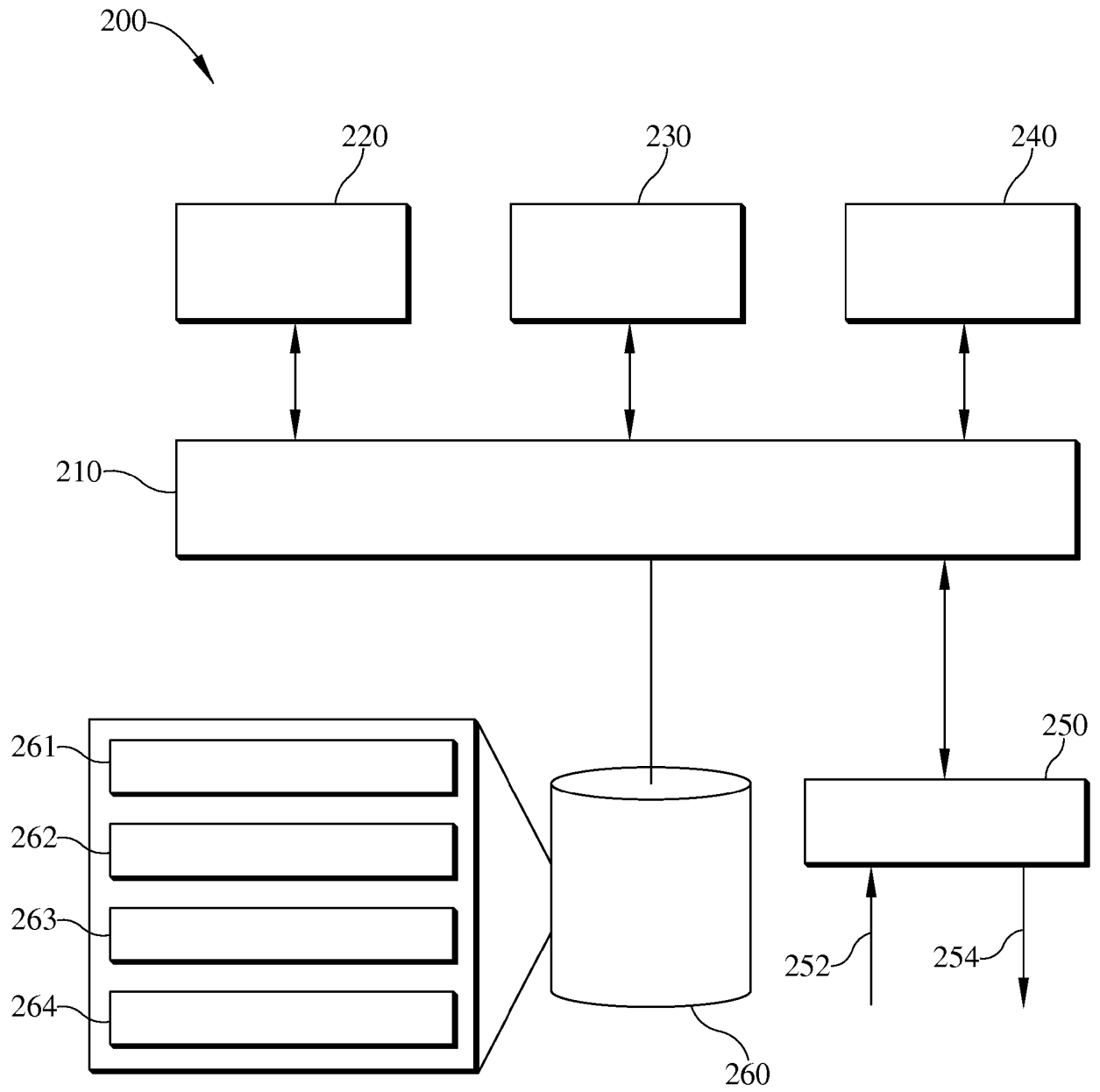


FIG. 1

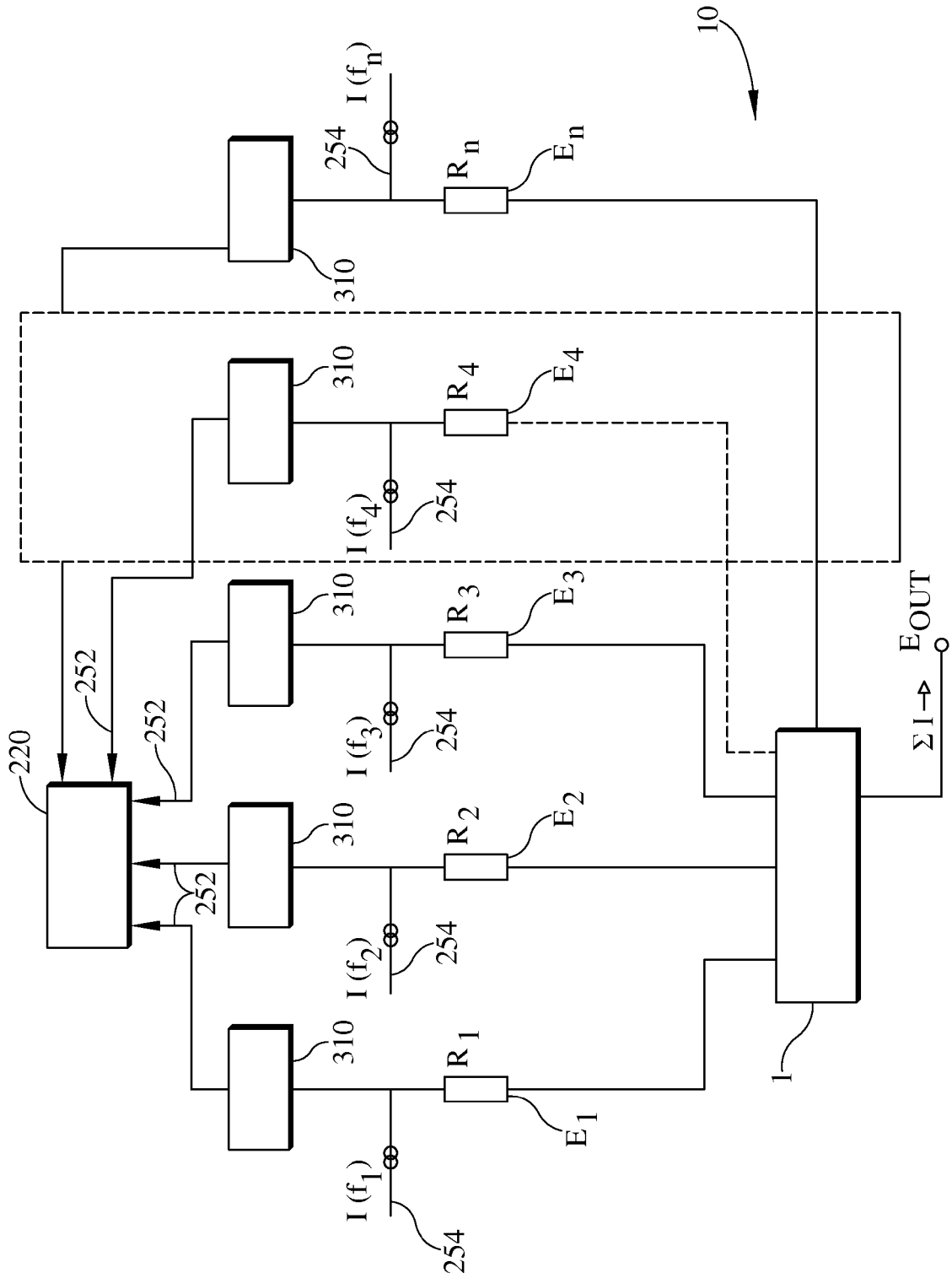


FIG. 2

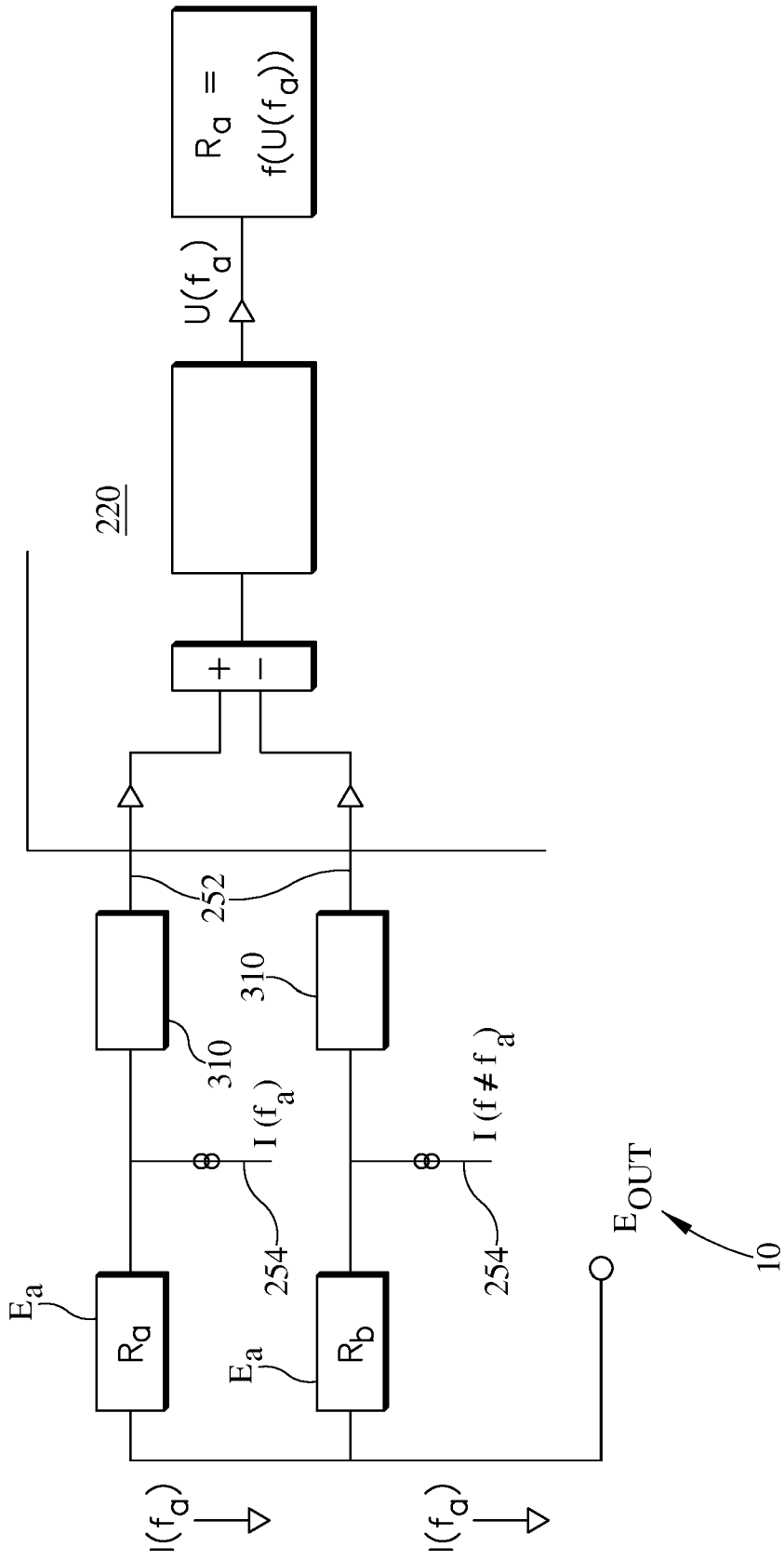


FIG. 3



EUROPEAN SEARCH REPORT

Application Number
EP 17 15 7122

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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
X	US 2014/194759 A1 (WEILAND JAMES MARTIN [US] ET AL) 10 July 2014 (2014-07-10) * abstract; figures 1,2 * * paragraphs [0007] - [0009], [0017] - [0027] *	1-14	INV. A61B5/04 A61B5/0428 A61B5/0408 A61B5/0424
X	US 2014/247058 A1 (MORTARA DAVID W [US]) 4 September 2014 (2014-09-04) * abstract; figures 1-3 * * paragraphs [0003] - [0005], [0011] - [0030] *	1-14	ADD. A61B5/00
X	US 2007/038257 A1 (GRAY JAMES M [US]) 15 February 2007 (2007-02-15) * abstract; figures 1-3 * * paragraphs [0008] - [0018], [0024] - [0038] *	1-3,7-14	TECHNICAL FIELDS SEARCHED (IPC) A61B A61N
A		4-6	
X	US 2008/051845 A1 (MENTELOS RICHARD A [US]) 28 February 2008 (2008-02-28) * abstract; figures 1-3 * * paragraphs [0007] - [0018], [0025] - [0045] *	1-14	
X	US 2003/006782 A1 (SHAMBROOM JOHN R [US] ET AL) 9 January 2003 (2003-01-09) * abstract; figures 1-6 * * paragraphs [0005], [0013] - [0024] *	1-14	
The present search report has been drawn up for all claims			
Place of search The Hague		Date of completion of the search 16 August 2017	Examiner Carta, Riccardo
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>			

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ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.

EP 17 15 7122

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This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on
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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2014194759 A1	10-07-2014	BR 112013031261 A2	25-04-2017
		CN 103717128 A	09-04-2014
		EP 2717766 A1	16-04-2014
		JP 6130829 B2	17-05-2017
		JP 2014519915 A	21-08-2014
		US 2014194759 A1	10-07-2014
		WO 2012168912 A1	13-12-2012
US 2014247058 A1	04-09-2014	US 2014247058 A1	04-09-2014
		US 2015327811 A1	19-11-2015
US 2007038257 A1	15-02-2007	NONE	
US 2008051845 A1	28-02-2008	EP 2077749 A1	15-07-2009
		EP 2460465 A1	06-06-2012
		JP 5249220 B2	31-07-2013
		JP 2010501253 A	21-01-2010
		US 2008051845 A1	28-02-2008
		WO 2008027237 A1	06-03-2008
US 2003006782 A1	09-01-2003	AT 378004 T	15-11-2007
		AU 2002327200 B2	14-08-2008
		BR 0210833 A	22-06-2004
		CA 2452367 A1	16-01-2003
		DE 60223548 T2	23-10-2008
		EP 1411830 A1	28-04-2004
		ES 2295399 T3	16-04-2008
		JP 4415215 B2	17-02-2010
		JP 2004531353 A	14-10-2004
		MX PA04000009 A	21-05-2004
		US 2003006782 A1	09-01-2003
		US 2005203437 A1	15-09-2005
		WO 03003921 A1	16-01-2003

专利名称(译)	一种用于在电生理学测量期间测量电极阻抗的装置和方法		
公开(公告)号	EP3363354A1	公开(公告)日	2018-08-22
申请号	EP20171157122	申请日	2017-02-21
[标]申请(专利权)人(译)	皇家飞利浦电子股份有限公司		
申请(专利权)人(译)	皇家飞利浦N.V.		
当前申请(专利权)人(译)	皇家飞利浦N.V.		
[标]发明人	FRANCK CHRISTOPH MUELLER ERWIN		
发明人	FRANCK, CHRISTOPH MUELLER, ERWIN		
IPC分类号	A61B5/04 A61B5/0428 A61B5/0408 A61B5/0424 A61B5/00		
CPC分类号	A61B5/04004 A61B5/04017 A61B5/04085 A61B5/04087 A61B5/0424 A61B5/0428 A61B5/04286 A61B5/6813 A61B5/7203 A61B5/7225 A61B5/725 A61B5/7257 A61B5/742 A61B2562/04		
代理机构(译)	德哈恩波尔ERIK		
外部链接	Espacenet		

摘要(译)

本公开涉及利用附接到患者的三个或更多个电极进行电生理学测量的方法，系统和设备。各种实施例中的系统可包括附接到患者的三个或更多个电极以及具有电耦合到电极的外部电路的至少一个模数转换器。该系统还可包括用于驱动模数转换过程的微处理器，电耦合到微处理器的各种输入和可变频率电流输出，用于接收来自电极的信号并将驱动电流信号发送到电极。

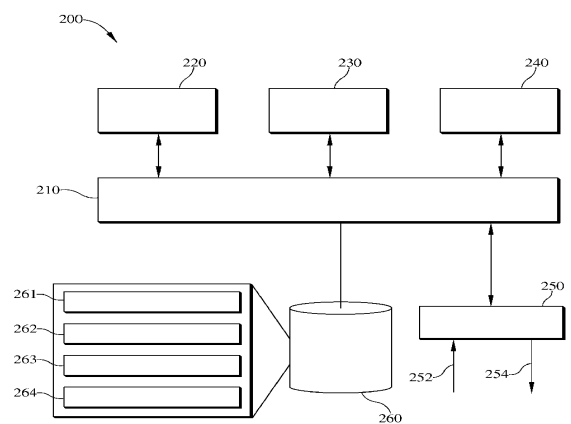


FIG. 1