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20

(72) 1 1608-9202

1 1630-17 1 101

(74)

:

(54)

가 , (blurring) , 1 , 2 1 가 1 , 1 (OFF)

3

1
2
3 1
4 5 3
6 2
7 6
8 13 3 8
14 9
15 14
16 15
* *
100, 200 : 1 110, 210 :
120 : 130, 230 :
140, 240 : 150, 220 : 2

(Electroluminescent Display Panel)

(Electroluminescen) (LCD) 가 , 가
가 , 가 LCD 가 , 가
(CNS, Car Navigation System), TV 가 ,
가 가 ,
가
1
가 (SL) (GL) 2
(13) 2 (11)(13) , 1 가 (GL) 2
(15) , 1 (11) (11,13) 2
(15) 1 3 (17) ,
(Vdd) (20) 1 4 1 PMOS
4 (19) , 1 4
(GL) 가 , (SL) (15) (15)
, 3 (17) 1, 2 (11,13) 가 4 (19) (15)
(20) , 1, 2 (11,13) (15)
가 (15)
(1f) (2f) (blurring)

가 .

(OFF)

가

(OFF)

1 PMOS

1 PMOS

1 PMOS

(OFF)

(OFF)

(GL)

(SL)

(GL)

가

(100)

1

가

1

(110)

2

(100)

(110)

2

(120)

(100)가

(120)

(110)

(130)

(130)

(OFF)

(140)

1

(100)

(GL)

(SL)

1 PMOS

(P1)

(GL)

(P2)

1

PMOS

(P1)

2 PMOS

(110)

(

120)

1 PMOS

(P1)

(Vdd)

3 PMOS

(P3)

3 PMOS

(P3)

(110)

4 PMOS

(P4)

(130)

4 PMOS

(P4)

1 (140) PMOS (110) (140) (110)
 가 , (LOW) 가 ()
 (110) (130)
 , 4 (110) , (140) (E) (110) 가 ()가
 (140) (130)
 , (GL) 가 (SL)
 , (110) 1, 2 PMOS (P1)(P2)
 (110) (P4) 3 PMOS (P3) 가 4 PMOS
 (130)가 (130)가
 t1 가 t1 가
 5 (1f) (2f) (1f)
) (110) (130)
 (blurring)
 , 6 2 (140) (E)가
 (GL(N)) (GL(N-1))
 4 (140) ()가 (14
 0) (110)
 7 (GL(N-1)) 가
 (GL(N)) (140) 4
 (110) (SL) 가 , 1, 2 PMOS (GL(N)) (P1)(P2)
 4 PMOS (P4) 3 PMOS (P3) (130)가 가
 8 3 (140) 2 PMOS (P2) 4 PMOS (P4)
 ()가 (110) (140) (E) (140)가
 4 PMOS (P4)
 (130)가 (GL) 가 (SL)
 (110)
 (130)가
 9 4 6 8 7 (140)
 2 PMOS (P2) 4 PMOS (P4)
 (140) (E)가 (GL(N)) (GL(N-1))
 10 13 1 4 (120) 4
 PMOS (P4) (130) (140)
 0) 2 (150) 5 8 2 (15
 NMOS (130) (140)가 (140)가
 14 가
 9 (SL)
 (GL) 가
 (200) , 1 가 1 (200) 2
 (Vdd) 1 1 (200) 2 (220) , 2 (210) ,
 (210) 1 (230) ,
 (200) 2 (220) 2, 3 PMOS (OFF) (240)
 (PM2)(PM3)

(SL) 1 (200) 1 PMOS (PM1) 15 (Vdd), 1
(200) 1 PMOS 2 (220) (PM1) (230) 1 PMOS
8 (SL) 1 PMOS (240) (E)
16 15 , (210) 1 ,
(230) , 가 ,
9 가 ,
가 , 가

(1f) , (2f) , (1f) 가 .
 (130) , (blurring) (110)
 가 (110)(210)
 , 9 (SL) 1 PMOS (PM1)
 (SL) 1 PMOS (PM1) 가 .

(57)

1.

1 2 가 1 1 가 1 2 가 (OFF)

2.

1 PMOS, 2 PMOS, 1 PMOS

3.

2 PMOS
3 PMOS
4 PMOS
3 PMOS

4.

3 , 4 PMOS

5.

1

6. 1 , 1

7. 3 , 2 PMOS 4 PMOS

8. 1 , PMOS

9.

10.

11. 1 , 2

12. 11 , NMOS

13. 2 , 1 PMOS 가 1 PMOS 1

1 가 가 1 1 PMOS 2 , (OFF)

14.

15. 13 , 2 2, 3 PMOS

16. 13 ,

17. 13 , 1 2

18. 13 , PMOS

19. 13 , 가 , (LOW)

가

20.

13

21.

18

2

3

22.

21

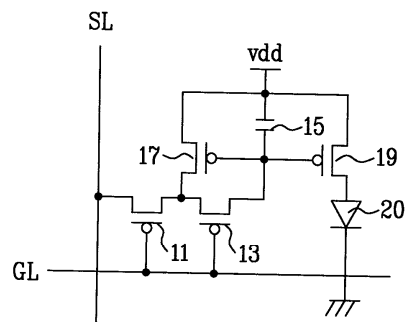
3

NMOS

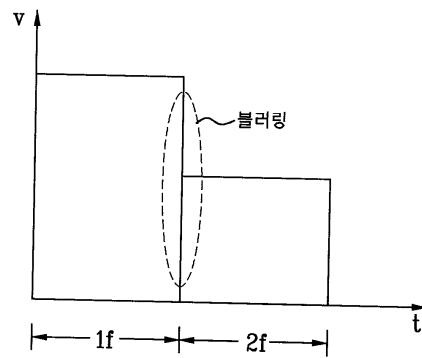
23.

(OFF)

1

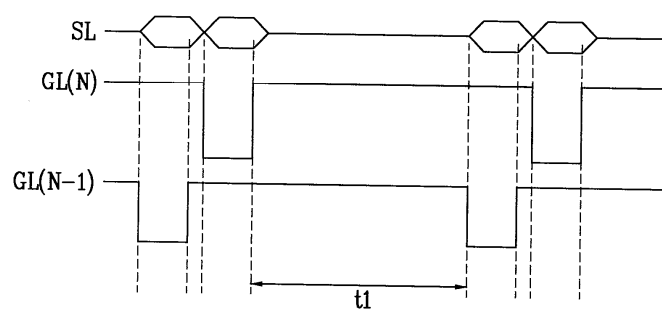


2

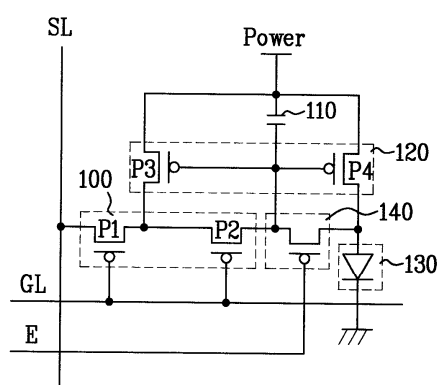




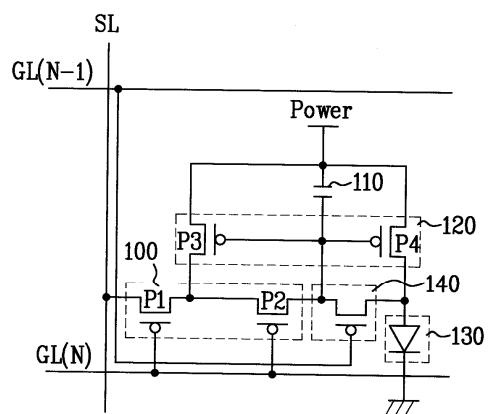
7



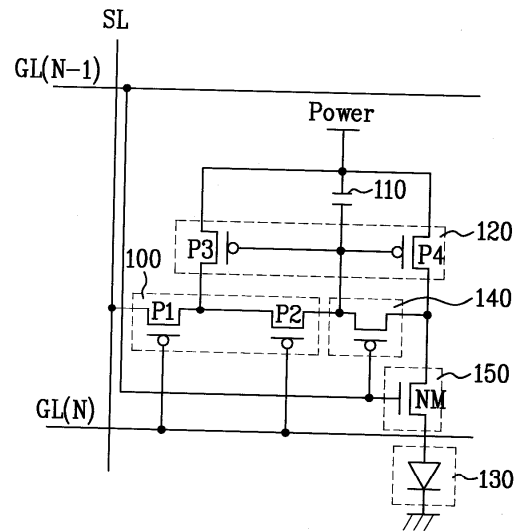
8



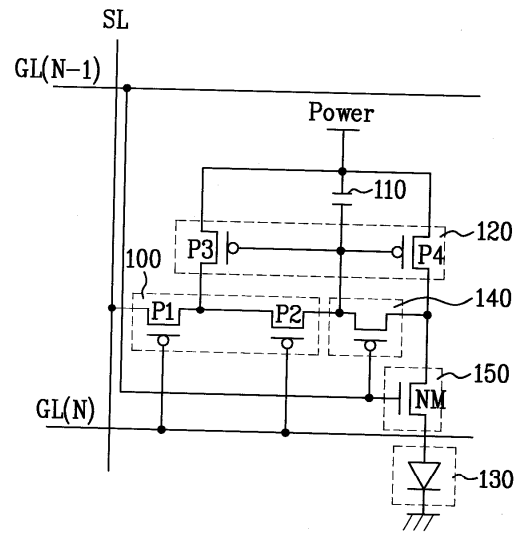
9



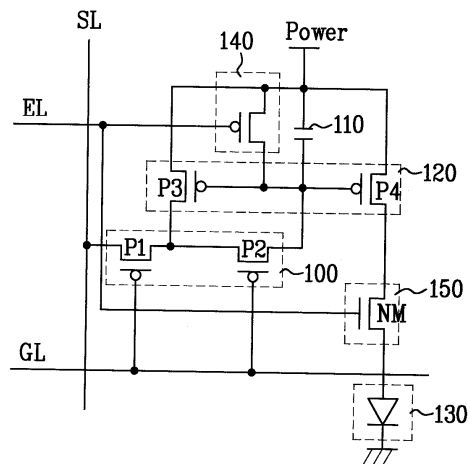
10



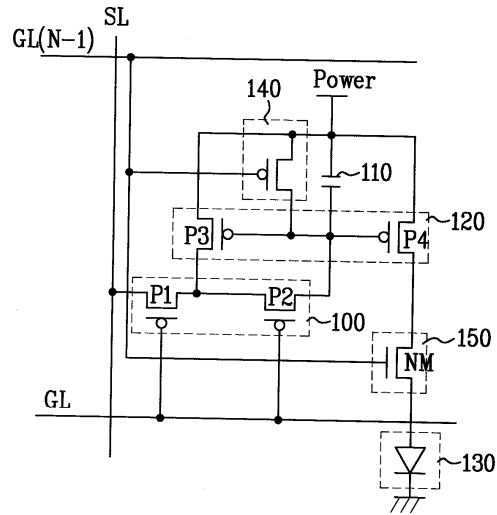
11



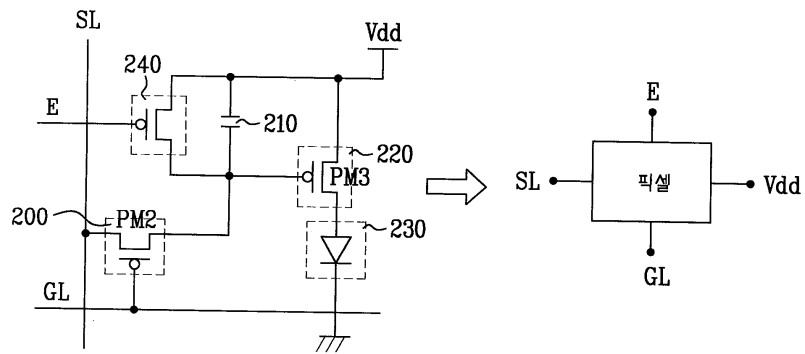
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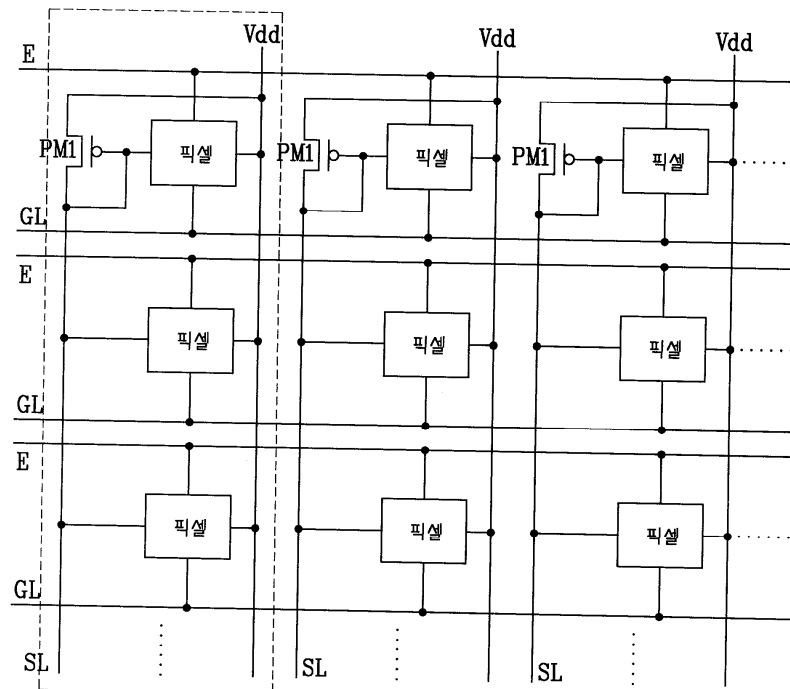
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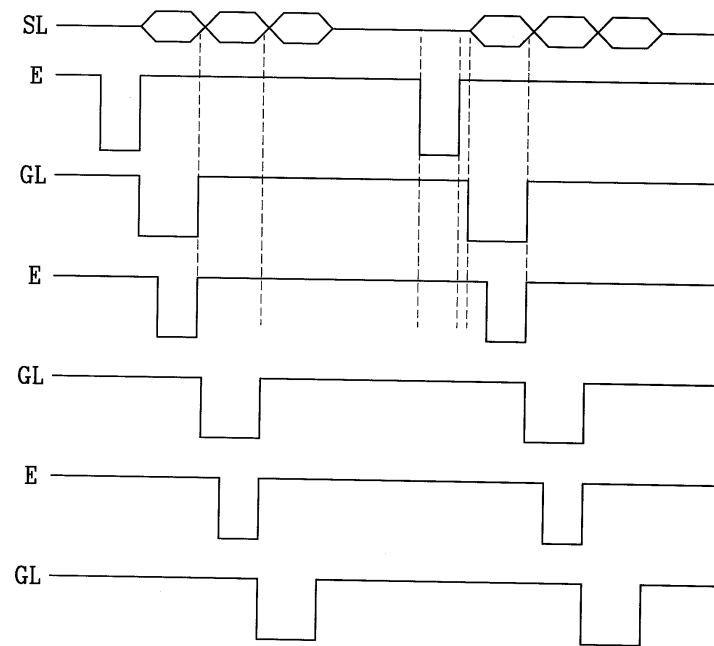
14



15



16



专利名称(译)	电致发光显示面板及其驱动方法		
公开(公告)号	KR100469344B1	公开(公告)日	2005-02-02
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[标]申请(专利权)人(译)	乐金显示有限公司		
申请(专利权)人(译)	LG显示器有限公司		
当前申请(专利权)人(译)	LG显示器有限公司		
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IPC分类号	G09G3/30		
代理人(译)	金勇 新昌		
其他公开文献	KR1020030035195A		
外部链接	Espacenet		

摘要(译)

本发明可以通过在上一帧操作之后的当前帧操作之前的预定时段初始化前一帧中的电容器的同时防止电致发光单元的发光来防止屏幕上的模糊现象，在构成由多条栅极线和多条源极线彼此交叉限定的每个单元像素的电致发光显示面板中，每个单位像素包括栅电极。用于存储在上一开关单元导通时通过第一开关单元传输的数据电压的电容器，以及至少两个晶体管的栅极端子直接连接到电容器，第一个开关单元关闭时存储在电容器中的电压，一种电致发光单元，其通过电流镜单元通过电源电压发光，并且在当前帧操作之前的前一栅极线的激活信号存储在上一帧的电容器中并且，发光抑制部分用于使电荷放电并且将电致发光部分关闭预定时间。3 指数方面 开关部分，电流镜部分，电致发光部分，

