

4	1	가	.
5	1	EL	.
6	4	가 가	5
7	6	.	.
8	2	가	.
9	2	EL	.
10	4	가 가	5
11	6	.	.

< >

10,36,56 : EL 12,32,52 :
14,34,54 : 16,20,42 : EL
30,50 : 38,58 :
40 : 60 : RC

, (Cathode Ray Tube)
(Liquid Crystal Display : 'LCD'), (Field Emission
Display), (Plasma Display Panel : 'PDP') (Electro Lu
minescence : 'EL')
EL
, EL
, 가 가
EL 1 (10) (GL1 GLm) (DL1 DLn) (GL1 GLm)
(DL1 DLn) , (PE)
(PE) (GL1 GLn) (D
L)

EL (14)가 (DL1 DLn) (12)가 (GL1 GLm) (12) (DL1 DLn) (GL1 (PE)
 GLm) (14) (14) (DL1 DLn) (PE) 2 (16)
 (GND) (12) EL (OLED) , EL (OLED) (16)
 2 1 (PE) (GL) (DL)
 4 TFT(T1, T2, T3, T4)
 2 (PE) (GND) EL (OLED) , EL (OLED) (DL)
 EL (16) EL (OLED), (VDD) 1 2 PMOS T
 FT(T1,T2) ; (DL) (GL) (GL) 3 PMOS
 TFT(T3) ; 1 PMOS TFT(T1) 2 PMOS TFT(T2) (GL) 3 PMOS TFT(T3)
 4 PMOS TFT(T4) ; 1 PMOS TFT(T1) 2 PMOS TFT(T2) (Cst)
 VDD)
 (GL) (LOW) 가 3 PMOS TFT(T3) 4 PMOS TFT
 (T4) - 3 PMOS TFT(T3) 가 4 PMOS TFT(T4)가 - (DL)
 가 3 PMOS TFT(T3) 4 PMOS TFT(T4)
 (Cst)
 (Cst) 1 PMOS TFT(T1) 2 PMOS TFT(T2) (VDD)
 (GL)
 (Cst) (DL) (DL) 1 (Holding)
 (Cst) 가 EL (OLED)
 (DL) 가 RGB
 1 (Cst) EL (OLED)
 (Id)
 PMOS TFT(T3) 4 PMOS TFT(T4)가 (Id) (Cst) / 3
 3 n (GLn) n+1 (GLn+1)
 (GL) (DL) (Precharge) (Id) /
 4 PMOS TFT(T4)가 (DL) (GLn) 가 3 PMOS TFT(T3)
 (P) (DL) (DL) n 가
 (Cst) EL (OLED)
 (DL) (On Current) 3 PMOS TFT(T3) 4 PMOS T
 FT(T4) (Cst) (Path)가
 가 / 가

VDD) (Cst) .

4 1 가 .

2 가 , (DL) (P)가 (DL) (Vgl1) 가 1 (DL) 가

(Vgl1) 가 (P)가 가 n (GLn) 1 (GLn) 가 1 (Vgl1) 가 1 (DL) 가

(Cst) (P) 3 PMOS TFT(T3) 4 PMOS TFT(T4) 가 (Kick Ba

ck) .

(P) 가 (DL) (Cst) ,

EL (OLED) .

가 가 가 (GL) 2 -12V 12V

가 , 가 가 (GL) 1 -12V

5 4 가 1

EL .

5 , EL (GL) (DL)

(PE) EL (36) , EL (36) (GL) (DL)

EL 34 (36) (DL) (30) (34) , (32) (32) , (

(PE) (GL) (DL)

(30) (32) (GCS) (34)

(30) (32) 2 (GL) - -

(Vgl1, Vgl2) (32) 가 (MULTIPLEXER ; 1 2 'MUX' , 40) ,

MUX(40) (38) (30) 가 (38) 가 .

(32) (30) (GCS), (32)

(GL) (SP) , () ,

(30) EL (36) (GL)

EL (36) (GL)

(30) (GL)

(34) (DL) (30) (PE) (38) (34) (30)

(GL) (Cst) 1 (P) (34) (DL) (32)

6 4 가 가 5

7 6 .

6 7 , EL (36) (GL) (32)

(DC/DC) , MUX(40) , (38) .

(P)가 가 DC/DC , MUX(40) (38) '1' 가 (34) '0' 가 MUX(40) '0' 1 MUX(40) (Vgl1) (32) MUX(40) 2 (Vgl2) (Vgl2) -12V 1 (Vgl1) -12V 2 (Vgl2) MUX(40) (32) (GL) .

8 2 가 .

2 가 , (DL) (P)가 (DL) (GLn) 1 (Vgl1) 가 (P)가 가 n (GLn) 1 (Vgl1) 가 3 PMOS TFT(T3) 4 PMOS TFT(T4) 가 (Kick Ba ck) (Cst) (P) n (P) 가 (DL) (GL) 가 1 (DL) (Cst) , EL (OLED) .

가 가 (GL) 2 (Vgl2) -12V 12V (Vgl1) -12V (Vgl1) 가 가 (GL) (GL) 1 -12V (Vgl1) 가 가 (DL) (Vgl2) 가 가 (GL) 1 (Vgl1) -12V 2 (Vgl2) 가 .

9 2 EL .

9 , EL (PE) EL (56) , EL (GL) (DL) (54) , (52) (54) (56) (DL) (50) . (52) (52) (54) (PE) (GL) (DL) (50) (52) (GCS) (54) .

(50) (52) (52) (GL) 가 - 가 RC (60) , RC (60) (58) (50) 가 (58) 가 .

(52) (GL) (50) (GCS), , (52) (50) EL (56) (GL) (52) , EL (56) (GL) . (50) (GL) . (54) (50) (50) (50)

(DL) (PE) (54) (50)
 (58) (P) (DL)
 (Cst) (54) (52)
 (GL) 1 (DL)
 10 4 가 가 5
 11 6
 10 11 , (52) ((GL) 1 2 (Vgl1, Vgl
) , DC/DC (56) ((GL)) 가 RC (60) ,
 2) RC (60) (58) RC
 , RC (60) (58) '1' 가 RC (60)
 (P)가 가 RC (60) '1' 가 RC (60)
 1 (Vgl1) (34) '1' 가 (52) (DL) 가 RC (6
 0) '0' 가 RC (60) 가 (58) '0' 가 가
 Vgl1) 2 (Vgl2) RC (60) 1 1 (Vgl
 1) 2 (58) RC (60) RC
 1 (Vgl1) -12V , 2 (Vgl2) -12V
 NMOS TFT EL NMOS TFT (16) PMOS TFT
 가 가 가 가

(57)

1.

1 가

2 가

2.

1

1 2 가

3.

2 ,

4.

1 ,

1 2

5.

1 ,

1 2

6.

1 ,

1

1

가

7.

m

n

m×n

-

;

;

;

;

가

가

8.

7 ,

/

가

9.

7 ,

.

10.

8 ,

1 2

가

.

11.

8 ,

,

.

12.

11 ,

가 , 1 2 (VDD) ;

가

;

3 ;

1 2 4 , 3 .

13.

m n m×n -

;

;

;

;

,

,

가

.

14.

13

/ 가

15.

13

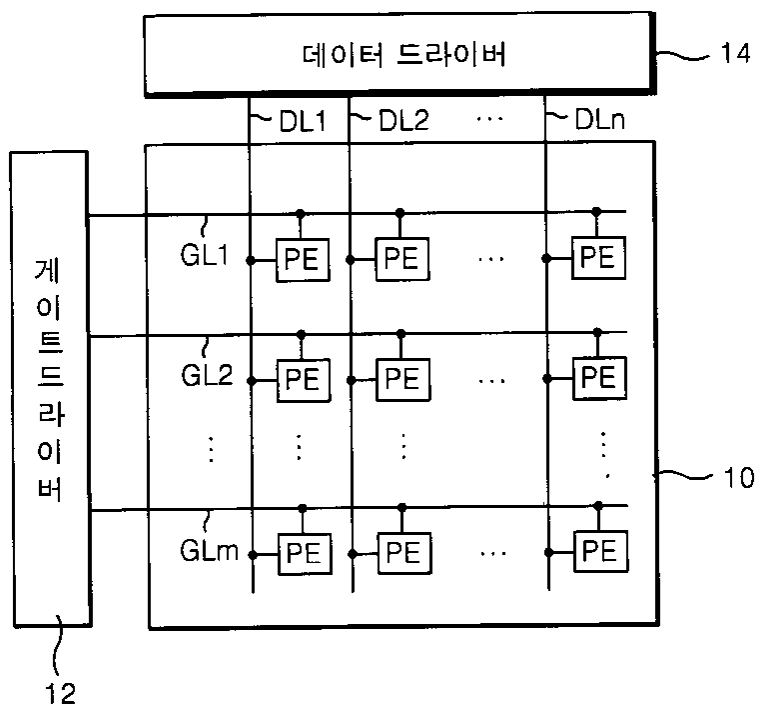
16.

13

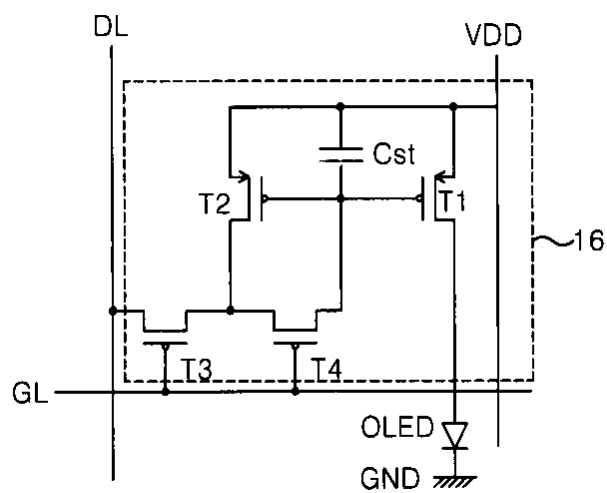
(R)

(C) RC

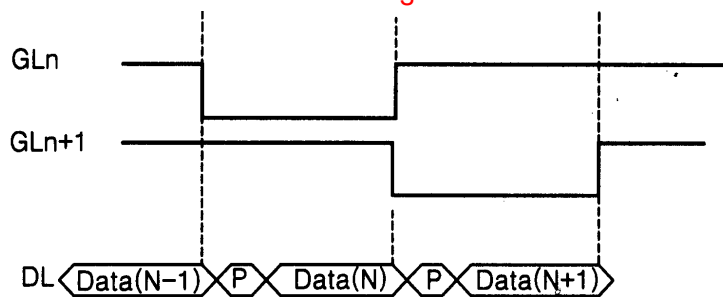
1



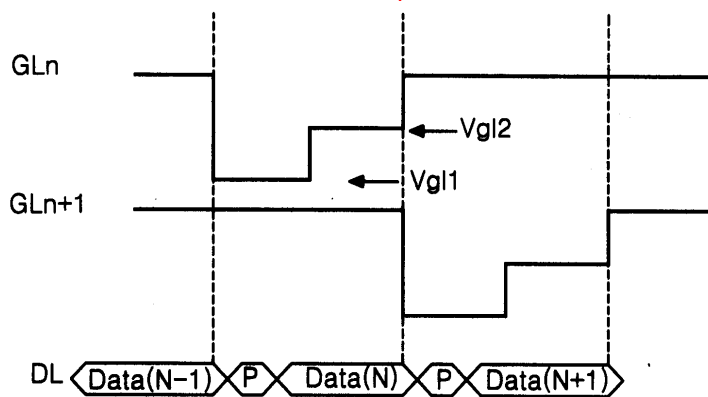
2



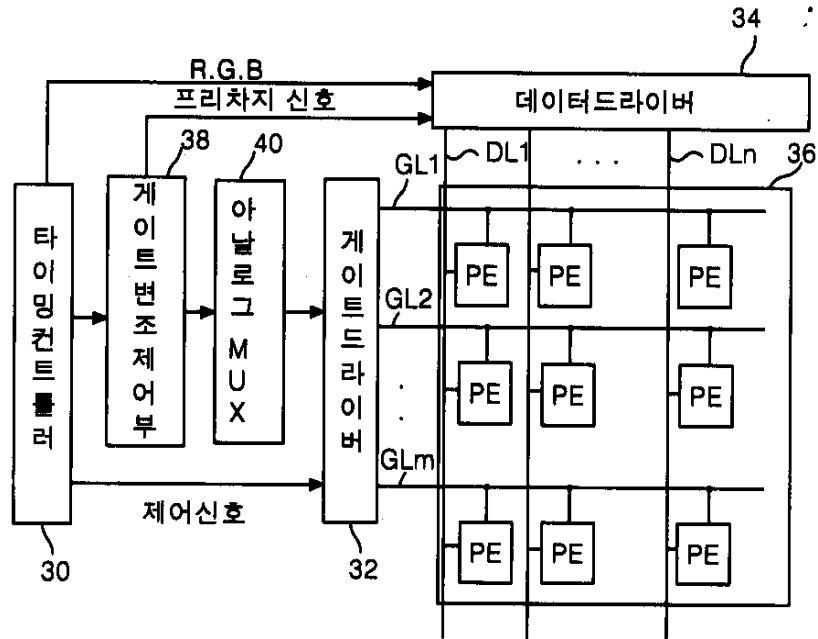
3



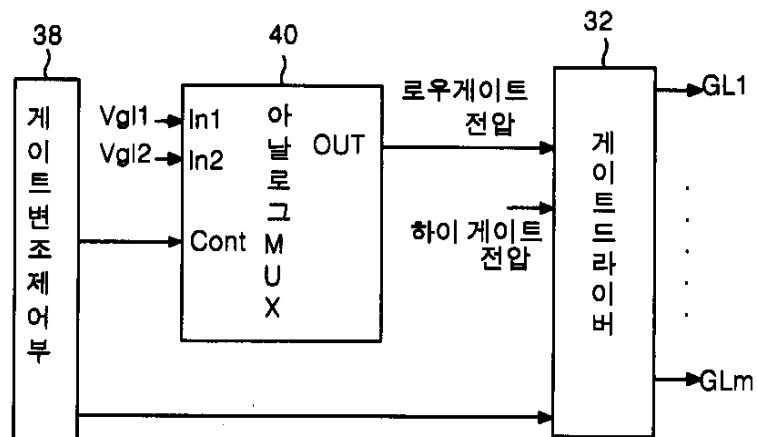
4



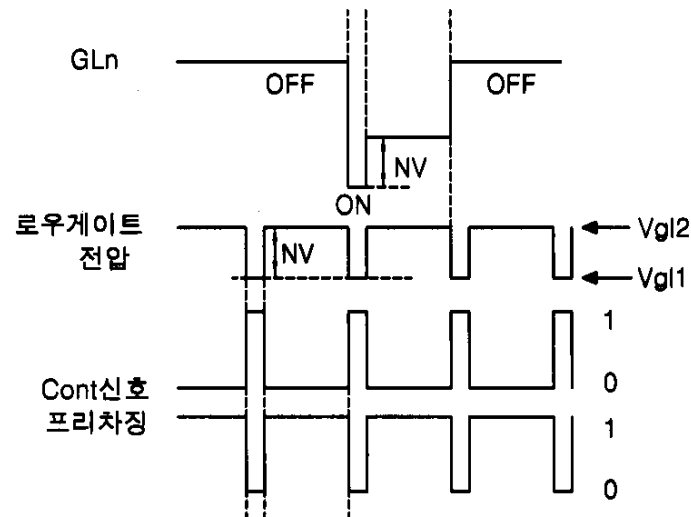
5



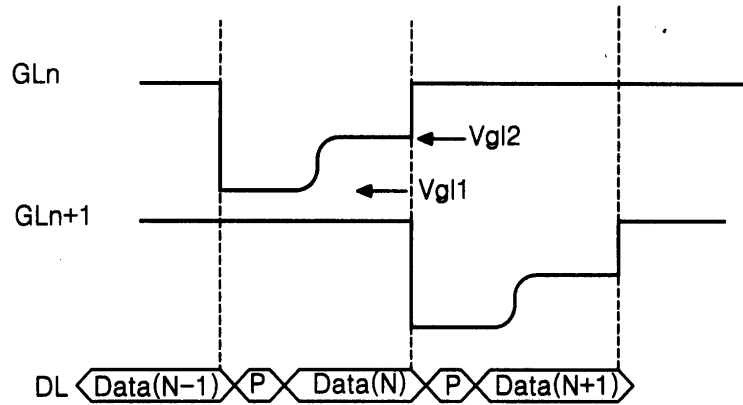
6



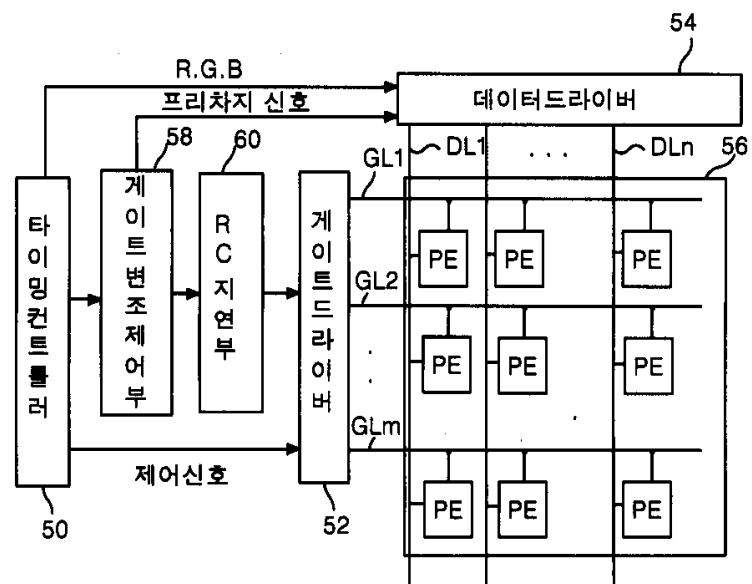
7



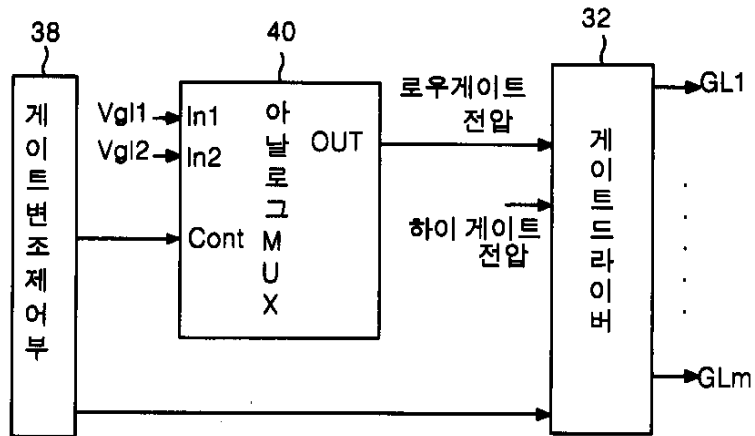
8



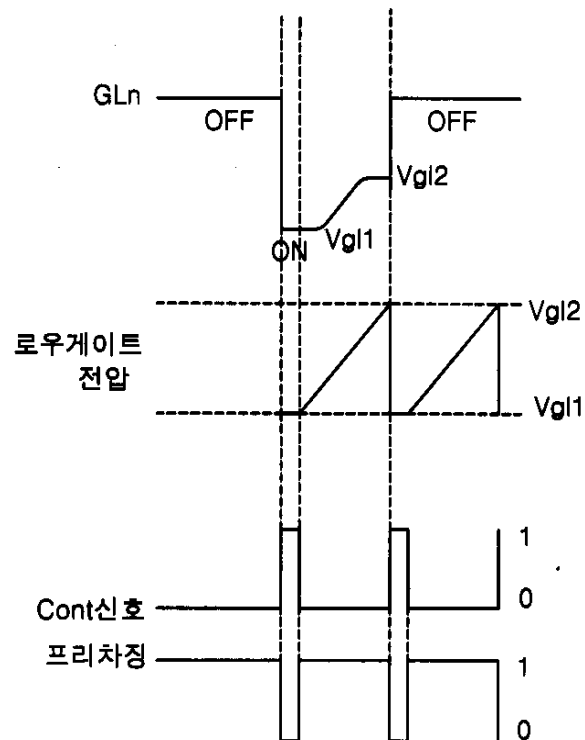
9



10



11



专利名称(译)	用于驱动电致发光面板的方法和设备		
公开(公告)号	KR1020030057965A	公开(公告)日	2003-07-07
申请号	KR1020010088085	申请日	2001-12-29
[标]申请(专利权)人(译)	乐金显示有限公司		
申请(专利权)人(译)	LG显示器有限公司		
当前申请(专利权)人(译)	LG显示器有限公司		
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IPC分类号	G09G3/30		
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其他公开文献	KR100486908B1		
外部链接	Espacenet		

摘要(译)

目的：提供一种用于驱动电致发光面板的方法和装置，通过向数据线施加更高的电压来传输预充电信号，与施加到用于传输数据信号的栅极线的电压相比，提高预充电效果。。组成：具有两个不同级别的脉冲型扫描信号提供给栅极线。将预充电信号提供给数据线，同时将第一扫描信号提供给栅极线。数据信号提供给数据线，而第二扫描信号提供给栅极线。第一扫描信号的电压高于第二扫描信号的电压。第一扫描信号的较高电压是正电压和负电压之一。第一扫描信号的供应时间短于第二扫描信号的供应时间。

