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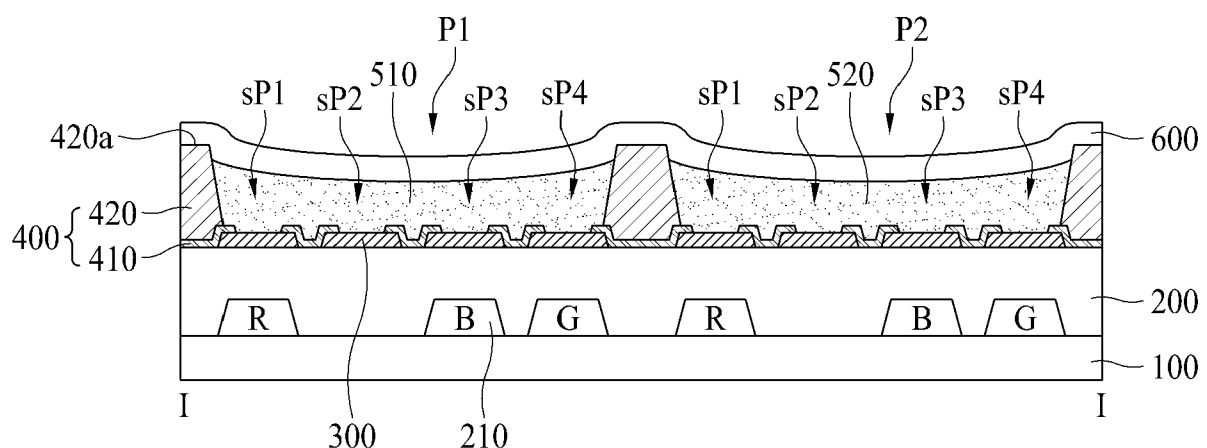
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(54) **ELECTROLUMINESCENT DISPLAY DEVICE**

(57) Discussed is an electroluminescent display device that may include a first emission layer provided to correspond to at least two among a plurality of sub pixels, a second emission layer provided to correspond to at least two among the plurality of sub pixels, wherein the

second emission layer is spaced apart from the first emission layer, and a bank layer provided along the boundary between the first emission layer and the second emission layer.

FIG. 3



Description

BACKGROUND

Field of the Disclosure

[0001] Embodiments of the present invention relate to an electroluminescent display device, and more particularly, to an electroluminescent display device which is manufactured by a solution process.

Discussion of the Related Art

[0002] An electroluminescent display device is provided in such a way that an emission layer is formed between two electrodes. In this case, the emission layer emits light by an electric field formed between the two electrodes, to thereby display an image.

[0003] The emission layer may be formed of an organic material which emits light when exciton produced by a bond of electron and hole falls to a ground state from an excited state, or an inorganic material such as quantum dot.

[0004] Hereinafter, a related art electroluminescent display device will be described with reference to FIGs. 1A and 1B.

[0005] FIG. 1A is a cross sectional view illustrating the related art electroluminescent display device, and FIG. 1B is a plane view illustrating the related art electroluminescent display device.

[0006] As shown in FIG. 1A, the related art electroluminescent display device may include a substrate 10, a first electrode 20, a bank layer 30, emission layers 41, 42 and 43, and a second electrode 50.

[0007] The first electrode 20 is provided on the substrate 10. The first electrode 20 is patterned by each sub pixel sP1, sP2, sP3.

[0008] The bank layer 30, which covers an end of the first electrode 20, is provided on the substrate 10. The bank layer 30 defines an area of the sub pixel sP1, sP2, sP3.

[0009] The emission layers 41, 42 and 43 are provided on the first electrode 20. The emission layer 41, 42 and 43 are individually provided in the sub pixels sP1, sP2, sP3. That is, the first emission layer 41 is provided in the first sub pixel sP1, the second emission layer 42 is provided in the second sub pixel sP2, and the third emission layer 43 is provided in the third sub pixel sP3. Accordingly, one pixel is defined by a combination of the first sub pixel sP1, the second sub pixel sP2 and the third sub pixel sP3.

[0010] The emission layers 41, 42 and 43 may be individually patterned for each sub pixel sP1, sP2, sP3 by a vacuum deposition process using a predetermined mask. However, if the emission layers 41, 42 and 43 are patterned by the vacuum deposition process, it is necessary to use a high-priced vacuum deposition apparatus, thereby causing the increase of manufacturing cost. Es-

pecially, if manufacturing a large-sized electroluminescent display device, it inevitably causes the increase of manufacturing cost due to the increased size of mask and vacuum deposition apparatus, thereby lowering productivity for mass production. Accordingly, in order to reduce the manufacturing cost, a solution process using an inkjet apparatus has been proposed so as to form the emission layers 41, 42 and 43.

[0011] As shown in FIG. 1B, the first emission layer 41, the second emission layer 42 and the third emission layer 43 are patterned, and the bank layer 30 is provided between the adjacent emission layers 41, 42 and 43. In this case, the first emission layer 41, the second emission layer 42 and the third emission layer 43 are aligned at fixed intervals.

[0012] However, in case of the related art electroluminescent display device, it has limitations on resolution.

[0013] In order to realize high resolution in the electroluminescent display device, it is necessary to decrease a size of the pixel. As shown in FIG. 1B, when the plurality of sub pixels sP1, sP2, sP3 are aligned, a size of each sub pixel sP1, sP2, sP3 has to be decreased so as to realize the high-resolution electroluminescent display device.

[0014] However, if the sub pixel sP1, sP2, sP3 is decreased in size, an area of the emission layer 41, 42 and 43 in each of the sub pixels sP1, sP2, sP3 is also decreased in size.

[0015] In order to realize high resolution in the electroluminescent display device according to the related art, it is necessary to decrease a size of each area in the emission layers 41, 42 and 43. To do this, it is necessary to reduce a size of a nozzle in the inkjet apparatus.

[0016] However, the size reduction of the nozzle of the inject apparatus is limited. So, as a drawback, by forming the small-sized area of each emission layer 41, 42 and 43 using the inkjet apparatus, the emission layers 41, 42 and 43 may be mixed together.

[0017] Accordingly, it is difficult to make the size of the area in each emission layer 41, 42 and 43 less than a predetermined value, which than makes it difficult to realize the high resolution electroluminescent display device.

SUMMARY

[0018] Accordingly, embodiments of the present invention are directed to an electroluminescent display device that substantially obviates one or more problems due to limitations and disadvantages of the related art.

[0019] An aspect of embodiments of the present invention is directed to provide an electroluminescent display device capable of realizing high resolution, and preventing emission layers - which should be separated from each other - from being mixed together, e.g. when manufacturing the electroluminescent display device.

[0020] Additional advantages and features of embodiments of the invention will be set forth in part in the de-

scription which follows and in part will become apparent to those having ordinary skill in the art upon examination of the following or may be learned from practice of embodiments of the invention. The objectives and other advantages of embodiments of the invention may be realized and attained by the structure particularly pointed out in the written description and claims hereof as well as the appended drawings.

[0021] The above identified objects are solved by the features of the independent claims. Additional advantageous embodiments are derived from the respective dependent claims. In the following, the expression "provided to correspond" may be interchanged by "provided in" or "provided for" or "provided on". So, the expression "provided to correspond" emphasizes the relationship between two layers or objects. According to an aspect, there is provided an electroluminescent display device that includes a first emission layer provided to correspond to at least two sub pixels among a plurality of sub pixels; a second emission layer provided to correspond to at least two sub pixels among the plurality of sub pixels, wherein the second emission layer is spaced apart from the first emission layer; and a bank layer provided along the boundary between the first emission layer and the second emission layer.

[0022] Each of the first emission layer and the second emission layer may be provided to correspond to one pixel among the plurality of pixels.

[0023] Each of the first emission layer and the second emission layer may be provided to correspond to at least two pixels among the plurality of pixels.

[0024] At least one emission layer among the first emission layer and the second emission layer may be provided to correspond to some sub pixels provided in one pixel among the plurality of pixels. Some sub pixels provided in another pixel among the plurality of pixels. Thus, the first emission layer and the second emission layer may be provided to correspond to a first subset of sub pixels provided in one pixel among the plurality of pixels. A second subset of sub pixels provided in another pixel among the plurality of pixels. The number of sub pixel per subset may be different.

[0025] The number of sub pixels in the first emission layer may be the same as the number of sub pixels in the second emission layer.

[0026] The number of sub pixels in the first emission layer may be smaller than the number of sub pixels in the second emission layer.

[0027] A size of an area of the first emission layer and a size of an area of the second emission layer may be different.

[0028] The second emission layer may be disposed in a periphery of the first emission layer.

[0029] The first emission layer and the second emission layer may emit white light.

[0030] At least one sub pixel among the plurality of sub pixels may include an additional color filter.

[0031] In another aspect of an embodiment of the

present invention, there is provided an electroluminescent display device that includes a first emission layer and a second emission layer provided to correspond to at least two sub pixels among a plurality of sub pixels; a first bank layer provided along the boundary between each of the plurality of sub pixels; and a second bank layer provided along the boundary between the first emission layer and the second emission layer.

[0032] Some of a plurality of first bank layers may be provided below the second bank layer. An upper surface of the remaining first bank layers of the plurality of first bank layers - which are not provided below the second bank layer - may be in contact with the first emission layer, the second emission layer, or the third emission layer.

[0033] The first bank layer may have hydrophilic properties. An upper portion of the second bank layer may have the hydrophobic properties.

[0034] The first emission layer, the second emission layer and the third emission layer may emit white light.

[0035] At least one sub pixel among the plurality of sub pixels may include an additional color filter.

[0036] In another aspect of an embodiment of the present invention, there is provided an electroluminescent display device that includes a first area having a first emission layer provided to correspond to some sub pixels among a plurality of sub pixels; and a second area having a second emission layer provided to correspond to some sub pixels among the plurality of sub pixels, wherein the second area is separated from the first area, and wherein the number of sub pixels in the second emission layer is larger than the number of sub pixels in the first emission layer.

[0037] The first emission layer may be provided to correspond to at least two sub pixels. The second emission layer may be provided to correspond to at least three other sub pixels.

[0038] The electroluminescent display device may further comprise a third area, which is separated from the first area and the second area. The third area may have a third emission layer provided to correspond to third sub pixels among the plurality of sub pixels. The number of sub pixels for the third emission layer may be larger than the number of sub pixels for the second emission layer.

[0039] The first emission layer and the second emission layer may be spaced apart from each other by a bank layer including a first bank layer and a second bank layer provided on the first bank layer.

[0040] The first bank layer may be additionally provided along the boundary of each of at least two sub pixels.

[0041] An entire upper surface of the first bank layer provided along the boundary of each of the sub pixels may be in contact with the first emission layer or the second emission layer.

[0042] The first emission layer and the second emission layer may emit white light.

[0043] At least one sub pixel among the plurality of sub pixels may include an additional color filter.

[0044] It is to be understood that both the foregoing general description and the following detailed description of embodiments of the present invention are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

BRIEF DESCRIPTION OF THE DRAWINGS

[0045] The accompanying drawings, which are included to provide a further understanding of embodiments of the invention and are incorporated in and constitute a part of this application, illustrate embodiments of the invention and together with the description serve to explain the principle of embodiments of the invention. In the drawings:

FIG. 1A is a cross sectional view illustrating a related art electroluminescent display device, and FIG. 1B is a plane view illustrating the related art electroluminescent display device;

FIG. 2 is a plane view illustrating an electroluminescent display device according to one embodiment of the present invention;

FIG. 3 is a cross sectional view illustrating the electroluminescent display device according to one embodiment of the present invention, which corresponds to a cross section along I-I line of FIG. 2;

FIG. 4 a plane view illustrating an electroluminescent display device according to another embodiment of the present invention;

FIG. 5 is a cross sectional view illustrating the electroluminescent display device according to another embodiment of the present invention, which corresponds to a cross section along I-I line of FIG. 4;

FIG. 6 is a cross sectional view illustrating the electroluminescent display device according to another embodiment of the present invention, which corresponds to a cross section along II-II line of FIG. 4;

FIG. 7 is a plane view illustrating an electroluminescent display device according to another embodiment of the present invention;

FIG. 8 is a plane view illustrating an electroluminescent display device according to another embodiment of the present invention;

FIG. 9 is a plane view illustrating an electroluminescent display device according to another embodiment of the present invention;

FIG. 10 is a plane view illustrating an electroluminescent display device according to another embodiment of the present invention;

FIG. 11 is a cross sectional view illustrating the electroluminescent display device according to another embodiment of the present invention, which corresponds to a cross section along I-I line of FIG. 10;

FIG. 12 is a cross sectional view illustrating the electroluminescent display device according to another embodiment of the present invention, which corresponds to a cross section along II-II line of FIG. 10;

and

FIG. 13 is a cross sectional view illustrating the electroluminescent display device according to another embodiment of the present invention, which corresponds to a cross section along III-III line of FIG. 10.

DETAILED DESCRIPTION OF THE EMBODIMENTS

[0046] Advantages and features of the present invention, and implementation methods thereof will be clarified through following embodiments described with reference to the accompanying drawings. The present invention may, however, be embodied in different forms and should not be construed as limited to the embodiments set forth herein. Rather, these embodiments are provided so that this disclosure will be thorough and complete, and will fully convey the scope of the present invention to those skilled in the art. Further, the present invention is only defined by scopes of claims.

[0047] A shape, a size, a ratio, an angle, and a number disclosed in the drawings for describing embodiments of the present invention are merely an example, and thus, the present invention is not limited to the illustrated details. Like reference numerals refer to like elements throughout. In the following description, when the detailed description of the relevant known function or configuration is determined to unnecessarily obscure the important point of the present invention, the detailed description will be omitted. In a case where 'comprise', 'have', and 'include' described in the present specification are used, another part may be added unless 'only~' is used. The terms of a singular form may include plural forms unless referred to the contrary.

[0048] In construing an element, the element is construed as including an error region although there is no explicit description.

[0049] In describing a position relationship, for example, when the positional order is described as 'on~', 'above~', 'below~', and 'next~', a case which is not contact may be included unless 'just' or 'direct' is used.

[0050] In describing a time relationship, for example, when the temporal order is described as 'after~', 'subsequent~', 'next~', and 'before~', a case which is not continuous may be included unless 'just' or 'direct' is used.

[0051] It will be understood that, although the terms "first", "second", etc. may be used herein to describe various elements, these elements should not be limited by these terms. These terms are only used to distinguish one element from another. For example, a first element could be termed a second element, and, similarly, a second element could be termed a first element, without departing from the scope of the present invention.

[0052] Features of various embodiments of the present invention may be partially or overall coupled to or combined with each other, and may be variously inter-operated with each other and driven technically as those skilled in the art can sufficiently understand. The embodiments of the present invention may be carried out inde-

pendently from each other, or may be carried out together in co-dependent relationship.

[0053] Hereinafter, an electroluminescent display device according to the embodiments of the present invention will be described with reference to the accompanying drawings. All the components of the electroluminescent display device according to all embodiments of the present invention are operatively coupled and configured.

[0054] FIG. 2 is a plane view illustrating an electroluminescent display device according to one embodiment of the present invention.

[0055] As shown in FIG. 2, the electroluminescent display device according to one embodiment of the present invention may include a first pixel P1, a second pixel P2, a third pixel P3, and a fourth pixel P4. The second pixel P2 is adjacent to the first pixel P1 in a first direction, for example, a horizontal direction. The third pixel P3 is adjacent to the first pixel P1 in a second direction, for example, a vertical direction which is different from the first direction. The fourth pixel P4 is adjacent to the first pixel P3 in the first direction.

[0056] Each of the first pixel P1, the second pixel P2, the third pixel P3, and the fourth pixel P4 may include a first sub pixel sP1, a second sub pixel sP2, a third sub pixel sP3 and a fourth sub pixel sP4.

[0057] The first sub pixel sP1, the second sub pixel sP2, the third sub pixel sP3 and the fourth sub pixel sP4 are arranged in the first direction, but are not limited to this structure. An arrangement of the sub pixels and the number of the sub pixels may be changed in various ways.

[0058] A first emission layer 510 is provided in the first pixel P1, a second emission layer 520 is provided in the second pixel P2, a third emission layer 530 is provided in the third pixel P3, and a fourth emission layer 540 is provided in the fourth pixel P4.

[0059] The first emission layer 510 is provided for the first sub pixel sP1, the second sub pixel sP2, the third sub pixel sP3 and the fourth sub pixel sP4 within the first pixel P1 in common. The first emission layer 510 is provided in each of the first sub pixel sP1, the second sub pixel sP2, the third sub pixel sP3 and the fourth sub pixel sP4, and is also provided along the boundary between each of the sub pixels sP1, sP2, sP3, sP4. Accordingly, the first emission layers 510 provided inside the respective first, second, third and fourth sub pixels sP1, sP2, sP3, sP4 are connected with each other, and furthermore, formed as one body.

[0060] However, it is not limited to this structure. The first emission layer 510 may be provided to correspond to at least two sub pixels among the first sub pixel sP1, the second sub pixel sP2, the third sub pixel sP3 and the fourth sub pixel sP4. Herein, the emission layer corresponding to the pixels P1, P2, P3, P4 or the sub pixels sP1, sP2, sP3, sP4 indicates that the emission layer is formed as one body in the pixels P1, P2, P3, P4 or the sub pixels sP1, sP2, sP3, sP4.

[0061] The second emission layer 520 is provided for the first sub pixel sP1, the second sub pixel sP2, the third sub pixel sP3 and the fourth sub pixel sP4 within the second pixel P2 in common. That is, the number of sub pixels sP1, sP2, sP3, sP4 in the second emission layer 520 may be the same as the number of sub pixels sP1, sP2, sP3, sP4 in the first emission layer 510. In this case, the first emission layer 510 and the second emission layer 520 may be formed for the same spraying process by the use of one inkjet nozzle.

[0062] The second emission layer 520 is provided in each of the first sub pixel sP1, the second sub pixel sP2, the third sub pixel sP3 and the fourth sub pixel sP4, and is also provided along the boundary between each of the sub pixels sP1, sP2, sP3, sP4. Accordingly, the second emission layers 520 provided inside the respective first, second, third and fourth sub pixels sP1, sP2, sP3, sP4 are connected with each other, and furthermore, formed as one body. However, it is not limited to this structure. The second emission layer 520 may be provided to correspond to at least two sub pixels among the first sub pixel sP1, the second sub pixel sP2, the third sub pixel sP3 and the fourth sub pixel sP4.

[0063] The third emission layer 530 and the fourth emission layer 540 are provided in the same manner as the first emission layer 510 and the second emission layer 520.

[0064] The first emission layer 510, the second emission layer 520, the third emission layer 530 and the fourth emission layer 540 are spaced apart from each other.

[0065] The second emission layer 520 is adjacent to the first emission layer 510 in the first direction. The first emission layer 510 and the second emission layer 520 emit a light of a same color such as a white color. The third emission layer 530 is adjacent to the first emission layer 510 in the second direction. The fourth emission layer 540 is adjacent to the third emission layer 530 in the first direction. The third emission layer 530 and the fourth emission layer 540 emit a light of a same color such as a white color. Also, the first emission layer 510, the second emission layer 520, the third emission layer 530, and the fourth emission layer 540 emit a light of a same color such as a white color.

[0066] In detail, the first emission layer 510, the second emission layer 520, the third emission layer 530 and the fourth emission layer 540 are spaced apart from each other by the bank layer 400 disposed therebetween.

[0067] The bank layer 400 is provided along the boundary between the first pixel P1 and the second pixel P2, the boundary between the first pixel P1 and the third pixel P3, the boundary between the third pixel P3 and the fourth pixel P4, and the boundary between the second pixel P2 and the fourth pixel P4, to thereby define an area for each of the first pixel P1, the second pixel P2, the third pixel P3 and the fourth pixel P4.

[0068] In detail, each of the plurality of sub pixels sP1, sP2, sP3, sP4 includes a circuit element for making the first emission layer 510, second emission layer 520, the

third emission layer 530, and the fourth emission layer 540 emit light. The circuit element may include a switching thin film transistor, a driving thin film transistor, a sensing thin film transistor and a capacitor, but is not limited to this structure.

[0069] According to one embodiment of the present invention, each of the first emission layer 510, the second emission layer 520, the third emission layer 530, and the fourth emission layer 540 is provided to correspond to at least two of the sub pixels sP1, sP2, sP3, sP4. Thus, even though the plurality of sub pixels sP1, sP2, sP3, sP4 are aligned in the first direction and each of the plurality of sub pixels sP1, sP2, sP3, sP4 is decreased in size, a size of an area for each of the first emission layer 510, the second emission layer 520, the third emission layer 530, and the fourth emission layer 540 is not largely decreased so that it is possible to form the first emission layer 510, the second emission layer 520, the third emission layer 530, and the fourth emission layer 540 by the use of inkjet apparatus without mixing the first emission layer 510, the second emission layer 520, the third emission layer 530, and the fourth emission layer 540 together, to thereby realize a high-resolution electroluminescent display device.

[0070] FIG. 3 is a cross sectional view illustrating the electroluminescent display device according to the embodiment of the present invention, which corresponds to a cross section along I-I line of FIG. 2.

[0071] As shown in FIG. 3, the electroluminescent display device according to one embodiment of the present invention may include a substrate 100, a circuit device layer 200, a first electrode 300, a bank layer 400, emission layers 510 and 520, and a second electrode 600.

[0072] The substrate 100 may be formed of glass or transparent plastic, but is not limited to these materials.

[0073] The circuit device layer 200 is provided on the substrate 100. The circuit device layer 200 may include a thin film transistor and a capacitor provided for each sub pixel sP1, sP2, sP3, sP4. The thin film transistor may include a switching thin film transistor, a driving thin film transistor, and a sensing thin film transistor. A structure of the circuit device layer 200 may be formed in various types generally known to those in the art.

[0074] A color filter 210 is provided in the circuit device layer 200. The color filter 210 may include a red color filter R, a blue color filter B, and a green color filter G, but not necessarily. In the drawings, the red color filter R is provided in the first sub pixel sP1, the color filter is not provided in the second sub pixel sP2, the blue color filter B is provided in the third sub pixel sP3, and the green color filter G is provided in the fourth sub pixel sP4, wherein the second sub pixel sP2 with no color filter emits white light, but is not limited to this structure. An arrangement of the red R, blue B and green G color filters may be changed in various ways.

[0075] The color filter 210 is provided in a path of a light emission. As shown in the drawings, if the color filter 210 is disposed below the first emission layer 510 and

the second emission layer 520, the light emitted from the first emission layer 510 and the second emission layer 520 advances toward the substrate 100, to thereby realize a bottom emission type electroluminescent display device. However, it is not limited to this structure. If the color filter 210 is provided above the second electrode 600, it is possible to realize a top emission type electroluminescent display device.

[0076] The first electrode 300 is provided on the circuit device layer 200. The first electrode 300 may function as an anode of the electroluminescent display device. If the electroluminescent display device according to the present invention is the bottom emission type, the first electrode 300 serves as a transparent electrode. Meanwhile, if the electroluminescent display device according to the present invention is the top emission type, the first electrode 300 serves as a reflective electrode.

[0077] The bank layer 400 covers an end of the first electrode 300, and the bank layer 400 is provided on the circuit device layer 200.

[0078] The bank layer 400 is provided along the boundary between the first pixel P1 and the second pixel P2. According as the bank layer 400 is provided along the boundary between the pixels P1, P2, it is possible to define the area of the emission layer 510 and 520.

[0079] The bank layer 400 may include a first bank layer 410 and a second bank layer 420.

[0080] The first bank layer 410 is additionally provided along the boundary between each of the plurality of sub pixels sP1, sP2, sP3, sP4 provided in each of the first pixel P1 and the second pixel P2. The first bank layer 410 is formed in a matrix configuration, to thereby define an area for each of the plurality of sub pixels sP1, sP2, sP3, sP4.

[0081] The first bank layer 410 is in contact with the first electrode 300 and the circuit device layer 200. A thickness of the first bank layer 410 is smaller than a thickness of the second bank layer 420, and a width of the first bank layer 410 is larger than a width of the second bank layer 420. Accordingly, an end of the first bank layer 410 is in contact with the emission layers 510 and 520. The first bank layer 410 with this structure has the same properties as those of the emission layers 510, that is, the hydrophilic properties. The first bank layer 410 having the hydrophilic properties may be formed of an inorganic insulating material such as silicon oxide. Accordingly, when coating the solution for the emission layer 510 and 520, the solution easily spreads on the first bank layer 410 so that the emission layer 510 and 520 spreads widely in each sub pixel sP1, sP2, sP3, sP4.

[0082] The second bank layer 420 is patterned on the first bank layer 410. The second bank layer 420 is provided along the boundary between the first pixel P1 and the second pixel P2. However, the second bank layer 420 may not be provided along the boundary between each of the plurality of sub pixels sP1, sP2, sP3, sP4. That is, an upper surface of the first bank layer 410 provided below the second bank layer 420 is in contact with

the second bank layer 420. However, the other portions of the upper surface of the first bank layer 410, which are not provided below the second bank layer 420, are in contact with the first emission layer 510 or the second emission layer 520, to thereby improve spreadability of the solution for the emission layers 510 and 520.

[0083] A width of the second bank layer 420 is smaller than a width of the first bank layer 410. The second bank layer 420 may be patterned for a sequential process of coating a mixture solution obtained by mixing the organic insulating material having the hydrophilic properties with a hydrophobic material such as fluorine, and carrying out a photolithography process. By light irradiated for the photolithography process, the hydrophobic material such as fluorine is transferred to an upper portion 420a of the second bank layer 420, whereby the upper portion 420a of the second bank layer 420 has the hydrophobic properties, and the other portions of the second bank layer 420 except the upper portion 420a have the hydrophilic properties, but are not limited to these structures. That is, the other portions of the second bank layer 420 being in contact with the first bank layer 410 have the hydrophilic properties, and the upper portion 420a of the second bank layer 420 has the hydrophobic properties. However, it is not limited to this structure. For example, the entire portions of the second bank layer 420 may have the hydrophobic properties.

[0084] The spreadability of the solution for the emission layers 510 and 520 may be improved by the first bank layer 410 and the predetermined portions of the second bank layer 420 which have the hydrophilic properties. Especially, according as the first bank layer 410 whose thickness is smaller than that of the second bank layer 420 is provided widely, it is possible to prepare a two-step structure having the hydrophilic properties by a combination of the first bank layer 410 and the second bank layer 420. Thus, the solution for the emission layers 510 and 520 easily spreads to an end area of each pixel P1, P2 so that it is possible to prevent the emission layers 510 and 520 from being upwardly rolled at the end area of each pixel P1, P2 with the large thickness.

[0085] Also, the upper portion 420a of the second bank layer 420, which has the hydrophobic properties, prevents the solution for the emission layers 510 and 520 from spreading to the adjacent pixels P1, P2 so that it is possible to prevent the first emission layer 510 from permeating into the second pixel P2 and being mixed with the second emission layer 520, and also to prevent the second emission layer 520 from permeating into the first pixel P1 and being mixed with the first emission layer 510.

[0086] The emission layers 510 and 520 are provided on the first electrode 300. The emission layers 510 and 520 include the first emission layer 510 provided in the first pixel P1, and the second emission layer 520 provided in the second pixel P2.

[0087] Each of the first emission layer 510 and the second emission layer 520 may be formed by a solution process using the inkjet apparatus. The first emission layer

510 and the second emission layer 520 may emit white light. According to the present invention, each emission layer 510 and 520 is provided to correspond to the plurality of sub pixels sP1, sP2, sP3, sP4. If each emission layer 510 and 520 has a predetermined color, it is difficult to display an image. Thus, each emission layer 510 and 520 emits white light, and some of the sub pixels sP1, sP2, sP3, sP4 may have color filters therein.

[0088] Each of the first emission layer 510 and the second emission layer 520, which emits white light, may be formed in a structure including a plurality of stacks emitting different-colored light. For example, the first emission layer 510 and the second emission layer 520 may be formed by a combination of a stack of emitting blue light and a stack of emitting yellow light, or by a combination of a stack of emitting blue light, a stack of emitting green light and a stack of emitting red light. At least one stack among the plurality of stacks may include at least one organic layer among a hole injecting layer, a hole transporting layer, an emitting layer, an electron transporting layer, and an electron injecting layer. In this case, each of the first emission layer 510 and the second emission layer 520 may include at least one organic layer among the plurality of organic layers. For example, each of the first emission layer 510 and the second emission layer 520 may be formed by a combination of a hole injecting layer, a hole transporting layer and an emitting layer, which are laminated by the solution process, and may be formed by a combination of an electron transporting layer and an electron injecting layer which are laminated by a deposition process such as evaporation. In this case, the organic layer formed by the deposition process may not be patterned by each of the first pixel P1 and the second pixel P2, but be provided on the entire surface of the substrate 100. If needed, the first emission layer 510 and the second emission layer 520 may be formed of an inorganic material such as quantum dot.

[0089] The second electrode 600 is provided on the emission layers 510 and 520 and the bank layer 400. The second electrode 600 may function as a cathode of the electroluminescent display device. If the electroluminescent display device according to the present invention is a top emission type, the second electrode 600 serves as a transparent electrode. Meanwhile, if the electroluminescent display device according to the present invention is a bottom emission type, the second electrode 600 serves as a reflective electrode.

[0090] FIG. 4 is a plane view illustrating an electroluminescent display device according to another embodiment of the present invention.

[0091] As shown in FIG. 4, the electroluminescent display device according to another embodiment of the present invention may include a plurality of pixels P1, P2, P3, P4 arranged in a first direction, for example, a horizontal direction, and arranged in a second direction, for example, a vertical direction.

[0092] Each of the plurality of pixels P1, P2, P3, P4 may include a first sub pixel sP1, a second sub pixel sP2,

a third sub pixel sP3 and a fourth sub pixel sP4. In the same manner as the aforementioned embodiment of FIG. 2, the first sub pixel sP1, the second sub pixel sP2, the third sub pixel sP3 and the fourth sub pixel sP4 are arranged in the first direction, but are not limited to this structure. An arrangement of the sub pixels and the number of the sub pixels may be changed in various ways.

[0093] Unlike the aforementioned embodiment of FIG. 2, one emission layer 500 is provided to correspond to the first pixel P1, the second pixel P2, the third pixel P3 and the fourth pixel P4.

[0094] The emission layer 500 is provided for the sub pixels sP1, sP2, sP3, sP4 of the respective pixels P1, P2, P3, P4 in common. Accordingly, the emission layer 500 is provided inside the respective first, second, third and fourth pixels P1, P2, P3, P4, and also provided along the boundary between each of the respective pixels P1, P2, P3, P4. The emission layers 500 provided inside the respective first, second, third and fourth pixels P1, P2, P3, P4 are connected with each other, and furthermore, formed as one body.

[0095] A bank layer 400 is provided in the periphery of the emission layer 500. The bank layer 400 surrounds the emission layer 500, to thereby define an area of the emission layer 500.

[0096] According to another embodiment of the present invention, the emission layer 500 is provided to correspond to the plurality of pixels P1, P2, P3, P4. Thus, even though each of the plurality of pixels P1, P2, P3, P4 is decreased in size, a size in the area of the emission layer 500 is not largely decreased so that it is possible to form the emission layer 500 by the use of inkjet apparatus with easiness, to thereby realize a high-resolution electroluminescent display device.

[0097] In FIG. 4, one emission layer 500 is provided to correspond to the four pixels P1, P2, P3, P4 arranged in the first direction and the second direction, but is not limited to this structure. The emission layer 500 may be provided to correspond to at least two of the pixels P1, P2, P3, P4. Also, one emission layer 500 may be provided to correspond to at least two pixels P1 and P2, or P3 and P4 arranged in the first direction, or may be provided to correspond to at least two pixels P1 and P3, or P2 and P4 arranged in the second direction.

[0098] FIG. 5 is a cross sectional view illustrating the electroluminescent display device according to another embodiment of the present invention, which corresponds to a cross section along I-I line of FIG. 4.

[0099] As shown in FIG. 5, the electroluminescent display device according to another embodiment of the present invention may include a substrate 100, a circuit device layer 200, a first electrode 300, a bank layer 400, an emitting layer 500, and a second electrode 600.

[0100] The substrate 100, the circuit device layer 200, the first electrode 300 and the second electrode 600 are the same as those of the aforementioned embodiment, whereby a detailed description for the substrate 100, the

circuit device layer 200, the first electrode 300 and the second electrode 600 will be omitted.

[0101] The bank layer 400 covers an end of the first electrode 300, and the bank layer 400 is provided on the circuit device layer 200. The bank layer 400 may include a first bank layer 410 and a second bank layer 420. A material and shape of the first bank layer 410 and the second bank layer 420 is the same as that of the aforementioned embodiment, whereby a detailed description for the same parts will be omitted. Hereinafter, only arrangement structure of the first bank layer 410 and the second bank layer 420 will be described in detail as follows.

[0102] In the same manner as the aforementioned embodiment of FIG. 3, the first bank layer 410 is provided along the boundary between the first pixel P1 and the second pixel P2, and also provided along the boundary between each of the plurality of sub pixels sP1, sP2, sP3, sP4 included in the respective pixels P1, P2.

[0103] Unlike the aforementioned embodiment of FIG. 3, the second bank layer 420 is not provided along the boundary between the first pixel P1 and the second pixel P2, but provided at both ends of the emission layer 500.

[0104] Accordingly, the emission layers 500 of the respective pixels P1, P2 are connected with each other inside the second bank layer 420, and are formed as one body. A material for the emission layer 500 is the same as that of the aforementioned embodiment.

[0105] FIG. 6 is a cross sectional view illustrating the electroluminescent display device according to another embodiment of the present invention, which corresponds to a cross section along II-II line of FIG. 4.

[0106] FIG. 6 shows that the emission layers 500 are connected with each other in the first pixel P1 and the third pixel P3, and are formed as one body, and other portions are nearly the same as those of FIG. 5, whereby a detailed description for the same parts will be omitted.

[0107] FIG. 7 is a plane view illustrating an electroluminescent display device according to another embodiment of the present invention.

[0108] As shown in FIG. 7, the electroluminescent display device according to another embodiment of the present invention may include a first area A1 and a second area A2.

[0109] The second area A2 corresponds to a peripheral area of the first area A1. A dotted line is an imaginary line for the division between the first area A1 and the second area A2. A first emission layer 510 is provided in the first area A1, and a second emission layer 520 is provided in the second area A2.

[0110] For a process of forming the emission layers 510 and 520, a solution dry speed for the emission layers 510 and 520 in the first area A1 corresponding to the central of a substrate may be different from a solution dry speed for the emission layers 510 and 520 in the second area A2 corresponding to the periphery of the substrate.

[0111] In more detail, the solution dry speed for the

emission layers 510 and 520 in the second area A2 corresponding to the periphery of the substrate may be rapider than the solution dry speed for the emission layers 510 and 520 in the first area A1 corresponding to the central of the substrate. In this case, a profile of the first emission layer 510 provided in the first area A1 is different from a profile of the second emission layer 520 provided in the second area A2 so that it is difficult to realize uniformity of image.

[0112] According to another embodiment of the present invention, the number of sub pixels sP1, sP2, sP3, sP4 for the first emission layer 510 of the first area A1 is different from the number of sub pixels sP1, sP2, sP3, sP4 for the second emission layer 520 of the second area A2. That is, a size of an area in the first emission layer 510 of the first area A1 is different from a size of an area in the second emission layer 520 of the second area A2.

[0113] In detail, the number of sub pixels sP1, sP2, sP3, sP4 in the second emission layer 520 of the second area A2 is larger than the number of sub pixels sP1, sP2, sP3, sP4 in the first emission layer 510 of the first area A1. For example, as shown in the drawings, the number of sub pixels sP1, sP2, sP3, sP4 in the second emission layer 520 of the second area A2 is '16', and the number of sub pixels sP1, sP2, sP3, sP4 in the first emission layer 510 of the first area A1 is '4', but not necessarily. The number of sub pixels sP1, sP2, sP3, sP4 in the second emission layer 520 of the second area A2 can be 2 or more than 2, and the number of sub pixels sP1, sP2, sP3, sP4 in the first emission layer 510 of the first area A1 can be 1 or more than 1.

[0114] In other words, the size of the area of the second emission layer 520 in the second area A2 is larger than the size of the area of the first emission layer 510 in the first area A1. That is, the size of the area of the second emission layer 520 in the second area A2, wherein the solution dry speed is relatively rapid, is larger than the size of the area of the first emission layer 510 in the first area A1 wherein the solution dry speed is relatively slow. Thus, even though the solution dry speed in the second emission layer 520 is rapider than the solution dry speed in the first emission layer 510, the amount of solution for the second emission layer 520 is relatively larger than the amount of solution for the first emission layer 510, whereby there is no large difference between a solution dry completion point for the second emission layer 520 and a solution dry completion point for the first emission layer 510. Thus, it is possible to decrease the difference between the profile of the first emission layer 510 and the profile of the second emission layer 520, thereby reducing a problem related with uniformity of image.

[0115] According to another embodiment of the present invention, there are the first area A1 and the second area A2, wherein the number of sub pixels sP1, sP2, sP3, sP4 for the first area A1 is different from the number of sub pixels sP1, sP2, sP3, sP4 for the second area A2. Thus, it is possible to appropriately adjust the solution

dry completion point for each of the first emission layer 510 and the second emission layer 520 with easiness, to thereby improve picture quality.

[0116] Meanwhile, the second area A2 having the second emission layer 520 with the relatively-large number of sub pixels sP1, sP2, sP3, sP4 may be disposed in at least one side of the substrate, and the first area A1 having the first emission layer 510 with the relatively-small number of sub pixels sP1, sP2, sP3, sP4 may be disposed in the remaining portions of the substrate.

[0117] FIG. 8 is a plane view illustrating an electroluminescent display device according to another embodiment of the present invention.

[0118] As shown in FIG. 8, the electroluminescent display device OLED may include a first area A1, a second area A2 and a third area A3.

[0119] The second area A2 corresponds to a peripheral area of the first area A1, and the third area A3 corresponds to a peripheral area of the second area A2. A dotted line is an imaginary line for the division among the first area A1, the second area A2 and the third area A3.

[0120] A solution dry speed in the third area A3 may be rapider than a solution dry speed in the second area A2, and the solution dry speed in the second area A2 may be rapider than a solution dry speed in the first area A1.

[0121] Accordingly, a size of an area for the third emission layer 530 of the third area A3 is larger than a size of an area for the second emission layer 520 of the second area A2, and the size of the area for the second emission layer 520 of the second area A2 is larger than a size of an area for the first emission layer 510 of the first area A1.

[0122] That is, the number of sub pixels sP1, sP2, sP3, sP4 in the third emission layer 530 of the third area A3 is larger than the number of sub pixels sP1, sP2, sP3, sP4 in the second emission layer 520 of the second area A2, and the number of sub pixels sP1, sP2, sP3, sP4 in the second emission layer 520 of the second area A2 is larger than the number of sub pixels sP1, sP2, sP3, sP4 in the first emission layer 510 of the first area A1.

[0123] For example, the number of sub pixels sP1, sP2, sP3, sP4 in the third emission layer 530 of the third area A3 can be 4 or more than 4, the number of sub pixels sP1, sP2, sP3, sP4 in the second emission layer 520 of the second area A2 can be 3 or more than 3, and the number of sub pixels sP1, sP2, sP3, sP4 in the first emission layer 510 of the first area A1 can be 2 or more than 2. However, the number of sub pixels sP1, sP2, sP3, sP4 in the first emission layer 510 of the first area A1 may be 1. In this case, the number of sub pixels sP1, sP2, sP3, sP4 in the second emission layer 520 of the second area A2 may be 2 or more than 2, and the number of sub pixels sP1, sP2, sP3, sP4 in the third emission layer 530 of the third area A3 may be 3 or more than 3.

[0124] According to another embodiment of the present invention, there are the first area A1, the second area A2 and the third area A3, wherein the number of

sub pixels in the first area A1, the number of sub pixels in the second area A2 and the number of sub pixels in the third area A3 are different from one another. Thus, it is possible to appropriately adjust the solution dry completion point for each of the first emission layer 510, the second emission layer 520 and the third emission layer 530 with easiness, to thereby improve picture quality.

[0125] FIG. 9 is a plane view illustrating an electroluminescent display device according to another embodiment of the present invention. FIG. 9 shows that a plurality of electroluminescent display devices OLED are provided on a substrate 100, which corresponds to the substrate 100 before a scribing process.

[0126] As shown in FIG. 9, the plurality of electroluminescent display devices OLED are provided on the substrate 100. The plurality of electroluminescent display devices OLED are separated by the scribing process after a process of forming the aforementioned various elements such as emission layers 510 and 520.

[0127] For the process of forming the emission layers 510 and 520, a solution dry speed for the emission layers 510 and 520 in a first area A1 corresponding to the center of the substrate 100 may be different from a solution dry speed for the emission layers 510 and 520 in a second area A2 corresponding to the periphery of the substrate 100. A dotted line is an imaginary line for the division between the first area A1 and the second area A2.

[0128] In more detail, the solution dry speed for the emission layers 510 and 520 in the second area A2 corresponding to the periphery of the substrate 100 may be rapider than the solution dry speed for the emission layers 510 and 520 in the first area A1 corresponding to the central of the substrate 100. In this case, a profile of the first emission layer 510 provided in the first area A1 is different from a profile of the second emission layer 520 provided in the second area A2 so that it is difficult to realize uniformity of image.

[0129] According to another embodiment of the present invention, the number of sub pixels sP1, sP2, sP3, sP4 in the first emission layer 510 of the first area A1 is different from the number of sub pixels sP1, sP2, sP3, sP4 in the second emission layer 520 of the second area A2. That is, a size of an area of the first emission layer 510 in the first area A1 is different from a size of an area of the second emission layer 520 in the second area A2.

[0130] In detail, the number of sub pixels sP1, sP2, sP3, sP4 in the second emission layer 520 of the second area A2 is larger than the number of sub pixels sP1, sP2, sP3, sP4 in the first emission layer 510 of the first area A1.

[0131] In other words, the size of the area of the second emission layer 520 in the second area A2 is larger than the size of the area of the first emission layer 510 in the first area A1.

[0132] That is, the size of the area in the second emission layer 520 of the second area A2, wherein the solution dry speed is relatively rapid, is larger than the size of the area in the first emission layer 510 of the first area A1

wherein the solution dry speed is relatively slow. Thus, even though the solution dry speed in the second emission layer 520 is rapider than the solution dry speed in the first emission layer 510, the amount of solution for the second emission layer 520 is relatively larger than the amount of solution for the first emission layer 510, whereby there is no large difference between a solution dry completion point for the second emission layer 520 and a solution dry completion point for the first emission layer 510. Thus, it is possible to decrease the difference between the profile of the first emission layer 510 and the profile of the second emission layer 520, thereby reducing a problem related with uniformity of image.

[0133] In this reason, for example, the first emission layer 510 of the first area A1 is provided to correspond to the first pixel P1 including the plurality of sub pixels sP1, sP2, sP3, sP4, and the second emission layer 520 of the second area A2 is provided to correspond to the first pixel P1 including the plurality of sub pixels sP1, sP2, sP3, sP4, the second pixel P2 including the plurality of sub pixels sP1, sP2, sP3, sP4, the third pixel P3 including the plurality of sub pixels sP1, sP2, sP3, sP4, and the fourth pixel P4 including the plurality of sub pixels sP1, sP2, sP3, sP4.

[0134] The number of sub pixels sP1, sP2, sP3, sP4 in the second emission layer 520 is larger than the number of sub pixels sP1, sP2, sP3, sP4 in the first emission layer 510. Also, the number of sub pixels sP1, sP2, sP3, sP4 in the second emission layer 520 and the number of sub pixels sP1, sP2, sP3, sP4 in the first emission layer 510 may be changed in various ways. For example, the number of sub pixels sP1, sP2, sP3, sP4 in the second emission layer 520 can be 3 or more than 3, and the number of sub pixels sP1, sP2, sP3, sP4 in the first emission layer 510 can be 2 or more than 2. If needed, the number of sub pixels sP1, sP2, sP3, sP4 in the second emission layer 520 may be 2 or more than 2, and the number of sub pixels sP1, sP2, sP3, sP4 in the first emission layer 510 may be 1.

[0135] An arrangement structure of the first area A1 and the second area A2 may not be the same in the plurality of electroluminescent display devices OLED separated by the scribing process. For example, in case of the electroluminescent display device OLED provided at four corners of the substrate 100 before the scribing process, two sides connected with each other among four sides, for example, upper and lateral sides or lower and lateral sides may form the second area A2. In case of the electroluminescent display device OLED provided at other portions except the four corners of the substrate 100, any one side, for example, a lateral side may form the second area A2. However, it is not limited to this structure. In the plurality of electroluminescent display devices OLED, the first area A1 and the second area A2 may be changed in various ways.

[0136] FIG. 10 is a plane view illustrating an electroluminescent display device according to another embodiment of the present invention. FIG. 10 shows that a plu-

rality of electroluminescent display devices OLED1, OLED2 are provided on a substrate 100, which corresponds to the substrate 100 before a scribing process.

[0137] As shown in FIG. 10, the plurality of electroluminescent display devices OLED1, OLED2 having the different sizes may be provided on the substrate 100.

[0138] Each of the electroluminescent display devices OLED1, OLED2 is provided with a plurality of pixels P1, P2, and each of the pixels P1, P2 is provided with a plurality of sub pixels sP1, sP2, sP3, sP4. An arrangement and number of the plurality of pixels P1, P2 and the plurality of sub pixels sP1, sP2, sP3, sP4 may be changed in various ways.

[0139] Also, emission layers 510 and 520 are provided in each of the electroluminescent display devices OLED1, OLED2. In this case, a size of the emission layer 510 and 520 may be identical in the respective electroluminescent display devices OLED1, OLED2. That is, if the emission layers 510 and 520 are coated in the same coating method by the use of the same inkjet apparatus, the same sized emission layers 510 and 520 may be formed on the respective electroluminescent display devices OLED1, OLED2.

[0140] The first electroluminescent display device OLED1 may include the pixels P1, P2 provided with the plurality of sub pixels sP1, sP2, sP3, sP4 arranged in the horizontal direction, and the plurality of pixels P1, P2 are arranged in the horizontal and vertical directions. In this case, for example, the emission layers 510 and 520 are provided for the two sub pixels sP1, sP2, sP3, sP4 arranged in the horizontal direction, and the two sub pixels sP1, sP2, sP3, sP4 arranged in the vertical direction.

[0141] In this case, the first emission layer 510 may be provided to correspond to the first and second sub pixels sP1, sP2 of the first pixel P1, and the first and second sub pixels sP1, sP2 of the second pixel P2, and the second emission layer 520 may be provided to correspond to the third and fourth sub pixels sP3, sP4 of the first pixel P1, and the third and fourth sub pixels sP3, sP4 of the second pixel P2.

[0142] That is, one emission layer 510 and 520 may be provided to correspond to the sub pixels sP1, sP2, sP3, sP4 in the different pixels P1, P2. However, one emission layer 510 and 520 may be provided to correspond to some sub pixels sP1, sP2, sP3, sP4 among the sub pixels sP1, sP2, sP3, sP4 in the respective pixels P1, P2. Accordingly, it is possible to appropriately adjust the area of the emission layer 510 and 520 in accordance with a nozzle of the inkjet apparatus. In this case, the number of pixels P1, P2 and the number of sub pixels sP1, sP2, sP3, sP4 in the emission layers 510 and 520 may be changed in various ways in consideration of the nozzle of the inkjet apparatus.

[0143] The second electroluminescent display device OLED2 may include the pixels P1, P2 provided with the plurality of sub pixels sP1, sP2, sP3, sP4 arranged in the vertical direction, and the plurality of pixels P1, P2 are arranged in the horizontal and vertical directions. In this

case, for example, the emission layers 510 and 520 may be provided to correspond to the three sub pixels sP1, sP2, sP3, sP4 arranged in the vertical direction. A size and arrangement of the sub pixels sP1, sP2, sP3, sP4 of the second electroluminescent display device OLED2 is different from a size and arrangement of the sub pixels sP1, sP2, sP3, sP4 of the first electroluminescent display device OLED1. In order to provide the same size of the emission layer 510 and 520 in the first electroluminescent display device OLED1 and the second electroluminescent display device OLED2, the number of sub pixels sP1, sP2, sP3, sP4 for the emission layer 510 and 520 of the second electroluminescent display device OLED2 may be different from the number of sub pixels sP1, sP2, sP3, sP4 for the emission layer 510 and 520 of the first electroluminescent display device OLED1. For example, the number of sub pixels sP1, sP2, sP3, sP4 for the emission layer 510 and 520 of the second electroluminescent display device OLED2 may be 3, and the number of sub pixels sP1, sP2, sP3, sP4 for the emission layer 510 and 520 of the first electroluminescent display device OLED1 may be 4.

[0144] Accordingly, the first emission layer 510 of the second electroluminescent display device OLED2 is provided to correspond to the first, second and third sub pixels sP1, sP2, sP3 of the first pixel P1, and the second emission layer 520 of the second electroluminescent display device OLED2 is provided to correspond to the fourth sub pixel sP4 of the first pixel P1, and the first and second sub pixels sP1, sP2 of the second pixel P2.

[0145] That is, one emission layer 510 and 520 is provided to correspond to the sub pixels sP1, sP2, sP3, sP4 of the different pixels P1, P2. However, one emission layer 510 and 520 may be provided to correspond to some sub pixels sP1, sP2, sP3, sP4 among the sub pixels sP1, sP2, sP3, sP4 of each pixel P1, P2. Also, the number of sub pixels sP1, sP2, sP3, sP4 in one emission layer 510 and 520 may be changed by each pixel P1, P2.

[0146] FIG. 11 is a cross sectional view illustrating the electroluminescent display device according to another embodiment of the present invention, which corresponds to a cross section along I-I line of FIG. 10.

[0147] As shown in FIG. 11, the electroluminescent display device according to another embodiment of the present invention may include a substrate 100, a circuit device layer 200, a first electrode 300, a bank layer 400, a first emission layer 510, and a second electrode 600.

[0148] The substrate 100, the circuit device layer 200, the first electrode 300, and the second electrode 600 are the same as those of the aforementioned embodiment, whereby a detailed description for the substrate 100, the circuit device layer 200, the first electrode 300, and the second electrode 600 will be omitted.

[0149] The bank layer 400 may include a first bank layer 410 and a second bank layer 420. A material and shape of the first bank layer 410 and the second bank layer 420 is the same as that of the aforementioned embodiment, whereby a detailed description for the same

parts will be omitted. Hereinafter, only arrangement structure of the first bank layer 410 and the second bank layer 420 will be described in detail as follows.

[0150] The first bank layer 410 is provided along the boundary between the first sub pixel sP1 of the first pixel P1 and the first sub pixel sP1 of the second pixel P2. However, the second bank layer 420 is not provided along the boundary between the first sub pixel sP1 of the first pixel P1 and the first sub pixel sP1 of the second pixel P2, but provided at both ends of the first emission layer 510. Accordingly, the first emission layers 510 of the respective pixels P1, P2 are connected with each other inside the second bank layer 420, and are formed as one body.

[0151] FIG. 12 is a cross sectional view illustrating the electroluminescent display device according to another embodiment of the present invention, which corresponds to a cross section along II-II line of FIG. 10.

[0152] As shown in FIG. 12, the electroluminescent display device according to another embodiment of the present invention may include a substrate 100, a circuit device layer 200, a first electrode 300, a bank layer 400, first and second emission layers 510 and 520, and a second electrode 600.

[0153] The substrate 100, the circuit device layer 200, the first electrode 300, and the second electrode 600 are the same as those of the aforementioned embodiment, whereby a detailed description for the substrate 100, the circuit device layer 200, the first electrode 300 and the second electrode 600 will be omitted.

[0154] The bank layer 400 may include a first bank layer 410 and a second bank layer 420.

[0155] The first bank layer 410 is provided along the boundary between each of the sub pixels sP1, sP2, sP3, sP4 of the first pixel P1. The second bank layer 420 is provided along the boundary between the first emission layer 510 and the second emission layer 520. That is, the second bank layer 420 is provided inside the first pixel P1, and more particularly, along the boundary between the second sub pixel sP2 and the third sub pixel sP3 of the first pixel P1.

[0156] Each of the first emission layer 510 and the second emission layer 520 is provided in the first pixel P1, and the first emission layer 510 and the second emission layer 520 are separated from each other by the second bank layer 420 inside the first pixel P1. In this case, the first emission layers 510 of the respective first and second sub pixels sP1, sP2 of the first pixel P1 are connected with each, and are formed as one body, and the second emission layers 520 of the respective third and fourth sub pixels sP3, sP4 of the first pixel P1 are connected with each, and are formed as one body.

[0157] FIG. 13 is a cross sectional view illustrating the electroluminescent display device according to another embodiment of the present invention, which corresponds to a cross section along III-III line of FIG. 10.

[0158] As shown in FIG. 13, the electroluminescent display device according to another embodiment of the

present invention may include a substrate 100, a circuit device layer 200, a first electrode 300, a bank layer 400, first and second emission layers 510 and 520, and a second electrode 600.

[0159] The substrate 100, the circuit device layer 200, the first electrode 300 and the second electrode 600 are the same as those of the aforementioned embodiment, whereby a detailed description for the substrate 100, the circuit device layer 200, the first electrode 300 and the second electrode 600 will be omitted.

[0160] The bank layer 400 may include a first bank layer 410 and a second bank layer 420.

[0161] The first bank layer 410 is provided along the boundary between each of the sub pixels sP1, sP2, sP3, sP4 of the first pixel P1 and the second pixel P2. The second bank layer 420 is provided along the boundary between the first emission layer 510 and the second emission layer 520. That is, the second bank layer 420 is provided inside the first pixel P1 and the second pixel P2, and more particularly, along the boundary between the third sub pixel sP3 and the fourth sub pixel sP4 of the first pixel P1 and the boundary between the second sub pixel sP2 and the third sub pixel sP3 of the second pixel P2.

[0162] The first emission layer 510 is provided in the first pixel P1. In detail, the first emission layers 510 provided in the respective first, second and third sub pixels sP1, sP2, sP3 inside the first pixel P1 are connected with each other, and furthermore, formed as one body.

[0163] The second emission layer 520 is provided in the first pixel P1 and the second pixel P2. In detail, the second emission layers 520 provided in the fourth sub pixel sP4 of the first pixel P1, and the first and second sub pixels sP1, sP2 of the second pixel P2 are connected with each other, and furthermore, formed as one body.

[0164] According to the embodiment(s) of the present invention, the emission layer is provided for the plurality of sub pixels. In this case, even though the plurality of sub pixels are decreased in size, the area of the emission layer is not decreased in size. As a result, it is possible to form the emission layers without being mixed together by the use of inkjet apparatus, to thereby realize the high-resolution electroluminescent display device.

[0165] It will be apparent to those skilled in the art that various modifications and variations can be made in the present invention without departing from the scope of the inventions. Thus, it is intended that the present invention covers the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.

Claims

1. An electroluminescent display device comprising:
 - a plurality of pixels (P1, P2, P3, P4);
 - a plurality of sub pixels (sP1, sP2, sP3, sP4)

- provided in each of the plurality of pixels (P1, P2, P3, P4);
a first emission layer (510) provided in at least two sub pixels among the plurality of sub pixels (sP1, sP2, sP3, sP4);
a second emission layer (520) provided in at least two other sub pixels among the plurality of sub pixels (sP1, sP2, sP3, sP4), wherein the other sub pixels are different from the sub pixels comprising the first emission layer (510), wherein the second emission layer (520) is spaced apart from the first emission layer (510); and
a bank layer (400) provided along a boundary between the first emission layer (510) and the second emission layer (520).
2. The electroluminescent display device according to claim 1, wherein each of the first emission layer (510) and the second emission layer (520) is provided in one pixel among the plurality of pixels (P1, P2, P3, P4).
 3. The electroluminescent display device according to one of the preceding claims, wherein each of the first emission layer (510) and the second emission layer (520) is provided in at least two pixels among the plurality of pixels (P1, P2, P3, P4).
 4. The electroluminescent display device according to one of the preceding claims, wherein at least one emission layer among the first emission layer (510) and the second emission layer (520) is provided in some sub pixels provided in one pixel among the plurality of pixels (P1, P2, P3, P4), and some sub pixels provided in another pixel among the plurality of pixels (P1, P2, P3, P4).
 5. The electroluminescent display device according to one of the preceding claims, wherein the number of sub pixels in the first emission layer (510) is the same as the number of sub pixels in the second emission layer (520).
 6. The electroluminescent display device according to one of the claims 1 to 4, wherein the number of sub pixels in the first emission layer (510) is smaller than the number of sub pixels in the second emission layer (520).
 7. The electroluminescent display device according to one of the preceding claims, wherein a size of an area of the first emission layer (510) and a size of an area of the second emission layer (520) are different.
 8. The electroluminescent display device according to one of the preceding claims, wherein the second emission layer (520) is disposed in a periphery of the first emission layer (510).
 9. The electroluminescent display device according to one of the preceding claims, wherein the first emission layer (510) and the second emission layer (520) emit white light, and at least one sub pixel among the plurality of sub pixels (sP1, sP2, sP3, sP4) includes an additional color filter (210).
 10. The electroluminescent display device of one of the preceding claims
a third emission layer (530) provided in at least two third sub pixels among the plurality of sub pixels (sP1, sP2, sP3, sP4), wherein the second emission layer (520) being adjacent to the first emission layer (510) in a first direction and wherein the third emission layer (530) being adjacent to the first emission layer (510) in a second direction different from the first direction;
a first bank layer (410) provided along a boundary of each of the plurality of sub pixels (sP1, sP2, sP3, sP4); and
a second bank layer (420) provided along a boundary between two layers among the first emission layer (510), the second emission layer (520) and the third emission layer (530),
wherein the first, second and third emission layers (510, 520, 530) emit a light of a same color.
 11. The electroluminescent display device according to claim 10, wherein some of a plurality of first bank layers (410) are provided below the second bank layer (420), and an upper surface of the remaining first bank layers of the plurality of first bank layers (410), which are not provided below the second bank layer (420), is in contact with the first emission layer (510), the second emission layer (520), or the third emission layer (530).
 12. The electroluminescent display device according to claims 10 or 11, wherein the first bank layer (410) has hydrophilic properties, and an upper portion of the second bank layer (420) has the hydrophobic properties.
 13. The electroluminescent display device according to one of the claims 10 to 12, wherein the first emission layer (510), the second emission layer (520) and the third emission layer (530) emit white light.
 14. The electroluminescent display device according to one of the preceding claims further comprising:
a first area (A1) having the first emission layer (510) provided in the first sub pixels among the plurality of sub pixels (sP1, sP2, sP3, sP4); and
a second area (A2) having the second emission layer (520) provided in the second sub pixels

among the plurality of sub pixels (sP1, sP2, sP3, sP4), wherein the second area (A2) is separated from the first area (A1),
wherein the number of sub pixels in the second emission layer (520) is larger than the number of sub pixels in the first emission layer (510). 5

15. The electroluminescent display device according to claim 14, further comprising a third area (A3) which is separated from the first area (A1) and the second area (A2),
wherein the third area (A3) has a third emission layer (530) provided to in the third sub pixels among the plurality of sub pixels (sP1, sP2, sP3, sP4), and the number of sub pixels for the third emission layer (530) is larger than the number of sub pixels for the second emission layer (520). 10 15

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FIG. 1A
RELATED ART

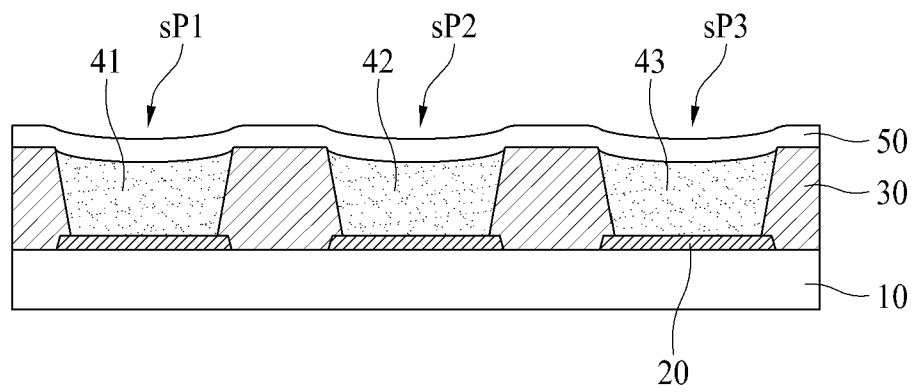


FIG. 1B
RELATED ART

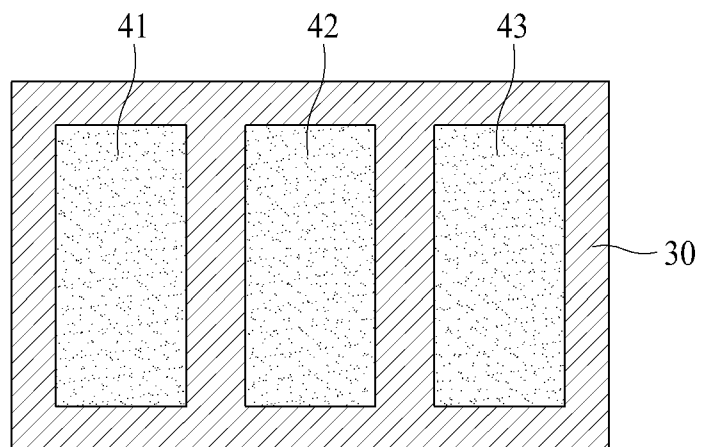


FIG. 2

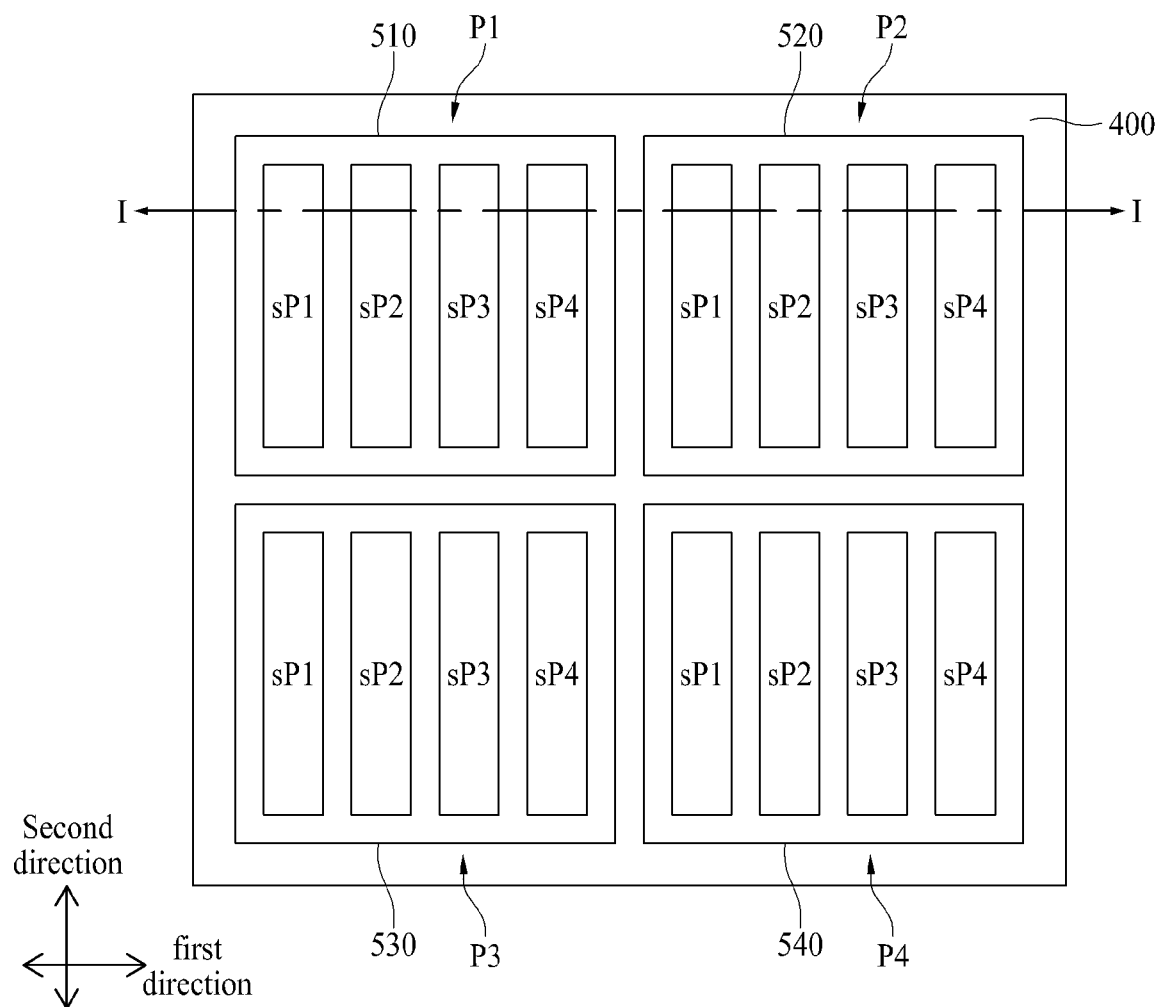


FIG. 3

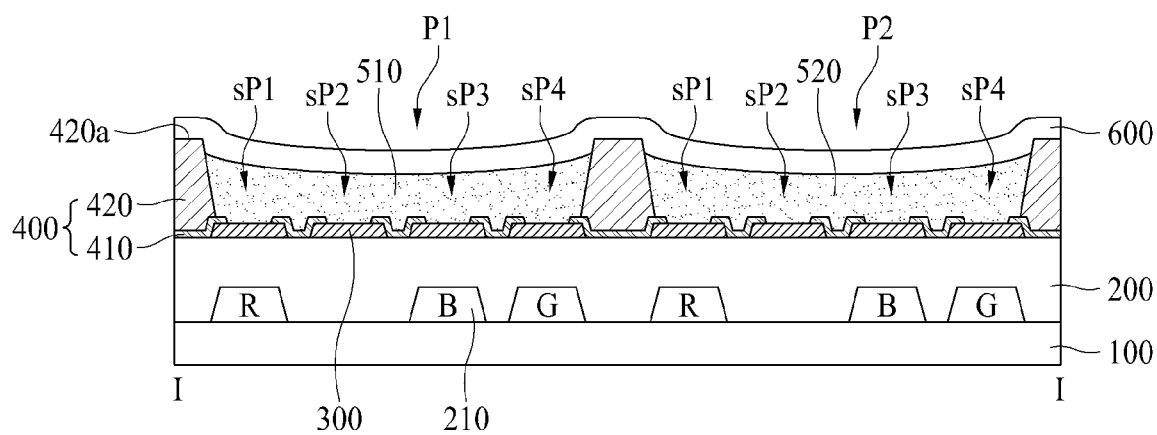


FIG. 4

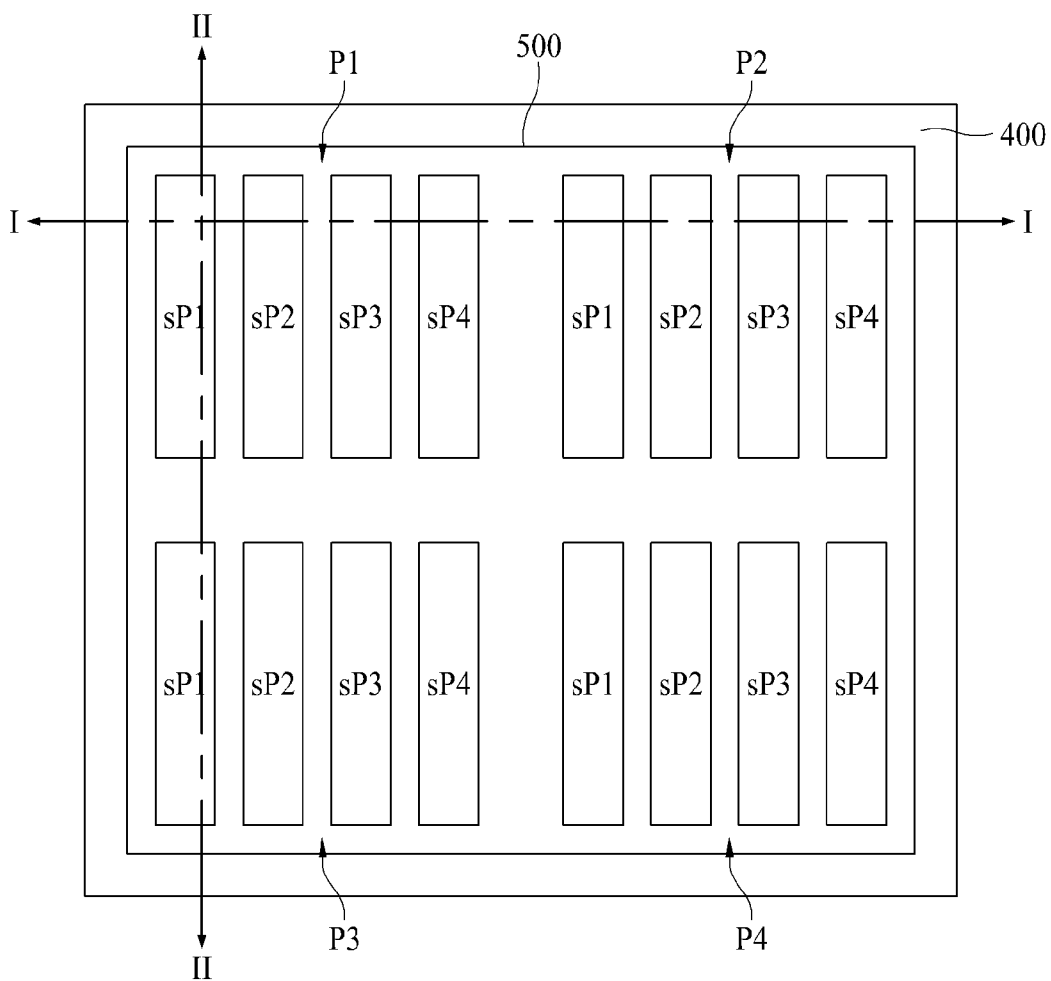


FIG. 5

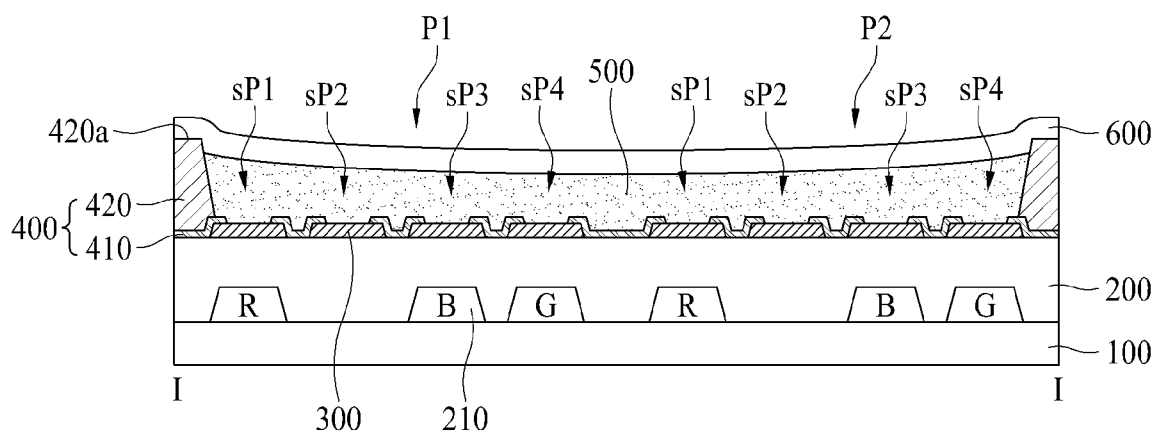


FIG. 6

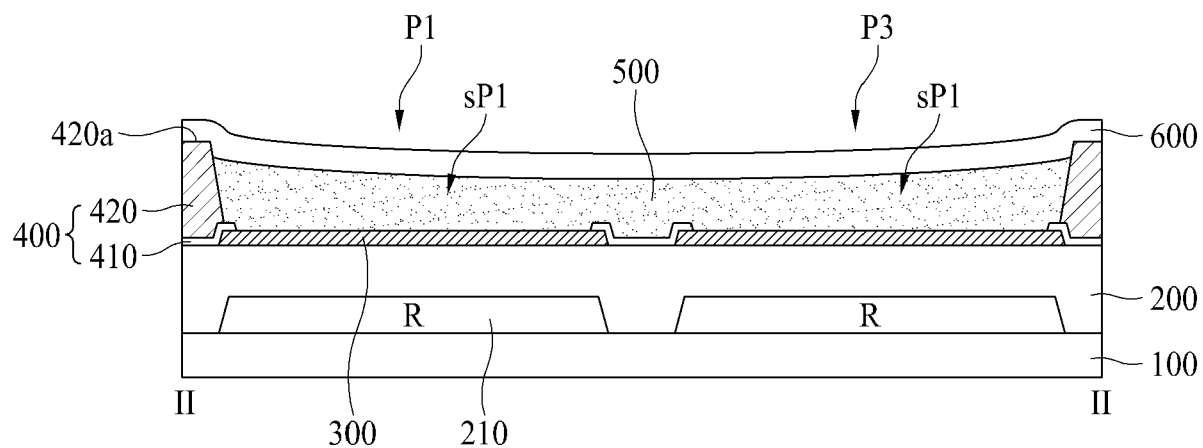


FIG. 7

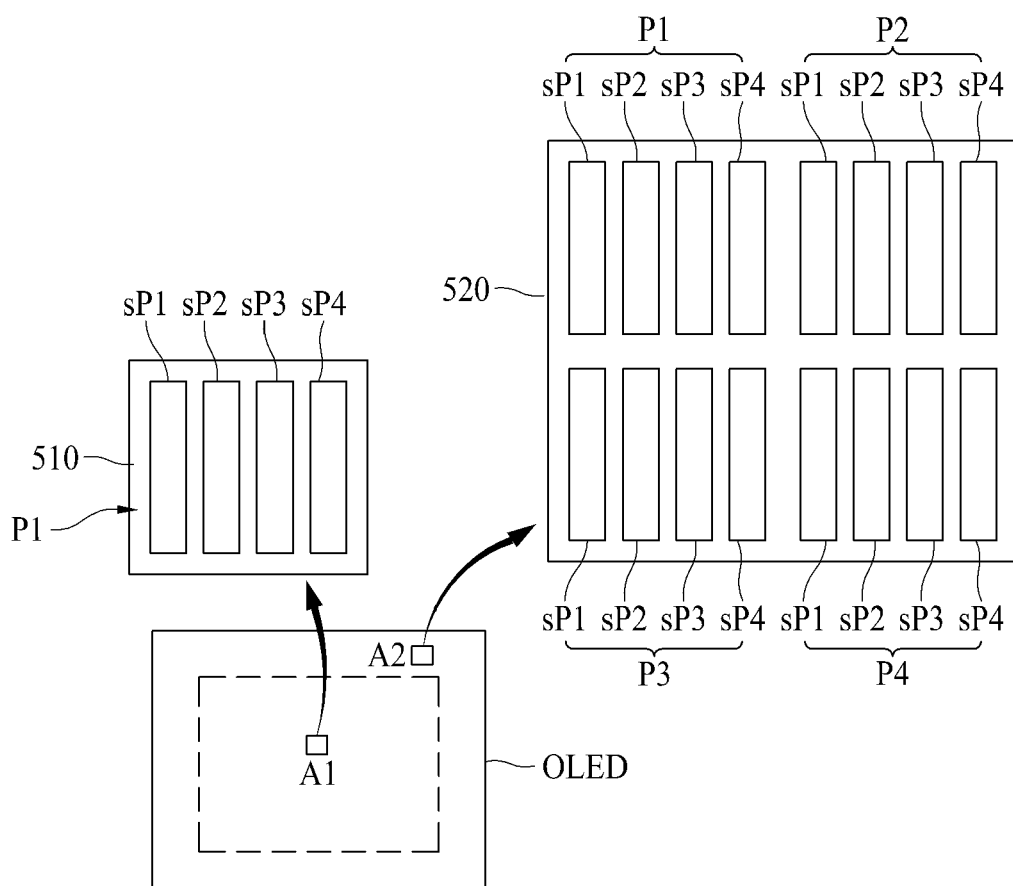


FIG. 8

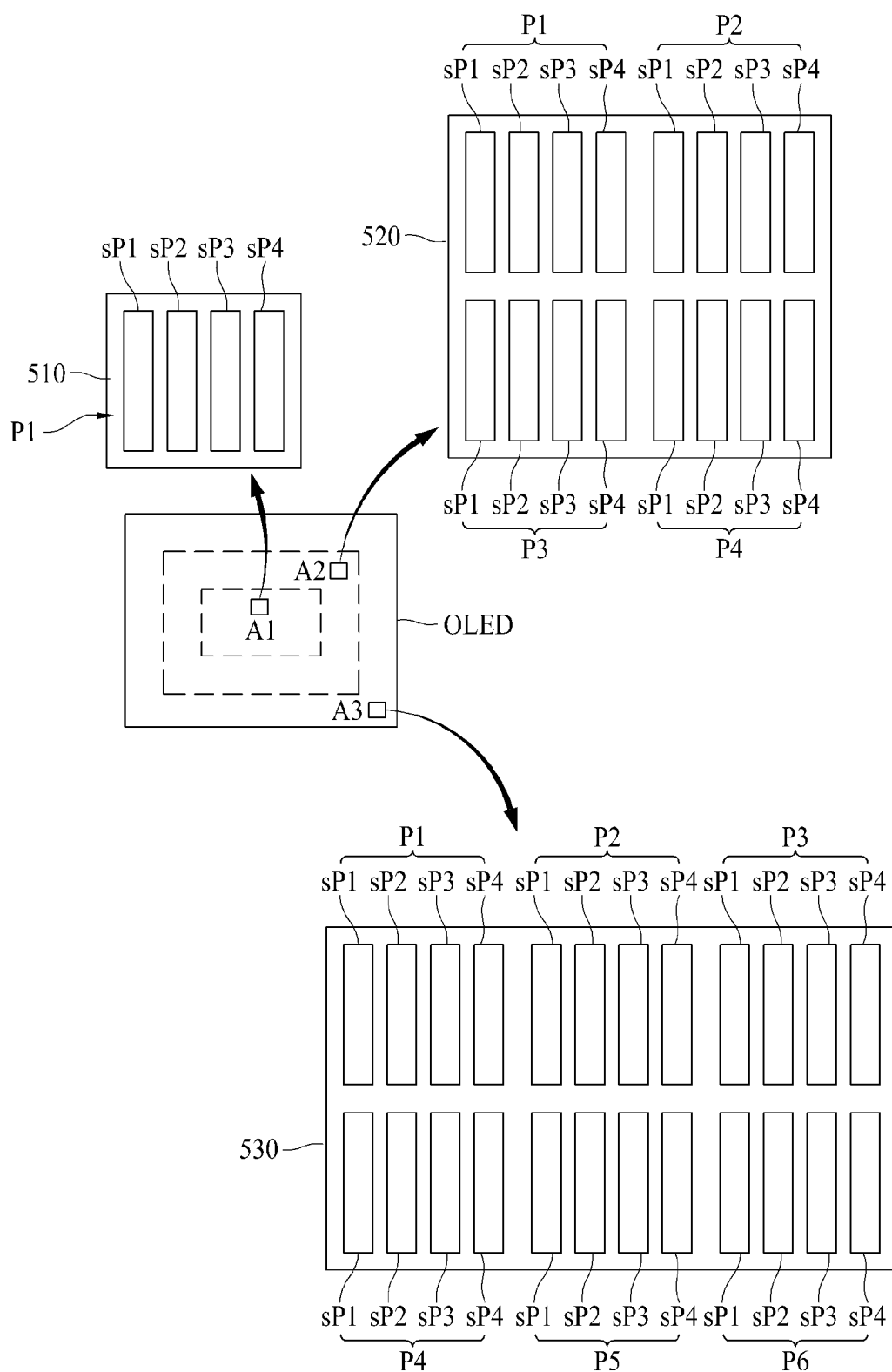


FIG. 9

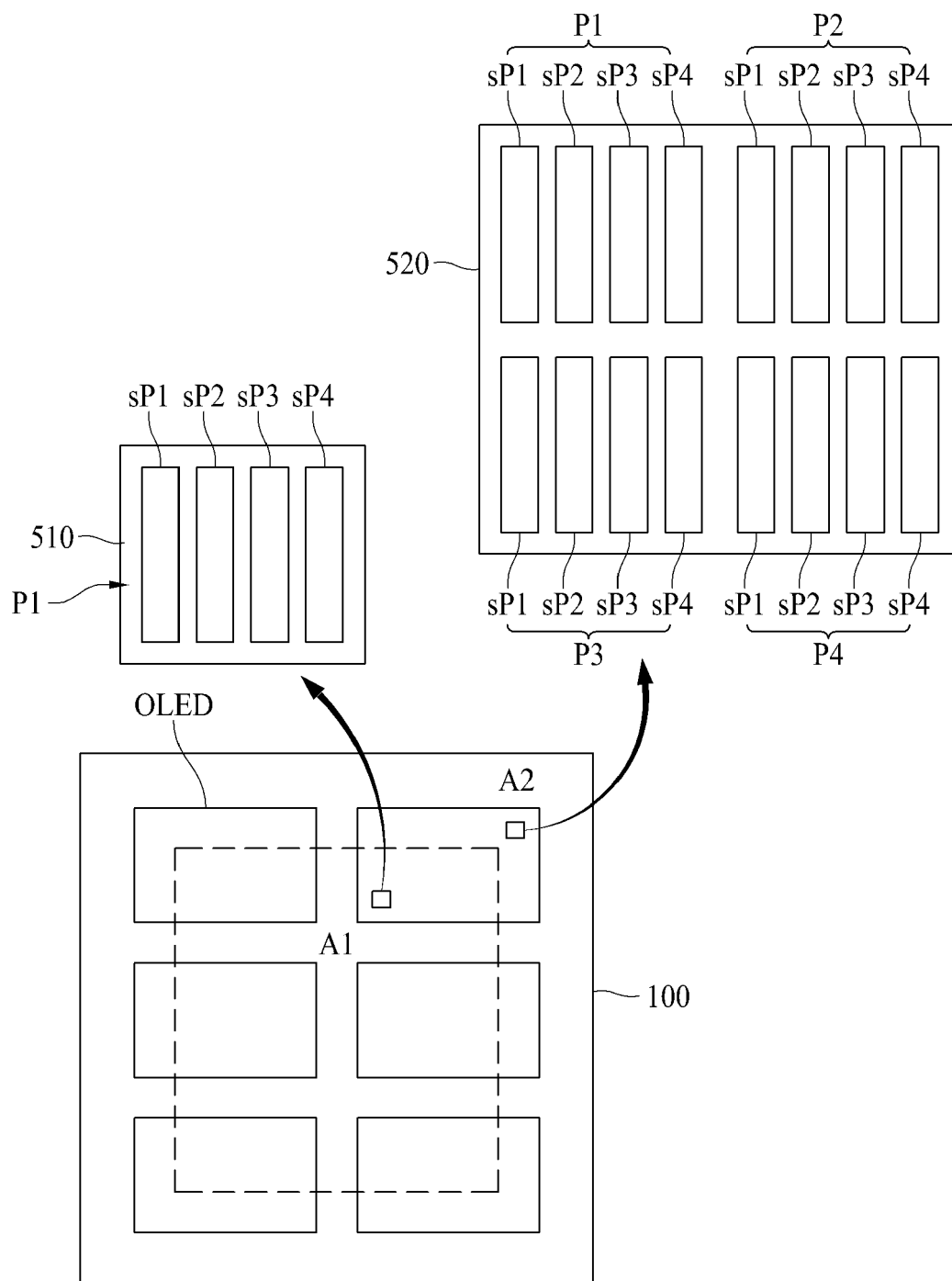


FIG. 10

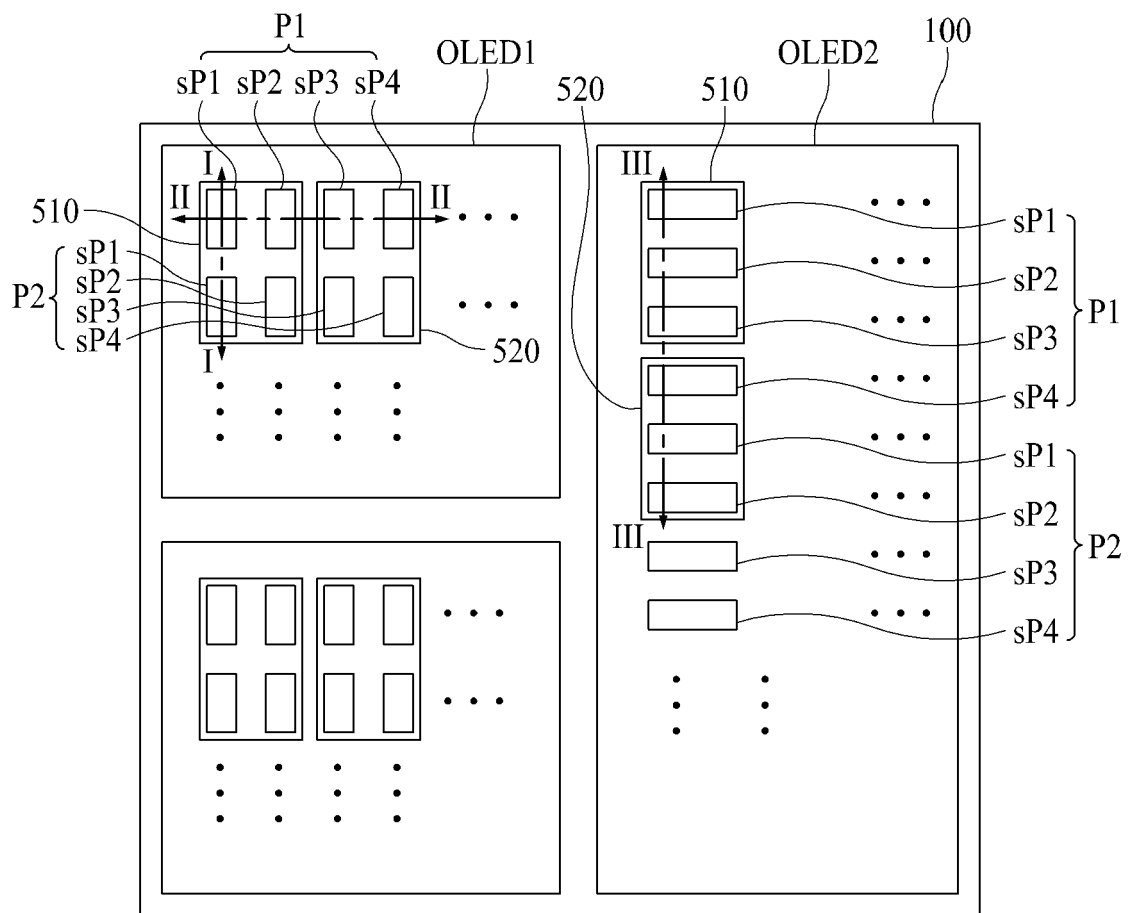


FIG. 11

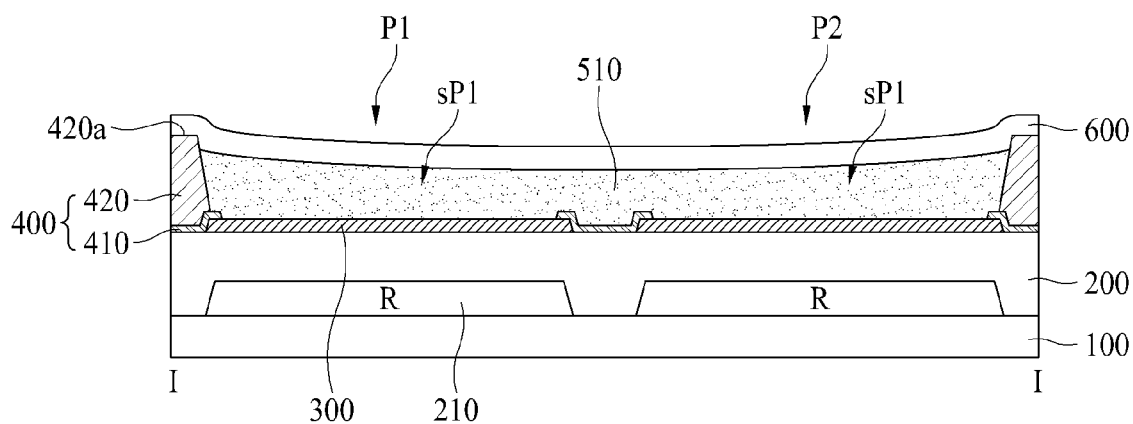


FIG. 12

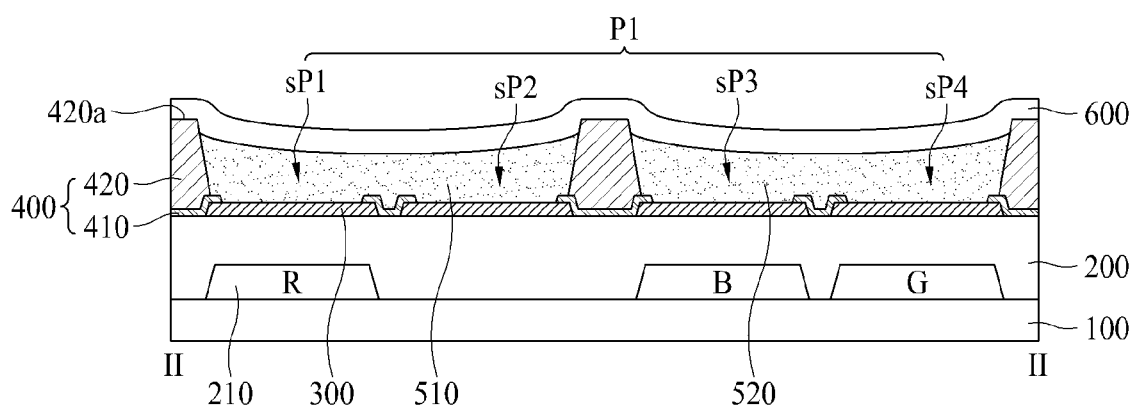
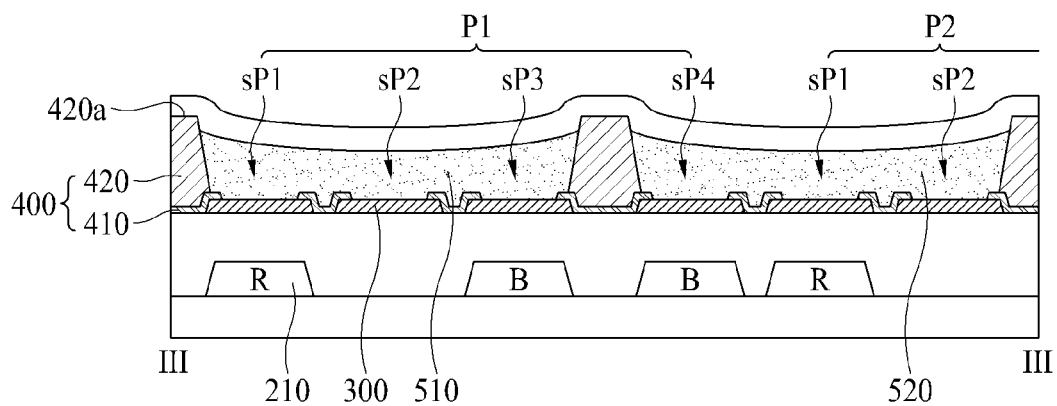


FIG. 13





EUROPEAN SEARCH REPORT

Application Number
EP 17 20 1896

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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
X	JP 2001 143868 A (SEIKO EPSON CORP) 25 May 2001 (2001-05-25)	1-8, 10-12, 14,15	INV. H01L27/32
Y	* page 3, paragraph 25; claims 1-6; figures 4, 5, 6, 7 *	9,13	
Y	US 2015/115251 A1 (JINTA SEIICHIRO [JP] ET AL) 30 April 2015 (2015-04-30) * page 10, paragraph 102; figure 3 *	9,13	
			TECHNICAL FIELDS SEARCHED (IPC)
			H01L
The present search report has been drawn up for all claims			
Place of search The Hague		Date of completion of the search 22 May 2018	Examiner Parashkov, Radoslav
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

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EPO FORM 1503 03/02 (P04C01)

**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 17 20 1896

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The members are as contained in the European Patent Office EDP file on
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22-05-2018

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		KR 20150050353 A	08-05-2015
		US 2015115251 A1	30-04-2015

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EPO FORM P0459

For more details about this annex : see Official Journal of the European Patent Office, No. 12/82

专利名称(译)	电致发光显示装置		
公开(公告)号	EP3343626A1	公开(公告)日	2018-07-04
申请号	EP2017201896	申请日	2017-11-15
[标]申请(专利权)人(译)	乐金显示有限公司		
申请(专利权)人(译)	LG DISPLAY CO. , LTD.		
当前申请(专利权)人(译)	LG DISPLAY CO. , LTD.		
[标]发明人	KIM KANGHYUN		
发明人	KIM, KANGHYUN		
IPC分类号	H01L27/32		
CPC分类号	H01L27/3211 H01L27/3218 H01L27/3246 H01L27/3213 H01L27/322 H01L51/0005 H01L2251/566		
审查员(译)	PARASHKOV , 拉多斯拉夫		
优先权	1020160182359 2016-12-29 KR		
外部链接	Espacenet		

摘要(译)

讨论的是一种电致发光显示装置,其可包括:第一发光层,被设置为对应于多个子像素中的至少两个;第二发光层,被设置为对应于所述多个子像素中的至少两个,其中,所述第二发光层第一发光层与第一发光层间隔开,并且沿第一发光层和第二发光层之间的边界设置堤岸层。

FIG. 3

