

(11) **EP 2 290 642 A3**

(12)

EUROPEAN PATENT APPLICATION

(88) Date of publication A3: **04.05.2011 Bulletin 2011/18**

(51) Int Cl.: **G09G 3/30** (2006.01)

(43) Date of publication A2: 02.03.2011 Bulletin 2011/09

(21) Application number: 10179391.7

(22) Date of filing: 11.09.2002

(84) Designated Contracting States: **DE FI FR GB NL**

(30) Priority: 21.09.2001 JP 2001290287 28.09.2001 JP 2001304643 27.05.2002 JP 2002152727

(62) Document number(s) of the earlier application(s) in accordance with Art. 76 EPC: 02020442.6 / 1 296 310 (71) Applicant: SEMICONDUCTOR ENERGY LABORATORY CO., LTD.
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(54) Semiconductor device for a TFTdisplay matrix

(57) A semiconductor device comprising: a first transistor; a second transistor; a third transistor; and an EL element, wherein one of a source and a drain of the first transistor is electrically connected to the EL element, wherein a gate of the first transistor is electrically connected to a gate of the second transistor, wherein one of

a source and a drain of the second transistor is electrically connected to a first line, wherein the other of the source and the drain of the first transistor is electrically connected to the other of the source and the drain of the second transistor, and wherein one of a source and a drain of the third transistor is electrically connected to the other of the source and the drain of the second transistor.

Fig. 1B

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EUROPEAN SEARCH REPORT

Application Number EP 10 17 9391

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Category	Citation of document with ir of relevant passa	idication, where appropriate, ages	Relevar to claim		ICATION OF THE ATION (IPC)
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A	* figure 3 * * page 9, line 1 -	•	4,11		
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	The present search report has I	·			
	Place of search Munich	Date of completion of the search 25 March 2011		Examiner Fulcheri	Alessandro
X : parti Y : parti docu	ATEGORY OF CITED DOCUMENTS icularly relevant if taken alone cularly relevant if combined with anothinent of the same category	T : theory or princ E : earlier patent after the filling o D : document cite L : document cite	iple underlying to document, but p date d in the applicat d for other reaso	the invention published on, or tion	
O: non-	nological background -written disclosure mediate document	& : member of the document		mily, correspondi	



Application Number

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CLAIMS INCURRING FEES
The present European patent application comprised at the time of filing claims for which payment was due.
Only part of the claims have been paid within the prescribed time limit. The present European search report has been drawn up for those claims for which no payment was due and for those claims for which claims fees have been paid, namely claim(s):
No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for those claims for which no payment was due.
LACK OF UNITY OF INVENTION
The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:
see sheet B
All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.
As all searchable claims could be searched without effort justifying an additional fee, the Search Division did not invite payment of any additional fee.
Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid, namely claims:
None of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims, namely claims:
The present supplementary European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims (Rule 164 (1) EPC).



LACK OF UNITY OF INVENTION SHEET B

Application Number

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The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

1. claims: 1-3, 5, 8-10

Semiconductor device.

The device comprises a fourth transistor, wherein one of a source and a drain of the fourth transistor is electrically connected to the gate of the second transistor.

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2. claims: 4, 6

Semiconductor device.

The other of the source and the drain of the third transistor is electrically connected to the gate of the second transistor.

3. claim: 7

Semiconductor device.

The other of the source and the drain of the fourth transistor is electrically connected to the second line.

4. claim: 11

Method of driving for a semiconductor device. A first current flows from the first line to a second line through at least the second transistor in a first period and a second current flows from the first line to the EL element through at least the second transistor and the first transistor in a second period.

ANNEX TO THE EUROPEAN SEARCH REPORT ON EUROPEAN PATENT APPLICATION NO.

EP 10 17 9391

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

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For more details about this annex : see Official Journal of the European Patent Office, No. 12/82

FORM P0459



专利名称(译)	用于TFT显示矩阵的半导体器件					
公开(公告)号	EP2290642A3	公开(公告)日	2011-05-04			
申请号	EP2010179391	申请日	2002-09-11			
[标]申请(专利权)人(译)	株式会社半导体能源研究所					
申请(专利权)人(译)	半导体能源研究所有限公司.					
当前申请(专利权)人(译)						
[标]发明人	所发明人 KIMURA HAJIME					
发明人	KIMURA, HAJIME					
IPC分类号	G09G3/30 G11C5/14 G09G3/32					
CPC分类号	H01L27/088 G09G3/3241 G09G3/3283 G09G2300/0426 G09G2300/0814 G09G2300/0852 G09G2300/0861 G09G2310/0251 G09G2320/0233 G09G2320/0252 H01L27/15					
优先权	2001290287 2001-09-21 JP 2001304643 2001-09-28 JP 2002152727 2002-05-27 JP					
其他公开文献	EP2290642B1 EP2290642A2					
外部链接	Espacenet					

摘要(译)

一种半导体器件,包括:第一晶体管;第二个晶体管;第三晶体管; EL元件,其中第一晶体管的源极和漏极中的一个电连接到EL元件,其中第一晶体管的栅极电连接到第二晶体管的栅极,其中源极和源极之一第二晶体管的漏极电连接到第一线,其中第一晶体管的源极和漏极中的另一个电连接到第二晶体管的源极和漏极中的另一个,并且其中源极和漏极中的一个第三晶体管的漏极电连接到第二晶体管的源极和漏极中的另一个。

Fig. 1B

