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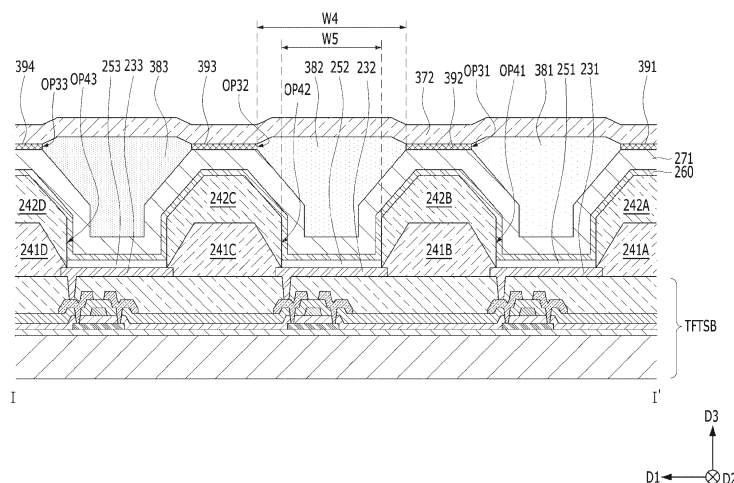
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(54) **DISPLAY APPARATUS AND METHOD OF MANUFACTURING THE SAME**

(57) A display apparatus includes a substrate, a patterned layer, a light-shielding layer, a first electrode, a second electrode, an electroluminescent layer (EL), a first insulating layer (FIL), and a colour filter (CF). The patterned layer is disposed on the substrate, and includes: first body portions, and a first opening between the first body portions. The light-shielding layer is disposed on the patterned layer, and includes: second body portions, and a second opening between the second

body portions. The first electrode is disposed on the substrate, the first and second openings overlap the first electrode. The second electrode overlaps the first electrode. The EL is disposed between the first and second electrodes. The FIL is disposed on the second electrode. The CF is disposed on the FIL. The EL is at least disposed in the first opening, and the FIL and the CF are at least disposed in the second opening.

FIG. 6



Description

[0001] The present invention relates to display technology, and, more particularly, to a display apparatus and a method of manufacturing the display apparatus.

[0002] Organic and inorganic light-emitting display devices are self-luminous display devices capable of providing relatively wide viewing angles and high contrast ratios, as well as relatively fast response times. As such, self-luminous display devices attract attention in many different applications, such as televisions, media players, notebook computers, gaming devices, tablets, monitors, navigational aids, pendant devices, billboards, watches, headphones, earpieces, consumer appliances, etc. Conventional organic light-emitting display devices, in which a formative substance of a light emitting layer includes organic matter, additionally offer relatively high brightness, low driving voltage, and a wide colour gamut.

[0003] Typically, a display device may include a light-shielding layer with openings (or apertures) through which light emitted from the light emitting layer is output. The light-shielding layer may prevent (or at least reduce) the light emitted from the light emitting layer from mixing with light from adjacent light emitting layers, as well as absorb light from an ambient environment. A conventional display device may also include colour filters to modify a colour of light emitted from the light emitting layer. A need, however, exists for efficient, cost-effective techniques to reduce the thickness of a display device, but provide sufficient light blocking, ambient light absorbing, and colour tuning features.

[0004] The above information disclosed in this section is only for enhancement of an understanding of the background of the inventive concepts, and, therefore, it may contain information that does not form prior art already known to a person of ordinary skill in the art.

[0005] One or more exemplary embodiments provide a display apparatus.

[0006] One or more exemplary embodiments provide a method of manufacturing a display apparatus.

[0007] Additional aspects will be set forth in the detailed description which follows, and, in part, will be apparent from the disclosure, or may be learned by practice of the inventive concepts.

[0008] According to one or more exemplary embodiments, a display apparatus includes a substrate, a patterned layer, a light-shielding layer, a first electrode, a second electrode, an electroluminescent layer, a first insulating layer, and a colour filter. The patterned layer is disposed on the substrate. The patterned layer includes: first body portions, and a first opening disposed between the first body portions. The light-shielding layer is disposed on the patterned layer. The light-shielding layer includes: second body portions, and a second opening disposed between the second body portions. The first electrode is disposed on the substrate. The first opening and the second opening overlap the first electrode. The second electrode overlaps the first electrode. The elec-

troluminescent layer is disposed between the first electrode and the second electrode. The first insulating layer is disposed on the second electrode. The colour filter is disposed on the first insulating layer. The electroluminescent layer is at least disposed in the first opening. The first insulating layer and the colour filter are at least disposed in the second opening.

[0009] According to one or more exemplary embodiments, a display apparatus includes a first electrode, a patterned layer, a light-shielding layer, an electroluminescent layer, a second electrode, and a colour filter. The patterned layer includes a first aperture exposing a portion of the first electrode. The light-shielding layer is disposed on the patterned layer. The light-shielding layer includes a second aperture overlapping the first aperture. The electroluminescent layer is disposed in a portion of the first aperture. The second electrode is disposed in portions of the second aperture and the first aperture. The electroluminescent layer is stacked between the first electrode and the second electrode. The colour filter extends into at least one of the second aperture and the first aperture. The second electrode overlaps the light-shielding layer, the patterned layer, and the electroluminescent layer.

[0010] According to one or more exemplary embodiments, a method of manufacturing a display apparatus includes: forming a patterned layer on a substrate including a first electrode, the patterned layer including a first aperture exposing a portion of the first electrode; forming a light-shielding layer on the patterned layer, the light-shielding layer including a second aperture overlapping the first aperture; depositing, through the second aperture, an electroluminescent material on the portion of first electrode; forming a second electrode on the electroluminescent material, the second electrode extending at least into the second aperture; and forming a colour filter overlapping the electroluminescent material, the colour filter extending at least into the second aperture.

[0011] The foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the claimed subject matter.

[0012] The accompanying drawings, which are included to provide a further understanding of the inventive concepts, and are incorporated in and constitute a part of this specification, illustrate exemplary embodiments of the inventive concepts, and, together with the description, serve to explain principles of the inventive concepts.

FIG. 1 is a perspective view of a display apparatus, according to one or more exemplary embodiments. FIG. 2 is an equivalent circuit diagram of a pixel of the display apparatus of FIG. 1, according to one or more exemplary embodiments.

FIG. 3 is a cross-sectional view of the display apparatus of FIG. 1 taken along sectional line I-I', according to one or more exemplary embodiments.

FIGS. 4A, 4B, 4C, 4D, and 4E are cross-sectional

views of a display apparatus at various stages of manufacture, according to one or more exemplary embodiments.

FIG. 5 is a cross-sectional view of the display apparatus of FIG. 1 taken along sectional line I-I', according to one or more exemplary embodiments.

FIG. 6 is a cross-sectional view of the display apparatus of FIG. 1 taken along sectional line I-I', according to one or more exemplary embodiments.

FIGS. 7A and 7B are cross-sectional views of a display apparatus at various stages of manufacture, according to one or more exemplary embodiments.

FIG. 8 is a cross-sectional view of the display apparatus of FIG. 1 taken along sectional line I-I', according to one or more exemplary embodiments.

FIG. 9 is a cross-sectional view of the display apparatus of FIG. 1 taken along sectional line I-I', according to one or more exemplary embodiments.

[0013] In the following description, for the purposes of explanation, numerous specific details are set forth in order to provide a thorough understanding of various exemplary embodiments. It is apparent, however, that various exemplary embodiments may be practiced without these specific details or with one or more equivalent arrangements. In other instances, well-known structures and devices are shown in block diagram form in order to avoid unnecessarily obscuring various exemplary embodiments.

[0014] Unless otherwise specified, the illustrated exemplary embodiments are to be understood as providing exemplary features of varying detail of various exemplary embodiments. Therefore, unless otherwise specified, the features, components, modules, layers, films, panels, regions, aspects, etc. (hereinafter collectively referred to as "elements"), of the various illustrations may be otherwise combined, separated, interchanged, and/or rearranged without departing from the disclosed exemplary embodiments.

[0015] The use of cross-hatching and/or shading in the accompanying drawings is generally provided to clarify boundaries between adjacent elements. As such, neither the presence nor the absence of cross-hatching or shading conveys or indicates any preference or requirement for particular materials, material properties, dimensions, proportions, commonalities between illustrated elements, and/or any other characteristic, attribute, property, etc., of the elements, unless specified. Further, in the accompanying figures, the size and relative sizes of elements may be exaggerated for clarity and/or descriptive purposes. When an exemplary embodiment may be implemented differently, a specific process order may be performed differently from the described order. For example, two consecutively described processes may be performed substantially at the same time or performed in an order opposite to the described order. Also, like reference numerals denote like elements.

[0016] When an element is referred to as being "on,"

"connected to," or "coupled to" another element, it may be directly on, connected to, or coupled to the other element or intervening elements may be present. When, however, an element is referred to as being "directly on," "directly connected to," or "directly coupled to" another element, there are no intervening elements present. Further, the D1-axis, the D2-axis, and the D3-axis are not limited to three axes of a rectangular coordinate system, and may be interpreted in a broader sense. For example, the D1-axis, the D2-axis, and the D3-axis may be perpendicular to one another, or may represent different directions that are not perpendicular to one another. For the purposes of this disclosure, "at least one of X, Y, and Z" and "at least one selected from the group consisting of X, Y, and Z" may be construed as X only, Y only, Z only, or any combination of two or more of X, Y, and Z, such as, for instance, XYZ, XYY, YZ, and ZZ. As used herein, the term "and/or" includes any and all combinations of one or more of the associated listed items.

[0017] Although the terms "first," "second," etc. may be used herein to describe various elements, these elements should not be limited by these terms. These terms are used to distinguish one element from another element. Thus, a first element discussed below could be termed a second element without departing from the teachings of the present disclosure.

[0018] Spatially relative terms, such as "beneath," "below," "under," "lower," "above," "upper," "over," and the like, may be used herein for descriptive purposes, and, thereby, to describe one element's relationship to another element(s) as illustrated in the drawings. Spatially relative terms are intended to encompass different orientations of an apparatus in use, operation, and/or manufacture in addition to the orientation depicted in the drawings. For example, if the apparatus in the drawings is turned over, elements described as "below" or "beneath" other elements or features would then be oriented "above" the other elements or features. Thus, the exemplary term "below" can encompass both an orientation of above and below. Furthermore, the apparatus may be otherwise oriented (e.g., rotated 90 degrees or at other orientations), and, as such, the spatially relative descriptors used herein interpreted accordingly.

[0019] The terminology used herein is for the purpose of describing particular embodiments and is not intended to be limiting. As used herein, the singular forms, "a," "an," and "the" are intended to include the plural forms as well, unless the context clearly indicates otherwise. Moreover, the terms "comprises," "comprising," "includes," and/or "including," when used in this specification, specify the presence of stated features, integers, steps, operations, elements, components, and/or groups thereof, but do not preclude the presence or addition of one or more other features, integers, steps, operations, elements, components, and/or groups thereof. It is also noted that, as used herein, the terms "substantially," "about," and other similar terms, are used as terms of approximation and not as terms of degree, and, as such,

are utilized to account for inherent deviations in measured, calculated, and/or provided values that would be recognized by one of ordinary skill in the art.

[0020] Various exemplary embodiments are described herein with reference to sectional illustrations that are schematic illustrations of idealized exemplary embodiments and/or intermediate structures. As such, variations from the shapes of the illustrations as a result, for example, of manufacturing techniques and/or tolerances, are to be expected. Thus, exemplary embodiments disclosed herein should not be construed as limited to the particular illustrated shapes of regions, but are to include deviations in shapes that result from, for instance, manufacturing. In this manner, regions illustrated in the drawings are schematic in nature and shapes of these regions may not illustrate the actual shapes of regions of a device, and, as such, are not intended to be limiting.

[0021] Unless otherwise defined, all terms (including technical and scientific terms) used herein have the same meaning as commonly understood by one of ordinary skill in the art to which this disclosure is a part. Terms, such as those defined in commonly used dictionaries, should be interpreted as having a meaning that is consistent with their meaning in the context of the relevant art and will not be interpreted in an idealized or overly formal sense, unless expressly so defined herein.

[0022] Although various exemplary embodiments are described with respect to organic light emitting display devices, it is contemplated that various exemplary embodiments are also applicable to other types of display devices, such as liquid crystal display devices, inorganic electroluminescent display devices, field emission display devices, plasma display devices, electrophoretic display devices, electrowetting display devices, and the like.

[0023] FIG. 1 is a perspective view of a display apparatus, according to one or more exemplary embodiments.

[0024] Referring to FIG. 1, display apparatus 10 includes display area DA and non-display area NDA disposed outside of display area DA. In one or more exemplary embodiments, display apparatus 10 may be deformable, e.g., bendable, foldable, flexible, etc. It is contemplated, however, that display apparatus 10 may be rigid, or may include one or more rigid portions and one or more deformable portions. Display apparatus 10 may be a display panel.

[0025] Display area DA may correspond to an active area of display apparatus 10. The active area is a region in which a function of display apparatus 10 is provided to a user, such as a display function. Display area DA may be configured to display an image by combining light from pixels PXL included as part of display area DA. Pixels PXL may be arranged in any suitable formation, such as a matrix formation. For example, pixels PXL may be arranged in first direction D1 and second direction D2 that intersects first direction D1. In this manner, light may be emitted from display apparatus 10 in third direction D3 intersecting first direction D1 and second direction

D2. It is noted that third direction D3 may be perpendicular to first direction D1 and second direction D2.

[0026] Non-display area NDA is disposed outside display area DA. Non-display area NDA may correspond to an inactive area of display apparatus 10. The inactive area is a region in which the function of display apparatus 10 is not provided. At least part of signal lines (not shown) and circuits (not illustrated) for operating pixels PXL may be disposed in non-display area NDA. In one or more exemplary embodiments, portions of the signal lines and the circuits may be covered with, for example, a light-blocking layer (not shown), such as a black matrix, that is disposed in non-display area NDA. It is noted that the light blocking layer may be disposed between pixels PXL of display area DA. In this manner, the light blocking layer may include apertures through which pixels PXL emit light.

[0027] Display apparatus 10 may further include a peripheral circuit (not shown) configured to drive pixels PXL of display area DA. The peripheral circuit may include any suitable component(s) for controlling pixels PXL, such as a gate driver, a data driver, and a power source. The peripheral circuit may receive an input image signal from a source (e.g., a source external to display apparatus 10) and control at least one of pixels PXL based on the input image signal. Signal lines (not illustrated) connecting pixels PXL to the peripheral circuit may pass through non-display area NDA and extend into display area DA. As such, pixels PXL may display an image based on signals received via the signal lines. An equivalent circuit of a representative pixel PXL is described in more detail in association with FIG. 2.

[0028] A cover window (not illustrated) may be provided on display apparatus 10. The cover window may cover, and, thereby, protect display apparatus 10 from (or reduce the extent of) external impacts, scratches, contaminants, etc. Display apparatus 10 may be coupled to the cover window via, for instance, a transparent adhesive layer; however, it is contemplated that any other suitable coupling mechanism may be utilized, e.g., chemical, mechanical, etc. It is also contemplated that the cover window may be formed as a portion (e.g., one or more layers) of display apparatus 10. Further, a touch screen (not shown) may be provided between display apparatus 10 and the cover window. It is also contemplated that the touch screen may be incorporated as a portion, e.g., one or more layers, of display apparatus 10.

[0029] FIG. 2 is an equivalent circuit diagram of a pixel of the display apparatus of FIG. 1, according to one or more exemplary embodiments.

[0030] Referring to FIG. 2, pixel 50 may be representative of pixels PXL of display apparatus 10. Pixel 50 may include organic light-emitting diode (OLED) 51 and pixel circuit 52 for controlling OLED 51. Pixel circuit 52 may include switching transistor 53, driving transistor 54, and sustain capacitor 55. Although reference will be made to this implementation, it is also contemplated that pixel 50 may embody many forms and include multiple and/or al-

ternative components and configurations. For instance, pixel 50 may include any suitable number of thin film transistors and any suitable number of capacitors. As such, the equivalent circuit diagram of FIG. 2 is merely illustrative; exemplary embodiments are not limited thereto or thereby.

[0031] Switching transistor 53 may include a gate electrode connected to scan line SL, a first electrode connected to data line DL, and a second electrode connected to a gate electrode of driving transistor 54. Scan line SL and data line DL may correspond to at least some of the above-noted signal lines, and, thereby, may be connected to a peripheral circuit, e.g., gate driver, data driver, etc. Switching transistor 53 may be turned on by a gate on voltage of a scan signal applied to scan line SL. A data voltage received via data line DL may be transmitted to the gate electrode of driving transistor 54 when switching transistor 53 is turned on.

[0032] Driving transistor 54 may include a gate electrode connected to the second electrode of switching transistor 53, a first electrode for receiving first power source voltage 56, and a second electrode connected to an anode of OLED 51. Driving transistor 54 may control a current flow to OLED 51 from first power source voltage 56 according to a data voltage applied to the gate electrode of driving transistor 54.

[0033] Sustain capacitor 55 may include a first electrode connected to the gate electrode of driving transistor 54 and the second electrode of switching transistor 53. Sustain capacitor 55 may include a second electrode for receiving first power source voltage 56. Sustain capacitor 55 may charge a data voltage received from driving transistor 54 and may maintain a charge when, for instance, switching transistor 53 is turned off.

[0034] OLED 51 may include an anode electrode connected to the second electrode of driving transistor 54 and a cathode electrode for receiving second power source voltage 57. In this manner, OLED 51 may emit light of any suitable colour, e.g., a primary colour. For example, OLED 51 may emit light having at least one of a red colour, a green colour, a blue colour, a white colour, etc. A desired image may be displayed on display apparatus 10 via a spatial or temporal sum of the output of pixels PXL.

[0035] Switching transistor 53 and driving transistor 54 may be p-channel field effect transistors. As such, the gate on voltage for turning on switching transistor 53 and driving transistor 54 may be a logic low level voltage and the gate off voltage for turning off the same may be a logic high level voltage. At least one of switching transistor 53 and driving transistor 54 may be an n-channel field effect transistor. As such, the gate on voltage for turning on the n-channel field effect transistor may be a logic high level voltage and the gate off voltage for turning the same off may be a logic low level voltage.

[0036] The peripheral circuit may apply the gate on voltage to scan line SL to turn on switching transistor 53. The peripheral circuit may apply the gate on voltage or

the gate off voltage as a data voltage to data line DL. In a case where the peripheral circuit applies the gate on voltage, sustain capacitor 55 may be charged with a data voltage from data line DL through switching transistor 53. In addition, the data voltage from data line DL may turn on driving transistor 54. A current corresponding to the data voltage may flow to OLED 51 through turned-on driving transistor 54 from first power source voltage 56. In this manner, OLED 51 may emit light according to the current.

[0037] FIG. 3 is a cross-sectional view of the display apparatus of FIG. 1 taken along sectional line I-I', according to one or more exemplary embodiments.

[0038] Referring to FIGS. 1 and 3, display apparatus 10 may include substrate 110, which may be a flexible substrate. In this manner, substrate 110 may be formed of any suitable material, such as, for example, a glass material, a quartz material, a ceramic material, a metal material, a plastic material, etc. For instance, substrate 110 may be formed of a polymer material, e.g., polyarylate (PAR), polybenzimidazole (PBI), polycarbonate (PC), polyetherimide (PEI), polyethylene naphthalate (PEN), polyethersulphone (PES), polyethylene terephthalate (PET), polysulfone, polyimide (PI), etc.

[0039] Although not illustrated, substrate 110 may be formed as a multilayered structure including one or more organic material layers (e.g. one or more polymer materials) and one or more inorganic material layers (e.g., one or more of silicon nitride (SiN_x), silicon oxide (SiO_x), silicon oxynitride (SiO_xNy), etc.). The one or more organic material layers and the one or more inorganic material layers may be stacked in any suitable fashion, e.g., alternately stacked, symmetrically ordered, etc. It is also contemplated that one or more conductive layers, e.g., metal layers, may be embedded in substrate 110.

[0040] Buffer layer 121 may be disposed on substrate 110 to block or reduce penetration of contaminants. Buffer layer 121 may be deposited via any suitable technique, e.g., plasma-enhanced chemical vapor deposition (PECVD), atmospheric pressure CVD (APCVD), low pressure CVD (LPCVD), and/or the like. Buffer layer 121 may be formed of an inorganic material, such as SiN_x , SiO_x , SiO_xNy , etc.

[0041] Thin film transistors TFT1, TFT2, and TFT3 are disposed on substrate 110 in association with pixels PXL. Each of thin film transistors TFT1 to TFT3 may include a respective active layer 122, gate electrode 124, source electrode 126, and drain electrode 127. Although illustrated as top-gate thin film transistors, it is contemplated that any suitable structure may be utilized in association with exemplary embodiments, such as bottom-gate thin film transistors, dual gate thin film transistors, etc.

[0042] Active layer 122 is disposed on substrate 110. Active layer 122 may be formed by patterning an inorganic semiconductor, such as a silicon semiconductor, oxide semiconductor, organic semiconductor, etc. For example, active layer 122 may be formed using silicon, such that an amorphous silicon layer is formed on a sur-

face of substrate 110 and then crystallized to form a polycrystalline silicon layer. The polycrystalline silicon layer may be patterned to form active layer 122, and doped with impurities to form a source region and a drain region. Active layer 122 includes a source region, a drain region, and a channel region between the source region and the drain region.

[0043] Gate insulating layer 123 is disposed on buffer layer 121 and active layer 122. Gate insulating layer 123 may be formed of any suitable material, such as SiO_x, SiN_x, SiO_xN_y, etc. Gate electrode 124 may be disposed on gate insulating layer 123 in a region overlapping active layer 122. Gate electrode 124 may be connected to switching transistor 53 of FIG. 2.

[0044] Interlayer insulating layer 125 is disposed on gate insulating layer 123 and gate electrode 124. Source electrode 126 and drain electrode 127 are disposed on interlayer insulating layer 125. Source electrode 126 and drain electrode 127 may respectively contact the source region and the drain region of active layer 122 via contact holes formed in gate insulating layer 123 and interlayer insulating layer 125. Source electrode 126 may be connected to first power source voltage 56, and drain electrode 127 may be connected to an anode electrode of a corresponding OLED, e.g., pixel electrode 131.

[0045] Thin film transistors TFT1 to TFT3 may be covered by passivation layer 128. Passivation layer 128 may include one or more inorganic insulating layers and/or one or more organic insulating layers. Passivation layer 128 may be formed as a stacked structure of the inorganic layer(s) and organic insulating layer(s). For example, the inorganic insulating layer may include one of SiO_x, SiN_x, SiON, Al₂O₃, TiO₂, Ta₂O₅, HfO₂, ZrO₂, barium strontium titanate (BST), lead zirconate titanate (PZT), and/or the like. The organic insulating layer may include a polymer, for example, one of polymethylmethacrylate (PMMA), polystyrene (PS), a polymer derivative having a phenolic group, an acrylic polymer, imide-based polymers, aryl ether-based polymers, amide-based polymers, fluorine-containing polymers, p-xylene-based polymers, vinyl alcohol-based polymer, and/or a blend thereof.

[0046] For descriptive convenience, substrate 110, buffer layer 121, thin film transistors TFT1 to TFT3, gate insulating layer 123, interlayer insulating layer 125, and passivation layer 128 may be considered as forming thin film transistor substrate TFTSB. In this manner, OLEDs may be formed on thin film transistor substrate TFTSB and may be respectively coupled to thin film transistors TFT1 to TFT3.

[0047] Pixel electrodes 131, 132, and 133 are disposed on passivation layer 128 and are respectively connected to thin film transistors TFT1 to TFT3. Each of pixel electrodes 131 to 133 contact drain electrode 127 of a corresponding thin film transistor through a contact hole formed in passivation layer 128. Each of pixel electrodes 131 to 133 may function as an anode electrode of a corresponding OLED; however, exemplary embodiments are not limited thereto or thereby. For instance, each of

pixel electrodes 131 to 133 may function as a cathode electrode of a corresponding OLED.

[0048] A patterned layer, such as a pixel definition layer, is disposed on thin film transistor substrate TFTSB.

The patterned layer includes first body portions 141A, 141B, 141C, and 141D and first openings OP11, OP12, and OP13 disposed between first body portions 141A to 141D. First body portions 141A to 141D may define openings (or apertures) through which one or more electroluminescent materials of electroluminescent layers 151, 152, and 153 are to be deposited. First openings OP11 to OP13, which are defined by first body portions 141A to 141D, expose respective portions of pixel electrodes 131 to 133. First body portions 141A to 141D may cover edges of pixel electrodes 131 to 133. First body portions 141A to 141D may have a first (e.g., bottom) surface contacting thin film transistor substrate TFTSB, a second (e.g., top) surface opposing the first surface, and other surfaces extending between the first surface and the second surface. The other surfaces may be inclined between the first surface and the second surface. First openings OP11 to OP13 may have first widths W1 extending between adjacent first body portions, e.g., between first body portion 141B and first body portion 141C. For instance, first widths W1 may represent maximum distances between adjacent first body portions, e.g., maximum widths of first openings OP11 to OP13. It is noted, however, that first widths W1 may be variable along the other surfaces extending between the first surface and the second surface of the first body portions 141A to 141D.

[0049] A light-shielding layer (e.g., a black matrix) is disposed on the patterned layer. The light-shielding layer includes second body portions 142A, 142B, 142C, and 142D disposed respectively on first body portions 141A to 141D. The light-shielding layer also includes second openings OP21, OP22, and OP23 respectively overlapping first openings OP11 to OP13. Second openings OP21 to OP23 may expose portions of first openings OP11 to OP13, and may be concentrically aligned with first openings OP11 to OP13. It is also contemplated that the second openings OP21 to OP23 may respectively communicate with (e.g., open into) first openings OP11 to OP13. Second body portions 142A to 142D may be respectively disposed directly on first body portions 141A to 141D. The light-shielding layer may be configured to prevent (or at least reduce) light emitted from electroluminescent layers 151, 152, and 153 from mixing with each other. The light-shielding layer may also be configured to absorb at least some light from an ambient environment to, for instance, reduce reflection of the light.

[0050] Second body portions 142A to 142D may have a first (e.g., bottom) surface contacting first body portions 141A to 141D, a second (e.g., top) surface opposing the first surface, and other surfaces extending between the first surface and the second surface. The other surfaces may be inclined between the first surface and the second surface. Second openings OP21 to OP23 have second widths W2 between adjacent second body portions, e.g.,

between second body portion 142B and second body portion 142C. For instance, second widths W2 may represent maximum distances between adjacent second body portions, e.g., maximum widths of second openings OP21 to OP23. Second widths W2 may be greater than first widths W1. It is noted, however, that second widths W2 may be variable along the other surfaces extending between the first surface and the second surface of the second body portions 142A to 142D. In this manner, a width (e.g., maximum width) of a second body portion (e.g., second body portion 142C) may be smaller than a maximum width of a first body portion (e.g., first body portion 141C). In one or more exemplary embodiments, a maximum width of a second body portion (e.g., second body portion 142C) may be equivalent (or substantially equivalent) to a minimum width of a first body portion (e.g., first body portion 141C).

[0051] Electroluminescent layers 151 to 153 are respectively disposed on portions of pixel electrodes 131 to 133. As such, electroluminescent layers 151 to 153 may be disposed in first openings OP11 to OP13. For example, electroluminescent layers 51 to 153 may be disposed on surfaces of first body portions 141A to 141D, as well as pixel electrodes 131 to 133. Although not illustrated, electroluminescent layers 51 to 153 may extend into second openings OP21 to OP23.

[0052] Common electrode 160 may be disposed on electroluminescent layers 51 to 153, first body portions 141A to 141D, and second body portions 142A to 142D. It is noted that common electrode 160 may contact electroluminescent layers 51 to 153. Common electrode 160 may be connected to second power source voltage 57 of FIG. 2. In one or more exemplary embodiments, common electrode 160 may function as a cathode electrode of one or more OLEDs of display apparatus 10; however, exemplary embodiments are not limited thereto or thereby. For instance, common electrode 160 may function as an anode electrode of one or more OLEDs of display apparatus 10.

[0053] First pixel electrode 131, first electroluminescent layer 151, and common electrode 160 may form a first OLED. Second pixel electrode 132, second electroluminescent layer 152, and common electrode 160 may form a second OLED. Third pixel electrode 133, third electroluminescent layer 153, and common electrode 160 may form a third OLED. It is noted that the first to third OLEDs may emit the same colour as one another, such as a white colour, a blue colour, etc. As another example, first to third OLEDs may emit different colours, such as red, green, and blue colours. It is contemplated, however, that any other suitable colour of light may be emitted from one or more of first to third OLEDs.

[0054] Thin film transistors TFT1 to TFT3 are connected to the first to third OLEDs, respectively. Each of thin film transistors TFT1 to TFT3 may be driving transistors 54 of respective OLEDs of FIG. 2. Although not illustrated in FIG. 3, it is contemplated that thin film transistor substrate TFTSB may further include switching transistor 53

and sustain capacitor 55.

[0055] First insulating layer 171 is disposed on common electrode 160. First insulating layer 171 may extend into first openings OP11 to OP13 and second openings OP21 to OP23. Second insulating layer 172 is disposed on first insulating layer 171, as well as disposed on colour filters 181, 82, and 183.

[0056] First insulating layer 171 and second insulating layer 172 may form an encapsulation film that covers pixel electrodes 131 to 133, common electrode 160, first body portions 141A to 141D, second body portions 142A to 142D, and electroluminescent layers 51 to 153. The encapsulation film may prevent (or at least reduce) electroluminescent layers 51 to 153 from being deteriorated by, for instance, moisture and/or oxygen from an ambient environment. Each of first insulating layer 171 and second insulating layer 172 may be (or include) at least one organic layer and/or inorganic layer. The inorganic layer may function to inhibit the penetration of moisture, oxygen, and/or the like, into electroluminescent layers 151 to 153. The organic layer may function to relieve stress of the inorganic layer, to complement defects of the inorganic layer, and/or to planarize first insulating layer 171 and second insulating layer 172.

[0057] Although two insulating layers are shown, exemplary embodiments are not limited thereto or thereby. For instance, more than two insulating layers may be provided for encapsulation. For example, one or more of first insulating layer 171 and second insulating layer 172 may correspond to stacks of organic and/or inorganic insulating layers.

[0058] Colour filters 181 to 183 are disposed on first insulating layer 171 and at least overlap electroluminescent layers 151 to 153, respectively. Colour filters 181 to 183 may filter light emitted from electroluminescent layers 151 to 153, respectively, such as tune a colour of the light. Each of colour filters 181 to 183 is configured to selectively pass (or transmit) light of a given range of wavelengths. For instance, each of colour filters 181 to 183 is one of a blue colour filter, a green colour filter, a red colour filter, a yellow colour filter, etc. In other words, any suitable colour filter may be utilized in association with exemplary embodiments.

[0059] According to one or more exemplary embodiments, colour filters 181 to 183 may include particles suspended therein. The particles may be configured to scatter light emitted from electroluminescent layers 151 to 153. For example, the particles may include any suitable material for scattering light, such as ZrO₂, TiO₂, etc. In one or more exemplary embodiments, the particles may correspond to quantum dots, e.g., semiconductor crystals, such as semiconductor nanocrystals. In this manner, the particles may be photo-active (e.g., photoluminescent) and/or electro-active (e.g., electroluminescent). As such, the particles may enhance (e.g., widen, purify, etc.) a colour gamut of pixels PXL of display apparatus 10.

[0060] Colour filters 181 to 183 are disposed between first insulating layer 171 and second insulating layer 172.

Colour filters 181 to 183 may include chromogenic materials and organic materials in which chromogenic materials are distributed. The chromogenic materials may be common pigments or dyes, and the organic materials may be common dispersing agents. Colour filters 181 to 183 may function to relieve stress of first insulating layer 171 and second insulating layer 172, to complement defects in, for instance, an inorganic layer, and/or to planarize surfaces of colour filters 181 to 183. Colour filters 181 to 183 may be disposed in concave portions of first insulating layer 171. The concave portions may correspond to first openings OP11 to OP13 and/or second openings OP21 to OP23.

[0061] According to one or more exemplary embodiments, the light-shielding layer may be provided to prevent (or reduce) light emitted from electroluminescent layers 151 to 153 from mixing and to reduce reflection of ambient light. Moreover, a determined thickness of a colour filter over a threshold thickness value may improve colour reproducibility of display apparatus 10. In other instances, a colour filter thicker than a determined threshold may increase reliability and manufacturing yield. Furthermore, a colour filter thicker than a determined threshold may enable a suitable amount of pigments to be provided such that a filtering function may be improved. It is noted, however, that the thickness of a colour filter is typically balanced with an attempt to reduce an overall thickness of a display apparatus.

[0062] According to one or more exemplary embodiments, first body portions 141A to 141D of the patterned layer and second body portions 142A to 142D of the light-shielding layer are stacked on one another, and colour filters 181 to 183 are disposed in first openings OP11 to OP13 and second openings OP21 to OP23 to not only increase thicknesses of colour filters 181 to 183 and to provide light-blocking effects, but also to reduce an overall thickness of display apparatus 10. To this end, the respective heights of first body portions 141A to 141D and second body portions 142A to 142D may be determined to provide a determined thickness of colour filters 181 to 183. It is also noted that reducing the overall thickness of display apparatus 10 may increase the flexibility of display apparatus 10.

[0063] In one or more exemplary embodiments, colour filters 181 to 183 may fill concave portions of first insulating layer 171, and may be disposed between first insulating layer 171 and second insulating layer 172. Colour filters 181 to 183 may planarize a surface on which second insulating layer 172 is disposed. In this manner, a surface (e.g., uppermost surface) of second insulating layer 172 may be planar.

[0064] Colour filters 181 to 183 may extend from second insulating layer 172 into second openings OP21 to OP23, respectively. To this end, colour filters 181 to 183 may have a first surface closer to second insulating layer 172 and a second surface opposing the first surface. Second body portions 142A to 142D may have a first surface closer to second insulating layer 172 and a second sur-

face opposing the first surface. In this manner, the second surface of colour filters 181 to 183 may be closer to thin film transistor substrate TFTSB than the first surface of second body portions 142A to 142D.

[0065] In one or more exemplary embodiments, colour filters 181 to 183 may extend into second openings OP21 to OP23 and first openings OP11 to OP13. First body portions 141A to 141D may have a first surface closer to second insulating layer 172 and a second surface opposing the first surface. The second surface of colour filters 181 to 183 may be closer to thin film transistor substrate TFTSB than the first surface of first body portions 141A to 141D.

[0066] For instance, the thicknesses of first body portions 141A to 141D may be less than or equal to 2 μm , such as about 1.5 μm . The thicknesses of second body portions 142A to 142D may be greater than or equal to 1 μm and less than or equal to 2.5 μm , such as greater than or equal to 1.5 μm and less than or equal to 2 μm . The thicknesses of pixel electrodes 131 to 133 may be less than or equal to 200nm (2000Å), such as greater than or equal to 100nm (1000Å) and less than or equal to 150nm (1500Å), e.g., about 120nm (1200 Å). The thicknesses of electroluminescent layers 151 to 153 may be less than or equal to 300nm (3000 Å), such as greater than or equal to 100nm (1000 Å) and less than or equal to 250nm (2500 Å), e.g., about 220nm (2200 Å). The thicknesses of common electrode 160 in regions overlapping electroluminescent layers 151 to 153 may be greater than or equal to 5nm (50 Å) and less than or equal to 75nm (750 Å), such as greater than or equal to 10nm (100 Å) and less than or equal to 70nm (700 Å), e.g., about 12nm (120 Å) to about 67nm (670 Å). The thicknesses of first insulating layer 171 in regions overlapping electroluminescent layers 151 to 153 may be less than or equal to 1.5 μm , such as about 1 μm . The thickness of second insulating layer 172 may be less than or equal to 800nm (8000 Å), such as greater than or equal to 500nm (5000 Å) and less than or equal to 750nm (7500 Å), e.g., about 700nm (7000 Å). The thicknesses of colour filters 181 to 183 may be greater than or equal to 2 μm and less than or equal to 5 μm , such as greater than or equal to 2.5 μm and less than or equal to 4.5 μm , e.g., about 3 μm to about 4 μm .

[0067] According to one or more exemplary embodiments, a height of the first surfaces of the colour filters 181 to 183 from an upper surface of thin film transistor substrate TFTSB may be less than or equal to a height of uppermost surfaces of first insulating layer 171 in regions overlapping first body portions 141A to 141D and second body portions 142A to 142D. Further, first insulating layer 171 and second insulating layer 172 may contact each other in regions overlapping first body portions 141A to 141D and second body portions 142A to 142D.

[0068] FIGS. 4A to 4E are cross-sectional views of a display apparatus at various stages of manufacture, according to one or more exemplary embodiments.

[0069] Referring to FIG. 4A, pixel electrodes 131 to

133 are formed on thin film transistor substrate TFTSB. Each of pixel electrodes 131 to 133 may be formed by patterning one or more deposited conductive layers into an island form. In one or more exemplary embodiments, at least portions of the islands may correspondingly overlap with first openings OP11 to OP13 of the patterned layer, as will become more apparent below. Pixel electrodes 131 to 133 may include a reflecting layer formed of at least one of silver (Ag), magnesium (Mg), aluminum (Al), platinum (Pt), palladium (Pd), gold (Au), nickel (Ni), neodymium (Nd), iridium (Ir), and chromium (Cr). Pixel electrodes 131 may include a transparent layer disposed on the reflecting layer. The transparent layer may be formed of at least one of indium tin oxide (ITO), indium zinc oxide (IZO), zinc oxide (ZnO), indium oxide (In₂O₃), indium gallium oxide (IGO), aluminum zinc oxide (AZO), etc. In this manner, pixel electrodes 131 to 133 may be formed with a single layer or a multiple layer configuration. For instance, pixel electrodes 131 to 133 may include a layer formation of ITO/Ti/ITO, Ti_xO_y/Ag/Ti_xO_y, etc.

[0070] A patterned layer may be formed on and overlapping portions of pixel electrodes 131 to 133. First apertures in the patterned layer may expose other portions of pixel electrodes 131 to 133. As seen in FIG. 4A, the patterned layer may include first body portions 141A to 141D disposed on thin film transistor substrate TFTSB. The first body portions 141A to 141D may include first openings OP11 to OP13 corresponding to the first apertures.

[0071] In one or more exemplary embodiments, a layer of organic insulating material may be deposited on pixel electrodes 131 to 133 and thin film transistor substrate TFTSB via any suitable deposition technique. The deposited organic insulating material may include, for instance, at least one of polystyrene, poly methyl methacrylate (PMMA), polyacrylonitrile (PAN), polyamide, polyimide, polyarylether, heterocyclic polymer, parylene, fluorine series polymer, epoxy resin, acrylic resin, benzocyclobutene series resin, siloxane series resin, and silane. Exemplary embodiments are not limited thereto or thereby. The deposited organic insulating material may then be patterned to form first body portions 141A to 141D including first openings OP11 to OP13.

[0072] As seen in FIG. 4B, a light-shielding layer including second openings OP21 to OP23 at least partially overlapping first openings OP11 to OP23 may be formed on the patterned layer. The light-shielding layer may include second body portions 142A to 142D disposed on first body portions 141A to 141D, respectively. Second body portions 142A to 142D may provide second openings OP21 to OP23 as second apertures.

[0073] In one or more exemplary embodiments, second body portions 142A to 142D may be formed of any suitable material for absorbing light, such as an organic material having a high light absorptivity. Second body portions 142A to 142D may include at least one of black pigment and black dye. For example, second body por-

tions 141A to 142D may include at least one of aniline black, carbon black, lamp black, graphite, paliogen black, and perylene black.

[0074] It is noted that electroluminescent layers 151 to 153 may be damaged or at least deteriorated by exposure to excessive heat and/or oxygen. According to one or more exemplary embodiments, the light-shielding layer may be formed before electroluminescent layers 151 to 153 are formed. In this manner, the light-shielding layer may be formed using a relatively high temperature process and/or a relatively low temperature process without affecting formation of electroluminescent layers 151 to 153. It is noted, however, that the light-shielding layer may be formed after formation of electroluminescent layers 151 to 153.

[0075] Referring to FIG. 4C, electroluminescent layers 151 to 153 may be at least deposited on exposed portions of pixel electrodes 131 to 133, respectively. In this manner, electroluminescent layers 151 to 153 may be deposited through first openings OP11 to OP13 and/or second openings OP21 to OP23. For instance, when the light-shielding layer is formed before formation of electroluminescent layers 151 to 153, a material forming electroluminescent layers 151 to 153 may be deposited through second openings OP21 to OP23 and first openings OP11 to OP13.

[0076] Common electrode 160 may be formed on electroluminescent layers 151 to 153, first body portions 141A to 141D, and second body portions 142A to 142D. For instance, common electrode 160 may be formed by depositing a transparent layer of at least one of molybdenum (Mo), tungsten (W), silver (Ag), magnesium (Mg), aluminum (Al), platinum (Pt), palladium (Pd), gold (Au), nickel (Ni), neodymium (Nd), iridium (Ir), chromium (Cr), lithium (Li), calcium (Ca), and alloys thereof. It is also contemplated that common electrode 160 may be formed of indium tin oxide (ITO), indium zinc oxide (IZO), zinc oxide (ZnO), indium oxide (In₂O₃), indium gallium oxide (IGO), aluminum zinc oxide (AZO), etc. It is also contemplated that a work-function of common electrode 160 may be smaller than a work-function of pixel electrodes 131 to 133.

[0077] Referring to FIG. 4D, first insulating layer 171 is formed on common electrode 160. First insulating layer 171 may be formed of at least one of an organic layer and inorganic layer. Any suitable deposition technique may be utilized to form first insulating layer 171. In one or more exemplary embodiments, an inorganic layer may include any suitable material to prevent or reduce penetration of external moisture, oxygen, and/or the like, such as metal oxides, metal nitrides, metal carbides, and compounds thereof, for example, at least one of aluminum oxide, silicon oxide, silicon nitride, etc. An organic layer may include any suitable material for relieving stress of the inorganic layer and/or to planar upper surfaces of first insulating layer 171 and/or later formed structures. The organic layer may be formed of, for instance, a polymeric organic compound including at least one of epoxy, acr-

ylate, and urethane acrylate.

[0078] Referring to FIG. 4E, colour filters 181 to 183 are formed in concave portions of first insulating layer 171 that correspond to first openings OP11 to OP13 and second openings OP21 to OP23. In one or more exemplary embodiments, colour filters 181 to 183 may be formed using an inkjet printing manner, however, exemplary embodiments are not limited thereto or thereby. For example, inkjet head unit 60 may be disposed above first insulating layer 171 and may deposit ink corresponding to materials of colour filters 181 to 183 in the concave portions of first insulating layer 171. The deposited materials may be cured by, for example, exposing the deposited material to a determined temperature and/or radiating the deposited material with ultraviolet rays.

[0079] Referring to FIG. 3, second insulating layer 172 may be formed on first insulating layer 171 and colour filters 181 to 183. Second insulating layer 172 may be formed of at least one of an organic layer and/or inorganic layer. It is noted that first insulating layer 171, second insulating layer 172, and colour filters 181 to 183 may function as an encapsulation layer configured to prevent or at least reduce exposure of electroluminescent layers 151 to 153 to contaminants, e.g., moisture, oxygen, and/or the like.

[0080] FIG. 5 is a cross-sectional view of the display apparatus of FIG. 1 taken along sectional line I-I', according to one or more exemplary embodiments. Display apparatus 10 of FIG. 5 is similar to display apparatus 10 of FIG. 3, and, as such, duplicative descriptions are primarily omitted to avoid obscuring exemplary embodiments.

[0081] Referring to FIG. 5, display apparatus 10 includes thin film transistor substrate TFTSB, pixel electrodes 231, 232, and 233, a patterned layer having first body portions 241A, 241B, 241C, and 241D, a light-shielding layer having second body portions 242A, 242B, 242C, and 242D, electroluminescent layers 251, 252, and 253, common electrode 260, first insulating layer 271, second insulating layer 272, and colour filters 281, 282, and 283.

[0082] First body portions 241A to 241D of the patterned layer include first openings OP11 to OP13 as described with reference to FIG. 3. Second body portions 242A to 242D of the light-shielding layer are disposed on first body portions 241A to 241D of the patterned layer, respectively. As seen in FIG. 5, the maximum widths of second body portions 242A to 242D of the light-shielding layer are wider than the maximum widths of second body portions 142A to 142D of FIG. 3, and, as such, reflection of ambient light may be decreased. In a conventional display apparatus, a light-shielding layer may be disposed above an encapsulation layer, and, as such, the light-shielding layer may be relatively much further from electroluminescent layers 251 to 253. In this manner, a viewing angle of light emitted from corresponding pixels may be narrowed.

[0083] According to one or more exemplary embodiments, the light-shielding layer includes second body

portions 242A to 242D disposed on first body portions 241A to 241D, each of which may be disposed below an encapsulation layer formed of, for instance, first insulating layer 271 and second insulating layer 272. Given that the light-shielding layer is relatively closer to electroluminescent layers 251 to 253 than in a conventional display apparatus, sufficient viewing angles may be obtained even though corresponding maximum widths of second body portions 242A to 242D of the light-shielding layer are formed wider than corresponding maximum widths of second body portions 142A to 142D in the display apparatus of FIG. 3.

[0084] In one or more exemplary embodiments, second body portions 242A to 242D correspondingly extend into first openings OP11 to OP13 that are disposed between first body portions 241A to 241D. Further, second body portions 242A to 242D may be respectively disposed on uppermost surfaces of first body portions 241A to 241D, as well as at least portions of the inclined surfaces of first body portions 241A to 241D extending between the uppermost surfaces and surfaces opposing the uppermost surfaces. For example, second body portion 242B extends into first openings OP11 and OP12, whereas second body portion 242C extends into first openings OP12 and OP13. In one or more exemplary embodiments, each of second body portions 242A to 242D may have third width W3 extending between opposing surfaces in the first direction D1. For instance, third width W3 may be a maximum width of a corresponding second body portion of second body portions 242A to 242D. Further, third width W3 may be equivalent (or substantially equivalent) to a maximum width of a corresponding first body portion of first body portions 241A to 241D.

[0085] Common electrode 260, first insulating layer 271, colour filters 281 to 283, and second insulating layer 272 are disposed on thin film transistor substrate TFTSB. It is contemplated that the shapes of common electrode 260, first insulating layer 271, colour filters 281 to 283, and second insulating layer 272 may vary depending on the shapes of second body portions 242A to 242D.

[0086] FIG. 6 is a cross-sectional view of the display apparatus of FIG. 1 taken along sectional line I-I', according to one or more exemplary embodiments. Display apparatus 10 of FIG. 6 is similar to display apparatus 10 of FIGS. 3 and 5, and, as such, duplicative descriptions are primarily omitted to avoid obscuring exemplary embodiments.

[0087] Referring to FIG. 6, a barrier layer including third body portions 391, 392, 393, and 394 is further disposed between first insulating layer 271 and second insulating layer 372 in regions overlapping second body portions 242A to 242D and first body portions 241A to 241D. Third body portions 391 to 394 include third openings (or apertures) OP31, OP32, and OP33 disposed therebetween. In one or more exemplary embodiments, third openings OP31 to OP33 may have fourth widths W4 greater than fifth widths W5. It is noted that fifth widths

W5 correspond to widths (e.g., minimum widths) of fourth openings OP41, OP42, and OP43 extending, in the first direction D1, between adjacent second body portions of second body portions 242A to 242D. It is also noted that second body portions 242A to 242D may at least extend in third direction D3 into first openings OP11 to OP13, such as illustrated in FIGS. 5 and 6. Further, widths (e.g., maximum widths) of third body portions 391 to 394 may be narrower than maximum widths of second body portions 242A to 242D. Exemplary embodiments, however, are not limited thereto or thereby. It is also contemplated that maximum widths of third body portions 391 to 394 may be greater than minimum widths of second body portions 242A to 242D. Also, third body portions 391 to 394 may be disposed on at least portions of top surfaces of first insulating layer 271 in regions overlapping second body portions 242A to 242D and first body portions 241A to 241D.

[0088] Colour filters 381, 382, and 383 are disposed in concave portions defined by first insulating layer 271 and third body portions 391 to 394. As such, colour filters 381 to 383 may extend into third openings OP31 to OP33. Second insulating layer 372 is disposed on colour filters 381 to 383 and third body portions 391 to 394. First insulating layer 271, second insulating layer 372, colour filters 381 to 383, and the barrier layer including third body portions 391 to 394 may function as an encapsulation layer configured to prevent or at least reduce contaminants from degrading electroluminescent layers 251 to 253.

[0089] According to one or more exemplary embodiments, third body portions 391 to 394 may include a lyophobic material resistive to materials of colour filters 381 to 383. For instance, third body portions 391 to 394 may include fluorine-based compounds, such as a fluorinated silane-based promoter, a fluorinated acrylic monomer, a fluorinated alkyl-based photo resist, and the like. In this manner, when colour filters 381 to 383 are being formed in corresponding concave portions of first insulating layer 271, the material of the colour filters 381 to 383 may be repelled from overlapping third body portions 391 to 394.

[0090] FIGS. 7A and 7B are cross-sectional views of a display apparatus at various stages of manufacture, according to one or more exemplary embodiments. A formation process of FIGS. 7A and 7B is similar to the formation process of FIGS. 4A to 4E, and, as such, duplicative descriptions are primarily omitted to avoid obscuring exemplary embodiments.

[0091] Referring to FIG. 7A, pixel electrodes 231 to 233, a patterned layer including first body portions 241A to 241D, a light-shielding layer including second body portions 242A to 242D, electroluminescent layers 251 to 253, common electrode 260, and first insulating layer 271 are formed on thin film transistor substrate TFTSB. A barrier layer including third body portions 391 to 394 may be formed on first insulating layer 271 in regions overlapping first body portions 241A to 241D and second body portions 242A to 242D.

[0092] Referring to FIG. 7B, colour filters 381 to 383 are formed in the concave portions of first insulating layer 271. For instance, inkjet head unit 60 may be disposed above concave portions of first insulating layer 271, and may deposit material corresponding to materials of colour filters 381 to 383 into the concave portions. Uppermost surfaces of third body portions 391 to 394 may have first height H1 from thin film transistor substrate TFTSB. The materials filled in the concave portions of first insulating layer 271 may have second height H2 from thin film transistor substrate TFTSB. In this manner, a determined amount of materials to form colour filters 381 to 383 may be filled in the concave portions of first insulating layer 271. In one or more exemplary embodiments, second height H2 may be less than first height H1. According to one or more exemplary embodiments, second height H2 may be substantially equivalent to first height H1. In this manner, second insulating layer 372 may be disposed on a substantially planar surface of colour filters 381 to 383 and an uppermost surface of first insulating layer 271.

[0093] According to one or more exemplary embodiments, second height H2 may be greater than first height H1, as shown in FIG. 7B. For example, inkjet head unit 60 may fill a determined amount of material into the concave portions of the first insulating layer 271. It is noted, however, that some additional material may be deposited in at least some of the concave portions of the first insulating layer 271 due to, for instance, manufacturing tolerances. Third body portions 391 to 394, however, may be formed to include a lyophobic material resistive to materials of colour filters 381 to 383. In this manner, third body portions 391 to 394 may prevent (or at least reduce) a potential of the materials of colour filters 381 to 383 from overlapping uppermost surfaces of third body portions 391 to 394. As such, a potential for the materials of colour filters 381 to 383 to be mixed with one another may be reduced. In this manner, purity of colour filters 381 to 383 may be maintained, and manufacturing yield may be improved.

[0094] Second insulating layer 372 may be formed on colour filters 381 to 383 and third body portions 391 to 394, as seen in FIG. 6. It is noted that an upper surface of second insulating layer 372 may have a shape depending on a shape of an upper surface of colour filters 381 to 383 and third body portions 391 to 394, however, exemplary embodiments are not limited thereto or thereby. For example, an uppermost surface of second insulating layer 372 may be planar.

[0095] FIG. 8 is a cross-sectional view of the display apparatus of FIG. 1 taken along sectional line I-I', according to one or more exemplary embodiments. Display apparatus 10 of FIG. 8 is similar to display apparatus 10 of FIGS. 3, 5, and 6, and, as such, duplicative descriptions are primarily omitted to avoid obscuring exemplary embodiments.

[0096] Referring to FIG. 8, a barrier layer including third body portions 491, 492, 493, and 494 is disposed be-

tween first insulating layer 271 and second insulating layer 372 in regions overlapping second body portions 242A to 242D and first body portions 241A to 241B. Colour filters 481, 482, and 483 are disposed in concave portions defined by first insulating layer 271 and at least third body portions 491 to 494. Second insulating layer 472 is disposed on colour filters 481 to 483 and third body portions 491 to 494. A shape of second insulating layer 472 may vary depending on shapes of colour filters 481 to 483 and third body portions 491 to 494.

[0097] According to one or more exemplary embodiments, third body portions 491 to 494 may be formed of any suitable material, such as, for instance, at least one of SiN_x , SiON , SiO_x , and/or the like. Uppermost surfaces of third body portions 491 to 494 may have third heights H3 from thin film transistor substrate TFTSB. To this end, uppermost surfaces of colour filters 481 to 483 may have fourth heights H4 from thin film transistor substrate TFTSB. Fourth heights H4 may be less than or equal to third heights H3.

[0098] As part of forming colour filters 481 to 483, materials deposited to form colour filters 481 to 483 may be filled to height H4, which may be greater than height H5 corresponding to an uppermost surface of first insulating layer 271. It is noted, however, that because height H5 is less than height H3, a potential for materials of colour filters 481 to 483 being mixed can be reduced via third body portions 491 to 494.

[0099] According to one or more exemplary embodiments, third body portions 491 to 494 form concave portions with first insulating layer 271 that are deeper than corresponding concave portions formed in the display apparatus of FIG. 6. In this manner, third body portions 491 to 494 may have thicknesses THC in the third direction D3, which may be determined according to thickness variations associated with manufacturing tolerances of associated processes utilized to form colour filters 481 to 483. It is noted that thickness THC may correspond to a maximum thickness of third body portions 491 to 494. Even though a height of uppermost surfaces of colour filters 481 to 483 increases to be greater than height H5, third body portions 491 to 494 may prevent (or reduce the potential for) the materials of colour filters 481 to 483 from being mixed with each other. In this manner, purity of colour filters 481 to 483 may be maintained, and manufacturing yield may be improved.

[0100] FIG. 9 is a cross-sectional view of the display apparatus of FIG. 1 taken along sectional line I-I', according to one or more exemplary embodiments. Display apparatus 10 of FIG. 9 is similar to display apparatus 10 of FIGS. 3, 5, 6, and 8, and, as such, duplicative descriptions are primarily omitted to avoid obscuring exemplary embodiments.

[0101] Referring to FIG. 9, colour filters 581, 582, and 583 are disposed in concave portions defined by first insulating layer 271 and third body portions 491 to 494. Second insulating layer 572 is disposed on colour filters 581 to 583 and third body portions 491 to 494. It is noted

that a shape of second insulating layer 572 may vary depending on shapes of colour filters 581 to 583 and third body portions 491 to 494.

[0102] According to one or more exemplary embodiments, portions of colour filters 581 to 583 may overlap third body portions 491 to 494. Top surfaces of colour filters 581 to 583 may have sixth heights H6 that are greater than third heights H3 corresponding to heights of uppermost surfaces of third body portions 491 to 494. Although the heights of the uppermost surfaces of colour filters 581 to 583 may increase as a result of manufacturing tolerances, third body portions 491 to 494 may still prevent (or reduce) materials for colour filters 481 to 483 from being mixed. In this manner, purity of colour filters 491 to 493 may be maintained, and manufacturing yield may be improved.

[0103] According to one or more exemplary embodiments, colour filters may be disposed between stacked first body portions of the patterned layer and second body portions of the light-shielding layer. In this manner, a corresponding display apparatus may have reduced thickness, but also include colour filters of sufficient thickness to improve display quality, as well as include sufficient levels of light-shielding features as previously described.

[0104] Although certain exemplary embodiments and implementations have been described herein, other embodiments and modifications will be apparent from this description.

Claims

1. A display apparatus, comprising:

a substrate;
a patterned layer disposed on the substrate, the patterned layer comprising first body portions and a first opening disposed between the first body portions; and
a light-shielding layer disposed on the patterned layer, the light-shielding layer comprising second body portions, wherein a second opening is disposed between the second body portions;
the display apparatus further comprising:

a first electrode disposed on the substrate, wherein the first opening and the second opening overlap the first electrode;
a second electrode overlapping the first electrode;
an electroluminescent layer disposed between the first electrode and the second electrode;
a first insulating layer disposed on the second electrode; and
a colour filter disposed on the first insulating layer,

- wherein:
- the electroluminescent layer is disposed in the first opening; and
the first insulating layer and the colour filter are disposed in the second opening. 5
2. The display apparatus of claim 1, wherein at least one of the colour filter and the first insulating layer extends into the first opening. 10
3. The display apparatus of claim 1 or 2, further comprising:
- a second insulating layer covering the first insulating layer and the colour filter, wherein:
- the first insulating layer covers the first body portions, the second body portions, and the second electrode; and
the colour filter is disposed between the first insulating layer and the second insulating layer. 20
4. The display apparatus of claim 3, wherein the second insulating layer contacts the first insulating layer in regions overlapping the second body portions. 25
5. The display apparatus of claim 3 or 4, further comprising:
- a barrier layer comprising:
- third body portions; and
a third opening disposed between the third body portions, wherein, in regions overlapping the second body portions, the third body portions are disposed between the second insulating layer and the first insulating layer. 30
6. The display apparatus of claim 5, wherein a first portion of the colour filter extends into the third opening and optionally wherein second portions of the colour filter overlap the third body portions. 35
7. The display apparatus of claim 5 or 6, wherein:
- the barrier layer comprises:
- a first surface disposed closer to the second insulating layer than the first insulating layer; and
a second surface opposing the first surface; 40
- the colour filter comprises:
- a third surface disposed closer to the second insulating layer than the substrate; and
a fourth surface opposing the third surface; and
in a direction normal to the substrate, the first surface and the third surface are equidistant from the substrate or the third surface is closer to the substrate than the first surface. 45
8. The display apparatus of any one of claims 5 to 7, wherein a maximum width of third opening is greater than a minimum width of the second opening.
9. The display apparatus of any one of claims 5 to 8, wherein the third body portions comprise a lyophobic material resistive to a material of the colour filter.
10. The display apparatus of any one of the preceding claims, wherein:
- the second body portions are disposed on the first body portions; and
the second body portions extend into the first opening. 50
11. The display apparatus of any one of the preceding claims, wherein maximum widths of the second body portions are substantially equal to maximum widths of the first body portions.
12. The display apparatus of any one of the preceding claims, wherein the colour filter comprises particles suspended therein, the particles being configured to scatter incident light.
13. A method of manufacturing a display apparatus, the method comprising:
- forming a patterned layer on a substrate comprising a first electrode, the patterned layer comprising a first opening exposing a portion of the first electrode;
forming a light-shielding layer on the patterned layer, the light-shielding layer comprising a second opening overlapping the first opening;
depositing, through the second opening, an electroluminescent material on the portion of the first electrode;
forming a second electrode on the electroluminescent material, the second electrode extending into the second opening; and
forming a colour filter overlapping the electroluminescent material, the colour filter extending into the second opening. 55
14. The method of claim 13, further comprising:

forming a first insulating layer, after forming the second electrode, the first insulating layer extending into the second aperture, wherein the first insulating layer is disposed between the colour filter and the second electrode. 5

15. The method of claim 14, further comprising:

forming, after forming the colour filter, a second insulating layer on the substrate, 10 wherein the colour filter is disposed between the second insulating layer and the first insulating layer, and/or further comprising:

forming, before forming the colour filter, a barrier layer comprising a third aperture overlapping the second aperture, 15 wherein forming the colour filter comprises depositing a colour filter material through the third aperture. 20

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FIG. 1

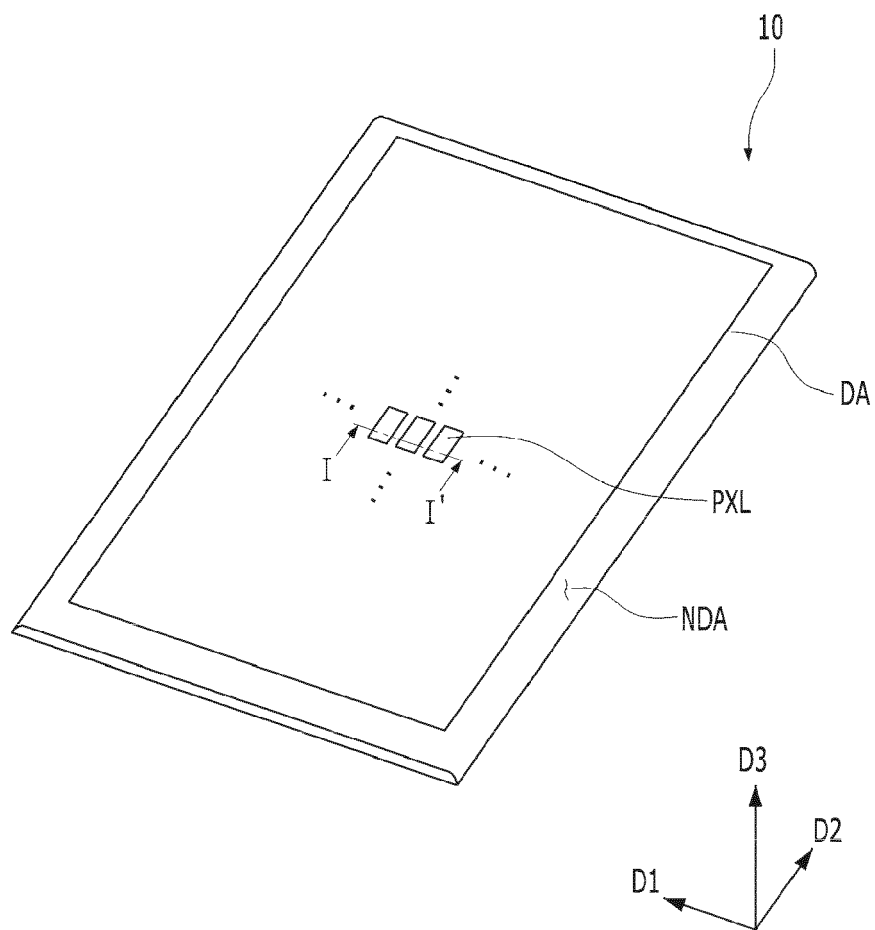


FIG. 2

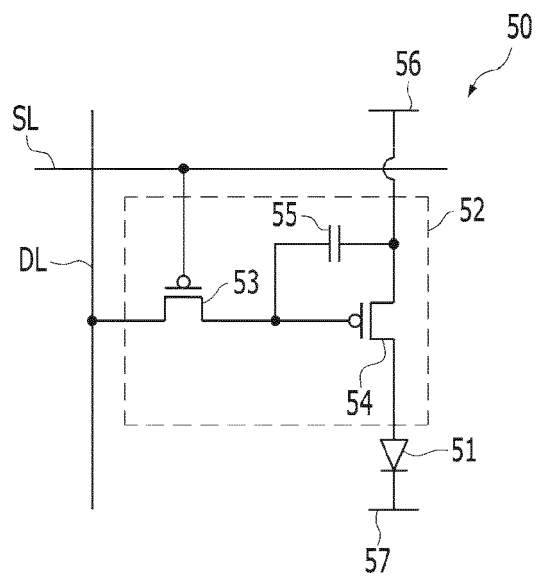


FIG. 3

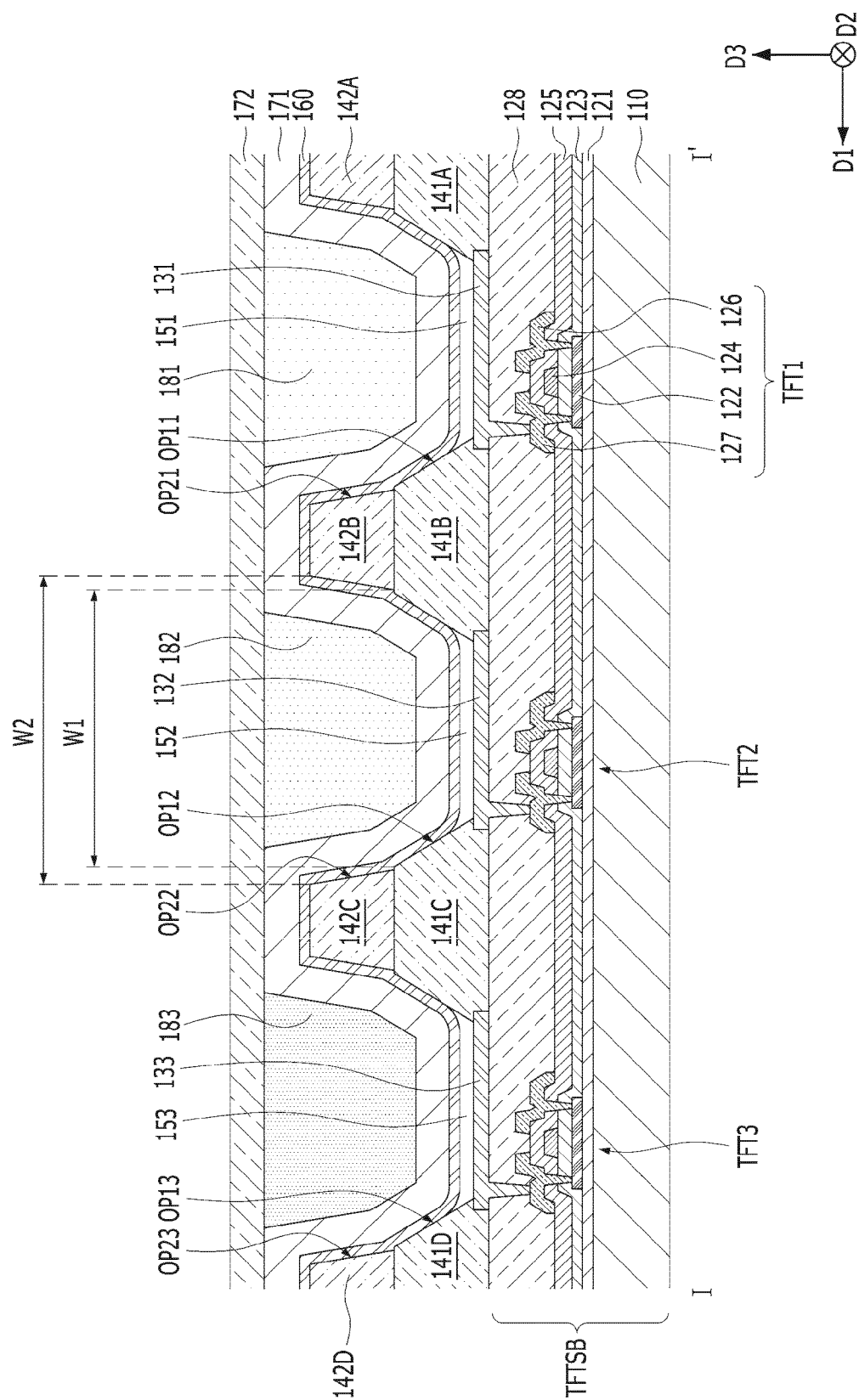


FIG. 4A

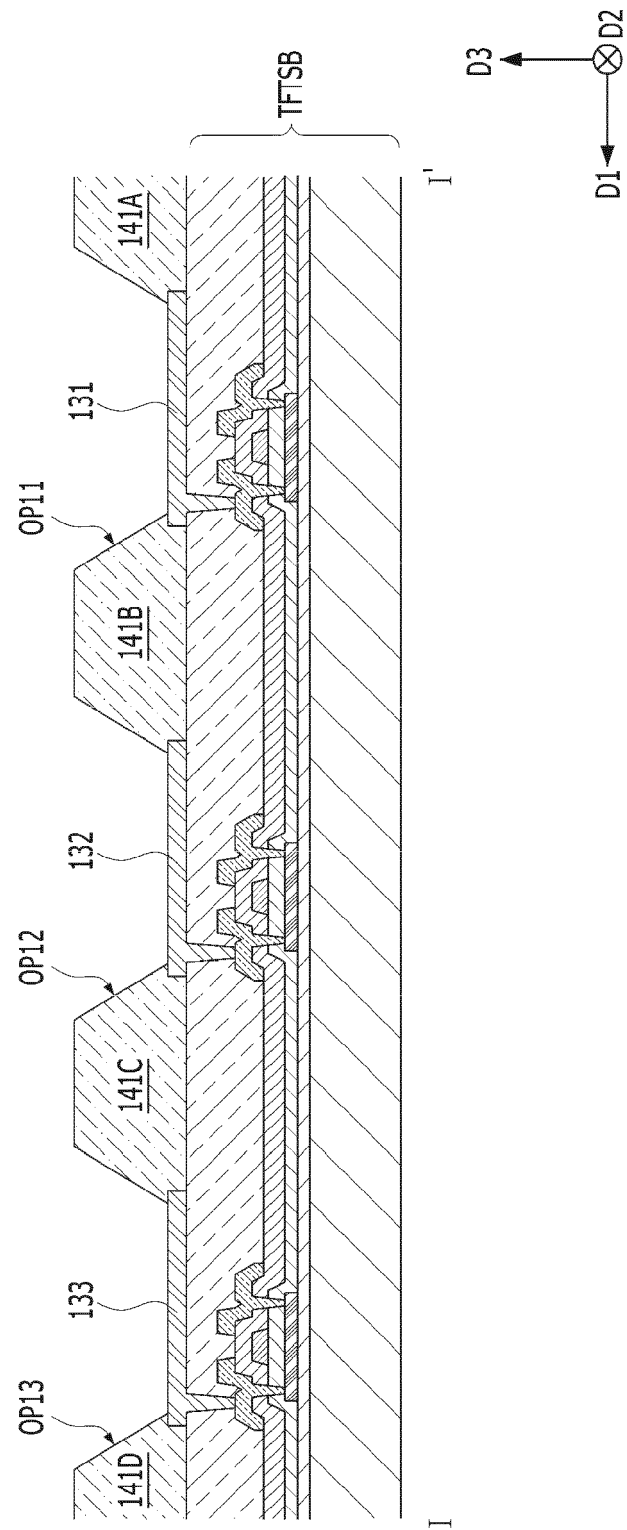


FIG. 4B

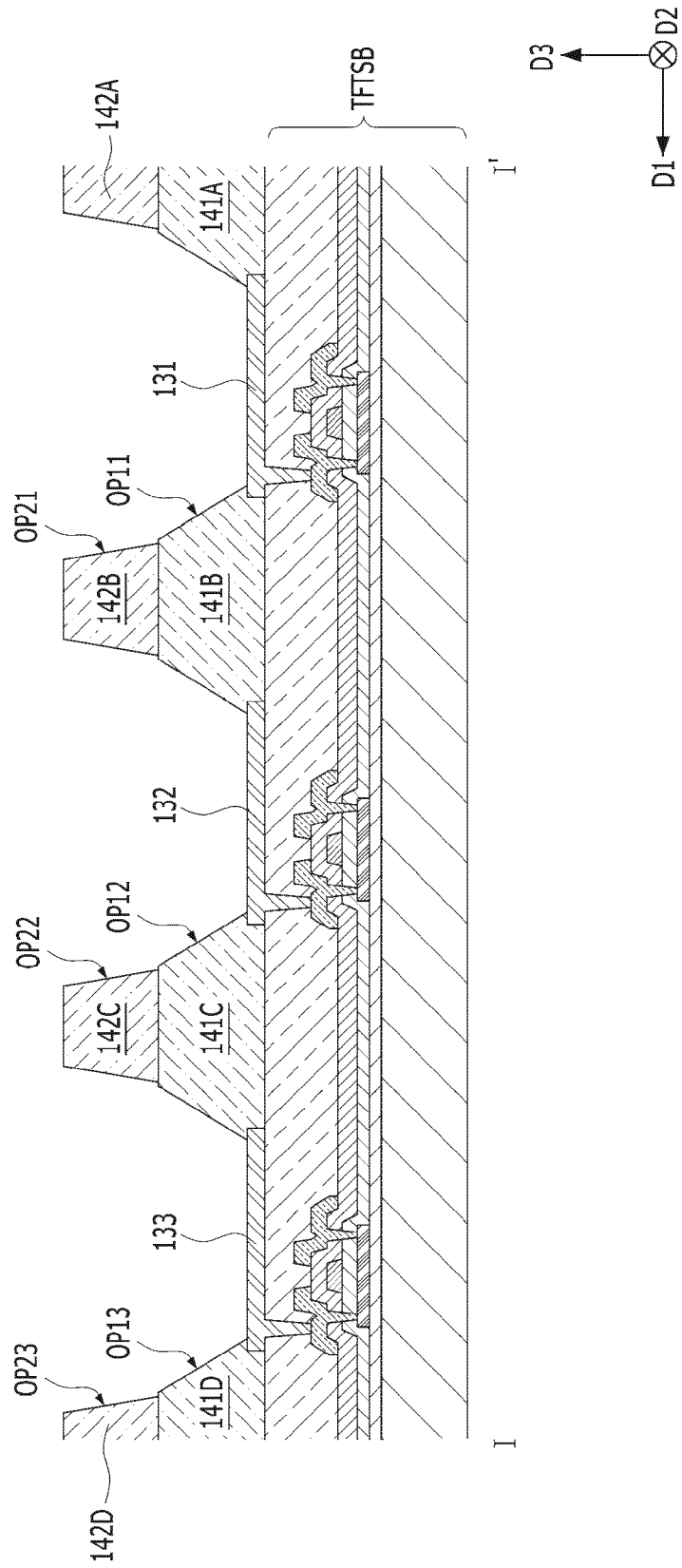


FIG. 4C

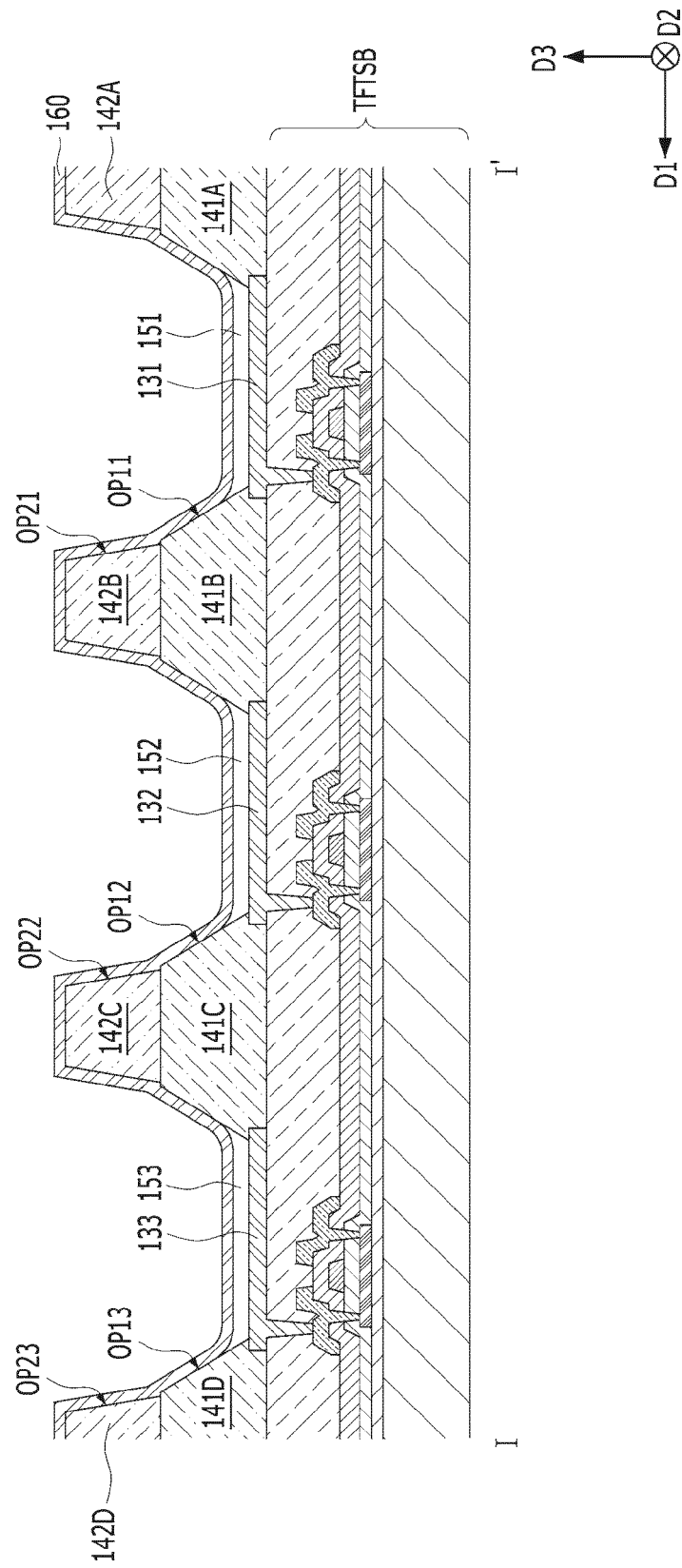


FIG. 4D

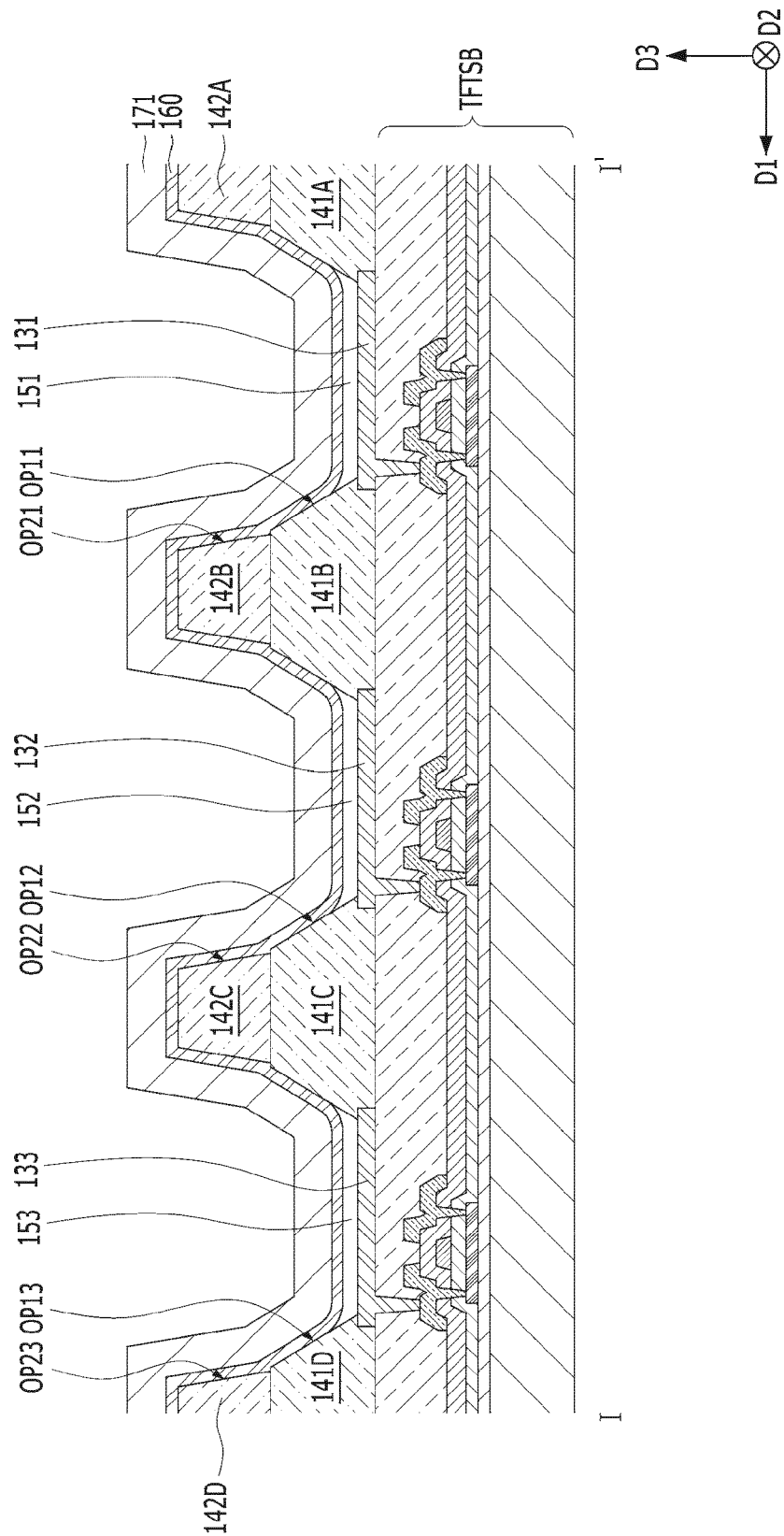


FIG. 4E

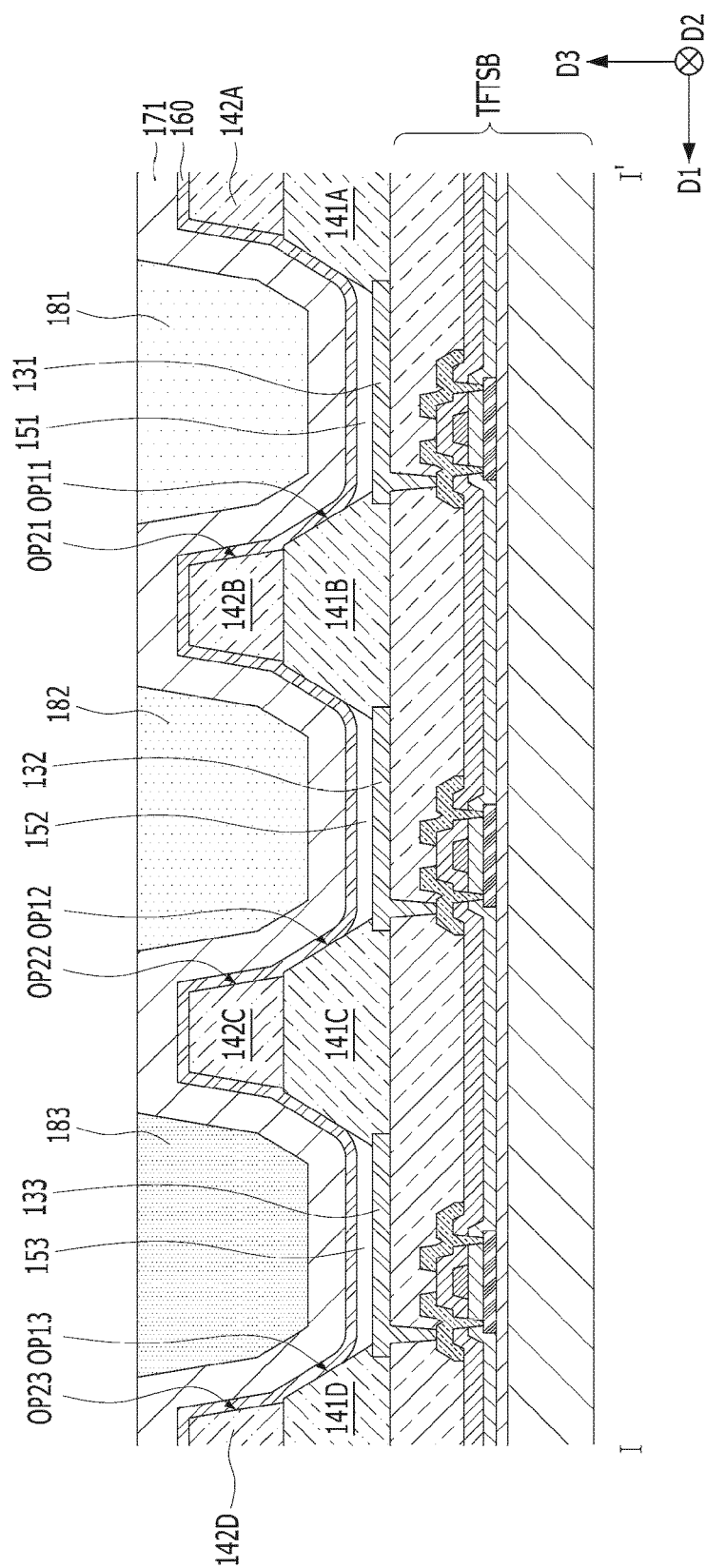
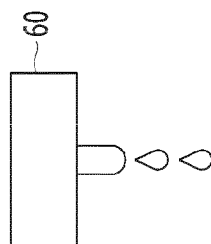


FIG. 5

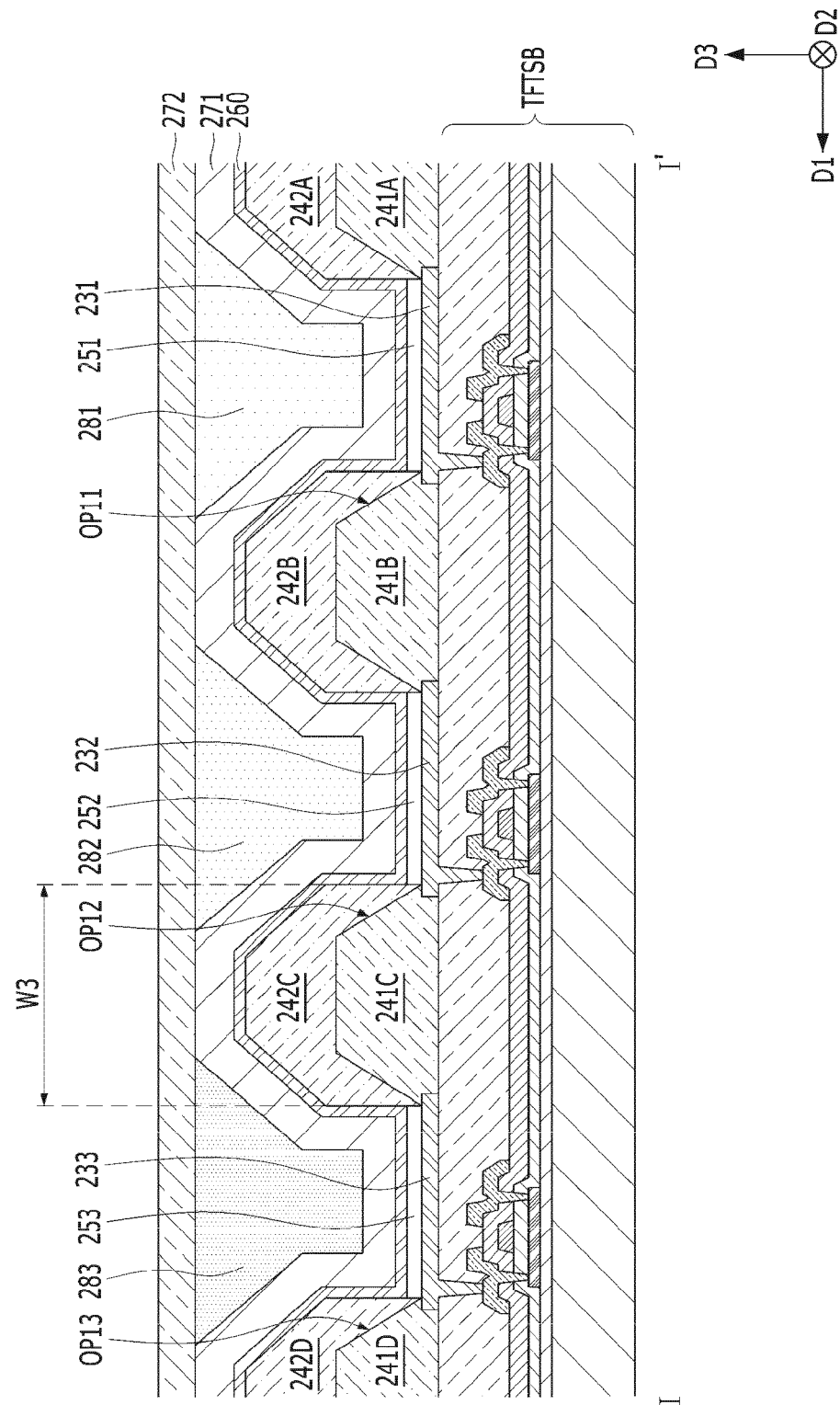


FIG. 6

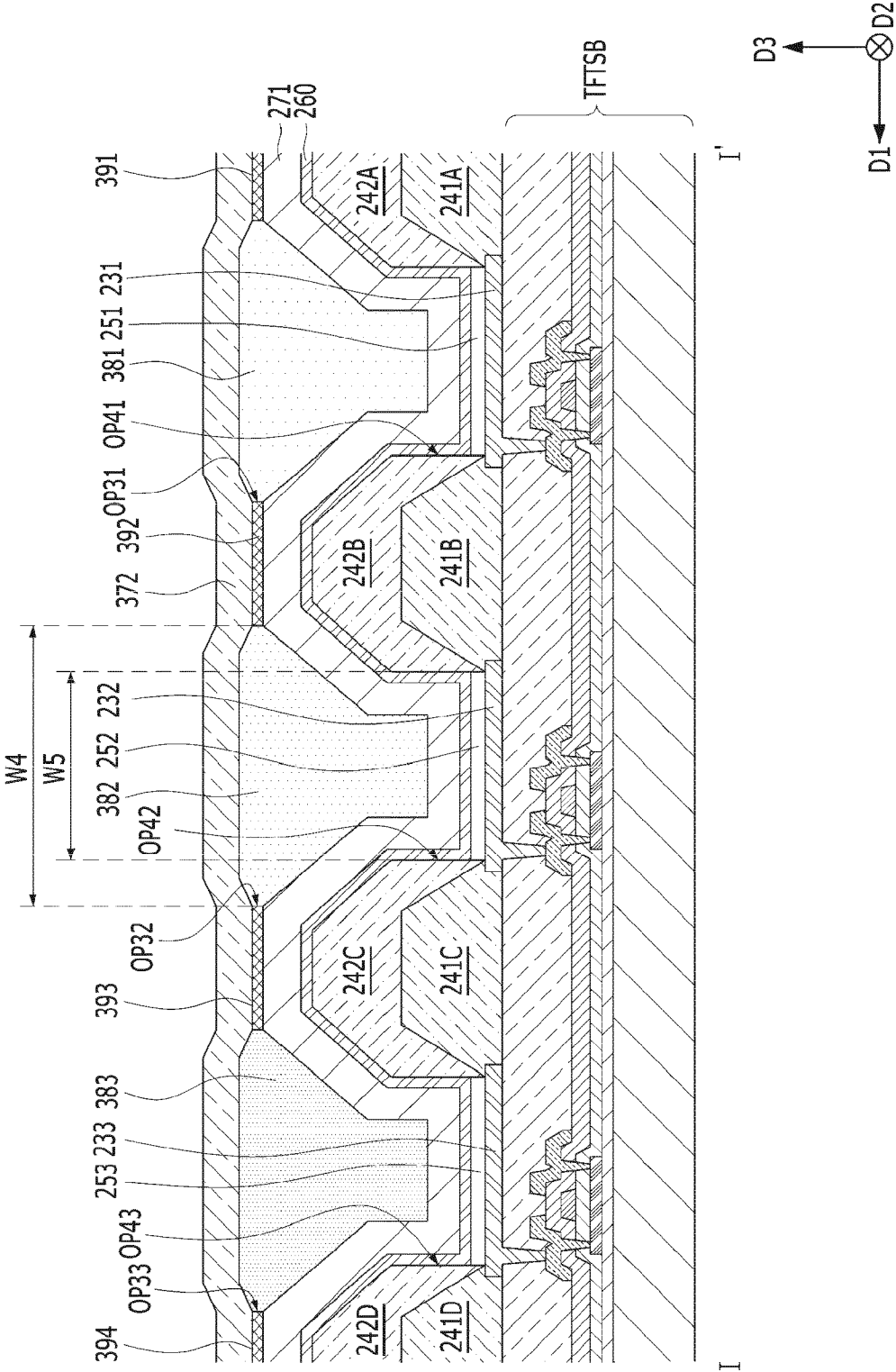


FIG. 7A

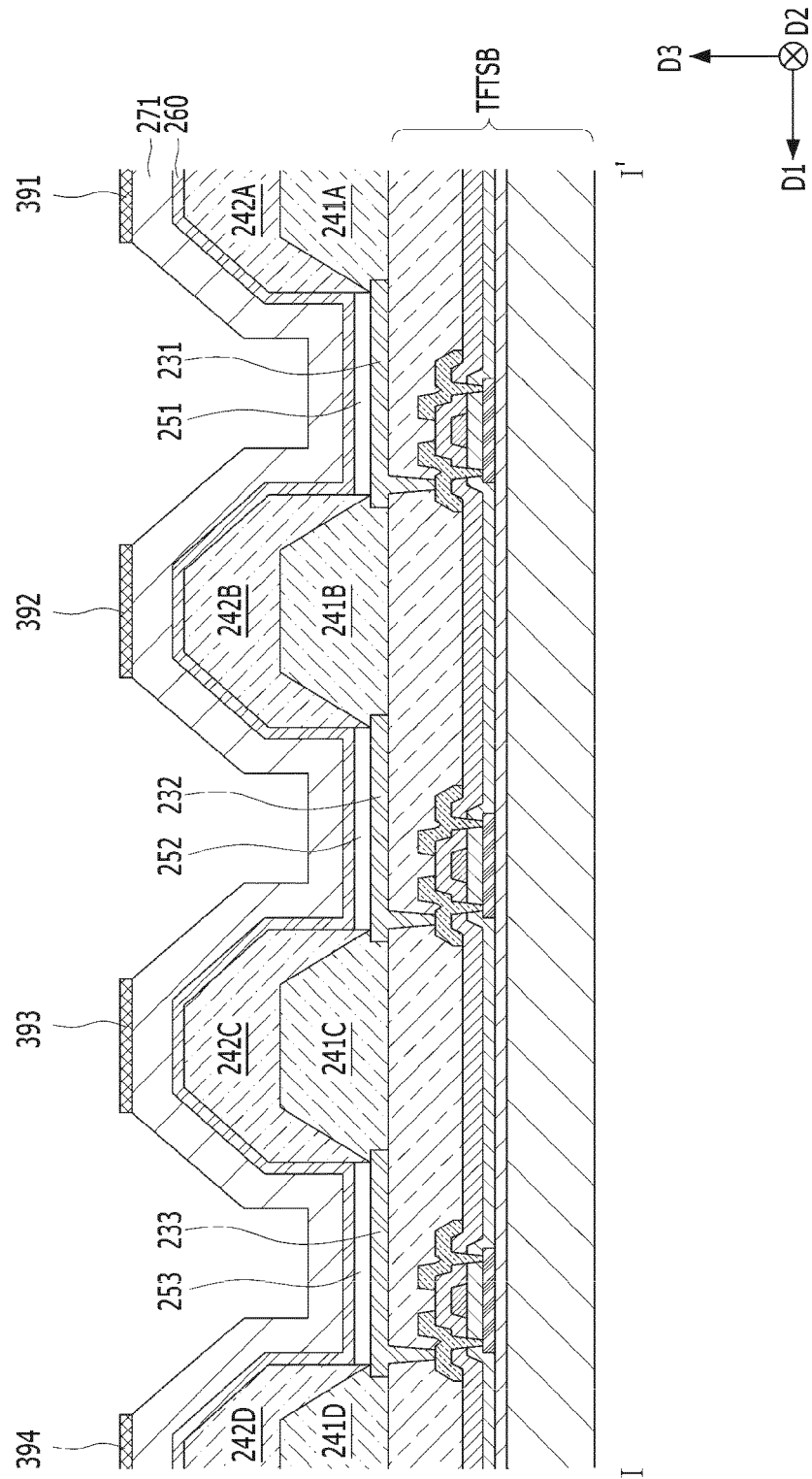


FIG. 7B

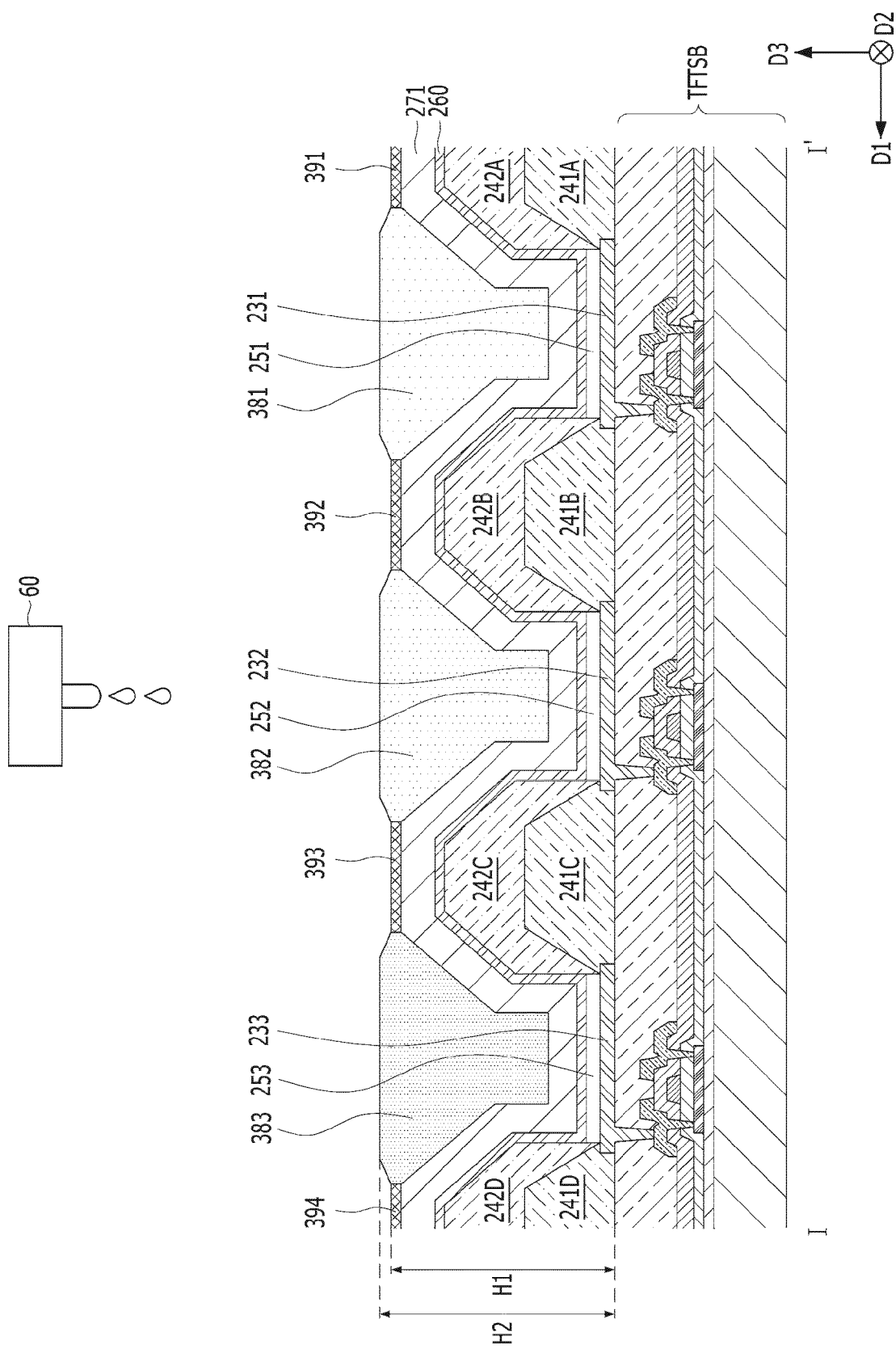


FIG. 8

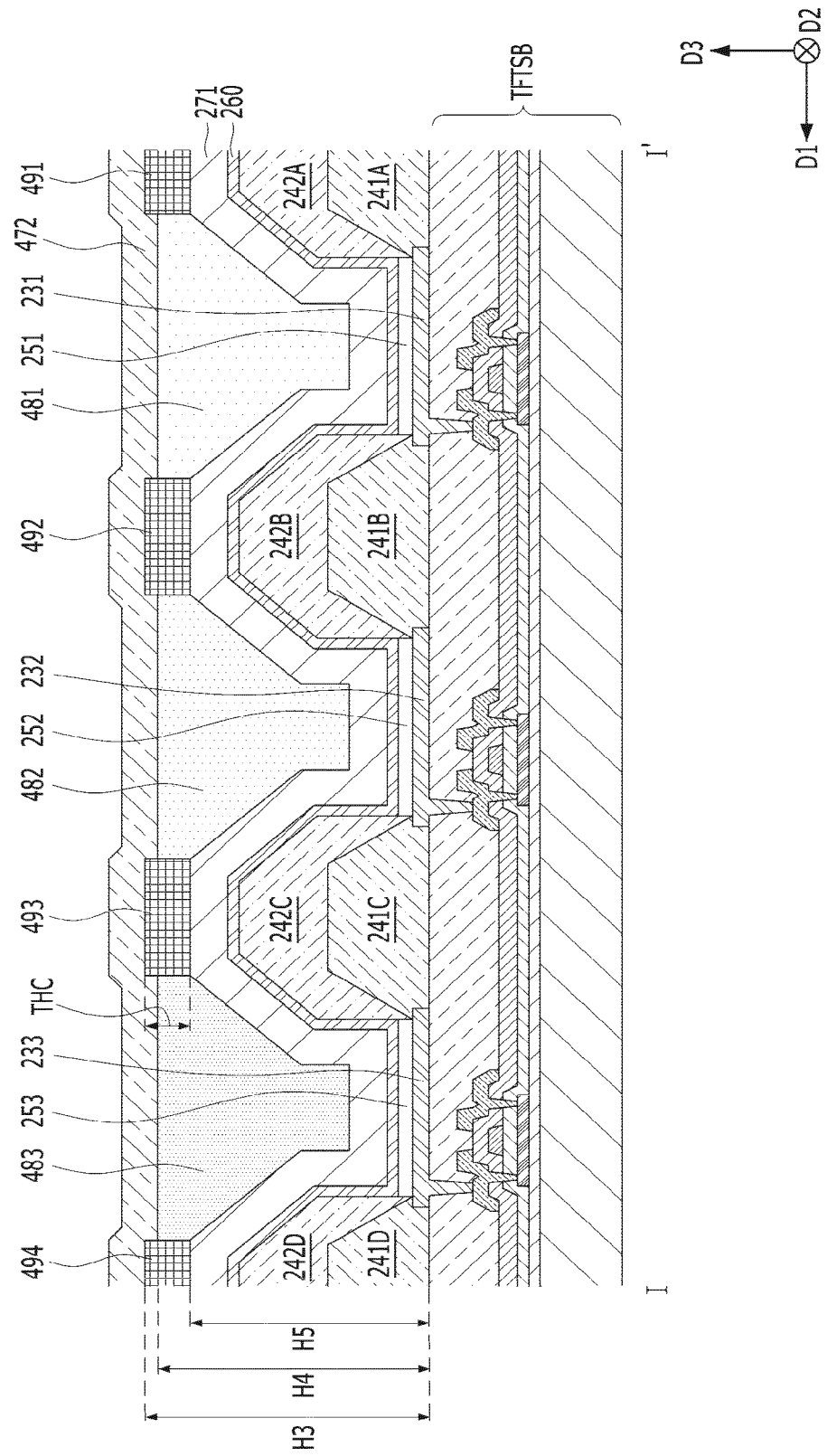
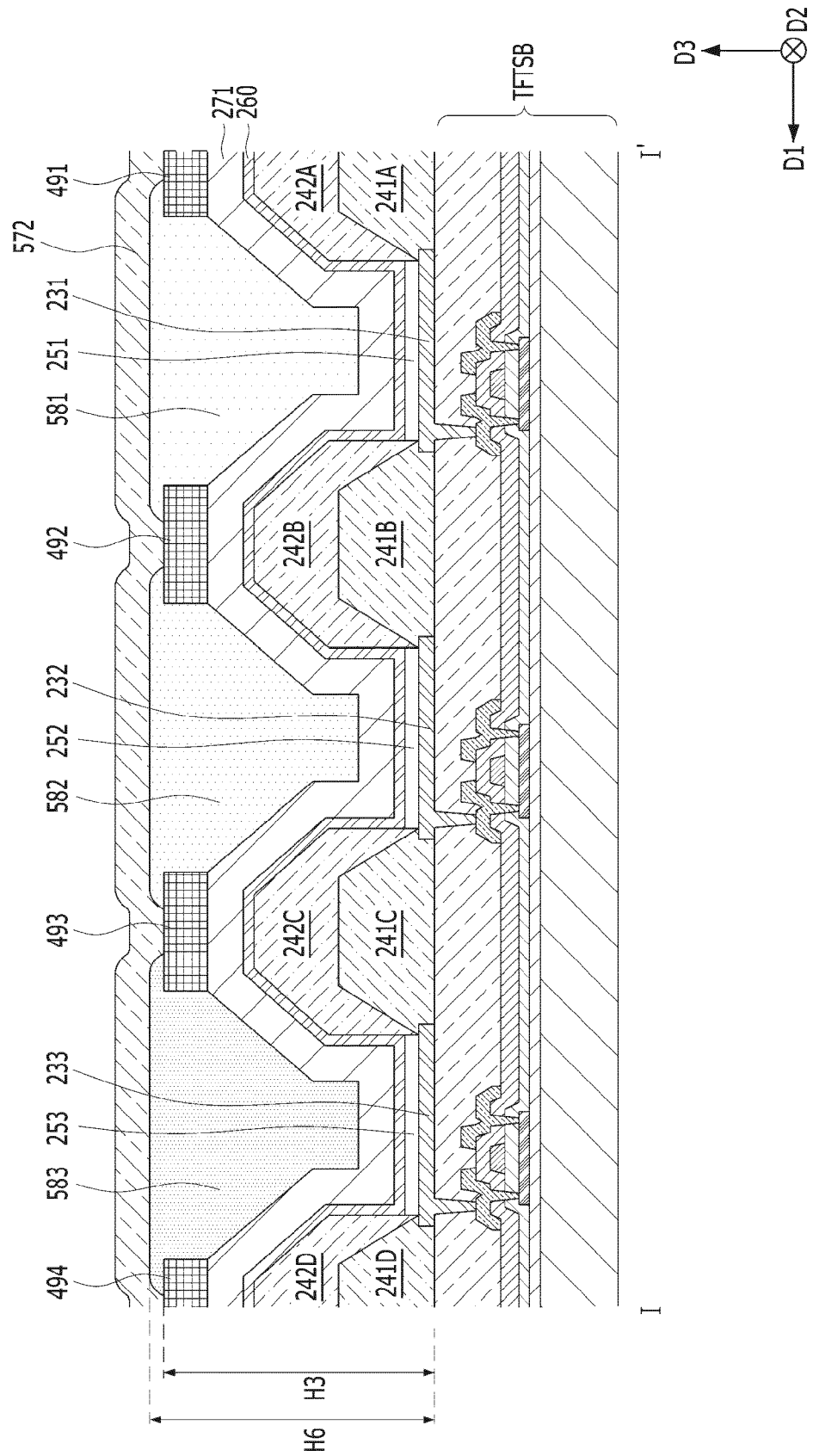


FIG. 9





EUROPEAN SEARCH REPORT

Application Number
EP 18 15 8415

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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
X	US 2016/322444 A1 (KANG SEOCK HWAN [KR] ET AL) 3 November 2016 (2016-11-03) * figure 3 *	1-15	INV. H01L27/32
X	US 2014/353630 A1 (BAEK SOO-MIN [KR] ET AL) 4 December 2014 (2014-12-04) * paragraph [0063]; figures 1-3 *	1-15	
			TECHNICAL FIELDS SEARCHED (IPC)
			H01L
The present search report has been drawn up for all claims			
Place of search Munich		Date of completion of the search 16 May 2018	Examiner Wolfbauer, Georg
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

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**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 18 15 8415

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This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
The members are as contained in the European Patent Office EDP file on
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16-05-2018

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EPO FORM P0459

For more details about this annex : see Official Journal of the European Patent Office, No. 12/82

专利名称(译)	显示装置及其制造方法		
公开(公告)号	EP3367441A1	公开(公告)日	2018-08-29
申请号	EP2018158415	申请日	2018-02-23
[标]申请(专利权)人(译)	三星显示有限公司		
申请(专利权)人(译)	三星DISPLAY CO. , LTD.		
当前申请(专利权)人(译)	三星DISPLAY CO. , LTD.		
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发明人	LEE, HYEONBUM KIM, HYOENG-KI LEE, DONGKI LEE, KYOUNG-HEON JUNG, JIN-WHAN		
IPC分类号	H01L27/32		
CPC分类号	H01L27/3244 H01L27/3272 H01L51/5284 H01L51/56 H01L27/322 H01L27/3246 H01L27/124 H01L27/1255		
优先权	1020170024294 2017-02-23 KR		
外部链接	Espacenet		

摘要(译)

一种显示装置，包括基板，图案化层，遮光层，第一电极，第二电极，电致发光层（EL），第一绝缘层（FIL）和滤色器（CF）。图案化层设置在基板上，并包括：第一主体部分和第一主体部分之间的第一开口。遮光层设置在图案化层上，并包括：第二主体部分和第二主体部分之间的第二开口。第一电极设置在基板上，第一和第二开口与第一电极重叠。第二电极与第一电极重叠。EL设置在第一和第二电极之间。FIL设置在第二电极上。CF布置在FIL上。EL至少设置在第一开口中，并且FIL和CF至少设置在第二开口中。

FIG. 6

