

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
17 July 2008 (17.07.2008)

PCT

(10) International Publication Number
WO 2008/084956 A1

(51) International Patent Classification:
H05B 33/10 (2006.01)

Gangnam-Gu, Seoul 135-789 (KR). **KIM, Soo Young** [KR/KR]; 18-108, Pohang Univ. of Science & Technology Dormitory, San 31, Hyoja-Dong, Nam-Gu, Pohang 790-784 (KR).

(21) International Application Number:
PCT/KR2008/000100

(74) Agent: **NAM, Seung-Hee**; 12F, Seo-jeon Bldg., 1330-9, Seocho-Dong, Seocho-Gu, Seoul 137-858 (KR).

(22) International Filing Date: 8 January 2008 (08.01.2008)

(25) Filing Language: Korean

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RS, RU, SC, SD, SE, SG, SK, SL, SM, SV, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(26) Publication Language: English

(30) Priority Data:
10-2007-0002084 8 January 2007 (08.01.2007) KR
10-2007-0044171 7 May 2007 (07.05.2007) KR

(71) Applicant (for all designated States except US): **POSTECH ACADEMY-INDUSTRY FOUNDATION** [KR/KR]; Pohang Univ. of Science & Technology, Hyoja-Dong, Nam-Gu, Pohang 790-784 (KR).

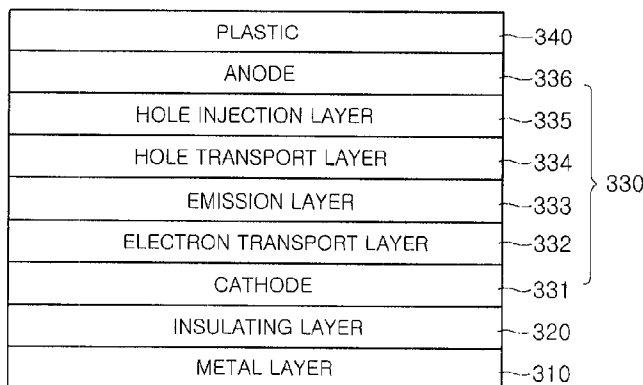
(72) Inventors; and

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI,

(75) Inventors/Applicants (for US only): **LEE, Jong Lam** [KR/KR]; 206-808, Hyundai Apt., Apgujeong-Dong,

[Continued on next page]

(54) Title: METHOD OF MANUFACTURING A FLEXIBLE DEVICE AND METHOD OF MANUFACTURING A FLEXIBLE DISPLAY



(57) Abstract: Provided are a method of separating a metal layer and an organic light emitting diode. A method of manufacturing a flexible device and a method of manufacturing a flexible display include forming a releasing layer on a substrate, forming a metal layer on the releasing layer, forming an insulating layer on the metal layer, forming a releasable layer on the insulating layer, bonding a plastic to the releasable layer, and separating the substrate and the releasing layer at an interface therebetween to manufacture a flexible device. Since the conventional process equipment using the glass substrate can be compatibly used, the manufacturing cost can be reduced. In addition, since the glass substrate has less limitation in the process temperature compared with the plastic substrate, an electric device having a superior performance can be manufactured. Furthermore, the glass substrate has good thermal/chemical stability and is less deformed compared with the plastic substrate, whereby process control such as substrate alignment becomes easy.

WO 2008/084956 A1



FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MT, NL,
NO, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG,
CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:
— *with international search report*

【DESCRIPTION】**【Invention Title】**

METHOD OF MANUFACTURING FLEXIBLE DEVICE AND METHOD OF MANUFACTURING FLEXIBLE DISPLAY

5 **【Technical Field】**

The present disclosure relates to a method of manufacturing a flexible device and a method of manufacturing a flexible display, and more particularly, to a method of manufacturing a flexible device and a method of manufacturing a flexible display capable of utilizing a stable glass substrate process technology.

【BACKGROUND ART】

An ordinary organic light emitting device has a stacked structure for maximizing light emitting efficiency through recombination of holes and electrons. Glass is used for a substrate, indium tin oxide (ITO) that is transparent and has a high work function and conductivity is used for an anode, and a metal having a low work function such as Mg/Ag or Al is used for a cathode. Holes injected from an anode and electrons injected from a cathode are recombined in an organic material layer that is an emission layer to generate excitons. As the excitons diffuse, light corresponding to a band gap of the emission layer is emitted toward a transparent electrode. A display device using the organic light emitting device can realize a good display image without drawbacks associated with image sticking and viewing angle. Further, the display device using the organic light emitting device has low power consumption because it is a self-light emitting device that does not require backlight. In addition, the organic light emitting device may be more affordable by more customers due to the low cost because it can be manufactured at a low temperature through simple processes. In particular, a flexible organic light emitting device has been focused recently because degree of freedom of a product can be remarkably improved.

30 To manufacture a flexible organic light emitting device, a method of

manufacturing an organic light emitting device on a mother substrate such as a thin glass substrate, a metal substrate and a plastic substrate have been studied, however, there are several limitations to be overcome. The thin glass substrate has a limitation in the flexibility. On the other hand, a rough surface of the metal substrate causes a decrease of device characteristic, and a good conductivity thereof generates electrical interference between devices. When a plastic substrate is used, there is a difficulty in chemical treatment. Further, there is also a difficulty in pattern formation and substrate alignment because the plastic substrate is too flexible. Therefore, mass production is difficult. In particular, since the plastic substrate has a low thermal stability, the process should be performed at a low temperature. Therefore, it is difficult to decrease a resistance of indium tin oxide (ITO) used for a cathode of the organic light emitting device to a value below 70 ohm/cm², by which an operation voltage of the organic light emitting device is increased. Additionally, a sealing process and a manufacturing process of an electric field device cannot be performed at a high temperature, which leads to a decrease in device characteristics.

As described above, there are limitations in manufacturing a flexible device, and even though the flexible device is manufactured, the device characteristic becomes poor compared to typical other devices.

【Disclosure】

【Technical Problem】

The present disclosure provides a method of manufacturing a flexible device and a method of manufacturing a flexible display capable of utilizing a conventional stable glass substrate process technology by forming a releasing layer, a metal layer, an insulating layer, and a semiconductor device layer on a glass substrate, bonding a flexible substrate onto the semiconductor device layer, and thereafter removing the rigid glass substrate.

【Technical Solution】

In accordance with an exemplary embodiment, a method of manufacturing a flexible device includes: forming a releasing layer on a mother substrate; forming a metal layer on the releasing layer; forming an insulating layer on the metal layer; forming a semiconductor device layer on the insulating layer; bonding a flexible substrate to the semiconductor device layer; and separating the mother substrate and the releasing layer at an interface therebetween to remove the mother substrate.

The releasing layer may be formed of one of MgO, MnO, Mn₃O₄, MoO_y, SnO₂, SeO_x, SiO_x, GaO_x, InO, Ti_xO_y, V_xO_y, ZrO_y, WO_y, Al₂O₃, SrO, Te_xO_y, TeO₂, ZnO, ITO, IZO, SiN, TiN, TaN, AlN, BN, MO₂N, VN, ZrN, NbN, CrN, Ga and combinations thereof. The releasing layer may be formed to have a thickness of 1 nm or more.

The metal layer may be formed of one of Ag, Au, Cu, Cr, W, Al, W, Mo, Zn, Ni, Pt, Pd, Co, In, Mn, Si, Ta, Ti, Sn, Zn, Pb, V, Ru, Ir, Zr, Rh and combinations thereof. The metal layer may be formed to have a thickness of 1 nm or more.

The insulating layer may be formed of an insulator including one of silicon oxide (SiO₂), silicon nitride (SiN_x), photoresist, polyimide and combinations thereof.

The semiconductor device layer may include at least one of an organic light emitting diode and an organic field transistor.

A glass substrate may be used as the mother substrate, and a plastic substrate may be used as the flexible substrate.

In accordance with another exemplary embodiment, a method of manufacturing a flexible display, the method includes: forming a releasing layer on a mother substrate; forming a metal layer on the releasing layer; forming an insulating layer on the metal layer; forming a semiconductor device layer comprising a light emitting device on the insulating layer; bonding a flexible substrate to the semiconductor device layer; and separating the mother substrate and the releasing layer at an interface

therebetween to remove the mother substrate.

The releasing layer may be formed of one of MgO, MnO, Mn₃O₄, MoO_y, SnO₂, SeO_x, SiO_x, GaO_x, InO, Ti_xO_y, V_xO_y, ZrO_y, WO_y, Al₂O₃, SrO, Te_xO_y, TeO₂, ZnO, ITO, IZO, SiN, TiN, TaN, AlN, BN, MO₂N, VN, ZrN, NbN, CrN, Ga and
5 combinations thereof.

The metal layer may be formed of one of Ag, Au, Cu, Cr, W, Al, W, Mo, Zn, Ni, Pt, Pd, Co, In, Mn, Si, Ta, Ti, Sn, Zn, Pb, V, Ru, Ir, Zr, Rh and combinations thereof.

The insulating layer may be formed of an insulator including one of
10 silicon oxide (SiO₂), silicon nitride (SiN_x), photoresist, polyimide and combinations thereof.

The semiconductor device layer may include at least one of an organic light emitting diode and an organic field transistor.

A glass substrate may be used as the mother substrate, and a plastic
15 substrate may be used as the flexible substrate.

【Advantageous Effects】

As described above, a flexible device is manufactured by forming a releasing layer on a substrate; stacking a metal layer, an insulating layer, and a released layer on the releasing layer; bonding a plastic substrate to the stacked structure; and separating the substrate and the releasing layer using an external force. Therefore, the following effects can be obtained.

The manufacturing cost can be reduced because a conventional equipment for a glass substrate can be used compatibly.

In addition, a device having a superior performance can be manufactured compared with a flexible device manufactured on a plastic substrate, since there is no limitation in a process temperature for manufacturing a device.

Furthermore, use of the glass substrate reduces deformation caused by heat and chemicals in manufacturing a device, and it is easy to align the substrate.

15 【Description of Drawings】

Exemplary embodiments can be understood in more detail from the following description taken in conjunction with the accompanying drawings, in which:

FIG. 1 is a cross-sectional view of a flexible electrical device in accordance with an exemplary embodiment;

FIGS. 2 to 6 are cross-sectional views illustrating a process of manufacturing a flexible electrical device in accordance with the exemplary embodiment;

FIGS. 7 to 10 are cross-sectional views illustrating a process of manufacturing a flexible electrical device in accordance with another exemplary embodiment;

FIGS. 11 and 12 are graphs showing measured results of an energy-dispersive X-ray spectrometer after a metal layer separation in accordance with an experimental example;

FIGS. 13 through 16 are photographs sequentially showing a process of

separating a metal layer in accordance with an experimental example; and

FIGS. 17 to 19 are photographs and graphs showing the characteristic of an organic light emitting device before and after a metal layer separation in accordance with an experimental example.

5 **【Best Mode】**

Preferred embodiments of the present disclosure will be described below in more detail with reference to the accompanying drawings. The present disclosure may, however, be embodied in different forms and should not be construed as limited to the embodiments set forth herein. Rather, these
10 embodiments are provided so that this disclosure will be thorough and complete, and will fully convey the scope of the present disclosure to those skilled in the art.

In the figures, the dimensions of layers and regions are exaggerated for clarity of illustration. Like reference numerals refer to like elements
15 throughout. It will also be understood that when a layer, a film, a region, or a substrate is referred to as being on another layer, film, region, or substrate, it can be directly on the other layer, film, region, or substrate, or intervening layers, films, regions, or substrates, may also be present.

<Embodiment 1>

20 FIG. 1 is a cross-sectional view of a flexible electrical device in accordance with an exemplary embodiment.

Referring to FIG. 1, the flexible electrical device includes an insulating layer 320 and a semiconductor device layer 330 disposed on a metal layer 310 serving as an auxiliary substrate. The flexible electrical device
25 further includes a flexible substrate 340 attached on a surface of the semiconductor device layer 330.

At least one semiconductor device including a semiconductor thin film is formed on the semiconductor device layer 330. For example, an organic light emitting diode (OLED), an organic field effect transistor or the like
30 may be formed. In this exemplary embodiment, OLEDs are arranged in a matrix

form in the semiconductor device layer 330 to serve as a unit pixel that uniquely emits at least one of three primary colors. Here, the OLED may be provided in plurality so as to correspond to a plurality of unit pixels, however, FIG. 1 illustrates only one OLED for convenience.

5 The OLED 330 includes a cathode 331, an electron transport layer 332, an emission layer 333, a hole transport layer 334, and an anode 336 that are sequentially provided from the bottom to the top. The OLED 330 may further include a hole injection layer 335 disposed between the hole transport layer 334 and the anode 336. When an external current is applied through the two
10 electrodes of the OLED 330, electrons injected from the electron transport layer 332 and holes injected from the hole transport layer 334 are recombined in the emission layer 333 to generate excitons. As the excitons diffuse, light corresponding to an energy band gap of the emission layer 333 is emitted. Here, the emission layer 333 may be formed as a multi-layered
15 semiconductor thin film having a single or multiple quantum well structure by alternately stacking a well layer containing an organic light emitting material and a barrier layer. Since a wavelength of an output light is varied depending on the contained organic light emitting material, a proper organic light emitting material may be selected depending on a required
20 output wavelength.

The OLED 330 of the this exemplary embodiment outputs one light of the three primary colors to reproduce natural colors. Meanwhile, alternatively, three primary color filters may be disposed on an entire surface of an OLED outputting a white color to reproduce natural colors. In addition, the OLED
25 330 of this exemplary embodiment is configured as a top emission type where light is emitted upwardly. Therefore, an upper portion of the emission layer 333 including the anode 336 and the flexible substrate 340 may be formed of a light-transmissive material, and at least one layer of a lower portion of the emission layer 333 including the cathode 331 and the metal layer 310 may
30 serve as a reflective layer. Alternatively, the OLED 330 of this exemplary

embodiment may be configured as a bottom emission type where light is emitted in a downward direction. In the bottom emission type, a lower portion of the emission layer 333 including the cathode 331 and the metal layer 310 may be formed of a light-transmissive material, and at least one layer of an upper portion of the emission layer 333 including the anode 336 and the flexible substrate 340 may serve as a reflective layer.

The flexible electrical device is obtained by forming a multi-layered thin film including a releasing layer (not shown), the metal layer 310, the insulating layer 320, and the semiconductor device layer 330, then attaching the flexible substrate 340 on the multi-layered thin film, and separating the releasing layer and the metal layer 310 at an interface therebetween to remove a mother substrate. The flexible substrate 340 may be formed after removing the mother substrate.

A manufacturing process of the flexible electrical device having the above structure will be described with reference to FIGS. 2 to 6. FIGS. 2 to 6 are cross-sectional views illustrating a process of manufacturing a flexible electrical device in accordance with the exemplary embodiment.

Referring to FIG. 2, a releasing layer 200 is formed on a mother substrate 100. A glass substrate which can be processed by a stable semiconductor process technology may be used as the mother substrate 100. The releasing layer 200 may be formed of a material having low surface energy, e.g., one of MgO, MnO, Mn₃O₄, MoO_y, SnO₂, SeO_x, SiO_x, GaO_x, InO, Ti_xO_y, V_xO_y, ZrO_y, WO_y, Al₂O₃, SrO, Te_xO_y, TeO₂, ZnO, ITO, IZO, SiN, TiN, TaN, AlN, BN, MO₂N, VN, ZrN, NbN, CrN, Ga and combinations thereof. The releasing layer 200 may be formed to have a thickness of approximately 1 nm or more. If the releasing layer 200 is too thin, a bonding force between the mother substrate 100 and a metal layer which is formed on the mother substrate 100 may become strong to make it difficult to separate the mother substrate 100.

Referring to FIG. 3, a metal layer 310 and an insulating layer 320 are sequentially stacked on entire surface of the resultant structure including

the releasing layer 200. The metal layer 310 may be formed of one of Ag, Au, Cu, Cr, W, Al, Mo, Zn, Ni, Pt, Pd, Co, In, Mn, Si, Ta, Ti, Sn, Zn, Pb, V, Ru, Ir, Zr, Rh and combinations thereof using various metal deposition methods, e.g., electron beam deposition, hot filament deposition, sputter deposition, or electroplating. The metal layer 310 is used as an auxiliary substrate for supporting the thin film structure existing thereon when the mother substrate 100 is separated and removed, and thus may be formed to have a thickness of approximately 1 μm or more. If the metal layer 310 is too thin, it may be torn out when the mother substrate 100 is separated and removed using an external force. The insulating layer 320 may be formed by depositing an insulator, e.g., silicon oxide (SiO_2), silicon nitride (SiN_x), or the like having good insulating and adhesive properties through metal organic chemical vapor deposition (MOCVD), or the like. The insulating layer 320 may be formed by applying an insulator such as photoresist or polyimide through spin coating in order to reduce the process time. The insulating layer 320 electrically separates electrical devices of a semiconductor device layer which is formed thereon to prevent mutual interference.

Referring to FIG. 4, a semiconductor device layer 330 is formed on an entire surface of the resultant structure including the insulating layer 320. For example, the semiconductor device layer 330 may include a green OLED outputting a wavelength range of approximately 520 nm. The green OLED is formed by sequentially stacking an Al cathode 331, an Alq_3 electron transport layer 332, an emission layer 333, an α -NPD hole transport layer 334, a CuPc hole injection layer 335, and an Au anode 336 from the bottom to the top. Of course, since a wavelength of an output light is varied depending on an organic light emitting material contained in the emission layer 333, a proper organic light emitting material may be selected depending on a required output wavelength.

Referring to FIG. 5, a flexible substrate 340 is bonded to an entire surface of the resultant structure including the semiconductor device layer

330. Here, the flexible substrate 340 may be formed of plastic which has good physical/chemical stability and good flexibility and is inexpensive.

Referring to FIG. 6, after the upper structure including the releasing layer 200, the metal layer 310, the insulating layer 320, the semiconductor device layer 330, and the flexible substrate 340 is formed on the mother substrate 100, the mother substrate 100 is then separated from the upper structure at the interface between the releasing layer 200 and the metal layer 310. That is, an external force, i.e., a shear stress, is applied to the mother substrate 100 and the upper structure in opposite directions, and thereby the releasing layer 200 and the metal layer 310 are separated at the interface therebetween. Here, if the separation is not easy, a laser lift-off process of radiating excimer laser to the releasing layer 200 from the bottom of the mother substrate 100 may be additionally performed. After the separated mother substrate 100 is removed, only the upper structure including the metal layer 310, the insulating layer 320, the semiconductor device layer 330, and the flexible substrate 340 is remained. Therefore, various flexible electrical devices may be manufactured using the above process.

<Embodiment 2>

A flexible electrical device in accordance with another exemplary embodiment can be manufactured by forming a releasing layer on a glass substrate that is a mother substrate, and patterning the releasing layer to form a releasing layer pattern in which predetermined regions are removed, stacking a metal layer, an insulating layer, and a semiconductor device layer on the releasing layer pattern, bonding plastic on the semiconductor device layer, and thereafter separating an interface between the mother substrate and the releasing layer pattern.

By forming the releasing layer pattern, a contact area between the releasing layer and the metal layer increases to maximize an adhesive force. Therefore, the semiconductor device layer can be easily formed even though an adhesive force between the releasing layer and the metal layer is small.

Hereinafter, a method of manufacturing the flexible electrical device in accordance with the present exemplary embodiment will be described in more detail.

FIGS. 7 to 10 are cross-sectional views illustrating a process of manufacturing a flexible electrical device in accordance with the present exemplary embodiment.

Referring to FIGS. 7 and 8, a releasing layer 410 is formed on a mother substrate 100, and a patterning process using a mask is performed to form a releasing layer pattern 420. In the patterning process, photoresist is applied on the releasing layer 410 to have a uniform thickness, a photolithography process using a mask is performed to form a photoresist pattern, and an etching process is performed on the releasing layer 410 using the photoresist pattern as an etch mask. As such, some region of the releasing layer 410 that is not shielded by the etch mask is removed to form the releasing layer pattern 420 in a predetermined form. By forming the releasing layer pattern, a contact area between the releasing layer 410 and a metal layer increases to maximize an adhesive force therebetween. Therefore, a semiconductor device layer can be easily formed even though an adhesive force between the releasing layer 410 and the metal layer is small.

Referring to FIG. 9, a multi-layered thin film including a metal layer 310, an insulating layer 320, a semiconductor device layer 330 is formed on an entire surface of the resultant structure including the releasing layer pattern 400, and a flexible substrate 340 is bonded to a surface of the multi-layered thin film. The flexible substrate may include a plastic substrate that has good flexibility and is inexpensive.

Referring to FIG. 10, after an upper structure including the releasing layer pattern 420, the metal layer 310, the insulating layer 320, the semiconductor device layer 330, and the flexible substrate 340 is formed on the mother substrate 100, the releasing layer pattern 420 and the metal layer 310 are separated from each other at the interface, whereby the mother

substrate 100 is separated from the upper structure. As described above, by forming the releasing layer pattern 420, a contact area between the releasing layer and the metal layer 310 increases to maximize an adhesive force therebetween, and even though an adhesive force between the releasing layer and the metal layer 310 is small, the semiconductor device layer 330 can be easily formed.

The flexible electrical device in accordance with the above described embodiments can be easily bent within a predetermined range due to the flexibility of the flexible substrate, and thus can be applied to various products. For example, a plurality of OLEDs may be formed in a matrix form so that each OLED can serve as a unit pixel uniquely emitting at least one of the three primary colors, to manufacture a display device capable of reproducing an image.

In addition, the flexible electrical device in accordance with the above described embodiments has various advantages because a glass substrate is used as a mother substrate. Since a conventional process equipment using the glass substrate can be used compatibly, the manufacturing cost can be reduced. In addition, since the glass substrate has less limitation in the process temperature compared with the plastic substrate, an electric device having a superior performance can be manufactured. Further, the glass substrate has good thermal/chemical stability and is less deformed compared with the plastic substrate, whereby process control such as substrate alignment becomes easier.

FIGS. 11 and 12 are graphs showing measured results of an energy-dispersive X-ray spectrometer after a metal layer separation in accordance with an experimental example. Specifically, FIGS. 11 and 12 shows measured results of a surface of an Ag metal layer and a surface of the glass substrate, respectively, after separating the metal layer with an external force when an MgO releasing layer and an Ag metal layer are used.

Referring to FIG. 11, after separation of the metal layer, a small amount of

MgOx remains under the Ag metal layer. Referring to FIG. 12, after separation of the metal layer, a large amount of MgOx remains on the glass substrate. This result indicates that separation occurs at the interface between the MgOx releasing layer and the Ag metal layer when the metal layer
5 is separated by an external force.

FIGS. 13 to 16 are photographs sequentially showing a process of a metal layer separation in accordance with an experimental example.

Referring to FIGS. 13 to 16, separation occurs at the interface between the metal layer and the releasing layer when an external force is applied,
10 because the Ag metal layer is formed on the MgO releasing layer, not directly on the glass substrate. Therefore, the glass substrate can be easily separated from the metal layer with a relatively small force (strength of a force: 1 gf/cm² or more).

FIGS. 17 to 19 are photographs and graphs showing the characteristic of
15 an organic light emitting device before and after a metal layer separation in accordance with an experimental example. FIG. 17 is a photograph showing an operation of the device before the metal layer separation, FIG. 18 is a photograph showing an operation of the device after the metal layer separation, and FIG. 19 shows a current density versus voltage graph and a
20 luminance versus current density graph before and after the metal layer separation.

Referring to FIG. 7A, the device operates normally before the metal layer separation. Referring to FIG. 7B, the device operates normally even after the metal layer separation. Referring to the current density versus
25 voltage graph of FIG. 19, the electrical characteristic does not change before and after the metal layer separation. Referring to the luminance versus current density graph of FIG. 19, there is no change in the optical characteristic of the device. These results indicate that a method of separating the metal layer in accordance with the embodiments is very
30 effective in realizing the flexible electrical device.

Although the method of manufacturing the flexible device and the method of manufacturing the flexible display have been described with reference to the specific embodiments and drawings, they are not limited thereto. Therefore, it will be readily understood by those skilled in the art that
5 various modifications and changes can be made thereto without departing from the spirit and scope of the present invention is defined by the appended claims.

【CLAIMS】**【Claims 1】**

A method of manufacturing a flexible device, the method comprising:
forming a releasing layer on a mother substrate;
5 forming a metal layer on the releasing layer;
forming an insulating layer on the metal layer;
forming a semiconductor device layer on the insulating layer;
bonding a flexible substrate to the semiconductor device layer; and
separating the mother substrate and the releasing layer at an interface
10 therebetween to remove the mother substrate.

【Claims 2】

The method of claim 1, wherein the releasing layer is formed of one of
MgO, MnO, Mn₃O₄, MoO_y, SnO₂, SeO_x, SiO_x, GaO_x, InO, TixO_y, VxO_y, ZrO_y, WO_y,
15 Al₂O₃, SrO, TexO_y, TeO₂, ZnO, ITO, IZO, SiN, TiN, TaN, AlN, BN, MO₂N, VN, ZrN,
NbN, CrN, Ga and combinations thereof.

【Claims 3】

The method of claim 1, wherein the releasing layer is formed to have a
20 thickness of approximately 1 nm or more.

【Claims 4】

The method of claim 1, wherein the metal layer is formed of one of Ag,
Au, Cu, Cr, W, Al, W, Mo, Zn, Ni, Pt, Pd, Co, In, Mn, Si, Ta, Ti, Sn, Zn, Pb,
25 V, Ru, Ir, Zr, Rh and combinations thereof.

【Claims 5】

The method of claim 4, wherein the metal layer is formed to have a
thickness of approximately 1 nm or more.

【Claims 6】

The method of claim 1, wherein the insulating layer is formed of an insulator comprising one of silicon oxide (SiO₂), silicon nitride (SiN_x), photoresist, polyimide and combinations thereof.

5

【Claims 7】

The method of claim 1, wherein the semiconductor device layer comprises at least one of an organic light emitting diode and an organic field transistor.

10

【Claims 8】

The method of any one of claims 1 to 7, wherein a glass substrate is used as the mother substrate.

15 **【Claims 9】**

The method of any one of claims 1 to 7, wherein a plastic substrate is used as the flexible substrate.

【Claims 10】

20 A method of manufacturing a flexible display where a light emitting device of a semiconductor device layer functions as a unit pixel, the method comprising:

forming a releasing layer on a mother substrate;

forming a metal layer on the releasing layer;

25 forming an insulating layer on the metal layer;

forming a semiconductor device layer comprising a light emitting device on the insulating layer;

bonding a flexible substrate to the semiconductor device layer; and

separating the mother substrate and the releasing layer at an interface

30 therebetween to remove the mother substrate.

【Claims 11】

The method of claim 10, wherein the releasing layer is formed of one of MgO, MnO, Mn₃O₄, MoO_y, SnO₂, SeO_x, SiO_x, GaO_x, InO, TixOy, VxOy, ZrOy, WOy, Al₂O₃, SrO, TexOy, TeO₂, ZnO, ITO, IZO, SiN, TiN, TaN, AlN, BN, MO₂N, VN, ZrN,
5 NbN, CrN, Ga and combinations thereof.

【Claims 12】

The method of claim 10, wherein the metal layer is formed of one of Ag, Au, Cu, Cr, W, Al, W, Mo, Zn, Ni, Pt, Pd, Co, In, Mn, Si, Ta, Ti, Sn, Zn, Pb,
10 V, Ru, Ir, Zr, Rh and combinations thereof.

【Claims 13】

The method of claim 10, wherein the insulating layer is formed of an insulator comprising one of silicon oxide (SiO₂), silicon nitride (SiN_x),
15 photoresist, polyimide and combinations thereof.

【Claims 14】

The method of claim 10, wherein the semiconductor device layer comprises at least one of an organic light emitting diode and an organic
20 field transistor.

【Claims 15】

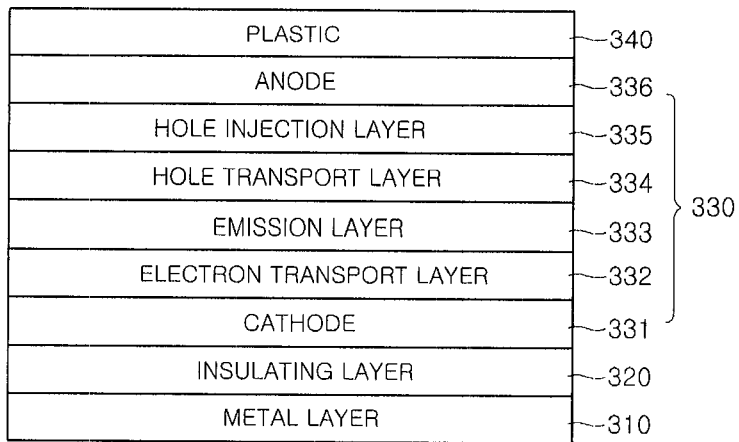
The method of any one of claims 10 to 14, wherein a glass substrate is used as the mother substrate.
25

【Claims 16】

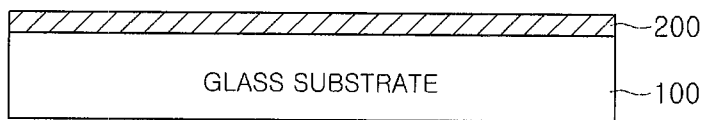
The method of any one of claims 10 to 14, wherein a plastic substrate is used as the flexible substrate.

【DRAWINGS】

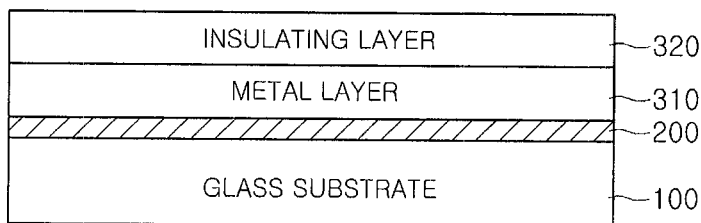
【Figure 1】



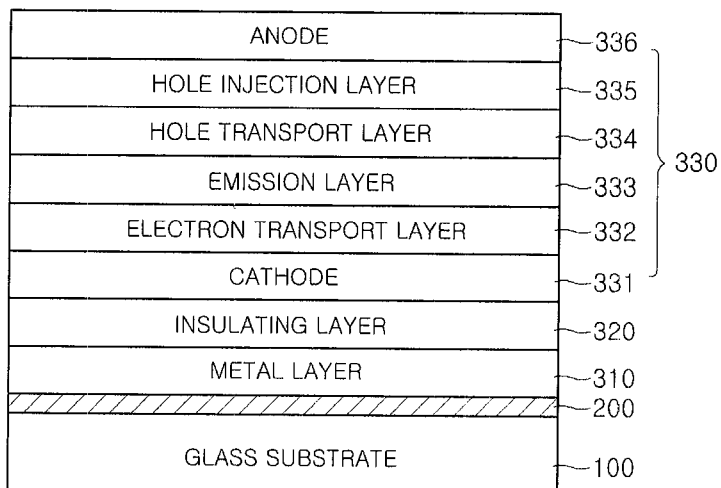
【Figure 2】



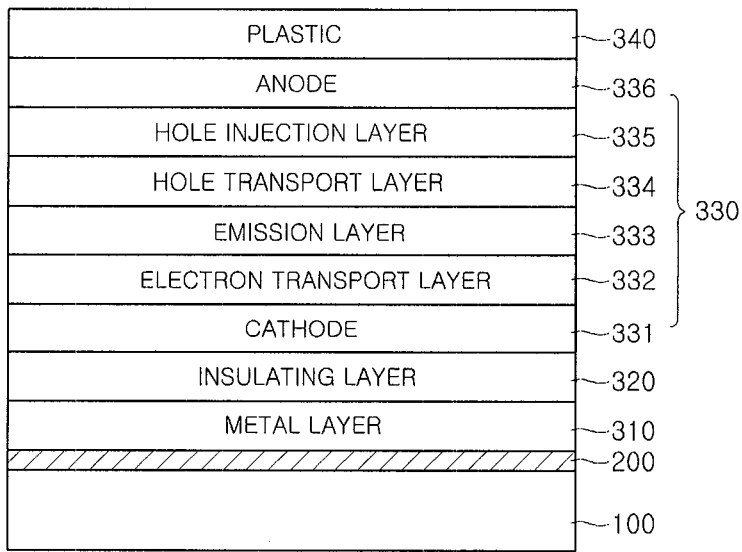
【Figure 3】



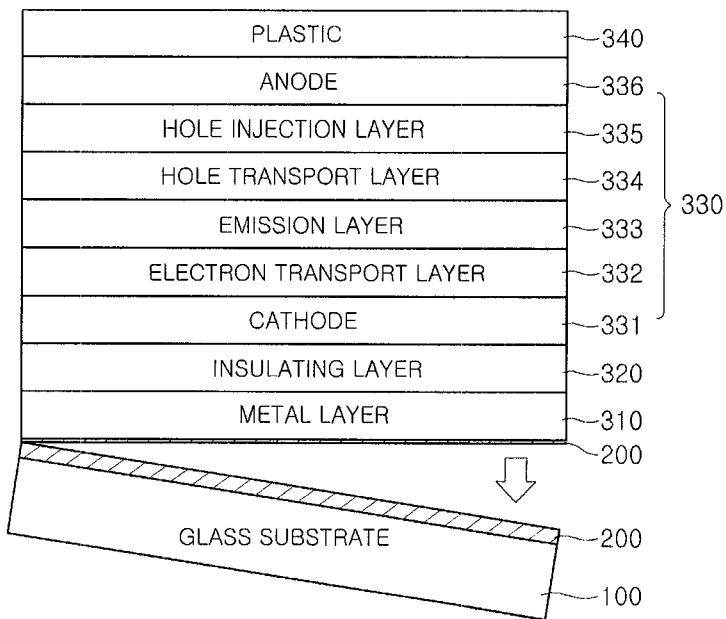
【Figure 4】



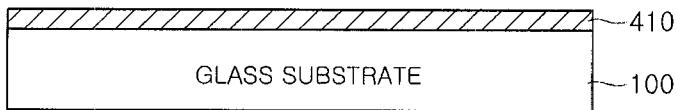
【Figure 5】



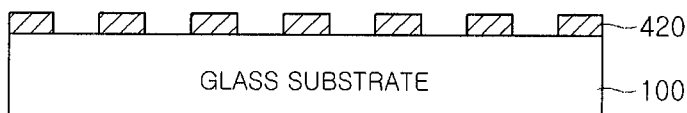
【Figure 6】



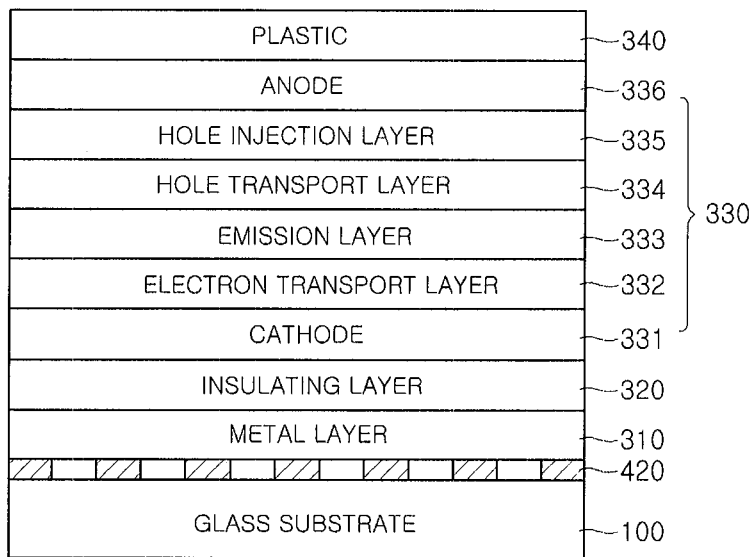
【Figure 7】



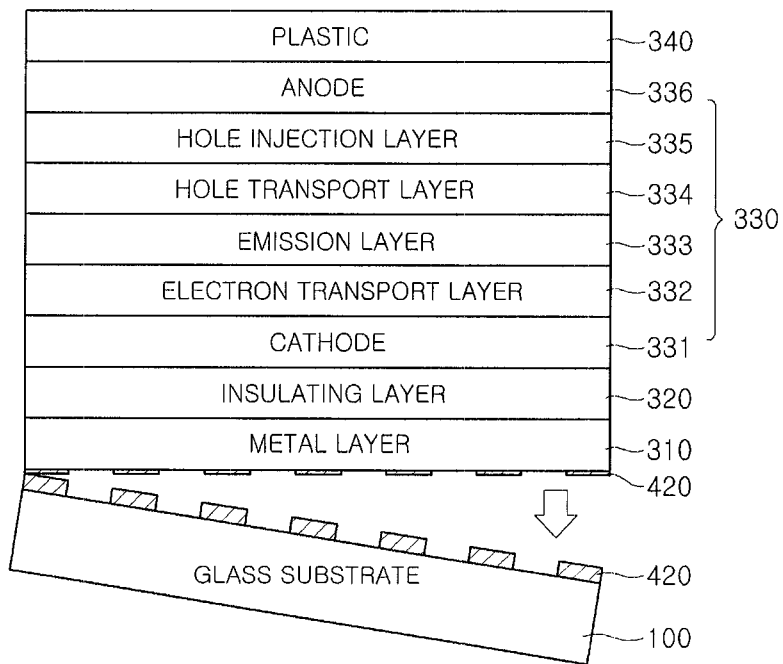
【Figure 8】



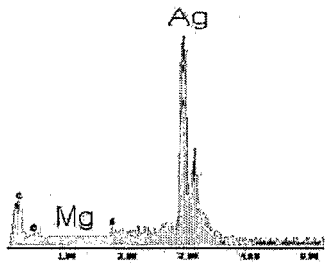
【Figure 9】



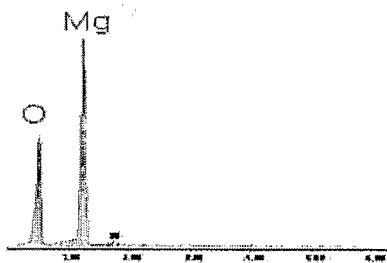
【Figure 10】



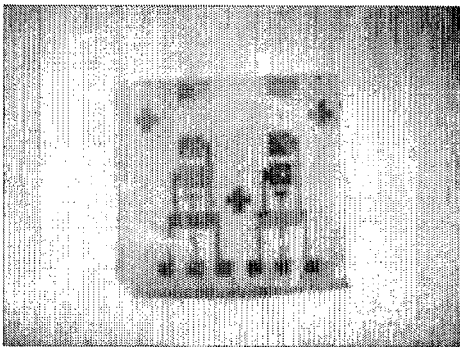
[Fig. 11]



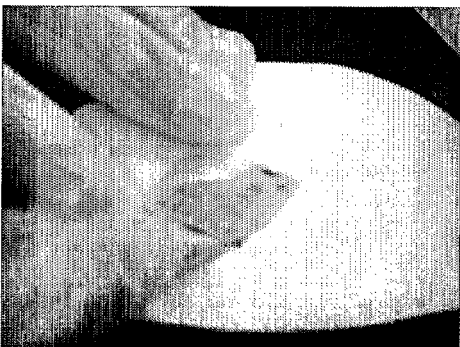
[Fig. 12]



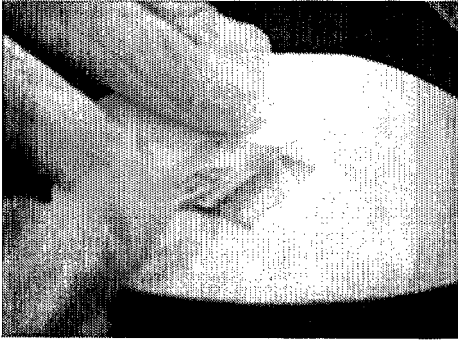
[Fig. 13]



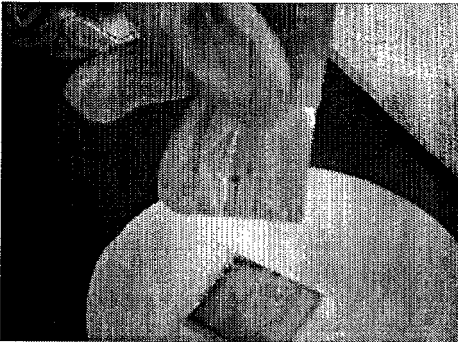
[Fig. 14]



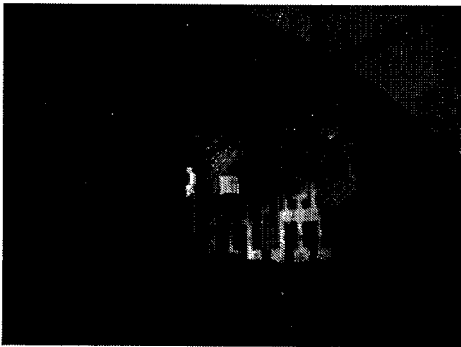
[Fig. 15]



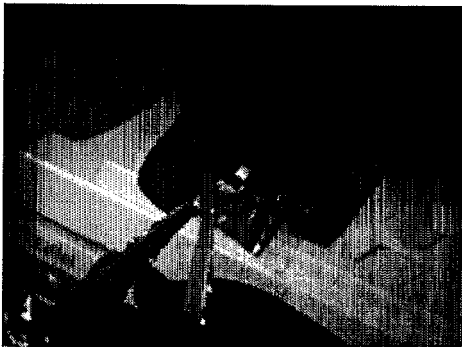
[Fig. 16]



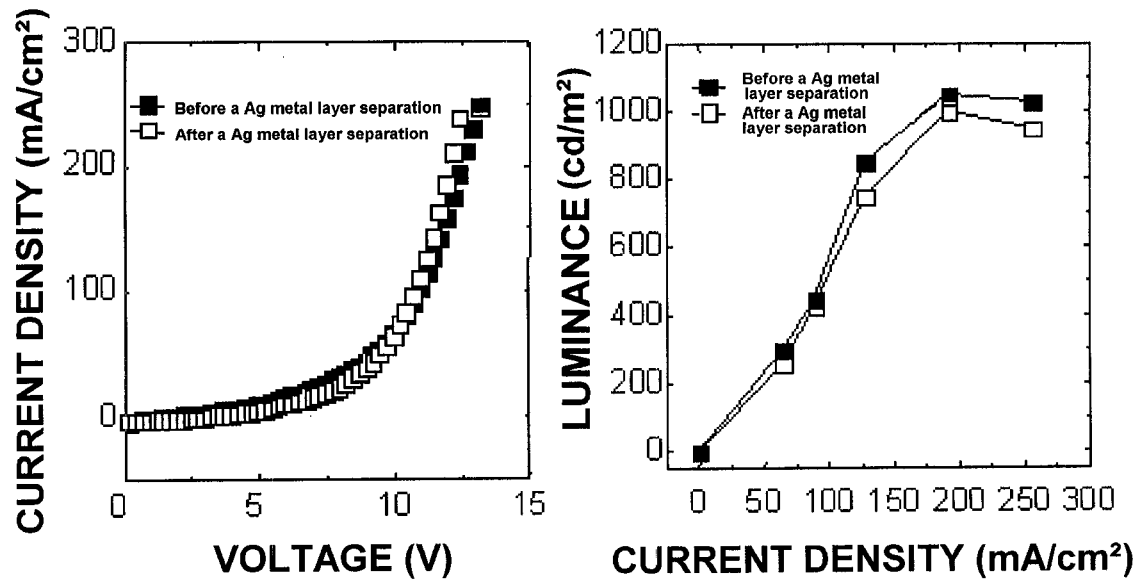
[Fig. 17]



[Fig. 18]



【Figure 19】



A. CLASSIFICATION OF SUBJECT MATTER***H05B 33/10(2006.01)i***

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 8 : H05B 33/

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean Utility models and applications for Utility models since 1975

Japanese Utility models and applications for Utility models since 1975

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKIPASS (KIPO internal) & keywords: "releasing or peeling", "flexible" and "manufacturing"

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2004-0266165 A1 (UTSUNOMIYA) 30 Dec. 2004 See abstract; figures 1-4 and their corresponding description.	1-16
A	US 2003-0217805 A1 (TAKAYAMA et al.) 27 Nov. 2003 See abstract; figures 1-6 and their corresponding description.	1-16
A	US 2003-0136966 A1 (INOUE et al.) 24 Jul. 2003 See whole document.	1-16
A	US 2005-0067952 A1 (FECHTER et al.) 31 Mar. 2005 See abstract; figures 1-7 and their corresponding description.	1-16

 Further documents are listed in the continuation of Box C. See patent family annex.

* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

13 MARCH 2008 (13.03.2008)

Date of mailing of the international search report

13 MARCH 2008 (13.03.2008)

Name and mailing address of the ISA/KR

Korean Intellectual Property Office
Government Complex-Daejeon, 139 Seonsa-ro, Seo-gu, Daejeon 302-701, Republic of Korea

Facsimile No. 82-42-472-7140

Authorized officer

KIM Chang Gyun

Telephone No. 82-42-481-8236



INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/KR2008/000100

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US20040266165 A1	30.12.2004	CN1574202A JP2004349540A2 KR2004100890A TW234268A	02.02.2005 09.12.2004 02.12.2004 11.06.2005
US20030217805 A1	27.11.2003	NONE	
US20030136966 A1	24.07.2003	CN1703938A EP01450587A1 JPW02003/053109 KR1020030090704 W02003053109A1	30.11.2005 25.08.2004 26.06.2003 28.11.2003 26.06.2003
US20050067952 A1	31.03.2005	CN1860576A EP01680798A2 JP19518219A KR2006065728A W02005034580A3	08.11.2006 19.07.2006 05.07.2007 14.06.2006 22.12.2005

专利名称(译)	制造柔性装置的方法和制造柔性显示器的方法		
公开(公告)号	EP2110003A1	公开(公告)日	2009-10-21
申请号	EP2008704640	申请日	2008-01-08
[标]申请(专利权)人(译)	浦项工科大学校产学协力团		
申请(专利权)人(译)	POSTECH奥斯卡产业基础		
当前申请(专利权)人(译)	浦项制铁		
[标]发明人	LEE JONG LAM KIM SOO YOUNG		
发明人	LEE, JONG LAM KIM, SOO YOUNG		
IPC分类号	H05B33/10 H01L51/56		
CPC分类号	H05B33/10 H01L51/003 H01L51/0097 H01L51/56 H01L2251/5338		
优先权	1020070044171 2007-05-07 KR 1020070002084 2007-01-08 KR		
其他公开文献	EP2110003B1 EP2110003A4		
外部链接	Espacenet		

摘要(译)

提供一种分离金属层和有机发光二极管的方法。制造柔性装置的方法和制造柔性显示器的方法包括在基板上形成释放层，在释放层上形成金属层，在金属层上形成绝缘层，在绝缘层上形成可释放层将塑料粘合到可剥离层上，并在其间的界面处分离基板和释放层，以制造柔性装置。由于可以兼容地使用使用玻璃基板的传统工艺设备，因此可以降低制造成本。另外，由于玻璃基板与塑料基板相对处理温度的限制较少，因此可以制造具有优异性能的电气设备。此外，与塑料基板相比，玻璃基板具有良好的热/化学稳定性并且变形较小，从而诸如基板对准的过程控制变得容易。