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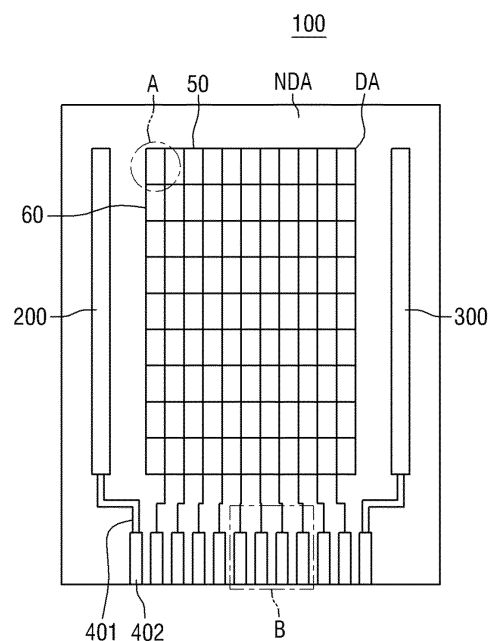
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(54) **Array substrate and organic light-emitting display including the same**

(57) An array substrate includes a substrate, a buffer layer disposed on the substrate, a first insulating layer disposed on the buffer layer, a second insulating layer disposed on the first insulating layer, a plurality of wiring

patterns disposed on the second insulating layer, and wherein the wiring patterns are separated from each other and a recess pattern disposed adjacent to the wiring patterns.

【 Fig. 1 】





EUROPEAN SEARCH REPORT

 Application Number
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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
X	US 2006/202206 A1 (KOYAMA JUN [JP] ET AL) 14 September 2006 (2006-09-14)	1	INV. H01L27/32 H01L27/12 H01L23/00
Y	* paragraph [0046] - paragraph [0072]; figure 1 *	2-15	

X	WO 2012/164882 A1 (SHARP KK [JP]; FUJIWARA MASAKI) 6 December 2012 (2012-12-06)	1	
Y	* figure 15 *	2-15	

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The present search report has been drawn up for all claims			
Place of search		Date of completion of the search	Examiner
Munich		27 January 2015	Bernabé Prieto, A
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document			

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
The members are as contained in the European Patent Office EDP file on
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27-01-2015

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For more details about this annex : see Official Journal of the European Patent Office, No. 12/82

专利名称(译)	阵列基板和包括其的有机发光显示器		
公开(公告)号	EP2816604A3	公开(公告)日	2015-03-11
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[标]申请(专利权)人(译)	三星显示有限公司		
申请(专利权)人(译)	三星DISPLAY CO. , LTD.		
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优先权	1020130069039 2013-06-17 KR 1020130125416 2013-10-21 KR		
其他公开文献	EP2816604A2		
外部链接	Espacenet		

摘要(译)

阵列基板包括基板，设置在基板上的缓冲层，设置在缓冲层上的第一绝缘层，设置在第一绝缘层上的第二绝缘层，设置在第二绝缘层上的多个布线图案，以及其中布线图案彼此分开，并且凹陷图案与布线图案相邻设置。

【 Fig. 1 】

