



(12) **EUROPEAN PATENT APPLICATION**

(88) Date of publication A3:
06.10.2010 Bulletin 2010/40

(51) Int Cl.:
G09G 3/32^(2006.01)

(43) Date of publication A2:
02.01.2002 Bulletin 2002/01

(21) Application number: **01114361.7**

(22) Date of filing: **13.06.2001**

(84) Designated Contracting States:
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE TR
Designated Extension States:
AL LT LV MK RO SI

- **Koyama, Jun**
Atsugi-shi,
Kanagawa-ken 243-0036 (JP)
- **Kazue, Hosoki**
Atsugi-shi,
Kanagawa-ken 243-0036 (JP)

(30) Priority: **13.06.2000 JP 2000176246**

(71) Applicant: **SEMICONDUCTOR ENERGY LABORATORY CO., LTD.**
Atsugi-shi, Kanagawa-ken 243-0036 (JP)

(74) Representative: **Grünecker, Kinkeldey, Stockmair & Schwanhäusser**
Anwaltssozietät
Leopoldstrasse 4
80802 München (DE)

(72) Inventors:
• **Yamazaki, Shunpei**
Atsugi-shi,
Kanagawa-ken 243-0036 (JP)

(54) **Display device**

(57) The image quality of a display device using a bottom gate TFT is improved. In particular, fluctuation in luminance is controlled and the frequency characteristic of a driver circuit is compensated by suppressing a change in amount of current flowing through an EL element which is caused by a change in surrounding temperature while the device is in use. A monitoring EL element is provided in addition to a pixel portion EL element. The monitoring EL element constitutes a temperature compensation circuit together with a buffer amplifier and the like. A current is supplied to the pixel portion EL element through the temperature compensation circuit. This makes it possible to keep the amount of current flowing through the pixel portion EL element constant against a change in temperature, and to control the fluctuation in luminance. An input signal is subjected to time base expansion to perform sampling with accuracy.

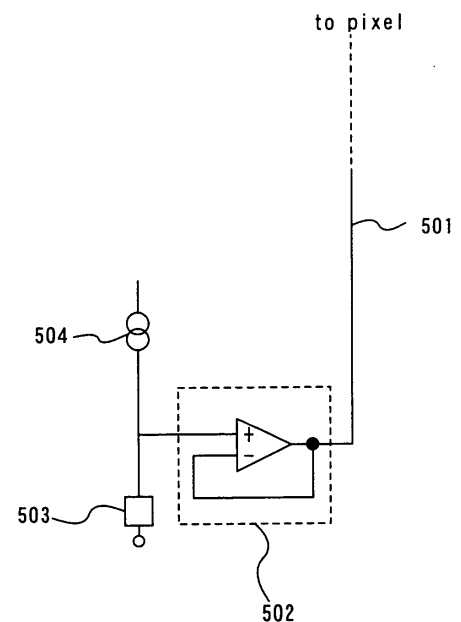


Fig. 1



EUROPEAN SEARCH REPORT

Application Number
EP 01 11 4361

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
X	US 5 594 463 A (SAKAMOTO MITSUNAO [JP]) 14 January 1997 (1997-01-14)	1-6,8, 10-20	INV. G09G3/32
Y	* column 6, line 22 - column 8, line 14 * * column 8, line 60 - column 9, line 48 * * figures 6-8 * * figures 10-12 *	7,9	
Y	----- US 5 170 158 A (SHINYA MASAKO [JP]) 8 December 1992 (1992-12-08) * figures 2,3 * * column 2, line 15 - column 3, line 10 *	7,9	
Y	----- EP 0 536 758 A1 (NEC CORP [JP]) 14 April 1993 (1993-04-14) * the whole document *	7,9	
X	----- EP 0 923 067 A1 (SEIKO EPSON CORP [JP]) 16 June 1999 (1999-06-16)	1-6,8, 10-20	
Y	* figure 8 * * sentence 39, paragraph 35 *	7,9	
X	----- WO 98/52182 A1 (UNISPLAY SA [CH]; SALAM HASSAN PADDY ABDEL [GB]) 19 November 1998 (1998-11-19)	1-6,8, 10-20	TECHNICAL FIELDS SEARCHED (IPC) G09G
Y	* page 12, line 33 - page 16, line 17 * * figures 2,4 *	7,9	
X	----- EP 1 005 013 A1 (LUCENT TECHNOLOGIES INC [US]) 31 May 2000 (2000-05-31)	1-6,8, 10-20	
Y	* figures 8-9 * * paragraph [0046] - paragraph [0057] *	7,9	
E	----- EP 1 148 466 A2 (SEMICONDUCTOR ENERGY LAB [JP]) 24 October 2001 (2001-10-24) * paragraph [0091] - paragraph [0099] * * paragraph [0234] - paragraph [0244] * * figure 5 * * figures 21,22,29 *	1-6,8, 10-20	
The present search report has been drawn up for all claims			
Place of search The Hague		Date of completion of the search 20 August 2010	Examiner Husselin, Stephane
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	

1
EPO FORM 1503 03.92 (P04C01)

**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 01 11 4361

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
The members are as contained in the European Patent Office EDP file on
The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

20-08-2010

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
US 5594463	A	14-01-1997	NONE	
US 5170158	A	08-12-1992	NONE	
EP 0536758	A1	14-04-1993	DE 69217801 D1	10-04-1997
			DE 69217801 T2	11-09-1997
			JP 2894039 B2	24-05-1999
			JP 5100632 A	23-04-1993
			US 5307085 A	26-04-1994
EP 0923067	A1	16-06-1999	DE 69825402 D1	09-09-2004
			DE 69825402 T2	04-08-2005
			WO 9840871 A1	17-09-1998
			JP 3887826 B2	28-02-2007
			KR 20000010923 A	25-02-2000
			TW 397965 B	11-07-2000
			US 2002180721 A1	05-12-2002
WO 9852182	A1	19-11-1998	NONE	
EP 1005013	A1	31-05-2000	DE 69900197 D1	30-08-2001
			DE 69900197 T2	22-11-2001
			JP 2000163015 A	16-06-2000
			KR 20000035688 A	26-06-2000
			TW 508554 B	01-11-2002
			US 6384804 B1	07-05-2002
EP 1148466	A2	24-10-2001	CN 1318818 A	24-10-2001
			CN 1598914 A	23-03-2005
			CN 1630306 A	22-06-2005
			CN 1642022 A	20-07-2005
			CN 101702303 A	05-05-2010
			KR 20050012703 A	02-02-2005
			TW 272571 B	01-02-2007
			TW 288577 B	11-10-2007
			TW 288385 B	11-10-2007
			TW 288908 B	21-10-2007
			US 2001030511 A1	18-10-2001
			US 2001033252 A1	25-10-2001

EPO FORM P0459

For more details about this annex : see Official Journal of the European Patent Office, No. 12/82

专利名称(译)	显示设备		
公开(公告)号	EP1168291A3	公开(公告)日	2010-10-06
申请号	EP2001114361	申请日	2001-06-13
[标]申请(专利权)人(译)	株式会社半导体能源研究所		
申请(专利权)人(译)	半导体能源研究所有限公司.		
当前申请(专利权)人(译)	半导体能源研究所有限公司.		
[标]发明人	YAMAZAKI SHUNPEI KOYAMA JUN KAZUE HOSOKI		
发明人	YAMAZAKI, SHUNPEI KOYAMA, JUN KAZUE, HOSOKI		
IPC分类号	G09G3/32 G09F9/30 G09G3/10 G09G3/20 G09G3/30 G09G3/38 H01J1/62 H01L27/15 H01L31/12 H05B33/00		
CPC分类号	G09G3/2022 G09G3/3233 G09G2300/0842 G09G2310/027 G09G2320/029 G09G2320/041 G09G2320 /043 G09G2320/045		
优先权	2000176246 2000-06-13 JP		
其他公开文献	EP1168291B1 EP1168291A2		
外部链接	Espacenet		

摘要(译)

使用底栅TFT的显示装置的图像质量得到改善。具体地，通过抑制在装置使用时由周围温度的变化引起的流过EL元件的电流量的变化来控制亮度的波动并且补偿驱动电路的频率特性。除了像素部分EL元件之外，还提供监视EL元件。监视EL元件与缓冲放大器等一起构成温度补偿电路。通过温度补偿电路向像素部分EL元件提供电流。这使得可以保持流过像素部分EL元件的电流相对于温度的变化恒定，并且可以控制亮度的波动。对输入信号进行时基扩展以精确地进行采样。

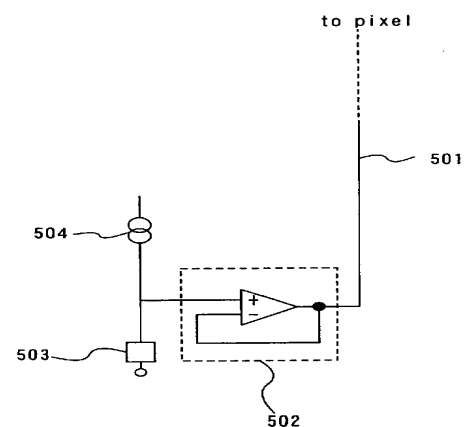


Fig. 1