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(54) **ACTIVE MATRIX DISPLAY COMPENSATION**

AKTIVMATRIX-ANZEIGEKOMPENSATION

COMPENSATION POUR AFFICHAGE A MATRICE ACTIVE

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EP 2 033 177 B1

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Description**FIELD OF THE INVENTION**

5 [0001] The present invention relates to an active matrix-type display apparatus for driving display elements.

BACKGROUND OF THE INVENTION

10 [0002] In recent years, it has become necessary that image display devices have high-resolution and high picture quality, and it is desirable for such image display devices to have low power consumption and be thin, lightweight, and visible from wide angles. With such requirements, display devices (displays) have been developed where thin-film active elements (thin-film transistors, also referred to as TFTs) are formed on a glass substrate, with display elements then being formed on top.

15 [0003] In general, a substrate forming active elements is such that patterning and interconnects formed using metal are provided after forming a semiconductor film of amorphous silicon or polysilicon etc. Due to differences in the electrical characteristics of the active elements, the former requires ICs (Integrated Circuits) for drive use, and the latter is capable of forming circuits for drive use on the substrate. In liquid crystal displays (LCDs) currently widely used, the amorphous silicon type is widespread for large-type screens, while the polysilicon type is more common in medium and small screens.

20 [0004] Typically, organic EL elements are used in combination with TFTs and utilize a voltage/current control operation so that current is controlled. The current/voltage control operation refers to the operation of applying a signal voltage to a TFT gate terminal so as to control current between the source and drain. As a result, it is possible to adjust the intensity of light emitted from the organic EL element and to control the display to the desired gradation.

25 [0005] However, in this configuration, the intensity of light emitted by the organic EL element is extremely sensitive to the TFT characteristics. In particular, for amorphous silicon TFTs (referred to as a-Si), it is known that comparatively large differences in electrical characteristics occur with time between neighboring pixels, due to changes in transistor threshold voltage. This is a major cause of deterioration of the display quality of organic EL displays, in particular, screen uniformity. Uncompensated, this effect can lead to "burned-in" images on the screen. Goh et al. (IEEE Electron Device Letters, Vol. 24, No. 9, pp. 583-585) have proposed a pixel circuit with a precharge cycle before data loading, to compensate for this effect. Compared to the standard OLED pixel circuit with a capacitor, a select transistor, a power transistor, and power, data, and select lines, Goh's circuit uses an additional control line and two additional switching transistors. Jung et al. (IMID '05 Digest, pp. 793-796) have proposed a similar circuit with an additional control line, an additional capacitor, and three additional transistors. While such circuits can be used to compensate for changes in the threshold voltage of the driving transistor, they add to the complexity of the display, thereby increasing the cost and the likelihood of defects in the manufactured product. Further, such circuitry generally comprises thin-film transistors (TFTs) and necessarily uses up a portion of the substrate area of the display. For bottom-emitting devices, the aperture ratio is important, and such additional circuitry reduces the aperture ratio, and can even make such bottom-emitting displays unusable. Thus, there exists a need to compensate for changes in the electrical characteristics of the pixel circuitry in an OLED display without reducing the aperture ratio of such a display.

35 [0006] Document WO 2005/069267 A1 concerns an active matrix electroluminescent display device having a shorting transistor connected between the gate and drain of a drive transistor. Means are provided for measuring a voltage on a data line. The shorting transistor can be used to discharge the voltage on the gate of the drive transistor until it switches off. By storing the resultant voltage on the data line through an address transistor, the data line is used as one of the control/measurement lines for a threshold measurement.

40 [0007] Document WO 2004/097782 A1 concerns an active matrix display device having an array of OLED display pixels operable in two modes in which the power supply line is modulated between a low voltage and a normal power supply voltage. In the first mode, a pixel drive transistor current is supplied to the display element and is selected to provide a desired pixel brightness. In the second mode, a voltage is provided to the drive transistor and is selected to provide a desired aging effect. However, no current flows through the display element.

SUMMARY OF THE INVENTION

45 [0008] It is therefore an object of the present invention to provide an apparatus of compensating for changes in the electrical characteristics of the pixel circuitry in an OLED display.

50 [0009] This object is achieved by an apparatus for selecting a stressing voltage for compensating for changes in the threshold voltages (V_{th}) for drive transistors in pixel drive circuits in an active matrix OLED display having a plurality of OLED light-emitting pixels arranged in an array, comprising:

the features of claim 1.

ADVANTAGES

[0010] It is an advantage of the present invention that it can compensate for changes in the electrical characteristics of the thin-film transistors of an OLED display. It is a further advantage of this invention that it can so compensate without reducing the aperture ratio of a bottom-emitting OLED display and without increasing the complexity of the within-pixel circuits. It is a further advantage of this invention that it reduces the power requirements of an OLED display and allows the apparatus generating the signal voltages to be designed for a smaller voltage range.

BRIEF DESCRIPTION OF THE DRAWINGS**[0011]**

FIG. 1 shows a schematic diagram of an OLED pixel drive circuit well-known in the art;

FIG. 2 shows a schematic diagram of one embodiment of a common OLED pixel drive circuit that is useful in this invention;

FIG. 3 shows a schematic diagram of another embodiment of a common OLED pixel drive circuit that is useful in this invention;

FIG. 4A through 4D show the stepwise results of the operations of this invention on a portion of an example pixel drive circuit;

FIG. 5A shows a schematic diagram of one embodiment of a circuit according to this invention for determining an error-correcting voltage for compensating for changes in the threshold voltages for a drive transistor in a pixel drive circuit in an active matrix OLED display;

FIG. 5B shows a portion of another embodiment of the above circuit;

FIG. 6 shows a block diagram of one embodiment of a method according to this invention for determining an error-correcting voltage for compensating for changes in the threshold voltages for a drive transistor in a pixel drive circuit in an active matrix OLED display;

FIG. 7A through 7C show the distribution of threshold voltages at different times of a display's lifetime, before and after the application of this invention;

FIG. 8 shows a block diagram of one embodiment of a method for determining an average threshold voltage for a display; and

FIG. 9 shows a graph of current vs. voltage in another embodiment of a method for determining an average threshold voltage for a display.

DETAILED DESCRIPTION OF THE INVENTION

[0012] Turning now to FIG. 1, there is shown a schematic diagram of one embodiment of an OLED pixel drive circuit that can be used in this invention. Such pixel drive circuits are well known in the art in active matrix OLED displays. OLED pixel drive circuit 100 has a data line 120, a power supply line 110, a select line 130, a drive transistor 170, a switch transistor 180, an OLED light-emitting pixel 160, and a capacitor 190. Drive transistor 170 has drain electrode 145, source electrode 155, and gate electrode 165. In pixel drive circuit 100, drain electrode 145 of drive transistor 170 is electrically connected to power supply line 110, while source electrode 155 is electrically connected to OLED light-emitting pixel 160. By electrically connected, it is meant that the elements are directly connected or connected via another component, e.g. a switch, a diode, another transistor, etc. It will be understood that embodiments are possible wherein the source and drain electrode connections are reversed. OLED light-emitting pixel 160 is a non-inverted OLED pixel, wherein the anode of the pixel is electrically connected to power line 110 and the cathode of the pixel is electrically connected to ground 150. Switch transistor 180 has gate electrode 195, as well as source and drain electrodes, together represented as source or drain electrodes 185 because such transistors are commonly bidirectional. Of the source and drain electrodes 185 of switch transistor 180, one is electrically connected to the gate electrode 165 of drive transistor 170, while the other is electrically connected to data line 120. Gate electrode 195 is electrically connected to select line 130. OLED light-emitting pixel 160 is powered by flow of current between power supply line 110 and ground 150. In this embodiment, power supply line 110 has a positive potential, relative to ground 150, for driving OLED light-emitting pixel 160. The normal driving potential will herein be referred to as the first voltage and is positive for this embodiment. It will cause current to flow through drive transistor 170 and OLED light-emitting pixel 160 in a first direction, that is, electrons will flow from ground 150 to power line 110, which will cause OLED light-emitting pixel 160 to produce light. The magnitude of the current-and therefore the intensity of the emitted light-is controlled by drive transistor 170, and more exactly by the magnitude of the signal voltage on gate electrode 165 of drive transistor 170. During a write cycle, select line 130 activates switch transistor 180 for writing and the signal voltage data on data line 120 is written to drive transistor 170 and stored on capacitor 190, which is connected between gate electrode 165 and power supply line 110.

[0013] Turning now to FIG. 2, there is shown a schematic diagram of another embodiment of an OLED pixel drive circuit that can be used in this invention. Pixel drive circuit 105 is constructed much as pixel drive circuit 100 described above. However, OLED light-emitting pixel 140 is an inverted OLED pixel, wherein the cathode of the pixel is electrically connected to power line 110 and the anode of the pixel is electrically connected to ground 150. In this embodiment, power supply line 110 must have a negative potential, relative to ground 150, for driving OLED light-emitting pixel 160. Therefore, the first voltage is negative relative to ground 150 for this embodiment and the first direction in which current flows so as to drive OLED light-emitting pixel 140 will be the reverse of that in FIG. 1. It will be understood in the examples to follow that one can reverse the potentials and current directions if necessary for the structure and function of the OLED pixel drive circuits, and that such modifications are within the scope of this invention.

[0014] The above embodiments are constructed wherein the drive transistors and switch transistors are n-channel transistors. It will be understood by those skilled in the art that embodiments wherein the drive transistors and switch transistors are p-channel transistors, with appropriate well-known modifications to the circuits, can also be useful in this invention.

[0015] In practice in active-matrix displays, the capacitance is often not provided as a separate entity, but in a portion of the thin-film transistor sections that form the drive transistor. FIG. 3 shows a schematic diagram of one embodiment of a common OLED pixel drive circuit 200 of this type, which is useful in this invention. Drive transistor 210 also incorporates a capacitor 230 connected between gate electrode 215 and power line 110. This will also be referred to as the gate-power capacitor, or C_{gp} . Drive transistor 210 generally inherently includes a smaller parasitic capacitor 230 connected between gate electrode 215 and OLED light-emitting pixel 160. This will also be referred to as the gate-OLED capacitor, or C_{go} . In some embodiments, the relative magnitude of C_{gp} and C_{go} can be reversed. As in pixel drive circuit 100, the first voltage is positive for normal operation of OLED light-emitting pixel 160. If the potential is reversed (e.g. power supply line 110 has a negative voltage relative to ground 150), OLED light-emitting pixel 160 will be in an inoperative condition and will function instead as a capacitor having a capacitance C_{OLED} . This potential, which is opposite in polarity to the first voltage, will herein be referred to as the second voltage. This will cause current to flow through drive transistor 210 in a second direction opposite to the above first direction. However, current flow in the second direction will only occur until the various capacitors in the circuit, including the OLED light-emitting pixel, become charged and cause the drive transistor to turn off. The use of this property of the pixel drive circuits described herein is an important feature of this invention, which will now be illustrated.

[0016] Turning now to FIG. 4A through 4D, there are shown the stepwise results of the operations of this invention on a portion of an example pixel drive circuit 200. In preparation for FIG. 4A, a potential of zero volts is placed on power supply line 110 and on gate electrode 215. It is not required for the practice of this invention that power supply line 110 or gate electrode 215 first be set to zero volts; however, doing so will make illustration of the use of this invention clearer. The switch transistor that electrically connects gate electrode 215 to data line 120 is turned off, so that gate electrode 215 is isolated. Then a second voltage of -20V is applied to power supply line 110. With a second voltage, OLED light-emitting pixel 160 is in an inoperative condition and acts as a capacitor. In the example shown here, the OLED capacitance C_{OLED} is 3.5pF, the gate-OLED capacitance C_{go} is 0.089pF, and the gate-power capacitance C_{gp} is 0.275pF. The voltages shown in FIG. 4A are those expected with these capacitances before any current flows if the gate and power supply potentials are both initially zero. If either the gate or power supply potential-or both-is not zero, the resulting voltages will be different, but will still be a function of the capacitances.

[0017] Current will then flow through drive transistor 210 in a second direction, that is, electrons will flow from power line 110 to ground 150, and charge the C_{OLED} capacitor. As the charge on C_{OLED} is increased, the potential between the source and drain electrodes of drive transistor 210 is reduced. Simultaneously, the potential on the gate electrode of drive transistor 210 (which is isolated by switch transistor 180) will shift to maintain the ratio of the potential difference from the gate to source and drain in proportion to the inverse of the ratio of respective capacitances:

$$V_{gp}/V_{go} = C_{go}/C_{gp} \quad (\text{Eq. 1})$$

[0018] The current flow will continue until the potential V_{go} between gate electrode 215 of drive transistor 210 and power supply line 110 falls to the value of the drive transistor threshold voltage, which causes the drive transistor to turn off. By turn off, it is meant that the current flow through drive transistor 210 is substantially zero. However, it is known in the art that transistors can leak small amounts of current under threshold voltage or lower conditions; such transistors can be successfully used in this invention. For illustration purposes, we are assuming in this example that the threshold voltage V_{th} of drive transistor 210 is 3.0V. FIG. 4B shows the resulting voltages stored on the capacitors at this point. These voltages are a function of the threshold voltage of the transistor. Thus, the gate voltage is a threshold-voltage-proportional signal, and can be related to the threshold voltage by Eq. 2, wherein PV_{DD2} represents the second voltage (e.g. -20V in this example) applied to power supply line 110:

$$V_{gate} = PV_{DD2} + V_{th} \quad (Eq. 2)$$

[0019] After the voltages have equilibrated as shown in FIG. 4b, select line 130 activates switch transistor 180 to connect gate electrode 215 to data line 120, wherein the gate electrode voltage will be changed by a transfer function, here represented by $f(x)$. The transfer function depends on the characteristics of switch transistor 180, the change in potential of select line 130, the circuit layout, the capacitance and impedance of the external circuits connected to data line 120, and the number of pixels on data line 120 that are switched. One skilled in the art can predict the transfer function based on the design, or can measure it. Thus, the voltage produced on data line 120 (V_{out}) is a threshold-voltage-related signal which is a function of the potential on the gate electrode of the drive transistor, given by:

$$V_{out} = f(V_{gate}) \quad (Eq. 3)$$

[0020] The transfer function $f(x)$ can be inverted, represented by $f^{-1}(x)$. The threshold voltage is calculated from the measured voltage by:

$$V_{th} = f^{-1}(V_{out}) - PV_{DD2} \quad (Eq. 4)$$

[0021] Alternatively, before activating switch transistor 180 and measuring the potentials, an additional step can be done wherein the potential of power supply line 110 can then be changed to a third voltage. This will redistribute the potentials based upon the capacitances, as shown in FIG. 4C. If the voltage is chosen correctly, such as zero in this example, current will flow through drive transistor 210 in the direction used to cause the OLED to emit light. No light will be emitted, as the OLED remains in a reverse bias condition. The current will continue to flow until the gate-to-OLED potential difference is equal to the threshold voltage of the drive transistor for current flow in the direction used for light emission. FIG. 4D shows the resulting voltages on the circuit at this point. The gate voltage can be related to the threshold voltage by:

$$V_{gate} = PV_{DD3} - \frac{V_{th}C_{gp}}{C_{go}} \quad (Eq. 5)$$

wherein PV_{DD3} represents the third voltage (e.g. zero in this example) applied to power supply line 110. In this case the threshold voltage can be calculated from the measured voltage by:

$$V_{th} = \frac{-C_{go}(f^{-1}(V_{out}) - PV_{DD3})}{C_{gp}} \quad (Eq. 6)$$

[0022] This last step of reducing the reverse driving potential (FIG. 4C and 4D) is useful in the case that the threshold voltage of the driving transistor 210 is different for forward and reverse operation.

[0023] As the threshold voltage of a transistor can change with usage, it can be necessary to calculate an adjustment for the threshold voltage. This is the difference between the currently-calculated threshold voltage and the initial threshold voltage:

$$\text{Adjustment} = V_{th} - V_{thi} \quad (Eq. 7)$$

where V_{thi} represents the initial threshold voltage of the transistor.

[0024] Turning now to FIG. 5A, and referring also to FIG. 3 through 4D, there is shown a schematic diagram of one embodiment of an apparatus of this invention for selecting a stressing voltage for compensating for changes in the threshold voltages for drive transistors in pixel drive circuits as described herein. Active matrix OLED display 250 has a plurality of OLED light-emitting pixels arranged in an array, each having a pixel drive circuit as described above (e.g. 200A and 200B). In normal operation, voltage supply 260, which is a positive power supply, applies a first voltage (also called PV_{DD1}) to power supply line 110 via switch 265 to cause current to flow in a first direction through the drive transistors as described above, which causes OLED light-emitting pixels 160 to produce light. The intensity of the emitted

light, which is proportional to the current through drive transistor 170, is responsive to the signal voltages set by data line 120, which is electrically connected to digital-to-analog converter 280. Digital-to-analog converter 280 converts a digital input representing the desired intensity of light emitted by a given pixel into an analog signal voltage, which a select line (e.g. 130A and 130B) allows to be written to the capacitors of the selected pixel circuit. Although not shown for clarity of illustration, it will be understood that OLED display 250 further includes multiple power supply lines and data lines, as known in the art.

[0025] In order to select a stressing voltage for compensating for changes in the threshold voltages (V_{th}) for the drive transistors of OLED display 250, it is necessary to apply a second voltage opposite in polarity to the first voltage to the power supply line and the pixel drive circuit and thus place the OLED in an inoperative condition, as described above. Voltage supply 270, which is a negative power supply in this embodiment, applies a second voltage (PV_{DD2}) opposite in polarity to the first voltage to power supply line 110 via switch 265. As described above, this causes current to flow through the drive transistor in a second direction opposite to the first direction of normal operation, until the potential on the gate electrode of the drive transistor causes the drive transistor to turn off. Switch 265 can also optionally switch the circuit to a third voltage state (PV_{DD3}), e.g. ground 150. During the second and third voltage operations, data line 120 can become an output line providing a threshold-voltage-related signal that is a function of the potential on gate electrode 215 of drive transistor 210. At another time during the process described herein, data line 120 is used to apply a stressing voltage to drive transistor 210, as will be described below. Switch 285 can be opened or closed as necessary.

[0026] In order to select the stressing voltage for individual drive transistors, one first obtains an average level of stress for the drive transistors of OLED display 250, and then compares the level of stress of individual drive transistors to the average. The term "level of stress" as used herein refers to changes in the threshold voltage of the drive transistor. Integrator line 385A connects data line 120 to integrator 390. To obtain an average level of stress after the voltages in the pixel drive circuits have equilibrated as described above in FIG. 4B or 4D, all select lines for all rows (e.g. 130A, 130B) are activated, turning on switch transistors 180 and opening data line 120 to gate electrodes 215 of all pixels in that column. The voltage then produced on data line 120 is a threshold-voltage-related signal that is an average of the threshold-voltage-related signals that would be provided by the individual pixels in the column. Data line 120 is connected via integrator line 385A to integrator 390. Other data lines for other columns of pixels (not shown) are also connected to integrator 390 via their corresponding integrator lines 385. Each data line thus has a threshold-voltage-related signal that is an average for the column. Integrator 390 is responsive to the plurality of threshold-voltage-related signals to produce an average threshold-voltage-related signal V_{out} for all pixels of OLED display 250. The average threshold-voltage-related signal is relayed to processor 315, which can calculate (via Eq. 4 or Eq. 6) the average threshold voltage or simply store the average threshold-voltage-related signal.

[0027] In the present embodiment, the target value of the threshold-voltage-related signal is based on the current average threshold voltage of the display. Other embodiments are possible, such as use of the initial value of the average threshold voltage of the display.

[0028] Once the average threshold voltage is known, the stressing voltage can be selected and applied on a row-by-row basis based on the threshold-voltage-related signal from each pixel. The process shown in FIG. 4 is repeated for each row of pixels in OLED display 250. Switch 285 is set to connect the output of digital-to-analog converter 280 to one input of voltage comparator circuit 370, and processor 315 causes digital-to-analog converter 280 to produce a voltage equal to the average threshold-voltage-related signal. One select line (e.g. 130A) is activated, turning on switch transistor 180 and opening data line 120 to a single pixel (e.g. 200A) in its column. The voltage then produced on data line 120 is a threshold-voltage-related signal for a single pixel, and the signal is delivered to a second input of voltage comparator circuit 370. Voltage comparator circuit 370 is responsive to the threshold-voltage-related signal and the average threshold-voltage-related signal. Its output can be positive or negative and goes to sample-and-hold element 360 and then to voltage selector switch 380, which selects the stressing voltage and selectively applies it to the gate electrode of the selected drive transistor. In this embodiment, voltage selector switch 380 is provided with a single stressing voltage V_s from stressing voltage source 365, which voltage selector switch 380 selects to apply or not apply based on the threshold-voltage-related signal. For example, the voltage from stressing voltage source 365 can be +15V. If the threshold-voltage-related signal of a pixel is less than the average, which indicates that the pixel is less stressed than average, voltage selector switch 380 can select to apply the stressing voltage to the pixel. If the threshold-voltage-related signal is greater than or equal to the average, voltage selector switch 380 can instead select a neutral or disconnected position, and thus not apply the stressing voltage.

[0029] After the stressing voltage is applied, processor 315 can provide an adjustment to the signal voltage applied to the gate electrodes of the drive transistors. This adjustment can be accomplished by shifting the analog reference voltage for the signal digital-to-analog converter 280. Because the practice of this invention reduces the threshold voltage range in the drive transistors, the shift applied to the signal voltages in order to compensate for the shift in the threshold voltage of the drive transistors can be the same for all drive transistors.

[0030] Turning now to FIG. 5B, there is shown another embodiment of a portion of the apparatus of FIG. 5A wherein one of a plurality of stressing voltages can be selected to be applied based on the threshold-voltage-related signal. In

this embodiment, voltage selector switch 395 is provided with three stressing voltages: a positive stressing voltage V_{s+} from stressing voltage source 365, a negative stressing voltage V_{s-} from stressing voltage source 375, and a zero voltage from ground 150. For example, if the threshold-voltage-related signal of a pixel is significantly less than the average, which indicates that the pixel is less stressed than average, voltage selector switch 380 can select to apply stressing voltage V_{s+} to the pixel. If the threshold-voltage-related signal is significantly greater than the average, voltage selector switch 380 can select to apply stressing voltage V_{s-} to the pixel, and thus reduce the stress level of the drive transistor. If the threshold-voltage-related signal is approximately average, voltage selector switch 380 can select to apply the zero voltage to the pixel.

[0031] Turning now to FIG. 6, and referring also to FIG. 3 through 5A, there is shown a block diagram of one embodiment of a method using the apparatus of this invention for selecting a stressing voltage for compensating for changes in the threshold voltages for drive transistors in pixel drive circuits in an active matrix OLED display, and for applying the stressing voltage to the pixels. At the start, an average threshold-voltage-related signal is determined for the entire OLED display 250 (Step 410). Step 410 will be described in greater detail below. Then, the gate voltages of an entire row are set to zero by setting all data lines 120 to zero and turning on switch transistors 180 by selecting the appropriate select line 130 (Step 420). Switch transistors 180 are then turned off (Step 430). Then a second voltage opposite in polarity to the first driving voltage is applied to OLED light-emitting pixel 160 by connecting negative voltage supply 270 to power supply line 110 via switch 265 (Step 440), thus placing the OLED in an inoperative condition. Then current is allowed to flow through the circuit (Step 450) to charge the capacitors: OLED 160, gate-OLED capacitor 220, and gate-power capacitor 230. Current flows until the voltage difference between gate electrode 215 and power supply line 110 equals the threshold voltage of drive transistor 210, which causes the drive transistor to turn off. The resulting voltages are as shown in FIG. 4B. Additionally, a third voltage can be applied, which would result in the voltages shown in FIG. 4D. During the time between Steps 430 and 460, switch 285 connects digital-to-analog converter 280 with voltage comparator circuit 370, and digital-to-analog converter 280 is caused to input the average threshold-voltage-related signal to voltage comparator circuit 370 (Step 445). Then switch transistors 180 are turned on for the row of pixel drive circuits 200 by selecting the appropriate select line 130 (Step 460). Voltage comparator circuit 370 compares the threshold-voltage-related signal with the average (Step 470), and thus indirectly measures the voltages stored on the capacitors of the pixel drive circuit, which will show whether the drive transistor 210 is stressed more or less than the average. If voltage comparator circuit 370 indicates that the drive transistor is stressed less than average (Step 475), voltage selector switch 380 can apply a stressing voltage to drive transistor 210 for a predetermined period (Step 480). Otherwise, Step 480 is skipped. If there are more rows of pixel drive circuits 200 in OLED display 250 (Step 485), the process is repeated. If there are no more rows of pixel circuits, the stressing process is complete. Processor 315 can provide an adjustment to the signal voltage to the gate electrodes of drive transistors 210 to compensate for changes in the average threshold voltage (Step 490). Step 490 need not follow immediately after Step 485. For example, Steps 410 to 485 can be done sequentially to all rows of pixel drive circuits 200 upon power-down of OLED display 250. Step 490 can then be done to the display the next time it is powered on.

[0032] Turning now to FIG. 7A, there is shown an initial distribution of threshold voltages of drive transistors in an OLED display, wherein the vertical axis represents the fraction of pixel drive circuits with a given threshold voltage. Turning now to FIG. 7B, there is shown a distribution of the threshold voltages in the same display as FIG. 7A, but after it has been operated for a time. The drive transistors now have higher threshold voltages than initially. Further, the threshold voltage range is broader, which makes it difficult to apply a single adjustment to the signal voltage to the entire display to compensate for the threshold voltage change. Some transistors will be overcompensated, while others will be undercompensated by a single adjustment. Turning now to FIG. 7C, there is shown a distribution of threshold voltages in the display of FIG. 7B after the use of this invention. A compensating stress signal, e.g. a voltage of 10-15 volts, has been applied to the pixels of FIG. 7B with a lower-than-average threshold voltage. This has increased their threshold voltages to average or slightly greater. The overall effect is to reduce the threshold voltage range in the drive transistors based on the threshold-voltage-related signals, which makes it easier to apply a single adjustment to the signal voltage to compensate for threshold voltage changes wherein the adjustment is the same for all drive transistors.

[0033] Other embodiments are possible. For example, instead of applying a positive voltage stress to drive transistors with less-than-average threshold voltages, one can apply a negative voltage stress to drive transistors with greater-than-average threshold voltages. Thus, the distribution of threshold voltages in FIG. 7B can be narrowed by lowering the threshold voltages of the more stressed drive transistors. In the embodiment of FIG. 5B, voltage selector 380 can have three inputs: zero voltage, large positive (e.g. +15V), and large negative (e.g. -15V). The large positive voltage can be applied to drive transistors with a less-than-average threshold voltage, while the large negative voltage can be applied to drive transistors with a greater-than-average threshold voltage. The zero voltage can be applied to drive transistors that have an average or near-average threshold voltage. Thus, the distribution of threshold voltages in FIG. 7B can be narrowed from both sides.

[0034] Turning now to FIG. 8, and referring also to FIG. 3 through 5A, there is shown a block diagram of one embodiment of a method for determining an average threshold-voltage-related signal for the display. At the start, the gate voltages

of the entire display are set to zero by setting all data lines 120 to zero volts and turning on switch transistors 180 for all pixel drive circuits (e.g. 200A, 200B, etc.) by selecting all select lines (e.g. 130A, 130B, etc.) (Step 510). Then all switch transistors 180 are turned off (Step 530). A second voltage opposite in polarity to the first driving voltage is applied to all OLED light-emitting pixels 160 by connecting negative voltage supply 270 to power supply line 110 via switch 265 (Step 540), thus placing the OLEDs in an inoperative condition. Then current is allowed to flow through the circuit (Step 550). In this case, current will flow to charge the capacitors: OLEDs 160, gate-OLED capacitors 220, and gate-power capacitors 230. Current flows until the voltage difference across gate-power capacitor 230 equals the threshold voltage of its particular drive transistor 210, as shown in FIG. 4B, which causes the drive transistors to turn off. A third voltage can also be used, as described above, to obtain a threshold-voltage-related signal for current flow in the OLED-on direction, as shown in FIG. 4D. All switch transistors 180 are turned on for all pixel drive circuits by selecting all select lines (Step 555). The average threshold-voltage-related signal can then be produced by integrator 390 and measured by processor 315 (Step 560). Since the data lines 120 of all pixel drive circuits are connected to processor 315 by integrator 390, the gate-source voltage read is an average for the entire display. The average threshold voltage V_{th} is related to the average threshold-voltage-related signal as described above. Processor 315 can calculate or find the average threshold voltage of drive transistors 210 in all pixel drive circuits (Step 570). This value can then be used in determining the relative stress levels of the drive transistors in order to select a stressing voltage, as described above. In particular, one can use Eq. 3 to calculate the average threshold-voltage-related signal expected for a single pixel. Alternatively, the average threshold-voltage-related signal measured in Step 560 can be used directly for the process described above in FIG. 6.

[0035] Other methods of obtaining an average threshold voltage, which will be apparent to those skilled in the art, can be used with this invention. For example, a threshold voltage can be determined for drive transistor 210 of each pixel drive circuit, and a numerical average calculated. A method for determining the threshold voltages for each of the drive transistors is taught by Hamer et al. U.S. Serial No. 11/427,104 filed June 28, 2006. Alternatively, as shown in FIG. 9, the current (i_{ds}) for the entire display can be measured while varying the gate voltage (V_{gs}) at a constant drive voltage (PV_{DD-CV}). This can produce curve 610, which can be extrapolated to average threshold voltage 620.

PARTS LIST

[0036]

30
 100 pixel drive circuit
 105 pixel drive circuit
 110 power supply line
 120 data line
 35 130 select line
 130A select line
 130B select line
 140 OLED light-emitting pixel
 145 drain electrode
 40 150 ground
 155 source electrode
 160 OLED light-emitting pixel
 165 gate electrode
 170 drive transistor
 45 180 switch transistor
 185 source or drain electrode
 190 capacitor
 195 gate electrode
 200 pixel drive circuit
 50 200A pixel drive circuit
 200B pixel drive circuit
 210 drive transistor
 215 gate electrode
 220 capacitor
 55 230 capacitor
 250 OLED display
 260 voltage supply
 265 switch

	270	voltage supply
	280	digital-to-analog converter
	285	switch
	315	processor
5	360	sample-and-hold element
	365	stressing voltage source
	370	voltage comparator circuit
	375	stressing voltage source
	380	voltage selector switch
10	385	integrator lines
	385A	integrator line
	390	integrator
	395	voltage selector switch
	410	block
15	420	block
	430	block
	440	block
	445	block
	450	block
20	460	block
	470	block
	475	decision block
	480	block
	485	decision block
25	490	block
	510	block
	530	block
	540	block
	550	block
30	555	block
	560	block
	570	block
	610	curve
	620	threshold voltage
35		

Claims

1. An apparatus for selecting a stressing voltage for compensating for changes in the threshold voltages (V_{ih}) for drive transistors in pixel drive circuits (200) in an active matrix OLED display (250) having a plurality of OLED light-emitting pixels arranged in an array, comprising:
- a) each pixel drive circuit (200) being electrically connected to a data line (120) and a power supply line (110), and having a drive transistor (210) having source, drain, and gate electrodes, and a switch transistor (180) having source, drain, and gate electrodes;
 - b) the source or drain electrode of each drive transistor (210) being electrically connected to its corresponding power supply line (110), and the other of the source or drain electrode being electrically connected to its corresponding OLED light-emitting pixel;
 - c) the source or the drain electrode of each switch transistor (180) being electrically connected to the gate electrode of its corresponding drive transistor (210), and the other of the source or drain electrode being electrically connected to its corresponding data line (120);
 - d) first means (260) adapted to apply a first voltage to the power supply lines (110) which is either positive or negative for causing current to flow in a first direction through the drive transistors (210) which causes the OLED light-emitting pixels to produce light in response to the signal voltages;
 - e) second means (270) adapted to apply a second voltage to the power supply lines (110) opposite in polarity to the first voltage so that current will flow through the drive transistors (210) in a second direction opposite to the first direction until the potential on the gate electrodes of the drive transistors (210) causes the drive transistors (210) to turn off; and

f) third means adapted to, after the drive transistors (210) are turned off by the second means (270), activate the switch transistors (180) to connect the gate electrodes of the drive transistors (210) to the data lines (120) to produce a plurality of threshold-voltage-related signals on the data lines (120), each of which is a function of the corresponding potentials on the gate electrodes of the drive transistors (210) and adapted to produce an average of said threshold-voltage-related signals per data line (120),

characterized by

g) fourth means (390) adapted to in response to the plurality of threshold-voltage-related signals produce an average threshold-voltage-related signal for all pixels of the OLED display (250);

h) fifth means (380) adapted to in response to the average threshold-voltage-related signal of the OLED display (250) select the stressing voltage;

i) sixth means adapted to turn on, on a row by row basis, a single switch transistor (180) among the switch transistors (180) connected to the same data line (120) to connect the gate electrode of a single pixel to the data line (120) to produce a threshold-voltage-related signal for said single pixel on the data line (120);

j) seventh means adapted to compare the single pixel threshold-voltage-related signal with the average threshold-voltage-related signal calculated by the fourth means (390) and select the drive transistor (210) to be stressed according to the result; and

k) eight means adapted to selectively apply the stressing voltage to the gate electrodes of selected drive transistors (210) based on the threshold-voltage-related signals to reduce the threshold voltage range among the drive transistors (210) of the OLED display (250).

2. The apparatus of claim 1, wherein the OLED light-emitting pixels are non-inverted OLED pixels and the first voltage is positive relative to a ground value.
3. The apparatus of claim 1, wherein the OLED light-emitting pixels are inverted OLED pixels and the first voltage is negative relative to a ground value.
4. The apparatus of claim 1, wherein the drive transistors (210) and switch transistors (180) are n-type transistors.
5. The apparatus of claim 1, wherein the drive transistors (210) and switch transistors (180) are p-type transistors.
6. The apparatus of claim 1, wherein a single stressing voltage is selected to be applied or not applied based on the threshold-voltage-related signal.
7. The apparatus of claim 1, wherein one of a plurality of stressing voltages is selected to be applied based on the threshold-voltage-related signal.
8. The apparatus of claim 1, further including providing an adjustment to a signal voltage for each drive transistor (210) wherein the adjustment from the initial threshold voltage is the same for all drive transistors (210).
9. The apparatus of claim 1, wherein the threshold-voltage-related signal is determined and the stressing voltage is selected for each row in the OLED display (250).

Patentansprüche

1. Vorrichtung zum Auswählen einer Belastungsspannung zur Kompensation von Änderungen in Schwellenwertspannungen (V_{ih}) für Ansteuerungstransistoren in Pixelansteuerungsschaltungen (200) in einer Active-Matrix-OLED Anzeige (250), die eine Vielzahl an OLED lichtemittierender in einer Anordnung angeordneter Pixel aufweist, umfassend:

a) jede Pixelansteuerungsschaltung (200) elektrisch verbunden zu einer Datenleitung (120) und einer Stromversorgungsleitung (110), und aufweisend einen Ansteuerungstransistor (210), der Source-, Drain-, und Gate-Elektroden aufweist, und einen Schalttransistor (180), der Source, Drain-, und Gate-Elektroden aufweist;

b) die Source- oder Drain-Elektrode jedes Ansteuerungstransistors (210) elektrisch verbunden mit ihrer entsprechenden Stromversorgungsleitung (110), und die andere der Source- oder Drain-Elektrode elektrisch verbunden mit ihrem entsprechenden lichtemittierenden OLED Pixel;

c) die Source- oder Drain-Elektrode jedes Schalttransistors (180) elektrisch verbunden mit der Gate-Elektrode ihres entsprechenden Ansteuerungstransistors (210), und die andere der Source- oder Drain-Elektrode elektrisch verbunden mit ihrer entsprechenden Datenleitung (120);

d) erste Mittel (260) ausgebildet zum Anlegen einer ersten Spannung an die Stromversorgungsleitungen (110), die entweder positiv oder negativ zum Bewirken eines Fließens eines Stroms in eine erste Richtung durch die Ansteuerungstransistoren (210) ist, der die lichtemittierenden OLED Pixel dazu veranlasst auf die Signalspannung Licht zu erzeugen;

e) zweite Mittel (270) ausgebildet zum Anlegen einer zweiten Spannung an die Stromversorgungsleitungen (110) mit entgegengesetzter Polarität zu der ersten Spannung, so dass Strom durch die Ansteuerungstransistoren (210) in eine zweite Richtung entgegengesetzt zu der ersten Richtung fließt, bis das Potential an den Gate-Elektroden der Ansteuerungstransistoren (210) die Ansteuerungstransistoren (210) dazu veranlasst, abzuschalten; und

f) dritte Mittel ausgebildet zum, nachdem die Ansteuerungstransistoren (210) durch die zweiten Mittel (270) ausgeschaltet wurden, Aktivieren der Schalttransistoren (180), um die Gate-Elektroden der Ansteuerungstransistoren (210) mit den Datenleitungen (120) zu verbinden, um eine Vielzahl an Schwellenwertspannungs-bezogenen Signalen auf den Datenleitungen (120) zu erzeugen, wobei jede dieser eine Funktion der entsprechenden Potentiale an den Gate-Elektroden der Ansteuerungstransistoren (220) ist und ausgebildet ist zum Erzeugen eines Durchschnitts der Schwellenwertspannungs-bezogenen Signals pro Datenleitung (120), **gekennzeichnet durch**

g) vierte Mittel (390) ausgebildet zum, auf die Vielzahl von Stellenwertspannungs-bezogenen Signalen, Erzeugen eines durchschnittlichen Spannungswert-bezogenen Signals für alle Pixel der OLED Anzeige (250);

h) fünfte Mittel (380) ausgebildet zum, auf die durchschnittlichen Schwellenwertspannungs-bezogenen Signale der OLED Anzeige (250), Auswählen der Belastungsspannung;

i) sechste Mittel ausgebildet zum reihenbasierten Einschalten eines einzelnen Schalttransistors (180) unter den Schalttransistoren (180), der mit derselben Datenleitung (180) verbunden ist, um die Gate-Elektrode eines einzelnen Pixels mit der Datenleitung (120) zu verbinden, um ein Schwellenwertspannungsbezogenes Signal für den einzelnen Pixel auf der Datenleitung (120) zu erzeugen;

j) siebte Mittel ausgebildet zum Vergleichen des einzelnen Pixel-Schwellenwertspannungs-bezogenen Signals mit dem durchschnittlichen Schwellenwertspannungs-bezogenen Signal, das von den vierten Mitteln (390) berechnet wurde, und Auswählen des entsprechend dem Ergebnis zu belastenden Ansteuerungstransistors (210); und

k) achte Mittel ausgebildet zum wahlweisen Anwenden der Belastungsspannung an die Gate-Elektroden von ausgewählten Ansteuerungstransistoren (210) basierend auf den Schwellenwertspannungs-bezogenen Signalen, um den Schwellenwertspannungsbereich der Ansteuerungstransistoren (210) der OLED Anzeige (250) zu verringern.

2. Vorrichtung nach Anspruch 1, bei der die lichtemittierenden OLED Pixel nichtinvertierte OLED Pixel sind und die erste Spannung positiv im Vergleich zu einem Grundwert ist.

3. Vorrichtung nach Anspruch 1, bei der die lichtemittierenden OLED Pixel invertierte OLED Pixel sind und die erste Spannung negativ im Vergleich zu einem Grundwert ist.

4. Vorrichtung nach Anspruch 1, bei der die Ansteuerungstransistoren (210) und die Schalttransistoren (180) N-Typ Transistoren sind.

5. Vorrichtung nach Anspruch 1, bei der die Ansteuerungstransistoren (210) und die Schalttransistoren (180) P-Typ Transistoren sind.

6. Vorrichtung nach Anspruch 1, bei der eine einzelne Belastungsspannung zum Anlegen oder nicht-Anlegen basierend auf dem Schwellenwertspannungs-bezogenen Signal ausgewählt wird.

7. Vorrichtung nach Anspruch 1, bei der eine aus einer Vielzahl von Belastungsspannungen zum Anlegen basierend auf dem Schwellenwertspannungs-bezogenen Signal ausgewählt wird.

8. Vorrichtung nach Anspruch 1, weiter beinhaltend Bereitstellen einer Anpassung an eine Signalspannung für jeden Ansteuerungstransistor (210), wobei die Anpassung von der ursprünglichen Schwellenwertspannung dieselbe für alle Ansteuerungstransistoren (210) ist.

9. Vorrichtung nach Anspruch 1, bei der das Schwellenwertspannungs-bezogene Signal bestimmt wird und die Belastungsspannung für jede Reihe in der OLED Anzeige (250) ausgewählt wird.

5 Revendications

1. Appareil permettant de sélectionner une tension de sollicitation afin de compenser des changements dans les tensions de seuil (V_{th}) pour des transistors d'attaque dans des circuits (200) d'attaque de pixels dans un dispositif d'affichage (250) OLED à matrice active ayant une pluralité de pixels d'émission de lumière OLED disposés dans une matrice, comprenant :

a) chaque circuit (200) d'attaque de pixels étant connecté électriquement à une ligne (120) de données et à une ligne (110) d'alimentation électrique, et ayant un transistor (210) d'attaque doté d'électrodes de source, de drain, et de grille, et d'un transistor (180) de commutation doté d'électrodes de source, de drain, et de grille ;

b) l'électrode de drain ou de source de chaque transistor (210) d'attaque étant électriquement connectée à sa ligne (110) d'alimentation électrique correspondante, et l'autre électrode de drain ou de source étant connectée électriquement à son pixel d'émission de lumière OLED correspondant ;

c) l'électrode de drain ou de source de chaque transistor (180) de commutation étant connectée électriquement à l'électrode de grille de son transistor (210) d'attaque correspondant, et l'autre électrode de drain ou de source étant connectée électriquement à sa ligne (120) de données correspondante ;

d) un premier moyen (260) adapté pour appliquer une première tension aux lignes (110) d'alimentation électrique qui est soit positive, soit négative, pour amener le courant à circuler dans une première direction par les transistors (210) d'attaque, ce qui amène les pixels d'émission de lumière OLED à produire de la lumière en réponse aux tensions de signaux ;

e) un deuxième moyen (270) adapté pour appliquer une deuxième tension aux lignes (110) d'alimentation électrique opposées en polarité à la première tension afin que le courant circule par les transistors (210) d'attaque dans une seconde direction opposée à la première direction jusqu'à ce que le potentiel sur les électrodes de grille des transistors (210) d'attaque amène les transistors (210) d'attaque à s'éteindre ; et

f) un troisième moyen adapté, après que les transistors (210) d'attaque sont éteints par le deuxième moyen (270), pour activer les transistors (180) de commutation pour connecter les électrodes de grille des transistors (210) d'attaque aux lignes (120) de données en vue de produire une pluralité de signaux liés à la tension de seuil sur les lignes (120) de données, dont chacune est fonction des potentiels correspondants sur les électrodes de grille des transistors (210) d'attaque, et adapté pour produire une moyenne desdits signaux liés à la tension de seuil par ligne (120) de données,

35 caractérisé par

g) un quatrième moyen (390) adapté, en réponse à la pluralité de signaux liés à la tension de seuil, pour produire un signal moyen lié à la tension de seuil pour tous les pixels du dispositif d'affichage (250) OLED ;

h) un cinquième moyen (380) adapté, en réponse au signal moyen lié à la tension de seuil du dispositif d'affichage (250) OLED, pour sélectionner la tension de sollicitation ;

i) un sixième moyen adapté pour allumer, sur une rangée par une base de rangée, un transistor (180) de commutation unique parmi les transistors (180) de commutation connectés à la même ligne (120) de données afin de connecter l'électrode de grille d'un pixel unique à la ligne (120) de données en vue de produire un signal lié à la tension de seuil pour ledit pixel unique sur la ligne (120) de données ;

j) un septième moyen adapté pour comparer le signal lié à la tension de seuil du pixel unique au signal moyen lié à la tension de seuil calculé par le quatrième moyen (390), et sélectionner le transistor (210) d'attaque à solliciter en fonction du résultat ; et

k) un huitième moyen adapté pour appliquer de manière sélective la tension de sollicitation aux électrodes de grille des transistors (210) d'attaque sélectionnés sur la base des signaux liés à la tension de seuil en vue de réduire la plage de tensions de seuil parmi les transistors (210) d'attaque du dispositif d'affichage (250) OLED.

2. Appareil selon la revendication 1, dans lequel les pixels d'émission de lumière OLED sont des pixels OLED non inversés, et la première tension est positive par rapport à une valeur de masse.

3. Appareil selon la revendication 1, dans lequel les pixels d'émission de lumière OLED sont des pixels OLED inversés, et la première tension est négative par rapport à une valeur de masse.

EP 2 033 177 B1

4. Appareil selon la revendication 1, dans lequel les transistors (210) d'attaque et les transistors (180) de commutation sont des transistors de type n.
- 5 5. Appareil selon la revendication 1, dans lequel les transistors (210) d'attaque et les transistors (180) de commutation sont des transistors de type p.
6. Appareil selon la revendication 1, dans lequel une tension de sollicitation unique est sélectionnée pour être appliquée ou non appliquée sur la base du signal lié à la tension de seuil.
- 10 7. Appareil selon la revendication 1, dans lequel l'une d'une pluralité de tensions de sollicitation est sélectionnée pour être appliquée sur la base du signal lié à la tension de seuil.
8. Appareil selon la revendication 1, comprenant en outre la fourniture d'un ajustement à une tension de signal pour chaque transistor (210) d'attaque, dans lequel l'ajustement à partir de la tension de seuil initiale est le même pour
15 tous les transistors (210) d'attaque.
9. Appareil selon la revendication 1, dans lequel on détermine le signal lié à la tension de seuil, et on sélectionne la tension de sollicitation pour chaque rangée dans le dispositif d'affichage (250) OLED.

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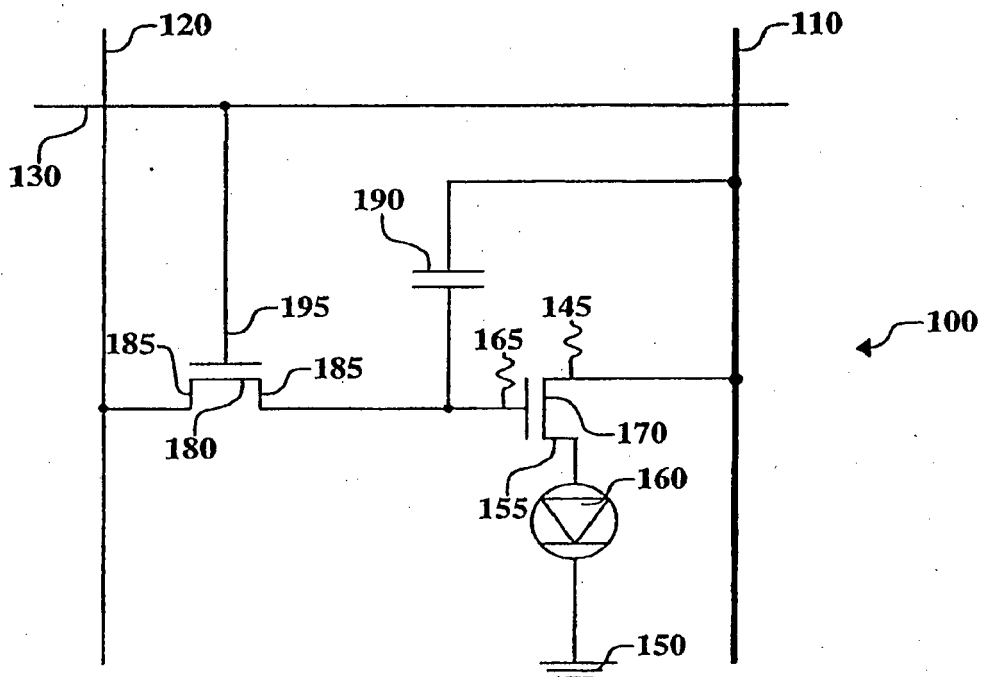


FIG. 1

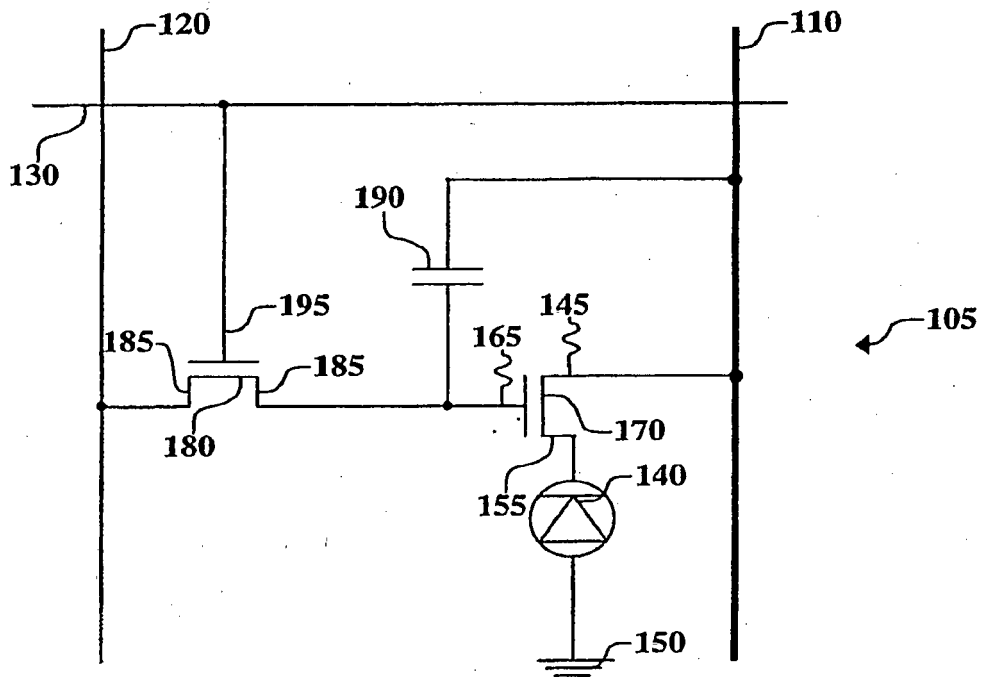


FIG. 2

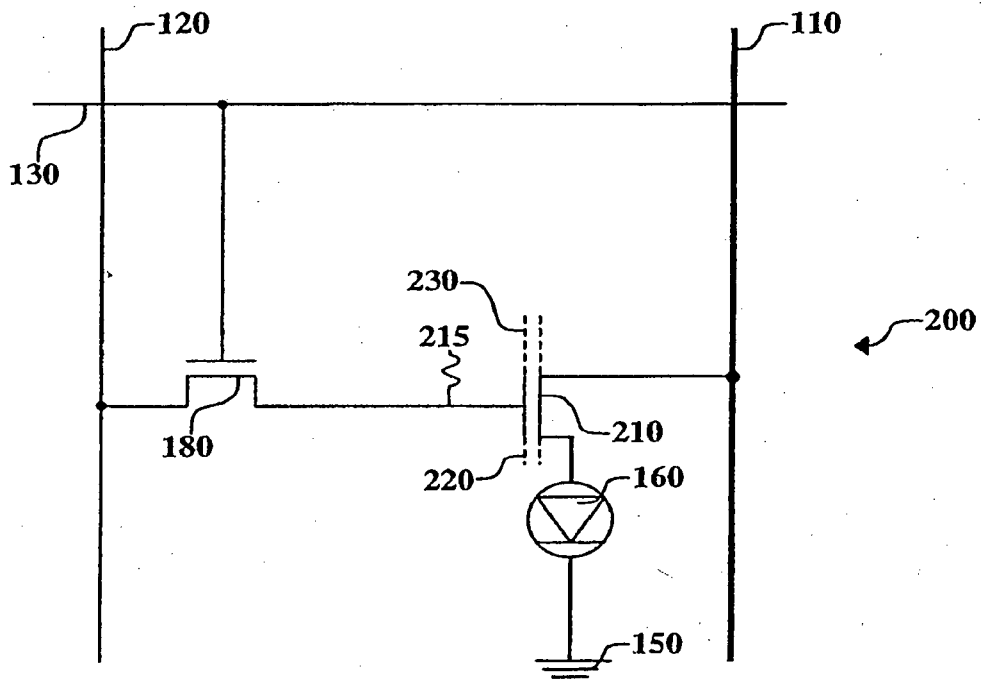


FIG. 3

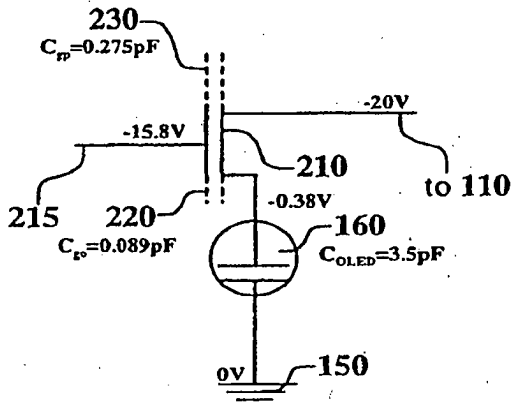


FIG. 4A

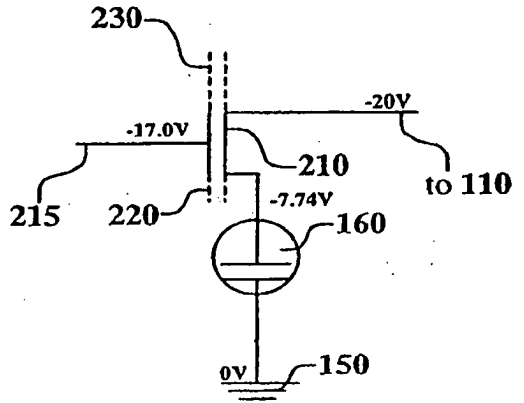


FIG. 4B

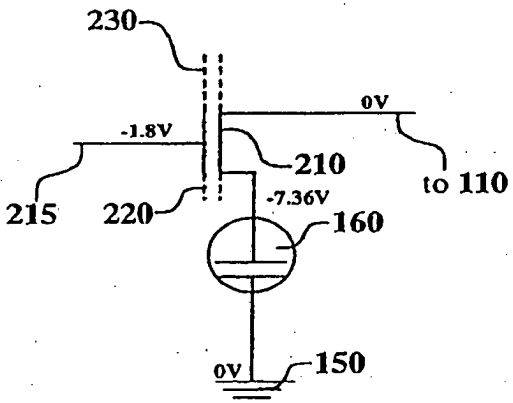


FIG. 4C

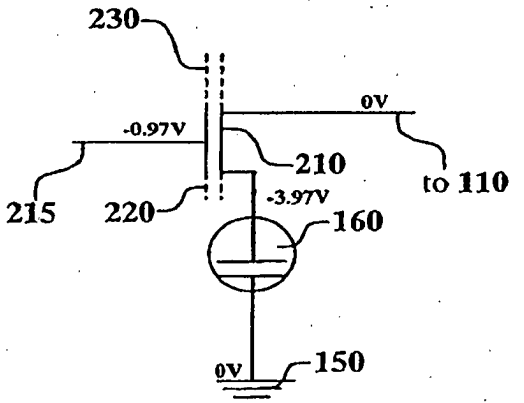


FIG. 4D

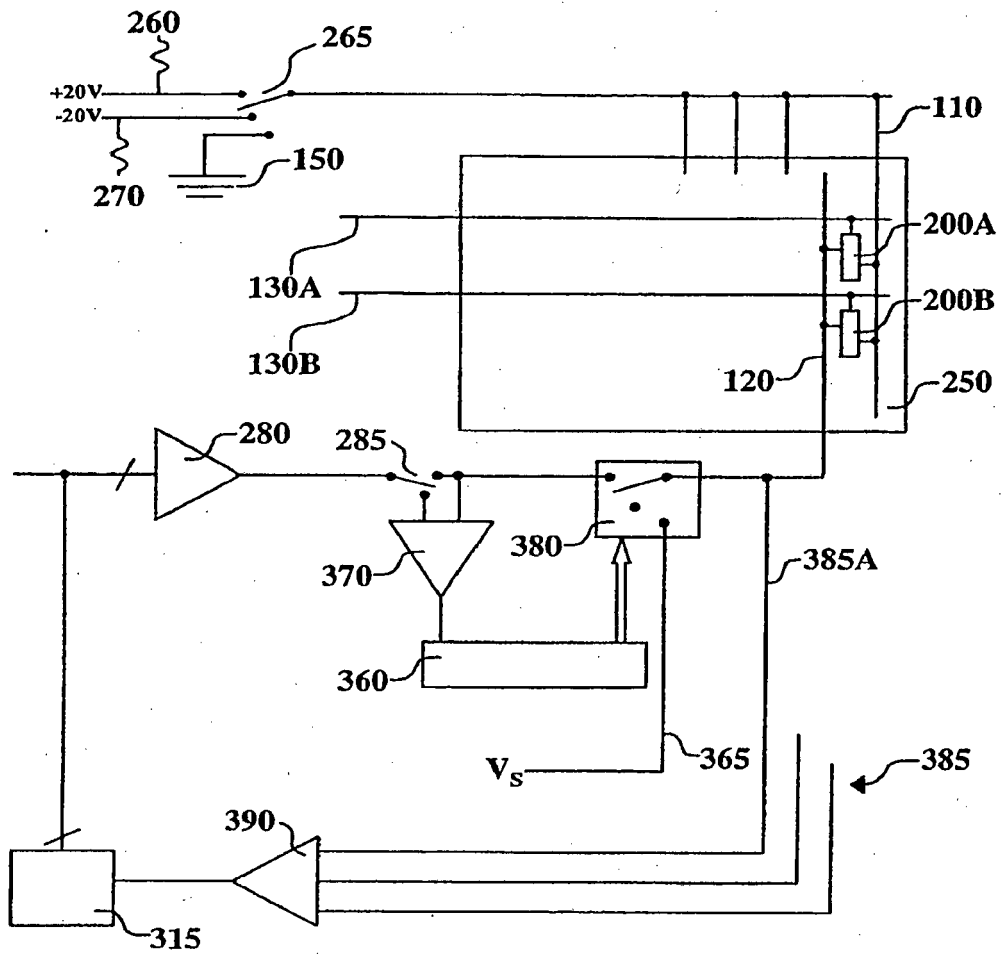


FIG. 5A

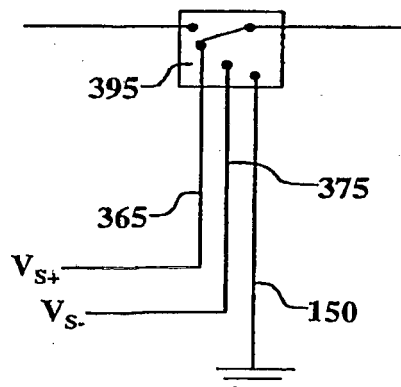


FIG. 5B

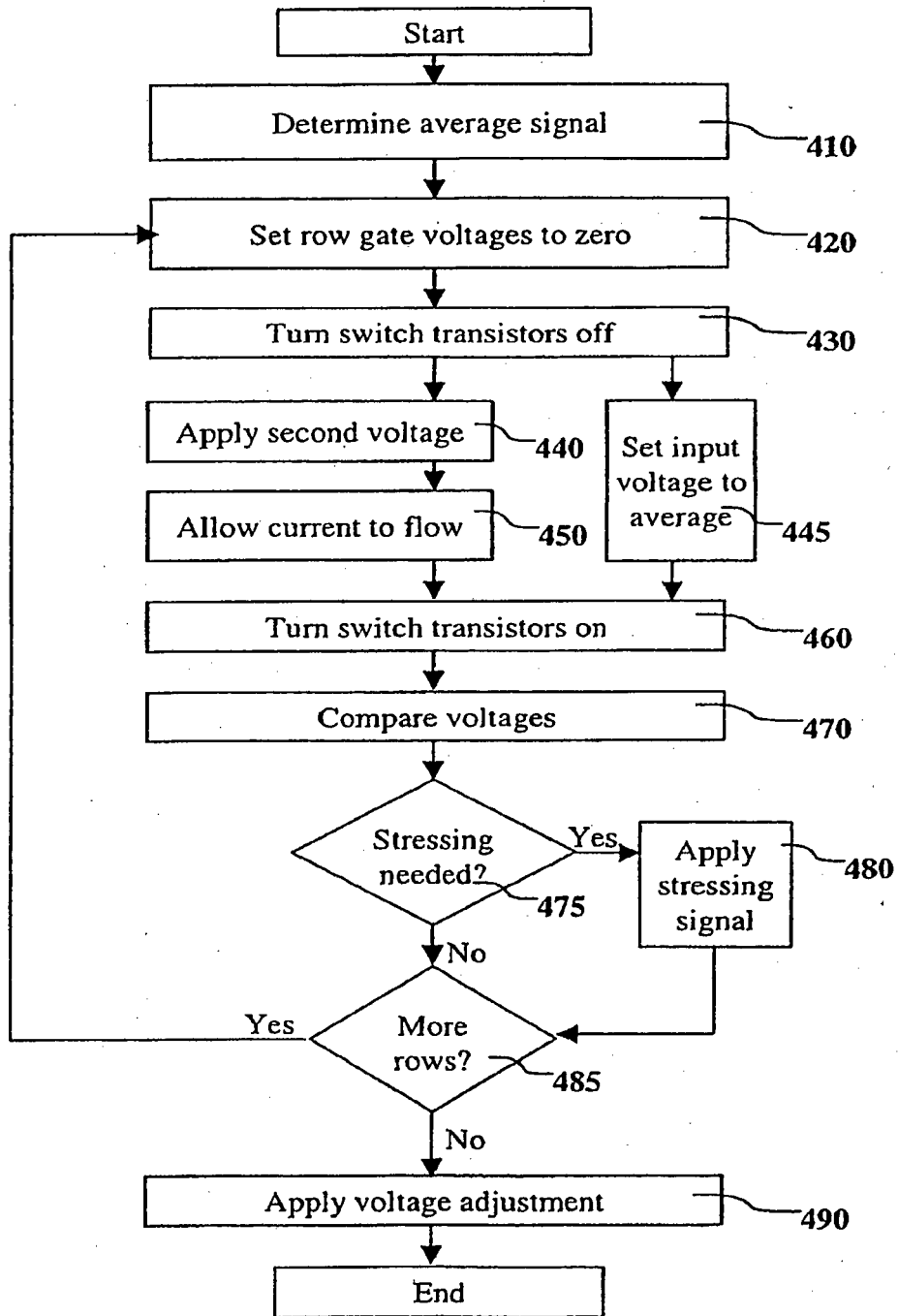


FIG. 6

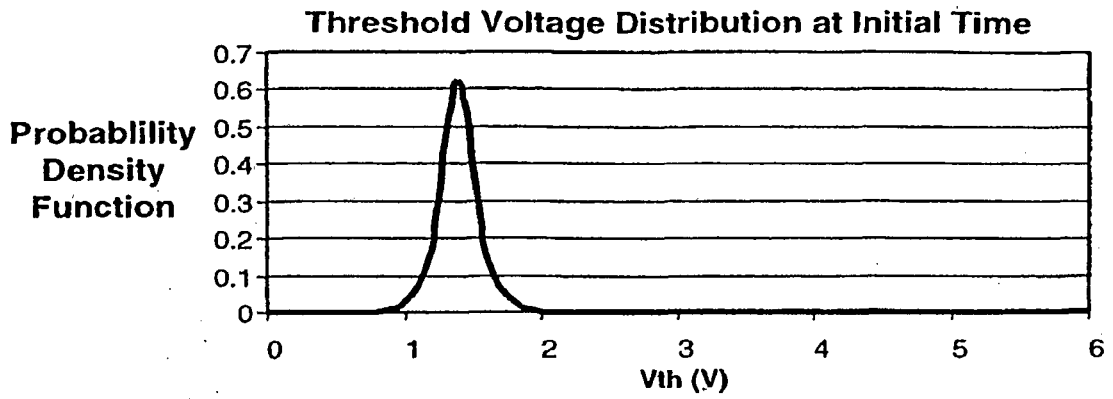


FIG. 7A

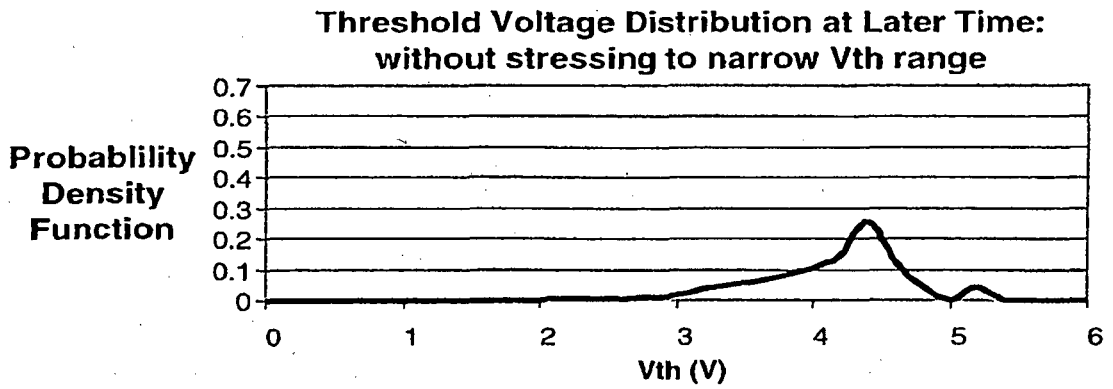


FIG. 7B

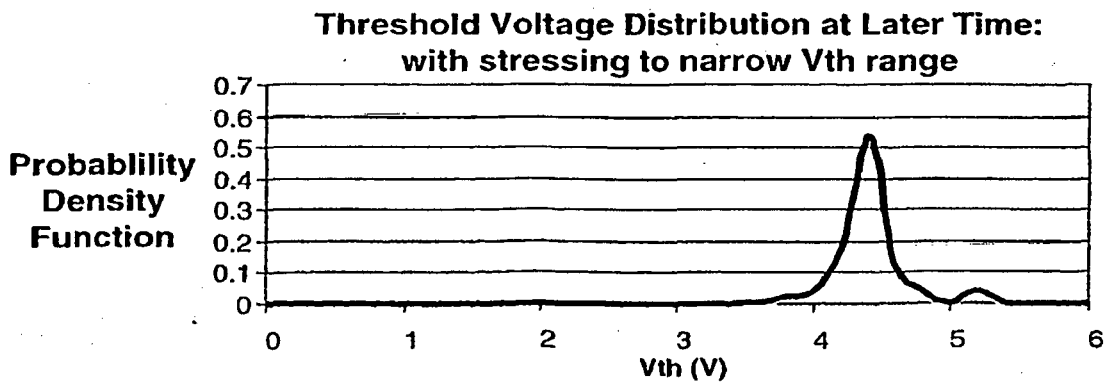


FIG. 7C

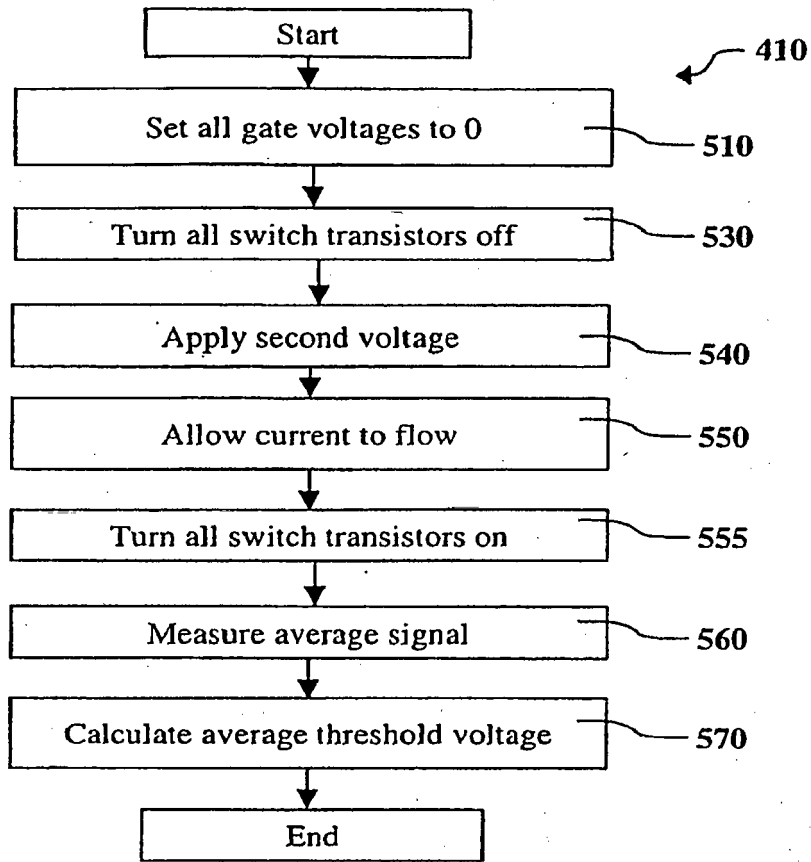


FIG. 8

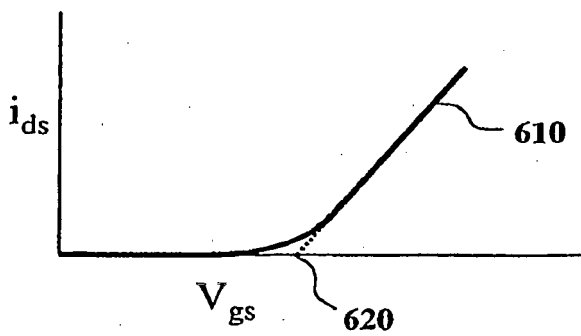


FIG. 9

REFERENCES CITED IN THE DESCRIPTION

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Non-patent literature cited in the description

- **GOH et al.** *IEEE Electron Device Letters*, vol. 24 (9), 583-585 [0005]
- **JUNG et al.** *IMID '05 Digest*, 793-796 [0005]

专利名称(译)	有源矩阵显示补偿		
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[标]申请(专利权)人(译)	伊斯曼柯达公司		
申请(专利权)人(译)	伊士曼柯达公司		
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发明人	HAMER, JOHN WILLIAM PARRETT, GARY		
IPC分类号	G09G3/32		
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优先权	11/427139 2006-06-28 US		
其他公开文献	EP2033177A2		
外部链接	Espacenet		

摘要(译)

公开了一种用于选择应力电压的装置，该应力电压用于补偿具有以阵列布置的多个OLED发光像素的有源矩阵OLED显示器中的像素驱动电路中的驱动晶体管的阈值电压 (V_{th}) 的变化。

