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(54) **DISPLAY SUBSTRATE AND DISPLAY APPARATUS**

(57) A display substrate (DP) and a display apparatus (DA). The display substrate (DP) comprises a base substrate (1); and a pixel defining layer (6), which is provided with a hollow part (61) and which is located above the base substrate (1). The hollow part (61) is enclosedly formed by means of side walls (SW) of the pixel defining layer (6) and a lower portion thereof exposes a part of a conductive surface (51). In the cross-sectional view of the display substrate (DP), the side walls (SW) are shown as two side edges (c) that are located on two opposite

sides of the hollow structure (61) respectively; each side edge (c) at least comprises a first straight line section (c; c1) that directly contacts the conductive surface, a first included angle between the first straight line section (c; c2) and the conductive surface (51) being greater than 20° and less than 35°. As such, uniformity in the deposited thickness of an organic light-emitting layer may be increased, further improving the display quality of the display substrate.

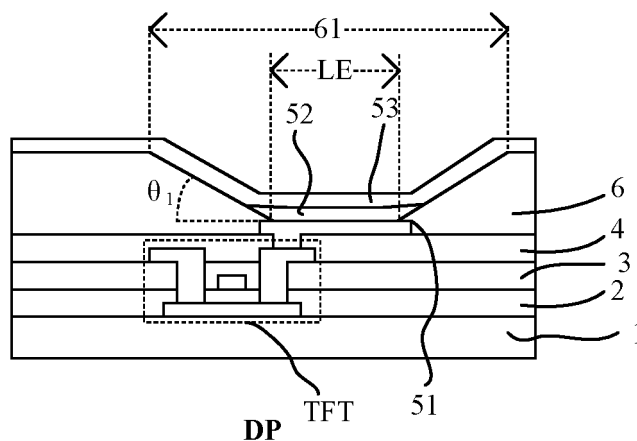


FIG. 3

Description

[0001] The present application claims priority of Chinese Patent Application No. 201721072741.6 filed on August 24, 2017, the disclosure of which is incorporated herein by reference in its entirety as part of the present application.

TECHNICAL FIELD

[0002] Embodiments of the present disclosure relate to a display substrate and a display device.

BACKGROUND

[0003] An Active Matrix Organic Light-Emitting Diode (AMOLED) display has a wide market application, due to advantages such as self-illumination, fast response, wide viewing angle, high brightness, brilliant colors, lightness and thinness, as compared with a traditional liquid crystal display. One of core structures of the AMOLED display is an OLED element, in which a light emitting region of a pixel is defined with a pixel defining layer having a hollow portion.

SUMMARY

[0004] According to embodiments of the disclosure, a display substrate is provided. The display substrate comprises: a base substrate; and a pixel defining layer having a hollow portion and located above the base substrate. The hollow portion is enclosed by sidewalls of the pixel defining layer, and exposes a portion of a conductive surface at a bottom of the hollow portion; in a cross-sectional view of the display substrate, the sidewalls are respectively indicated by two side edges located on two opposite sides of the hollow portion, each of the side edges at least includes a first straight line segment in direct contact with the conductive surface, and a first included angle between the first straight line segment and the conductive surface is greater than 20° and less than 35°.

[0005] In one example, the first included angle is further greater than 23° and less than 30°.

[0006] In one example, the hollow portion is symmetrical about a symmetry axis.

[0007] In one example, in the cross-sectional view of the display substrate, the first straight line segment meets a surface of the pixel defining layer facing away from the base substrate.

[0008] In one example, in the cross-sectional view of the display substrate, each of the side edges further includes a second straight line segment that meets a surface of the pixel defining layer facing away from the base substrate and a third straight line segment connecting the first straight line segment and the second straight line segment.

[0009] In one example, the second straight line seg-

ment is parallel to the first straight line segment, and the third straight line segment is parallel to the conductive surface.

[0010] In one example, the display substrate has a light emitting region, and an orthogonal projection of the portion of the conductive surface exposed by the hollow portion of the pixel defining layer on the base substrate completely coincides with an orthogonal projection of the light emitting region on the base substrate.

[0011] In one example, the pixel defining layer has a thickness of 1.0 μm to 3.0 μm.

[0012] In one example, the display substrate further comprises: a driving unit provided on the base substrate; a passivation layer covering the driving unit; a first electrode provided on the passivation layer, and the conductive surface being a surface of the first electrode facing away from the base substrate; an organic light emitting layer provided within the hollow portion; and a second electrode covering the organic light emitting layer and the pixel defining layer.

[0013] According to embodiments of the disclosure, a display substrate is provided. The display substrate comprises: a base substrate; and a pixel defining layer having a hollow portion and located above the base substrate.

The hollow portion is enclosed by sidewalls of the pixel defining layer, and exposes a portion of a conductive surface at a bottom of the hollow portion; in a cross-sectional view of the display substrate, the sidewalls include two side edges respectively located on two opposite sides of the hollow structure, each of the side edges is an arc-shaped line segment, the arc-shaped line segment has a first end point that meets the conductive surface and a second end point that meets a surface of the pixel defining layer facing away from the base substrate, a second included angle between a virtual straight line connecting the first end point and the second end point and the conductive surface is greater than 20°, and a third included angle between a tangent line at any point on the arc-shaped line segment and the conductive surface is less than 35°.

[0014] According to the embodiments of the disclosure, the second included angle is greater than 23°, and the third included angle is less than 30°.

[0015] According to the embodiments of the disclosure, the virtual straight connection line is located inside the hollow portion; or, the virtual straight connection line is located outside the hollow portion.

[0016] According to the embodiments of the disclosure, the display substrate has a light emitting region, and an orthogonal projection of the portion of the conductive surface exposed by the hollow portion of the pixel defining layer on the base substrate completely coincides with an orthogonal projection of the light emitting region on the base substrate.

[0017] According to the embodiments of the disclosure, the pixel defining layer has a thickness of 1.0 μm to 3.0 μm.

[0018] According to the embodiments of the disclo-

sure, the display substrate further comprises: a driving unit provided on the base substrate; a passivation layer covering the driving unit; a first electrode provided on the passivation layer, and the conductive surface being a surface of the first electrode facing away from the base substrate; an organic light emitting layer provided within the hollow portion; and a second electrode covering the organic light emitting layer and the pixel defining layer.

[0019] According to the embodiments of the disclosure, a display device is provided. The display device comprises any one of the display substrates as described above.

BRIEF DESCRIPTION OF THE DRAWINGS

[0020] In order to more clearly illustrate the technical solutions of the embodiments of the present disclosure, the drawings used in the description of the embodiments will be briefly described below. It is obvious that the drawings in the following description are only some embodiments of the present disclosure, those skilled in the art can obtain other drawings based on these drawings without any inventive work.

FIG. 1 is a cross-sectional structural schematic diagram of an AMOLED display;

FIG. 2 is a cross-sectional structural schematic diagram of a display substrate provided by embodiments of the present disclosure;

FIG. 3 is a cross-sectional structural schematic diagram of the display substrate provided by the embodiments of the present disclosure;

FIG. 4 is a cross-sectional structural schematic diagram of the display substrate provided by the embodiments of the present disclosure;

FIG. 5 is a cross-sectional structural schematic diagram of the display substrate provided by the embodiments of the present disclosure;

FIG. 6 is a cross-sectional structural schematic diagram of the display substrate provided by the embodiments of the present disclosure;

FIG. 7 is a cross-sectional structural schematic diagram of the display substrate provided by the embodiments of the present disclosure;

FIG. 8 is a cross-sectional structural schematic diagram of the display substrate provided by the embodiments of the present disclosure; and

FIG. 9 is a schematic diagram of a display device provided by the embodiments of the present disclosure.

DETAILED DESCRIPTION

[0021] Hereinafter, the technical solutions in the embodiments of the present disclosure will be described in a clearly and fully understandable way in connection with the drawings related to the embodiments of the present disclosure. It is obvious that the described embodiments

are just a part but not all of the embodiments of the present disclosure. Based on the described embodiments herein, those skilled in the art can obtain other embodiment(s), without any inventive work, which should be within the scope of the present disclosure.

[0022] It should be noted that, unless otherwise defined, all terms used herein (including technical and scientific terms) have a same meaning as commonly understood by those skilled in the art to which the present disclosure pertains. It will be further understood that terms, such as those defined in commonly used dictionaries, should be interpreted as having a meaning that is consistent with their meaning in the context of the relevant art and will not be interpreted in an ideal or excessively formal sense unless clearly defined in the present specification.

[0023] For example, in the specification and claims of the present disclosure, words such as "first", "second" and the like do not denote any order, quantity, or importance, but rather are used for distinguishing different components. Words such as "include" or "comprise" and the like denote that elements or objects appearing before the words of "include" or "comprise" cover the elements or the objects enumerated after the words of "include" or "comprise" or equivalents thereof, not exclusive of other elements or objects. Directional or positional relationships shown by terms such as "upper/above", "lower/below" are directional or positional relationships shown as in the drawings, which only means to facilitate description of the technical solution or the present disclosure and simplify the description, but do not indicate or imply that the devices or components must have specific directions, or be constructed or operated in the specific directions, and are not limitative of the present disclosure.

[0024] Embodiments of the present disclosure provide a display substrate and a display device; in a case where an organic light emitting layer is deposited within a hollow portion of a pixel defining layer, a specific angle of a side edge of the pixel defining layer is designed to weaken an extent to which the organic light emitting layer slides and is accumulated by gravity at corners of the hollow portion, so as to improve evenness of a thickness of the organic light emitting layer, and further improve a display quality of the display substrate.

[0025] As shown in FIG. 1, it is a structural schematic diagram of an AMOLED display. A thin film transistor (TFT, denoted as TFT in the diagram) for driving an OLED element is formed on a base substrate 1; a gate insulating layer 2 for isolating a gate electrode of the TFT from a semiconductor layer of the TFT, and an interlayer insulating layer 3 for isolating the gate electrode from a source electrode and a drain electrode of the TFT are further formed on the base substrate 1. A passivation layer 4 for covering the TFT is formed on the TFT. A via hole that exposes the drain electrode of the TFT is formed in the passivation layer 4. An anode 51 of the OLED element is formed on the passivation layer 4 and is in electrical connection with the drain electrode through the above-

described via hole. A pixel defining layer 6 is located on the passivation layer 4 and the anode 51, and a hollow portion 61 (i.e., an opening) of the pixel defining layer 6 exposes a portion of the anode 51. An organic light emitting layer 52 of the OLED element is deposited on the anode 51, and a cathode 53 of the OLED element covers the organic light emitting layer 52 and the pixel defining layer 6.

[0026] The organic light emitting layer 52 is usually deposited within the hollow portion 61 of the pixel defining layer 6 in a mode of evaporation, spin coating or ink jet printing; in this case, with reference to FIG. 1, an included angle (denoted as δ in the diagram) between a side edge of the pixel defining layer 6 and a surface of the substrate is relatively large, that is, a slope of the hollow portion 61 of the pixel defining layer 6 is relatively steep, thus the organic light emitting layer 52, subjected to a gravity action, is apt to accumulate at corners of the hollow portion 61 during deposition (as shown by a portion circled by a dotted line in the diagram), so that a thickness of a portion of the organic light emitting layer 52 at a boundary between the pixel defining layer 6 and the anode 51 is larger than a thickness of a portion of the organic light emitting layer 52 at a central region of the anode 51, resulting in brighter luminescence in the central region and darker luminescence at the boundary between the pixel defining layer 6 and the anode 51, causing uneven brightness of the OLED element, and affecting the display quality.

[0027] As shown in FIG. 2, the embodiments of the present disclosure provide a display substrate, comprising a base substrate 1, and a pixel defining layer 6 provided above the base substrate 1; the pixel defining layer 6 including a plurality of hollow portions 61 (only one is shown in the diagram) spaced from each other; and along a direction perpendicular to the base substrate 1, a cross-sectional view of the hollow portion 61 including: a first edge a and a second edge b parallel to the base substrate 1 and opposite to each other, two side edges c respectively connecting the first edge a and the second edge b. The first edge a is larger than the second edge b, and the second edge b is between the base substrate 1 and the first edge a; and an included angle between the side edge c and an extension line of the second edge b is a first included angle (denoted as θ_1 in the diagram), and the first included angle θ_1 is greater than 20° and less than 35° . Here, the first edge a and the second edge b are both virtual edges instead of actually existing edges.

[0028] The pixel defining layer 6 is located above the base substrate 1, and other structural layers may be further provided therebetween.

[0029] In a case where the above-described first included angle θ_1 is less than 35° , a sliding effect of an organic light emitting layer by gravity is relatively small, so that the organic light emitting layer deposited within the hollow portion 61 has an even thickness. It is considered that, with respect to the pixel defining layer 6 having a certain area, in a case where the number of hollow portions 61 is determined, the smaller the angle of the

side edge c of the hollow portion 61 is, the smaller the effective light-emitting area of the hollow portion 61 (i.e., corresponding to a pixel region of the display substrate) is; and thus, in consideration of a requirement on the pixel light-emitting area after the pixel defining layer 6 is applied to the display substrate, the above-described first included angle θ_1 is greater than 20° while being less than 35° .

[0030] The above-described first included angle θ_1 is further greater than 23° and less than 30° .

[0031] Under the premise that the first included angle θ_1 between the side edge c and the extension line of the second edge b is greater than 20° and less than 35° , with respect to any one of the hollow portions 61 which is a stereoscopic region, there are a plurality of cross-sectional views thereof along the direction perpendicular to the base substrate 1, the first included angles θ_1 , each of which is between the side edge c and the extension line of the second edge b, in the cross-sectional views of any one of the hollow portions 61 are the same or different. In consideration of simplification of process, the first included angles θ_1 , each of which is between the side edge c and the extension line of the second edge b, in the cross-sectional views of any one hollow portion 61 are set to a same value. For example, the hollow portion 61 is symmetrical about a symmetry axis AX. The symmetry axis AX is perpendicular to the base substrate 1.

[0032] With respect to different hollow portions 61 within a same cross section, the first included angles θ_1 , each of which is between the side edge c and the extension line of the second edge b, in the cross-sectional views of the hollow portions 61 are the same or different. In consideration of simplification of process, the first included angles θ_1 , each of which is between the side edge c and the extension line of the second edge b, in the cross-sectional views of the hollow portions 61 are set to a same value. Further, on the above-described basis, as shown in FIG. 3, there are a plurality of light emitting regions LE (only one is shown in FIG. 3) comprised in the above-described display substrate. The hollow portions 61 of the pixel defining layer 6 shown in FIG. 2 are in one-to-one correspondence with the light emitting regions LE.

[0033] The above-described display substrate is, for example, an array substrate; and with reference to FIG. 3, the above-described display substrate further comprises: a driving unit (denoted as TFT in the diagram) provided on the base substrate 1; a passivation layer 4 covering the driving unit TFT. A gate electrode of the TFT is isolated by a gate insulating layer 2 from a semiconductor layer of the TFT, and the gate electrode of the TFT is isolated by an interlayer insulating layer 3 from a source electrode and a drain electrode of the TFT. The above-described display substrate for example further comprises a planarization layer (not shown) provided on the passivation layer 4. The above-described display substrate for example further comprises: a plurality of anodes 51

spaced from each other and provided on the passivation layer 4, the hollow portion 61 of the above-described pixel defining layer 6 corresponding to a portion of the anode 51; an organic light emitting layer 52 provided within the hollow portion 61; and a cathode 53 covering the organic light emitting layer 52 and the pixel defining layer 6.

[0034] Here, since the first edge a is larger than the second edge b and the second edge b is located between the base substrate 1 and the first edge a, the first edge a is necessarily an upper edge in the cross-sectional view of the hollow portion 61 facing away from the base substrate 1, and the opposite second edge b is necessarily a lower edge in the cross-sectional view of the hollow portion 61 close to the base substrate 1, that is, the cross-sectional view of the hollow portion 61 is an inverted trapezoid.

[0035] In consideration of a fact that the organic light emitting layer 52 to be deposited needs to have a certain thickness, a thickness of the pixel defining layer 6 is, for example, 1.0 μm to 3.0 μm , so as to provide a deposition space with a certain depth for the organic light emitting layer 52.

[0036] As shown in FIG. 4 or FIG. 5, the embodiments of the present disclosure provides the display substrate, comprising the base substrate 1, and the pixel defining layer 6 provided above the base substrate 1; the pixel defining layer 6 including the plurality of hollow portions 61 (only one is shown in the diagram) spaced from each other; and along the direction perpendicular to the base substrate 1, the cross-sectional view of the hollow portion 61 including: a first edge a and a second edge b parallel to the base substrate 1 and opposite to each other, and two minor arc-shaped side edges c respectively connecting the first edge a and the second edge b. The first edge a is larger than the second edge b, and the second edge b is located between the base substrate 1 and the first edge a; the side edge c includes a first end point (denoted as A in the diagram) connected with the first edge a, and a second end point (denoted as B in the diagram) connected with the second edge b; an included angle between a line connecting the first end point A and the second end point B and the extension line of the second edge b is a second included angle (denoted as θ_2 in the diagram), and the second included angle θ_2 is greater than 20° ; and an included angle between a tangent line at any point on the side edge c and the extension line of the second edge b is a third included angle θ_3 (denoted as θ_3 in the diagram), and the third included angle θ_3 is less than 35° . Here, both the first edge a and the second edge b are virtual edges instead of actually existing edges.

[0037] It should be noted that, the pixel defining layer 6 is located above the base substrate 1, and other structural layers may be further provided therebetween.

[0038] The pixel defining layer 6 for example is made of a photoresist resin material, in a case where the hollow portion 61 of the pixel defining layer 6 is formed in a mode of exposure and development, the side edge of the pixel

defining layer 6 is not a perfect straight line but a curved minor arc (i.e., a center angle corresponding to the arc is less than 180°). The second included angle θ_2 between the connection line of the two end points and the extension line of the second edge b is greater than 20° , and the third included angle θ_3 between the tangent line at any point on the minor arc and the extension line of the second edge b is less than 35° , so the sliding effect of the organic light emitting layer at the side edge of the hollow portion 61 by gravity is relatively small, so that the organic light emitting layer deposited within the hollow portion 61 has an even thickness.

[0039] The above-described second included angle θ_2 is further greater than 23° , and the above-described third included angle θ_3 is further less than 30° .

[0040] Under the premise that the second included angle θ_2 between the connection line of the two end points and the extension line of the second edge b is greater than 20° and the third included angle θ_3 between the tangent line at any point on the minor arc and the extension line of the second edge b is less than 35° , with respect to any one of the hollow portions 61 which is the stereoscopic region, there are the plurality of cross-sectional views thereof along the direction perpendicular to the base substrate 1, the second included angles θ_2 , each of which is between the connection line of two end points of the side edge c and the extension line of the second edge b, in the cross-sectional views of any one of the hollow portions 61 are the same or different, and the third included angles θ_3 , each of which is between the tangent line at any point on the side edge c and the extension line of the second edge b, in the cross-sectional views of any one of the hollow portions 61 are the same or different. In consideration of simplification of process, the second included angles θ_2 , each of which is between the connection line of two end points of the side edge c and the extension line of the second edge b, in the cross-sectional views of any one of the hollow portions 61 are set to a same value, and the third included angles θ_3 , each of which is between the tangent line at any point on the side edge c and the extension line of the second edge b, in the cross-sectional views of any one of the hollow portions 61 are set to a same value.

[0041] With respect to different hollow portions 61 within the same cross section, the second included angles θ_2 , each of which is between the connection line of two end points of the side edge c and the extension line of the second edge b, in the cross-sectional views of the hollow portions 61 are the same or different, and the third included angles θ_3 , each of which is between the tangent line at any point on the side edge c and the extension line of the second edge b, in the cross-sectional views of the hollow portions 61 are the same or different. In consideration of simplification of process, the second included angles θ_2 , each of which is between the connection line of two end points of the side edge c and the extension line of the second edge b, in the cross-sectional views of the hollow portions 61 are set to a same value,

and the third included angles θ_3 , each of which is between the tangent line at any point on the side edge c and the extension line of the second edge b, in the cross-sectional views of the hollow portions 61 are set to a same value. For example, the hollow portion 61 is symmetrical about the symmetry axis AX. The symmetry axis AX is perpendicular to the base substrate 1.

[0042] With reference to FIG. 4, the side edge c is recessed toward a region where the first edge a and the second edge b are opposite to each other, that is, is recessed inward with respect to the hollow portion 61, and the connection line of the two end points of the minor arc is located outside the hollow portion 61.

[0043] For example, with reference to FIG. 5, the side edge c protrudes from the region where the first edge a and the second edge b are opposite to each other, that is, protrudes outward with respect to the hollow portion 61, and the connection line of the two end points of the minor arc is located inside the hollow portion 61.

[0044] Here, the connection line of the two end points of the minor arc is a virtual straight line.

[0045] Further, on the above-described basis, as shown in FIG. 6, there are the plurality of light emitting regions LE (only one is shown in FIG. 6) comprised in the above-described display substrate. The hollow portions 61 of the pixel defining layer 6 shown in FIG. 4 or FIG. 5 are in one-to-one correspondence with the light emitting regions LE.

[0046] The above-described display substrate for example is the array substrate; and with reference to FIG. 6, the display substrate comprises: the driving unit (denoted as TFT in the diagram) provided on the base substrate 1; and the passivation layer 4 covering the driving unit TFT. The gate electrode of the TFT is isolated by the gate insulating layer 2 from the semiconductor layer of the TFT, and the gate electrode of the TFT is isolated by the interlayer insulating layer 3 from the source electrode and the drain electrode of the TFT. The above-described display substrate for example further comprises the planarization layer (not shown) provided on the passivation layer 4. The above-described display substrate for example further comprises: the plurality of anodes 51 spaced from each other and provided on the passivation layer 4, the hollow portion 61 of the above-described pixel defining layer 6 corresponding to a portion of the anode 51; the organic light emitting layer 52 provided within the hollow portion 61; and the cathode 53 covering the organic light emitting layer 52 and the pixel defining layer 6.

[0047] Here, description is provided in the above-described FIG. 6 merely with the pixel defining layer 6 recessed inward with respect to the hollow portion 61 as illustrated in the foregoing FIG. 4 as an example, which is equally applicable to the pixel defining layer 6 protruding outward with respect to the hollow portion 61 as illustrated in FIG. 5, and will not be repeated here. Here, since the first edge a is larger than the second edge b, and the second edge b is located between the base substrate 1 and the first edge a, the first edge a is necessarily the

upper edge in the cross-sectional view of the hollow portion 61 facing away from the base substrate 1, and the opposite second edge b is necessarily the lower edge in the cross-sectional view of the hollow portion 61 close to the base substrate 1, that is, the cross-sectional view of the hollow portion 61 is an inverted trapezoid whose side edges are recessed minor arcs or protruding minor arcs.

[0048] For example, in consideration of the fact that the organic light emitting layer 52 to be deposited needs to have a certain thickness, the thickness of the pixel defining layer 6 is, for example, 1.0 μm to 3.0 μm , so as to provide the deposition space with a certain depth for the organic light emitting layer 52.

[0049] As shown in FIG. 7, the embodiments of the present disclosure provide the display substrate, comprising the base substrate 1, and the pixel defining layer 6 provided above the base substrate 1; the pixel defining layer 6 including the plurality of hollow portions 61 (only one is shown in the diagram) spaced from each other and at least one step provided inside the hollow portion 61; along the direction perpendicular to the base substrate 1, the cross-sectional view of the hollow portion 61 including: the first edge a and the second edge b provided parallel to the base substrate 1 and opposite to each other, two polyline-shaped side edges c respectively connecting the first edge a and the second edge b. The first edge a is larger than the second edge b, and the second edge b is located between the base substrate 1 and the first edge a; the side edge c includes at least one first line segment c1 parallel to the base substrate 1 and at least two second line segments c2; and included angles respectively between the second line segments c2 and the extension line of the second edge b are fourth included angles (denoted as θ_4 in the diagram), each of the fourth included angles θ_4 is greater than 20° and less than 35° . Here, the first edge a and the second edge b are both virtual edges instead of actually existing edges.

[0050] The pixel defining layer 6 is located above the base substrate 1, and other structural layers may be further provided therebetween.

[0051] With reference to FIG. 7, description is provided in the embodiments of the present disclosure merely with a case where the number of steps inside the above-described hollow portion 61 is one as an example, that is, in the cross-sectional view of the hollow portion 61, the side edge c includes one first line segment c1 parallel to the layer plane (i.e., a cross section of the step presented as a line segment) and two second line segments c2; the number of steps will not be limited in the embodiments of the present disclosure, and the specific number of steps may be flexibly designed according to a size of the hollow portion 61 and a weakening effect on the sliding of the organic light emitting layer.

[0052] In a case where the organic light emitting layer of the OLED element is formed within the hollow portion 61 of the above-described pixel defining layer 6, the organic light emitting layer has a portion provided on the step, and the step protruding toward the inside of the

hollow portion 61 weakens an extent to which the organic light emitting layer slides and is accumulated by gravity, so that the portion of the organic light emitting layer at the boundary of the anode and the pixel defining layer 6 and the portion of the organic light emitting layer at the central region of the anode have even thicknesses, which further improves the display quality.

[0053] In a case the fourth included angle θ_4 is less than 35° , the sliding effect of the organic light emitting layer caused by gravity is relatively small, so that the organic light emitting layer deposited within the hollow portion 61 has an even thickness. It is considered that, with respect to the pixel defining layer 6 having a certain area, in a case where the number of hollow portions 61 is determined, the smaller the inclined angle of the second line segment c2 of the side edge c of the hollow portion 61 is, the smaller the effective light-emitting area of the hollow portion 61 (i.e., corresponding to the pixel region of the display substrate) is; and thus, in consideration of the requirement on the pixel light-emitting area after the pixel defining layer 6 is applied to the display substrate, the above-described fourth included angle θ_4 is greater than 20° while being less than 35° . The above-described fourth included angle θ_4 is further greater than 23° and less than 30° .

[0054] Under the premise that the fourth included angle θ_4 between the second line segment c2 and the extension line of the second edge b is greater than 20° and less than 35° , with respect to any one of the hollow portions 61, the fourth included angles θ_4 respectively between the second line segments c2 of each side edge c and the extension line of the second edge b are the same or different. In consideration of simplification of process, the fourth included angle θ_4 respectively between the second line segments c2 of each side edge c and the extension line of the second edge b of any one hollow portion 61 are set to a same value, that is, the second line segments c2 of a same side edge c are parallel to one another.

[0055] With respect to any one of the hollow portions 61 which is the stereoscopic region, there are a plurality of cross-sectional views thereof along the direction perpendicular to the base substrate 1, the fourth included angles θ_4 , each of which is between the second line segment c2 of the side edge c and the extension line of the second edge b, in the cross-sectional views of any one of the hollow portions 61 are the same or different. In consideration of simplification of process, the fourth included angles θ_4 , each of which is between the second line segment c2 of the side edge c and the extension line of the second edge b, in the cross-sectional views of any one of hollow portions 61 are set to a same value.

[0056] With respect to different hollow portions 61 within the same cross section, the fourth included angles θ_4 , each of which is between the second line segment c2 of the side edge c and the extension line of the second edge b, in the cross-sectional views of the hollow portions 61 are the same or different. In consideration of simplifica-

tion of process, the fourth included angles θ_4 , each of which is between the second line segment c2 of the side edge c and the extension line of the second edge b, in the cross-sectional views of the hollow portions 61 are set to a same value. For example, the hollow portion 61 is symmetrical about the symmetry axis AX. The symmetry axis AX is perpendicular to the base substrate 1.

[0057] Further, on the above-described basis, as shown in FIG. 8, there are the plurality of light emitting regions LE (only one is shown in FIG. 8) comprised in the above-described display substrate. The hollow portions 61 of the pixel defining layer 6 shown in FIG. 7 are in one-to-one correspondence with the light emitting regions LE.

[0058] The above-described display substrate for example is the array substrate; and with reference to FIG. 8, the display substrate comprises: the driving unit (denoted as TFT in the diagram) provided on the base substrate 1; and the passivation layer 4 covering the driving unit TFT. The gate electrode of the TFT is isolated by the gate insulating layer 2 from the semiconductor layer of the TFT, and the gate electrode of the TFT is isolated by the interlayer insulating layer 3 from the source electrode and the drain electrode of the TFT. The above-described display substrate for example further comprises the planarization layer (not shown) provided on the passivation layer 4. The above-described display substrate for example further comprises: the plurality of anodes 51 spaced from each other and provided on the passivation layer 4, the hollow portion 61 of the above-described pixel defining layer 6 corresponding to a portion of the anode 51; the organic light emitting layer 52 provided within the hollow portion 61; and the cathode 53 covering the organic light emitting layer 52 and the pixel defining layer 6.

[0059] The organic light emitting layer 52 has a portion located on the step inside the hollow portion 61, and the step weakens an extent to which the organic light emitting layer 52 slides and is accumulated by gravity, so that the portion of the organic light emitting layer 52 at the boundary of the anode 51 and the pixel defining layer 6 and the portion of the organic light emitting layer 52 at the central region of the anode 51 have even thicknesses, which further improves the display quality.

[0060] Here, since the first edge a is larger than the second edge b, the step is located inside the hollow portion 61 and the second edge b is located between the base substrate 1 and the first edge a, the first edge a is necessarily the upper edge in the cross-sectional view of the hollow portion 61 facing away from the base substrate 1, and the opposite second edge b is necessarily the lower edge in the cross-sectional view of the hollow portion 61 close to the base substrate 1, that is, the cross-sectional view of the hollow portion 61 is an inverted trapezoid whose side edges have steps.

[0061] For example, in consideration of the fact that the organic light emitting layer 52 to be deposited needs to have a certain thickness, the thickness of the pixel defining layer 6 is, for example, $1.0\ \mu\text{m}$ to $3.0\ \mu\text{m}$, so as

to provide the deposition space with a certain depth for the organic light emitting layer 52.

[0062] In the above-described respective embodiments, the above-described display substrate further comprises a gate line and a gate line lead wire provided in a same layer as the gate electrode of the TFT; and a data line and a data line lead wire provided in a same layer as the source electrode and the drain electrode of the TFT. A pattern layer including the gate line, the gate electrode of the TFT and the gate line lead wire, and a pattern layer including the data line, the source electrode and the drain electrode of the TFT, and the data line lead wire for example are made of copper (Cu), aluminum (Al), molybdenum (Mo), titanium (Ti), chromium (Cr), tungsten (W), or other metal element materials, or an alloy of these materials. A specific structure of the above-described respective pattern layers for example is a single-layer structure, or a multilayer structure, for example, Mo\Al\Mo, Ti\Cu\Ti, Mo\Ti\Cu, or other stacked structures as designed.

[0063] The TFT for example is an amorphous silicon TFT, a poly-silicon TFT, or an oxide semiconductor TFT; and a structure thereof for example is a top gate type or a bottom gate type.

[0064] For example, in some TFT structures, the gate electrode is a portion of the gate line (i.e., there is no independent gate electrode pattern branching from the gate line); the source electrode is a portion of the data line (i.e., there is no independent source electrode pattern branching from the data line); and a specific structure of the TFT will not be limited in the embodiments of the present disclosure, which may follow various TFT structures in the prior art, or a type and/or a structure of the TFT is flexibly selected according to a specific structural requirement on the above-described display substrate.

[0065] The gate insulating layer 2 for example is made of silicon nitride or silicon oxide, and a specific structure thereof for example is a single-layer structure, or a multilayer structure, for example, silicon oxide/silicon nitride stacked as designed.

[0066] The passivation layer 4 covering the TFT for example is made of silicon nitride or silicon oxide, and a structure thereof for example is a single-layer structure, or a multilayer structure, for example, silicon oxide/silicon nitride stacked as designed.

[0067] The planarization layer (not shown in the above-described accompanying drawings) for example is made of a resin material.

[0068] The anode 51 for example is made of indium tin oxide (ITO), indium zinc oxide (IZO), fluorine-doped tin oxide (FTO) or other transparent metal oxide conductive materials, or a multilayer stacked structure of the above-described transparent metal oxide conductive material and a metal material (for example, silver (Ag)), for example, it is prepared with three layers of ITO\Ag\ITO.

[0069] The cathode 53 for example is made of aluminum (Al) and/or silver (Ag), or other metal materials, and

a structure thereof for example is a single-layer structure or a multilayer stacked structure.

[0070] In the above-described embodiments, the hollow portion 61 is formed to be enclosed by sidewalls SW of the pixel defining layer 6 and exposes a portion of an upper surface of the anode 51 at the bottom thereof. For example, the upper surface of the anode 51 is parallel to an upper surface of the base substrate.

[0071] On the above-described basis, the embodiments of the present disclosure further provide a display device DA, comprising the display substrate DP according to the above-described respective embodiments. The above-described display device for example is an OLED display device, such as an OLED display, an OLED television, a digital photo frame, a mobile phone, a tablet personal computer, a navigator, or any other product or component having a display function.

[0072] The above are only specific embodiments of the present disclosure, but the scope of the embodiment of the present disclosure is not limited thereto, and any skilled in the art, within the technical scope disclosed by the present disclosure, can easily think of variations or replacements, which should be covered within the protection scope of the present disclosure. Therefore, the scope of the present disclosure should be the scope of the following claims.

[0073] The foregoing embodiments merely are exemplary embodiments of the present disclosure, and not intended to define the scope of the present disclosure, and the scope of the present disclosure is determined by the appended claims.

Claims

1. A display substrate comprising:

a base substrate; and
a pixel defining layer having a hollow portion and located above the base substrate, wherein,
the hollow portion is enclosed by sidewalls of the pixel defining layer, and exposes a portion of a conductive surface at a bottom of the hollow portion;

in a cross-sectional view of the display substrate, the sidewalls are respectively indicated by two side edges located on two opposite sides of the hollow portion, each of the side edges at least includes a first straight line segment in direct contact with the conductive surface, and a first included angle between the first straight line segment and the conductive surface is greater than 20° and less than 35°.

2. The display substrate according to claim 1, wherein, the first included angle is further greater than 23° and less than 30°.

3. The display substrate according to claim 1 or 2, wherein, the hollow portion is symmetrical about a symmetry axis.
4. The display substrate according to any one of claims 1 to 3, wherein, in the cross-sectional view of the display substrate, the first straight line segment meets a surface of the pixel defining layer facing away from the base substrate.
5. The display substrate according to any one of claims 1 to 3, wherein, in the cross-sectional view of the display substrate, each of the side edges further includes a second straight line segment that meets a surface of the pixel defining layer facing away from the base substrate and a third straight line segment connecting the first straight line segment and the second straight line segment.
6. The display substrate according to claim 5, wherein, the second straight line segment is parallel to the first straight line segment, and the third straight line segment is parallel to the conductive surface.
7. The display substrate according to any one of claims 1 to 6, wherein, the display substrate has a light emitting region, and an orthogonal projection of the portion of the conductive surface exposed by the hollow portion of the pixel defining layer on the base substrate completely coincides with an orthogonal projection of the light emitting region on the base substrate.
8. The display substrate according to any one of claims 1 to 7, wherein, the pixel defining layer has a thickness of 1.0 μm to 3.0 μm .
9. The display substrate according to any one of claims 1 to 8, further comprising:
 - a driving unit provided on the base substrate;
 - a passivation layer covering the driving unit;
 - a first electrode provided on the passivation layer, and the conductive surface being a surface of the first electrode facing away from the base substrate;
 - an organic light emitting layer provided within the hollow portion; and
 - a second electrode covering the organic light emitting layer and the pixel defining layer.
10. A display substrate, comprising:
 - a base substrate; and
 - a pixel defining layer having a hollow portion and located above the base substrate, wherein, the hollow portion is enclosed by sidewalls of the pixel defining layer, and exposes a portion of a conductive surface at a bottom of the hollow portion;
- in a cross-sectional view of the display substrate, the sidewalls include two side edges respectively located on two opposite sides of the hollow portion, each of the side edges is an arc-shaped line segment, the arc-shaped line segment has a first end point that meets the conductive surface and a second end point that meets a surface of the pixel defining layer facing away from the base substrate, a second included angle between a virtual straight line connecting the first end point and the second end point and the conductive surface is greater than 20°, and a third included angle between a tangent line at any point on the arc-shaped line segment and the conductive surface is less than 35°.
11. The display substrate according to claim 10, wherein, the second included angle is greater than 23°, and the third included angle is less than 30°.
12. The display substrate according to claim 10 or 11, wherein,
 - the virtual straight connection line is located inside the hollow portion; or,
 - the virtual straight connection line is located outside the hollow portion.
13. The display substrate according to any one of claims 10 to 12, wherein, the display substrate has a light emitting region, and an orthogonal projection of the portion of the conductive surface exposed by the hollow portion of the pixel defining layer on the base substrate completely coincides with an orthogonal projection of the light emitting region on the base substrate.
14. The display substrate according to any one of claims 10 to 13, wherein, the pixel defining layer has a thickness of 1.0 μm to 3.0 μm .
15. The display substrate according to any one of claims 10 to 14, further comprising:
 - a driving unit provided on the base substrate;
 - a passivation layer covering the driving unit;
 - a first electrode provided on the passivation layer, and the conductive surface being a surface of the first electrode facing away from the base substrate;
 - an organic light emitting layer provided within the hollow portion; and
 - a second electrode covering the organic light emitting layer and the pixel defining layer.
16. A display device, comprising the display substrate according to any one of claims 1 to 15.

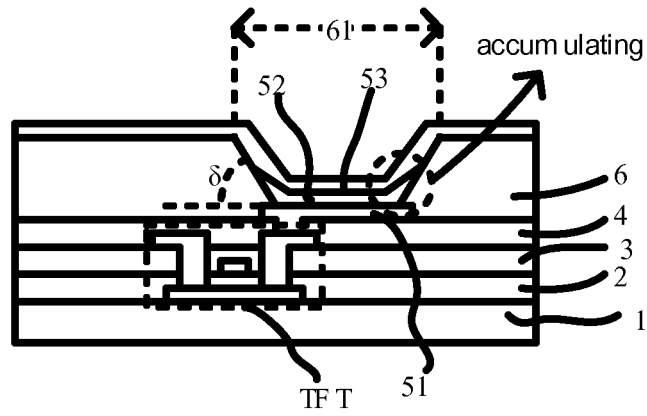


FIG. 1

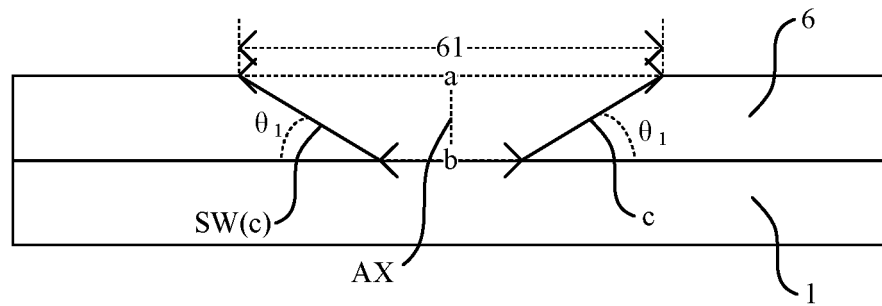


FIG. 2

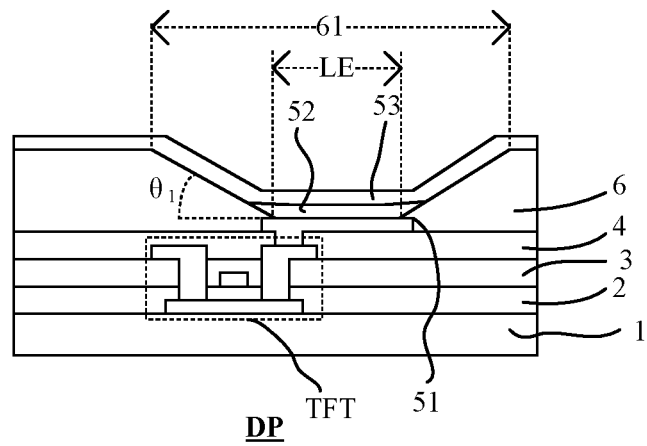


FIG. 3

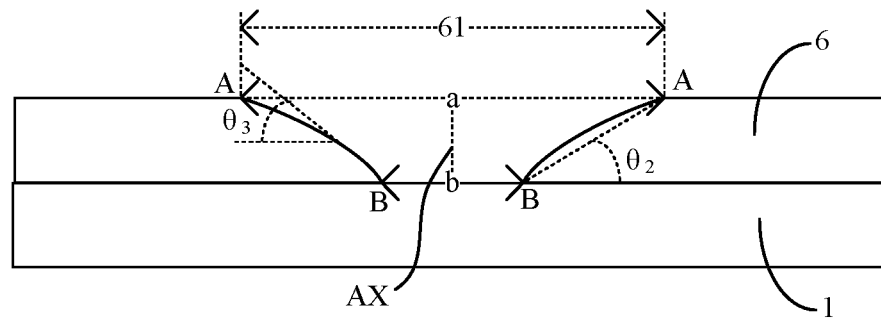


FIG. 4

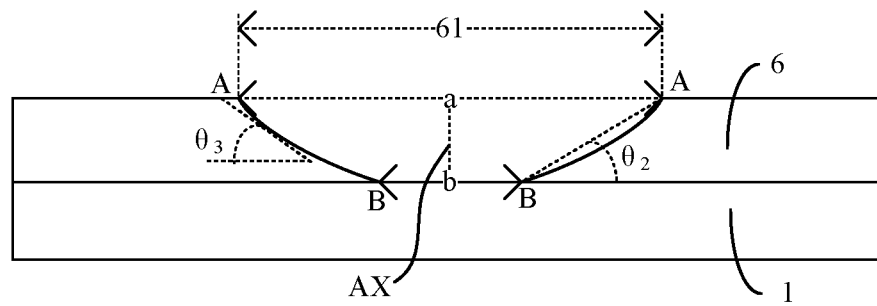


FIG. 5

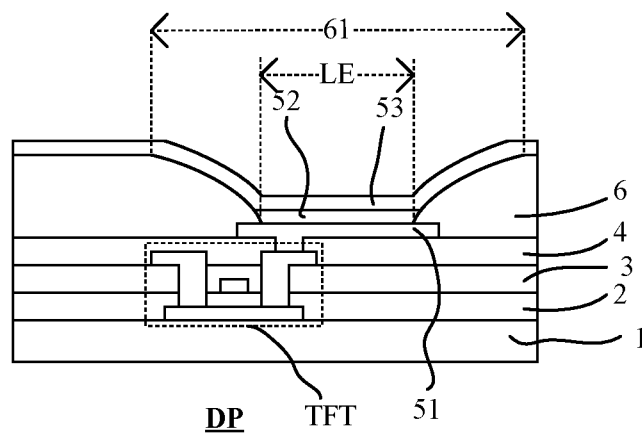


FIG. 6

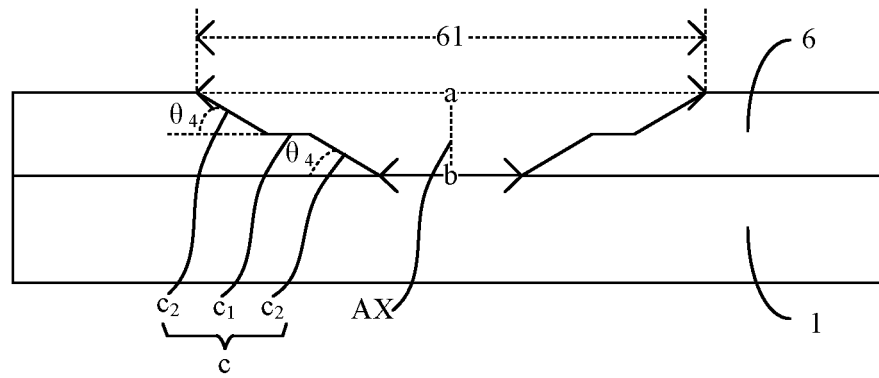


FIG. 7

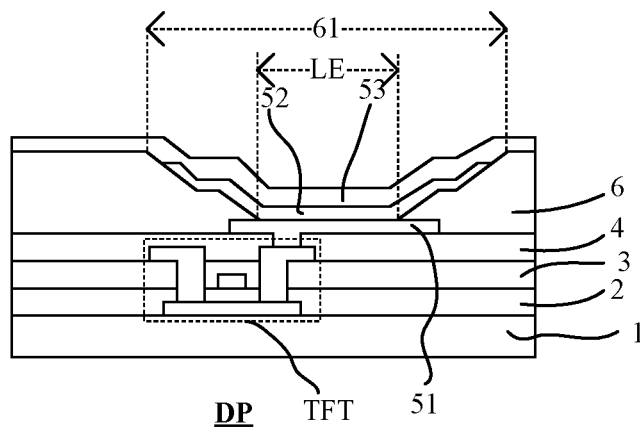


FIG. 8

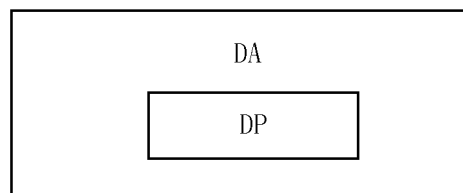


FIG. 9

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2018/090839

5	A. CLASSIFICATION OF SUBJECT MATTER		
	H01L 27/32(2006.01)i		
	According to International Patent Classification (IPC) or to both national classification and IPC		
10	B. FIELDS SEARCHED		
	Minimum documentation searched (classification system followed by classification symbols)		
	H01L27/-		
	Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
15	Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
	CNABS; CNTXT; VEN; WOTXT; USTXT; EPTXT; CNKI: 像素, 象素, 定义, 限定, 有机显示, 有机电致, 有机发光, 倾斜角, 倾角, 夹角, pixel, define, organic display, organic EL, OLED, inclination angle, included angle		
	C. DOCUMENTS CONSIDERED TO BE RELEVANT		
20	Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
	PX	CN 207052608 U (BOE TECHNOLOGY GROUP CO., LTD.) 27 February 2018 (2018-02-27) entire document	1-16
25	X	CN 104037357 A (BOE TECHNOLOGY GROUP CO., LTD.) 10 September 2014 (2014-09-10) description, paragraphs 26-57	1-4, 7-16
	Y	CN 104037357 A (BOE TECHNOLOGY GROUP CO., LTD.) 10 September 2014 (2014-09-10) description, paragraphs 26-57	5-6
30	Y	CN 101026186 A (AU OPTRONICS CORP.) 29 August 2007 (2007-08-29) description, page 4, paragraph 5 to page 6, paragraph 1	5-6
	X	CN 102960067 A (PANASONIC CORPORATION) 06 March 2013 (2013-03-06) description, paragraphs 71-244	1-4, 7-16
35	Y	CN 102960067 A (PANASONIC CORPORATION) 06 March 2013 (2013-03-06) description, paragraphs 71-244	5-6
	☒ Further documents are listed in the continuation of Box C. ☒ See patent family annex.		
40	* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier application or patent but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family		
45			
	Date of the actual completion of the international search		Date of mailing of the international search report
	05 September 2018		18 September 2018
50	Name and mailing address of the ISA/CN		Authorized officer
	State Intellectual Property Office of the P. R. China No. 6, Xitucheng Road, Jimenqiao Haidian District, Beijing 100088 China		
55	Facsimile No. (86-10)62019451		Telephone No.

Form PCT/ISA/210 (second sheet) (January 2015)

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2018/090839

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	CN 104779269 A (SHENZHEN CHINA STAR OPTOELECTRONICS TECHNOLOGY CO., LTD.) 15 July 2015 (2015-07-15) entire document	1-16
A	US 2010044692 A1 (SAMSUNG SDI CO., LTD.) 25 February 2010 (2010-02-25) entire document	1-16

INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.

PCT/CN2018/090839

Patent document cited in search report	Publication date (day/month/year)	Patent family member(s)	Publication date (day/month/year)
CN 207052608 U	27 February 2018	None	
CN 104037357 A	10 September 2014	US 2016276418 A1	22 September 2016
		WO 2015184712 A1	10 December 2015
		US 9525013 B2	20 December 2016
		CN 104037357 B	27 January 2016
CN 101026186 A	29 August 2007	None	
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		WO 2012049712 A1	19 April 2012
		CN 102960067 B	09 March 2016
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Form PCT/ISA/210 (patent family annex) (January 2015)

REFERENCES CITED IN THE DESCRIPTION

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Patent documents cited in the description

- CN 201721072741 [0001]

专利名称(译)	显示基板和显示装置		
公开(公告)号	EP3675174A1	公开(公告)日	2020-07-01
申请号	EP2018847997	申请日	2018-06-12
[标]申请(专利权)人(译)	京东方科技集团股份有限公司		
申请(专利权)人(译)	京东方科技集团股份有限公司.		
当前申请(专利权)人(译)	京东方科技集团股份有限公司.		
[标]发明人	CHENG HONGFEI		
发明人	CHENG, HONGFEI		
IPC分类号	H01L27/32		
CPC分类号	H01L27/3246 H01L27/3248 H01L27/3258 H01L27/32		
优先权	201721072741.6 2017-08-24 CN		
外部链接	Espacenet		

摘要(译)

显示基板(DP)和显示装置(DA)。显示基板(DP)包括基础基板(1)。像素限定层(6),其具有中空部(61)并且位于基础基板(1)的上方。中空部分(61)借助于像素限定层(6)的侧壁(SW)被封闭地形成,并且其下部暴露出导电表面(51)的一部分。在显示基板(DP)的截面图中,侧壁(SW)表示为分别位于中空结构(61)的两个相对侧的两个侧边缘(c)。每个侧边缘(c)至少包括直接接触导电表面的第一直线部分(c; c1),第一直线部分(c; c2)和导电表面(51)之间的第一夹角更大。小于 20° 且小于 35° 。这样,可以增加有机发光层的沉积厚度的均匀性,从而进一步提高显示基板的显示质量。

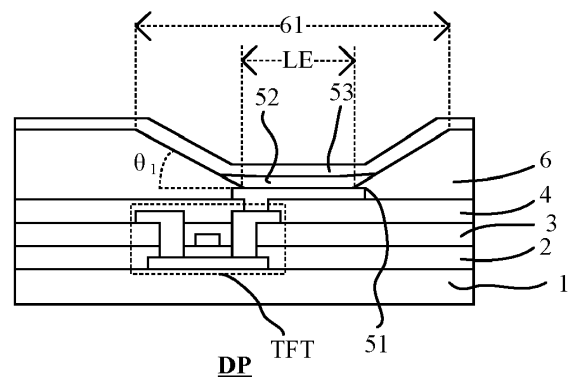


FIG. 3