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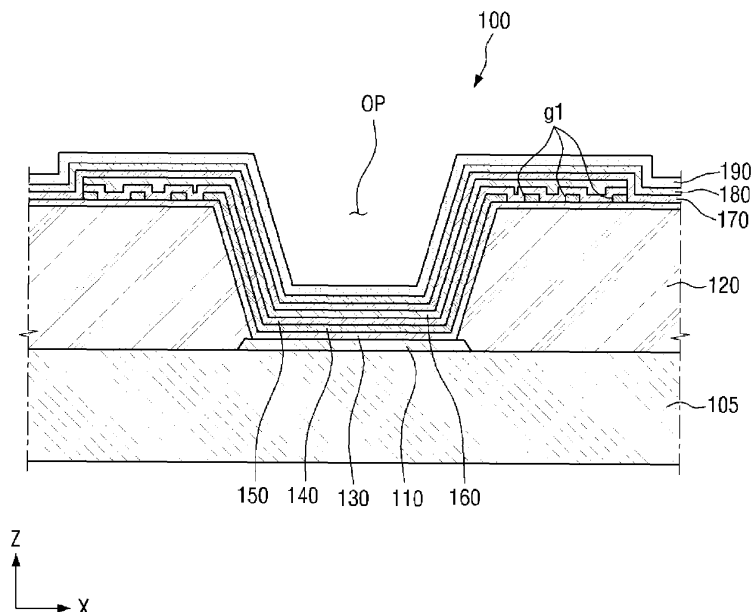
LIGHT-EMITTING DISPLAY DEVICE AND METHOD OF MANUFACTURING THE SAME

(57)

A light-emitting display device includes a substrate (105) having a plurality of pixels (PX). A first electrode (110) is provided on the substrate (105) for each pixel, and a pixel defining layer (120) defines each of the pixels (PX). The pixel defining layer (120) has an opening to expose the first electrode (110). A charge injection layer (130) is on the first electrode (110), and a surface processing layer (140) is on the charge injection layer (130). The surface processing layer (140) extends from

inside the opening of the pixel defining layer (120) to a top surface of the pixel defining layer (120). The surface processing layer (140) including a plurality of grooves in a portion extending on the top surface of the pixel defining layer (120). A charge transport layer (150) is on the surface processing layer (140), a light-emitting layer (160) is on the charge transport layer (150), and a second electrode (190) is on the light-emitting layer (160).

FIG. 2



## Description

### FIELD OF THE INVENTION

**[0001]** One or more embodiments described herein relate to a light-emitting display device, and a method of manufacturing a light-emitting display device.

### DESCRIPTION OF THE RELATED ART

**[0002]** An organic light-emitting display is self-luminous and has a wide viewing angle, high contrast ratio, and fast response time. Each pixel of this display has an organic light-emitting layer between an anode electrode and cathode electrode.

**[0003]** When positive and negative voltages are applied to these electrodes, holes move from the anode electrode to the organic light-emitting layer via a hole injection layer and a hole transport layer, and electrons move from the cathode electrode to the organic light-emitting layer via an electron injection layer and an electron transport layer. The electrons and holes recombine in the organic light-emitting layer to generate excitons. As the excitons change from an excited state to a ground state, the organic light-emitting layer emits light to form an image.

**[0004]** The organic light-emitting display may also include a pixel defining layer having an opening that exposes the anode electrode of each pixel. The hole injection layer, the hole transport layer, the organic light-emitting layer, the electron transport layer, the electron injection layer, and the cathode electrode may be formed on the anode electrode, which is exposed through the opening of the pixel defining layer. The hole transport layer and the organic light-emitting layer may be formed using, for example, an inkjet print method or a nozzle print method.

**[0005]** A lyophilic surface processing layer may be formed on the anode electrode, for example, to improve wettability of a hole transport layer solution. However, when the hole transport layer solution is ejected onto the surface processing layer, the solution may errantly spread toward an adjacent pixel due to its ejection pressure and speed.

**[0006]** As a result, the hole transport layer may be formed up to part of the adjacent pixel. The organic light-emitting layer may also be formed on the hole transport layer up to part of the adjacent pixel. Therefore, organic light-emitting layers that emit light of different colors may overlap each other in adjacent pixels. Consequently, an unwanted mixed color may be displayed, to thereby degrade display quality.

### SUMMARY

**[0007]** In accordance with one or more embodiments, a light-emitting display device includes a pixel defining layer on the substrate to define each pixel, the pixel de-

fining layer having an opening to expose the first electrode; a charge injection layer on the first electrode; a surface processing layer on the charge injection layer, the surface processing layer extending from inside the opening of the pixel defining layer to a top surface of the pixel defining layer, the surface processing layer including a plurality of grooves in a portion extending on the top surface of the pixel defining layer; a charge transport layer on the surface processing layer; a light-emitting layer on the charge transport layer; and a second electrode on the light-emitting layer.

**[0008]** The grooves may be on at least one side of the opening along a first direction, and the first direction may be toward an adjacent pixel which emits light of a different color from a color of light emitted from a pixel in which the surface processing layer is formed. The grooves may be shaped substantially as straight lines that extend along a second direction crossing the first direction. The grooves may have widths that increase in a direction toward the adjacent pixel. The grooves may have substantially equal widths. The grooves may have an oblique shape between the first direction and a second direction crossing the first direction. The grooves may have a lattice shape.

**[0009]** The pixels may include pixels arranged in a row and that emit light of a same color, and the surface processing layer may be formed in units of the pixels. The charge transport layer may be conformally formed along the surface processing layer.

**[0010]** In accordance with another embodiment, a light-emitting display device may include a substrate including a plurality of pixels; a first electrode on the substrate for each pixel; a pixel defining layer on the substrate to define each pixel, the pixel defining layer has an opening to expose the first electrode; a first common layer on the first electrode; a surface processing layer on the first common layer, the surface processing layer extending from inside the opening of the pixel defining layer to a top surface of the pixel defining layer, the surface processing layer including a plurality of grooves in a portion extending on the top surface of the pixel defining layer; a light-emitting layer on the surface processing layer; and a second electrode on the light-emitting layer.

**[0011]** The grooves may be on at least one side of the opening in a first direction, and the first direction may be toward an adjacent pixel which emits light of a different color from a color of light emitted from a pixel in which the surface processing layer is formed. The grooves may be shaped substantially in straight lines that extend along a second direction crossing the first direction. The grooves may have an oblique shape between the first direction and a second direction crossing the first direction. The grooves may have a lattice shape.

**[0012]** The pixels may include pixels arranged in a row and that emit light of a same color, and the surface processing layer may be formed in units of the pixels. The light-emitting layer may be conformally formed along the surface processing layer.

**[0013]** In accordance with another embodiment, a method for manufacturing a light-emitting display device includes forming a first electrode for each of a plurality of pixels on a substrate; forming a pixel defining layer on the substrate to define each of the pixels, the pixel defining layer having an opening to expose the first electrode of each of the pixels; forming a charge injection layer on the first electrode; forming a surface processing layer on the charge injection layer, the surface processing layer extending from inside the opening of the pixel defining layer to a top surface of the pixel defining layer and having a plurality of grooves in a portion extending on the top surface of the pixel defining layer; forming a charge transport layer on the surface processing layer; forming a light-emitting layer on the charge transport layer; and forming a second electrode on the light-emitting layer.

**[0014]** The operation of forming the charge transport layer may include providing a charge transport layer solution into the opening of the pixel defining layer and drying the charge transport layer solution, and the operation of forming the light-emitting layer may include providing a light-emitting layer solution into the opening of the pixel defining layer and drying the light-emitting layer solution. The charge transport layer solution and the light-emitting layer solution may include a same solvent. The charge injection layer and the light-emitting layer may be formed using an inkjet print method or a nozzle print method.

#### BRIEF DESCRIPTION OF THE DRAWINGS

**[0015]** Features will become apparent to those of skill in the art by describing in detail exemplary embodiments with reference to the attached drawings in which:

FIG. 1 illustrates an embodiment of pixels of a light-emitting display device;  
 FIG. 2 illustrates a view along section line A-A in FIG. 1;  
 FIG. 3 illustrates an embodiment of a surface processing layer;  
 FIG. 4 illustrates an embodiment of a method for ejecting a first charge transport layer solution onto the surface processing layer;  
 FIG. 5 illustrates an embodiment of a first charge transport layer;  
 FIGS. 6-12 illustrate various embodiments of a surface processing layer;  
 FIG. 13 illustrates a light-emitting display device of another embodiment;  
 FIG. 14 illustrates a light-emitting display device of another embodiment;  
 FIG. 15 illustrates a light-emitting display device of another embodiment; and  
 FIGS. 16-24 illustrate an embodiment of a method for manufacturing a light-emitting display device.

#### DETAILED DESCRIPTION

**[0016]** Example embodiments are described more fully hereinafter with reference to the accompanying drawings; however, they may be embodied in different forms and should not be construed as limited to the embodiments set forth herein. Rather, these embodiments are provided so that this disclosure will be thorough and complete, and will fully convey exemplary implementations to those skilled in the art.

**[0017]** In the drawing figures, the dimensions of layers and regions may be exaggerated for clarity of illustration. It will also be understood that when a layer or element is referred to as being "on" another layer or substrate, it can be directly on the other layer or substrate, or intervening layers may also be present. Further, it will be understood that when a layer is referred to as being "under" another layer, it can be directly under, and one or more intervening layers may also be present. In addition, it will also be understood that when a layer is referred to as being "between" two layers, it can be the only layer between the two layers, or one or more intervening layers may also be present. Like reference numerals refer to like elements throughout.

**[0018]** FIG. 1 illustrates a plurality of pixels PX of a light-emitting display device 100 according to one embodiment, and FIG. 2 is a cross-sectional view along line A-A in FIG. 1. Referring to FIGS. 1 and 2, the light-emitting display device 100 includes a substrate 105, a first electrode 110, a pixel defining layer 120, a first charge injection layer 130, a surface processing layer 140, a first charge transport layer 150, a light-emitting layer 160, a second charge transport layer 170, a second charge injection layer 180, and a second electrode 190. These members are sequentially stacked in a predetermined (e.g., Z) direction in FIG. 2.

**[0019]** The substrate 105 includes a display area DA and a non-display area NDA. The pixels PX are in the display area DA, and the non-display area NDA is outside the display area DA. The pixels PX may include, for example, red pixels (or sub-pixels) R that emit red light, green pixels (or sub-pixels) G that emit green light, and blue pixels (or sub-pixels) B that emit blue light. In FIG. 1, pixels PX that emit light of the same color are arranged in a row along a predetermined (e.g., Y) direction, and pixels PX that emit light of different colors are alternately arranged in a row along a predetermined (e.g., X) direction. The arrangement of pixels PX may be different in other embodiments.

**[0020]** When the pixels PX that emit light of the same color are arranged in a row, a first charge transport layer solution 150a (see FIG. 4) and a light-emitting layer solution may be ejected in one direction, when the first charge transport layer 150 and the light-emitting layer 160 are formed using an inkjet print method or a nozzle print method. Therefore, the ejection process may be performed easily. For example, the inkjet print method may dispense a solution to be printed at a desired position in

the form of ink droplets. The nozzle print method may cause a solution to be printed to flow along a line including a desired position.

**[0021]** The substrate 105 may include an insulating substrate. The insulating substrate may include, for example, a transparent glass material containing SiO<sub>2</sub> as a main component. In one embodiment, the insulating substrate may include an opaque or plastic material. The insulating substrate may be, for example, a flexible substrate.

**[0022]** The substrate 105 may include or support other structures formed on the insulating substrate. Examples include various types of wiring, electrodes, and insulating layers. In one embodiment, the substrate 105 may include a plurality of thin-film transistors (TFTs) on the insulating substrate. Drain electrode of each TFT may be electrically connected to a first electrode 110. Also, each TFT may include an active region that includes, for example, amorphous silicon, polycrystalline silicon, or monocrystalline silicon. In another embodiment, each TFT may include an active region that includes an oxide semiconductor.

**[0023]** The first electrode 110 is formed on the substrate 105 in each pixel PX. The first electrode 110 may be an anode electrode, which provides holes to the light-emitting layer 160 in response to a signal transmitted to a corresponding TFT, or a cathode electrode, which provides electrons to the light-emitting layer 160 in response to the signal transmitted to the TFT. The first electrode 110 may be used as a transparent electrode or a reflective electrode. When used as a transparent electrode, the first electrode 110 may include, for example, indium tin oxide (ITO), indium zinc oxide (IZO), zinc oxide (ZnO), or In<sub>2</sub>O<sub>3</sub>. When used as a reflective electrode, the first electrode 110 may include a reflective layer using Ag, Mg, Al, Pt, Pd, Au, Ni, Nd, Ir, Cr, or a compound of the same, and may include ITO, IZO, ZnO or In<sub>2</sub>O<sub>3</sub> on the reflective layer. The first electrode 110 may be formed by, but not limited to, a photolithography process.

**[0024]** The pixel defining layer 120 defines each pixel PX on the substrate 105 and has an opening OP that exposes the first electrode 110. Accordingly, the first charge injection layer 130 is formed on the first electrode 110 through the opening OP of the pixel defining layer 120. The pixel defining layer 120 may include an insulating material. For example, the pixel defining layer 120 may include at least one organic material, e.g., benzocyclobutene (BCB), polyimide (PI), polyamide (PA), acrylic resin, or phenolic resin. In another example, the pixel defining layer 120 may include an inorganic material, e.g., silicon nitride. The pixel defining layer 120 may be formed, for example, by a photolithography process or another process.

**[0025]** In the current embodiment, the pixel defining layer 120 includes an insulating material that causes a contact angle of the first charge transport layer solution 150a (see, e.g., FIG. 4) relative to the pixel defining layer 120 and a contact angle of the first charge transport layer

solution 150a (see, e.g., FIG. 4) relative to the surface processing layer 140 to be different when the first charge transport layer 150 is formed using an inkjet print method or nozzle print method.

**[0026]** For example, the pixel defining layer 120 may include an insulating material that causes the contact angle of the first charge transport layer solution 150a relative to the pixel defining layer 120 to be greater than the contact angle of the first charge transport layer solution 150a relative to the surface processing layer 140. In one embodiment, the pixel defining layer 120 may include an insulating material that causes the contact angle of the first charge transport layer solution 150a relative to the pixel defining layer 120 to be 40 degrees or more.

**[0027]** The first charge injection layer 130 may be on the first electrode 110 exposed through the opening OP of the pixel defining layer 120 to entirely cover the pixel defining layer 120. The first charge injection layer 130 may be a hole injection layer receiving holes from the first electrode 120, or an electron injection layer receiving electrons from the first electrode 120. A case where the first charge injection layer 130 is a hole injection layer will be described as an example.

**[0028]** The first charge injection layer 130 is a buffer layer that lowers an energy barrier between the first electrode 110 and the first charge transport layer 150. The first charge injection layer 130 facilitates injection of holes from the first electrode 110 to the first charge transport layer 150. The first charge injection layer 130 may include, for example, an organic compound such as but not limited to 4,4',4"-tris(3-methylphenylphenylamino)triphenylamine (MTDATA), copper phthalocyanine (CuPc), or poly(3,4-ethylenedioxythiophene, polystyrene sulfonate) (PEDOT/PSS). The first charge injection layer 130 may be coated, for example, by a slit coating process.

**[0029]** The surface processing layer 140 is formed on the first charge injection layer 130 in each pixel PX. The surface processing layer 140 may be formed, for example, on the first charge injection layer 130 to extend from inside the opening OP of the pixel defining layer 120 to a top surface of the pixel defining layer 120. The surface processing layer 140 may have a plurality of grooves g1 in a portion extending on the top surface of the pixel defining layer 120. The surface processing layer 140 may be formed, for example, by a photolithography process.

**[0030]** The surface processing layer 140 may include a conductive primer that causes the surface processing layer 140 to be more lyophilic to the first charge transport layer solution 150a (see, e.g., FIG. 4) than the pixel defining layer 120, e.g., causes the contact angle of the first charge transport layer solution 150a (see FIG. 4) relative to the surface processing layer 140 to be smaller than the contact angle of the first charge transport layer solution 150a (see, e.g., FIG. 4) relative to the pixel defining layer 120.

**[0031]** For example, the surface processing layer 140 may include a conductive primer that causes the contact angle of the first charge transport layer solution 150a

relative to the surface processing layer 140 to be 20 degrees or less. In this case, when the first charge transport layer solution 150a is ejected onto the surface processing layer 140 (inside the opening OP of the pixel defining layer 120 in each pixel PX) using an inkjet print method or nozzle print method, the first charge transport layer solution 150a may be stably confined within the corresponding pixel PX without spreading to the exposed top surface of the pixel defining layer 120 (e.g., due to high wettability of the first charge transport layer solution 150a relative to the surface processing layer 140). Also, the first charge transport layer 150 may be uniformly formed on the surface processing layer 140. The quality of high wettability may mean, for example, that a liquid is widely spread over the surface of a solid to contact a wide area of the surface.

**[0032]** The first charge transport layer 150 is formed on the surface processing layer 140. The first charge transport layer 150 may be conformally formed along the surface processing layer 140 having the grooves g1. The first charge transport layer 150 at least partially fills the grooves g1 of the surface processing layer 140.

**[0033]** The first charge transport layer 150 may be a hole transport layer receiving holes from the first charge injection layer 130 via the surface processing layer 140, or an electron transport layer receiving electrons from the first charge injection layer 130 via the surface processing layer 140. A case where the first charge transport layer 130 is a hole transport layer will be described as an example.

**[0034]** The first charge transport layer 150 may transport holes from the first charge injection layer 130 via the surface processing layer 140 to the light-emitting layer 160. The first charge transport layer 150 may include, for example, an organic compound such as but not limited to N,N'-diphenyl-N,N'-bis(3-methylphenyl)-1,1'-biphenyl-4,4'-diamine (TPD) or N,N'-di(naphthalen-1-yl)-N,N'-diphenyl-benzidine (NPB). The first charge transport layer 150 may be formed, for example, using an inkjet print method or nozzle print method.

**[0035]** The light-emitting layer 160 is formed on the first charge transport layer 150. The light-emitting layer 160 emits light when holes from the first electrode 110 and electrons from the second electrode 190 recombine. The holes and electrons provided to the light-emitting layer 160 combine to form excitons. When the excitons change from an excited state to a ground state, the light-emitting layer 160 emits light. The light-emitting layer 160 may include, for example, a red light-emitting layer to emit red light, a green light-emitting layer to emit green light, and a blue light-emitting layer to emit blue light. The light-emitting layer 160 may be formed, for example, using an inkjet print method or nozzle print method.

**[0036]** The red light-emitting layer may include one red light-emitting material or a host and a red dopant. Examples of the host of the red light-emitting layer include but are not limited to Alq3, 4,4'-N,N'-dicarbazol-biphenyl (CBP), poly(n-vinylcarbazole) (PVK), 9,10-Di(naphthyl-

2-yl)anthracene (ADN), 4,4',4"-tris(N-carbazolyl)triphenylamine (TCTA), 1,3,5-tris(N-phenylbenzimidazol-2-yl)benzene (TPBI), 3-tert-butyl-9,10-di(naphth-2-yl)anthracene (TBADN), terfluorene (E3), distyrylarylene (DSA). In addition, examples of the red dopant include but are not limited to PtOEP, Ir(piq)3 and Btp2Ir(acac).

**[0037]** The green light-emitting layer may include one green light-emitting material or a host and a green dopant. The host of the red light-emitting layer may be used as the host of the green light-emitting layer. Examples of the green dopant may include but are not limited to Ir(ppy)3, Ir(ppy)2(acac) and Ir(mppy)3.

**[0038]** The blue light-emitting layer may include one blue light-emitting material or a host and a blue dopant. The host of the red light-emitting layer may be used as the host of the blue light-emitting layer. Examples of the blue dopant include but are not limited to F2Irpic, (F2ppy)2Ir(tmd), Ir(dfppz)3, terfluorene, 4,4'-bis(4-diphenylaminostyryl) biphenyl (DPAVB), 2,5,8,11-tetra-*tert*-butyl phenylene (TBPe).

**[0039]** The second charge transport layer 170 may be formed on the light-emitting layer 160. The second charge transport layer 170 may be an electron transport layer receiving electrons from the second electrode 190 via the second charge injection layer 180, or a hole transport layer receiving holes from the second electrode 190 via the second charge injection layer 180. A case where the second charge transport layer 170 is an electron transport layer will be described as an example.

**[0040]** The second charge transport layer 170 may transport electrons from the second electrode 190 via the second charge injection layer 180 to the light-emitting layer 160. The second charge transport layer 170 may include, for example, an organic compound such as but not limited to 4,7-diphenyl-1,10-phenanthroline (Bphen), BAlq, tris(8-quinolinolate)aluminum (Alq3), berylliumbis(benzoquinolin-10-olate) (Bebq2), or TPBI. The second charge transport layer 170 may be formed, for example, by a deposition process.

**[0041]** The second charge injection layer 180 may be formed on the second charge transport layer 170. The second charge injection layer 180 may be an electron injection layer receiving electrons from the second electrode 190, or a hole injection layer receiving holes from the second electrode 190. A case where the second charge injection layer 180 is an electron injection layer will be described as an example.

**[0042]** The second charge injection layer 180 is a buffer layer that lowers an energy barrier between the second charge transport layer 170 and the second electrode 190. The second charge injection layer 180 facilitates the injection of electrons from the second electrode 190 to the second charge transport layer 170. The second charge injection layer 180 may include, for example, LiF or CsF. The second charge injection layer 180 may be formed, for example, by a deposition process.

**[0043]** The second electrode 190 may be formed on the second charge injection layer 180. The second elec-

trode 190 may be a cathode electrode providing electrons to the light-emitting layer 160 or an anode electrode providing holes to the light-emitting layer 160. Like the first electrode 110, the second electrode 190 may be used as a transparent electrode or a reflective electrode. The second electrode 190 may be formed, for example, by a deposition process.

**[0044]** The light-emitting display device 100 may further include an encapsulation substrate on the second electrode 190. The encapsulation substrate may be an insulating substrate. A spacer may be between the second electrode 190 on the pixel defining layer 120 and the encapsulation substrate. The encapsulation layer made of an insulating material may cover and thus protect the entire structure. In other embodiments, the encapsulation substrate may be omitted.

**[0045]** FIG. 3 illustrates an embodiment of a surface processing layer, which, for example, may correspond to the surface processing layer 140 in FIG. 2. FIG. 4 illustrates an example of a method for ejecting the first charge transport layer solution 150a onto the surface processing layer 140 in FIG. 3. FIG. 5 is a cross-sectional view of the first charge transport layer 150 formed by drying the charge transport layer solution 150a in FIG. 4.

**[0046]** Referring to FIG. 3, the surface processing layer 140 has the grooves g1 in at least one side portion thereof. The grooves g1 may be on at least one side of the opening OP of the pixel defining layer 120. A direction of one side portion or one side may be a first direction D1 toward an adjacent pixel PX, which emits light of a color different from the color of light emitted from a corresponding pixel PX in which the surface processing layer 140 is formed. For example, in the case where a red pixel R emitting red light and a green pixel G emitting green light are adjacent to each other, the surface processing layer 140 formed in the red pixel R has the grooves g1 in a side portion thereof in a direction in which the green pixel G is disposed. In FIG. 3, the grooves g1 are formed in two side portions of the surface processing layer 140, based on the assumption that pixels PX emitting light of different colors are disposed on two sides of one pixel PX.

**[0047]** The grooves g1 may be shaped, for example, in straight lines that extend along a second direction D2 crossing or perpendicular to the first direction D1 toward the adjacent pixel PX. The grooves g1 form roughness at an edge of the surface processing layer 140. Accordingly, when the first charge transport layer solution 150a is ejected from an ejection device 10 onto the surface processing layer 140 using, e.g., a nozzle print method, the grooves g1 of the surface processing layer 140 may increase the contact angle of the first charge transport layer solution 150a relative to the edge of the surface processing layer 140. As a result, the first charge transport layer solution 150a may be confined in the grooves g1, and thus the first charge transport layer solution 150a is prevented from spreading beyond the surface processing layer 140.

**[0048]** For example, the grooves g1 of the surface

processing layer 140 may increase resistance to a force F (e.g., generated by ejection pressure and ejection speed) with which the first charge transport layer solution 150a ejected from the ejection device 10 spreads beyond the surface processing layer 140. This may confine the first charge transport layer solution 150a in the grooves g1 while preventing the first charge transport layer solution 150a from spreading beyond the surface processing layer 140. Thus, it is possible to reduce or prevent the first charge transport layer solution 150a from flowing to an adjacent pixel PX (for example, a pixel PX that emits light of a different color) and unwantedly spreading to part of the adjacent pixel PX.

**[0049]** In one embodiment, the widths w1, w2, or w3 of the grooves g1 may increase toward an adjacent pixel PX. The grooves g1 may therefore reduce or prevent a sharp increase in the contact angle of the first charge transport layer solution 150a relative to the edge of the surface processing layer 140. As a result, the first charge transport layer solution 150a may spread over a large area of the surface processing layer 140. Accordingly, when the first charge transport layer solution 150a ejected onto the surface processing layer 140 dries to form the first charge transport layer 150 as in FIG. 5, the first charge transport layer 150 may be formed over a large area of the surface processing layer 140.

**[0050]** The first charge transport layer 150, formed by the drying of the first charge transport layer solution 150a, forms pinning points P with the surface processing layer 140 as illustrated in FIG. 5. In this case, the light-emitting layer 160 may be formed to be aligned with the pinning points P by ejecting a light-emitting layer solution, which contains the same solvent as the first charge transport layer solution 150a, onto the first charge transport layer 150 using an inkjet print method or nozzle print method, and drying the light-emitting layer solution. Thus, edges of the light-emitting layer 160 may be aligned with the edges of the first charge transport layer 150.

**[0051]** FIGS. 6 through 12 illustrate various embodiments of the surface processing layer 140. Referring to FIG. 6, a surface processing layer 140a includes a plurality of grooves g2 having a lattice shape in at least one side portion thereof. The grooves g2 are formed by orthogonal intersection of first grooves g21 and second grooves g22. The first grooves g21 are shaped in straight lines that extend along a second direction D2 crossing or perpendicular to a first direction D1 toward an adjacent pixel PX. The second grooves g22 are shaped in straight lines that extend along the first direction D1. The first grooves g21 may become wider along the first direction D1.

**[0052]** When a force F (with which a first charge transport layer solution ejected onto the surface processing layer 140a by moving an ejection device 10 spreads beyond the surface processing layer 140a) is generated in the first direction D1 and a diagonal direction between the first direction D1 and the second direction D2, the grooves g2 may effectively prevent the first charge trans-

port layer solution from spreading along not only the first direction D1 but also the diagonal direction.

**[0053]** Referring to FIG. 7, a surface processing layer 140b includes a plurality of oblique grooves g3 in at least one side portion thereof. The grooves g3 are formed obliquely between a first direction D1 toward an adjacent pixel PX and a second direction D2 crossing or perpendicular to the first direction D1. The grooves g3 may have equal widths.

**[0054]** When a force F (with which a first charge transport layer solution ejected onto the surface processing layer 140b by moving an ejection device 10 spreads beyond the surface processing layer 140b) is generated in the first direction D1 and a diagonal direction between the first direction D1 and the second direction D2, the grooves g3 may effectively prevent the first charge transport layer solution from spreading along not only the first direction D1, but also the diagonal direction.

**[0055]** Referring to FIG. 8, a surface processing layer 140c includes a plurality of grooves g4 formed obliquely in different directions in both side portions thereof. For example, the grooves g4 include first grooves g41 and second grooves g42. The first grooves g41 are formed to have a first oblique shape between a first direction D1 toward an adjacent pixel PX and a second direction D2 crossing or perpendicular to the first direction D1. The second grooves g42 are formed to have a second oblique shape which is symmetrical to the first oblique shape with respect to the second direction D2.

**[0056]** When a force F (with which a first charge transport layer solution ejected onto the surface processing layer 140c by moving an ejection device 10 spreads beyond the surface processing layer 140c) is generated in a diagonal direction between the first direction D1 and the second direction D2 at one side portion of the surface processing layer 140c, the first oblique shape is orthogonal to the diagonal direction. Accordingly, the first grooves g41 having the first oblique shape may effectively reduce or prevent the first charge transport layer solution from spreading from the side portion of the surface processing layer 140c in the diagonal direction between the first direction D1 and the second direction D2.

**[0057]** When the force F (with which the first charge transport layer solution ejected onto the surface processing layer 140c by moving the ejection device 10 spreads beyond the surface processing layer 140c) is generated in a diagonal direction between the first direction D1 and the second direction D2 at the other side portion of the surface processing layer 140c, the second oblique shape is orthogonal to the diagonal direction. Accordingly, the second grooves g42 having the second oblique shape may effectively prevent the first charge transport layer solution from spreading from the side portion of the surface processing layer 140c in the diagonal direction between the first direction D1 and the second direction D2.

**[0058]** Thus, when the force F (with which the first charge transport layer solution ejected onto the surface processing layer 140c by moving the ejection device 10

spreads beyond the surface processing layer 140c) is generated in diagonal directions between the first direction D1 and the second direction D2, the grooves g4 may effectively reduce or prevent the first charge transport layer solution from spreading in different diagonal directions.

**[0059]** Referring to FIG. 9, a surface processing layer 140d includes a plurality of grooves g5 having a lattice shape. The grooves g5 are formed by orthogonal intersection of first oblique grooves g51 and second oblique grooves g52 in at least one side portion thereof. Even when a movement start point of an ejection device 10 changes, the grooves g5 may effectively reduce or prevent a first charge transport layer solution from spreading, not only in a first direction D1 but also in a diagonal direction between the first direction D1 and a second direction D2.

**[0060]** Referring to FIG. 10, a surface processing layer 140e includes a plurality of grooves g6 having equal widths in at least one side portion thereof. The surface processing layer 140e having the grooves g6 may be applied in a limited space, for example, in a case where there is a fine pitch between adjacent pixels PX.

**[0061]** Referring to FIG. 11, a surface processing layer 140f is formed in units of pixels PX that are arranged in a row and emit light of the same color. For example, the surface processing layer 140f may be formed in every two or more pixels PX to be larger in size than the surface processing layer 140 of FIG. 3. However, the surface processing layer 140f may be formed in the same shape as the surface processing layer 140 to have a plurality of grooves g7 extending along a second direction D2 crossing or perpendicular to a first direction D1 toward an adjacent pixel PX which emits light of a different color. When formed by a photolithography process, the surface processing layer 140f may facilitate a patterning process.

**[0062]** Referring to FIG. 12, a surface processing layer 140g includes a plurality of grooves g8 in all side portions thereof. The grooves g8 are formed in the same shape as the grooves g1 of the surface processing layer 140 of FIG. 3. When pixels PX emitting light of different colors are disposed on all sides of a pixel PX, the grooves g8 may prevent a first charge transport layer solution from spreading in all directions from the surface processing layer 140g when the first charge transport layer solution is ejected onto the surface processing layer 140g using an inkjet print method.

**[0063]** As described above, each of the light-emitting devices according to the aforementioned embodiments include a surface processing layer (140, 140a through 140g) having roughness formed on an edge thereof by a plurality of grooves (g1 through g8). The roughness increases resistance to a force F (e.g., generated by ejection pressure and ejection speed) with which a first charge transport layer solution (150a) ejected onto the surface processing layer (140, 140a through 140g) using an inkjet print method or nozzle print method spreads beyond the surface processing layer (140, 140a through

140g). This may reduce or prevent the first charge transport layer solution (150a) from spreading beyond the surface processing layer (140, 140a through 140g).

**[0064]** Accordingly, the light-emitting display devices according to the one or more embodiments may reduce or prevent the first charge transport layer solution (150a) from spreading to an adjacent pixel PX, for example, a pixel PX that emits light of a different color and undesiredly forming a first charge transport layer (150) up to part of the adjacent pixel PX.

**[0065]** Therefore, the light-emitting display devices according to one or more embodiments may prevent a light-emitting layer (160) from being formed on the first charge transport layer (150) up to part of the adjacent pixel PX, thereby avoiding a situation where light-emitting layers emitting light of different colors overlap each other in adjacent pixels PX. Consequently, this may reduce or prevent degradation of display quality due to the display of an unwanted mixed color on the light-emitting display devices being driven.

**[0066]** FIG. 13 is a cross-sectional view of another embodiment of a light-emitting display device 200, which may have the same configuration as the light-emitting display device 100 in FIG. 2 except for a first charge transport layer 250 and a light-emitting layer 260.

**[0067]** The first charge transport layer 250 is similar to the first charge transport layer 150 of FIG. 2. However, the first charge transport layer 250 is not formed up to edges of a surface processing layer 140, but rather fills one or more of a plurality of grooves g1. When a first charge transport layer solution is ejected onto the surface processing layer 140 using an inkjet print method or nozzle print method to form the first charge transport layer 250, if the ejection pressure is low, the first charge transport layer solution may not spread to the edges of the surface processing layer 140. As a result, the above structure may be formed.

**[0068]** The light-emitting layer 260 is similar to the light-emitting layer 160 of FIG. 2. Here, the light-emitting layer 260 is formed on the first charge transport layer 250 and extends up to the edges of the surface processing layer 140. This structure may be formed because the surface processing layer 140 is lyophilic, not only to the first charge transport layer solution but also to a light-emitting layer solution, when the light-emitting layer solution has the same solvent as the first charge transport layer solution. The grooves g2 through g7 in the embodiments of FIGS. 6 through 12 may be applied to the grooves g1 of the surface processing layer 140.

**[0069]** Like the light-emitting display device 100, the light-emitting display device 200 includes the surface processing layer 140 having the grooves g1 in at least one side thereof. The surface processing layer 140 having the grooves g1 prevents the light-emitting layer 260 from being formed on the first charge transport layer 250 up to part of an adjacent pixel PX, thereby avoiding a situation where light-emitting layers emitting light of different colors overlap each other in adjacent pixels PX.

This may prevent degradation of display quality due to the display of an unwanted mixed color on the light-emitting display device 200 being driven.

**[0070]** FIG. 14 is a cross-sectional view of another embodiment of a light-emitting display device 300, which may have the same configuration as the light-emitting display device 100 of FIG. 2 except a first charge transport layer 350 and a light-emitting layer 360.

**[0071]** The first charge transport layer 350 is similar to the first charge transport layer 150 of FIG. 2. However, the first charge transport layer 350 is formed on a surface processing layer 140, but not on the surface processing layer 140 between a plurality of grooves g1. For example, a height of the first charge transport layer 350 in the grooves g1 may be equal to or less than a height of the grooves g1. This structure may be formed when a first charge transport layer solution in the grooves g1 of the surface processing layer 140 dries, after being ejected onto the surface processing layer 140 using an inkjet print method or nozzle print method, to form the first charge transport layer 350.

**[0072]** The light-emitting layer 360 is similar to the light-emitting layer 160 of FIG. 2. However, the light-emitting layer 360 is formed on the first charge transport layer 350, and contacts the surface processing layer 140 in the grooves g1 of the surface processing layer 140. This structure may be formed because the surface processing layer 140 is lyophilic, not only to the first charge transport layer solution but also to a light-emitting layer solution, when the light-emitting layer solution has the same solvent as the first charge transport layer solution.

**[0073]** The grooves g2 through g7 in the embodiments of FIGS. 6 through 12 may be applied to the grooves g1 of the surface processing layer 140.

**[0074]** The light-emitting device 300 according to the current embodiment, like the light-emitting display device 100, includes the surface processing layer 140 having the grooves g1 in at least one side thereof. The surface processing layer 140 having the grooves g1 prevents the light-emitting layer 360 from being formed on the first charge transport layer 350 up to part of an adjacent pixel PX, thereby avoiding a situation where light-emitting layers emitting light of different colors overlap each other in adjacent pixels PX. This may reduce or prevent degradation of display quality due to the display of an unwanted mixed color on the light-emitting display device 300 being driven.

**[0075]** FIG. 15 is a cross-sectional view of another embodiment of a light-emitting display device 400 which includes a substrate 105, a first electrode 110, a pixel defining layer 420, a first common layer 430, a surface processing layer 440, a light-emitting layer 450, a second common layer 460, and a second electrode 470.

**[0076]** The pixel defining layer 420 is similar to the pixel defining layer 120 of FIG. 2. However, the pixel defining layer 420 may include an insulating material that causes a contact angle of a light-emitting layer solution relative to the pixel defining layer 420 and a contact angle of the

light-emitting layer solution relative to the surface processing layer 440 to be different, when the light-emitting layer 450 is formed using an inkjet print method or nozzle print method.

**[0077]** For example, the pixel defining layer 420 may include an insulating material that causes the contact angle of the light-emitting layer solution relative to the pixel defining layer 420 to be greater than the contact angle of the light-emitting layer solution relative to the surface processing layer 440. In one embodiment, the pixel defining layer 420 may be made of an insulating material that causes the contact angle of the light-emitting layer solution to the pixel defining layer 420 to be 40 degrees or more.

**[0078]** The first common layer 430 may be formed on the first electrode 110 exposed through an opening OP of the pixel defining layer 420 to entirely cover the pixel defining layer 420. The first common layer 430 includes a first charge injection layer. The first charge injection layer contacts the first electrode 110. In addition, the first common layer 430 may further include a first charge transport layer on the first charge injection layer. The first charge injection layer and the first charge transport layer may include the same material as the first charge injection layer 130 and the first charge transport layer 140 of FIG. 2.

**[0079]** The surface processing layer 440 has a plurality of grooves g1 in at least one side portion thereof, and may be similar to the surface processing layer 140 of FIG. 2. However, the surface processing layer 440 may include a conductive primer that causes the surface processing layer 440 to be more lyophilic to the light-emitting layer solution than the pixel defining layer 420, e.g., causes the contact angle of the light-emitting layer solution relative to the surface processing layer 440 to be smaller than the contact angle of the light-emitting layer solution relative to the pixel defining layer 420. In one embodiment, the surface processing layer 440 may include a conductive primer that causes the contact angle of the light-emitting layer solution relative to the surface processing layer 440 to be 20 degrees or less.

**[0080]** Thus, when the light-emitting layer solution is ejected onto the surface processing layer 440 inside the opening OP of the pixel defining layer 420 in each pixel PX by using an inkjet print method or nozzle print method, the light-emitting layer solution may be stably confined within the corresponding pixel PX without spreading to an exposed top surface of the pixel defining layer 420 due to high wettability of the light-emitting layer solution to the surface processing layer 440. Also, the light-emitting layer 450 may be uniformly formed on the surface processing layer 440.

**[0081]** The light-emitting layer 450 may be similar to the light-emitting layer 160 of FIG. 2. However, the light-emitting layer 450 is formed on the surface processing layer 440. The light-emitting layer 450 may be conformally formed along the surface processing layer 440 having the grooves g1 that form roughness. The light-emitting

layer 450 at least partially fills the grooves g1 of the surface processing layer 440.

**[0082]** Because the light-emitting layer 450 is formed directly on the surface processing layer 440, when the light-emitting layer solution is ejected onto the surface processing layer 440 using a nozzle print method to form the light-emitting layer 450, the contact angle of the light-emitting layer solution relative to an edge of the surface processing layer 440 may be prevented from increasing. This may prevent the light-emitting layer solution from spreading beyond the surface processing layer 440, thereby confining the light-emitting layer solution in the grooves g1 of the surface processing layer 440.

**[0083]** The second common layer 460 is formed on the light-emitting layer 450. The second common layer 460 includes a second charge transport layer. The second charge transport layer contacts the light-emitting layer 460. In addition, the second common layer 430 may include a second charge injection layer on the second charge transport layer. The second charge transport layer and the second charge injection layer may include the same material as the second charge transport layer 170 and the second charge injection layer 180 of FIG. 2. The second electrode 470 may be identical to the second electrode 190 of FIG. 2.

**[0084]** The grooves g2 through g7 in the embodiments of FIGS. 6 through 12 may be applied to the grooves g1 of the surface processing layer 440.

**[0085]** The light-emitting device 400 according to the current embodiment, like the light-emitting display device 100, includes the surface processing layer 440 having the grooves g1 in at least one side thereof. The surface processing layer 440 having the grooves g1 prevents the light-emitting layer 450 from being formed on the surface processing layer 440 up to part of an adjacent pixel PX, thereby avoiding a situation where light-emitting layers emitting light of different colors overlap each other in adjacent pixels PX. This may prevent degradation of display quality due to the display of an unwanted mixed color on the light-emitting display device 400 being driven.

**[0086]** FIGS. 16 through 24 are cross-sectional views illustrating an embodiment of a method for manufacturing a light-emitting display device. Referring to FIG. 16, a first electrode 110 is formed on a substrate 105 in each of a plurality of pixels PX (see FIG. 1) defined in the substrate 105. The first electrode 110 may be formed, for example, by depositing a transparent electrode material or a reflective material on the substrate 105 and patterning the transparent electrode material or the reflective material.

**[0087]** Referring to FIG. 17, a pixel defining layer 120 is formed on the substrate 105 to define each pixel PX (see FIG. 1) and has an opening OP that exposes the first electrode 110. The pixel defining layer 120 may be formed, for example, by depositing an insulating material on the whole surface of the substrate 105 to cover the first electrode 110 using a deposition method and patterning the deposited insulating material.

**[0088]** The pixel defining layer 120 may include an insulating material that causes a contact angle of a first charge transport layer solution 150a (see FIG. 20) relative to the pixel defining layer 120 to be different from a contact angle of the first charge transport layer solution 150a (see FIG. 4) relative to a surface processing layer 140 (see FIG. 19) when a first charge transport layer 150 is formed using an inkjet print method or nozzle print method. For example, the pixel defining layer 120 may include an insulating material that causes the contact angle of the first charge transport layer solution 150a (see FIG. 20) relative to the pixel defining layer 120 to be 40 degrees or more

**[0089]** Referring to FIG. 18, a first charge injection layer 130 is formed on the first electrode 110. The first charge injection layer 130 may be formed not only on the first electrode 110 but also on the whole surface of the pixel defining layer 120 using, e.g., slit coating.

**[0090]** Referring to FIG. 19, the surface processing layer 140 is formed on the first charge injection layer 130, to extend from inside the opening OP of the pixel defining layer 120 to a top surface of the pixel defining layer 120. The surface processing layer 140 may have a plurality of grooves g1 in a portion extending on the top surface of the pixel defining layer 120. The surface processing layer 140 may be formed, for example, by a photolithography process. The surface processing layer 140 may be shaped as illustrated in FIG. 3. The grooves g1 of the surface processing layer 140 may also be formed as the grooves g2 through g8as in the embodiments of FIGS. 6 through 13.

**[0091]** The surface processing layer 140 may include a conductive primer that causes the surface processing layer 140 to be more lyophilic to the first charge transport layer solution 150a (see FIG. 20) than the pixel defining layer 120, e.g., causes the contact angle of the first charge transport layer solution 150a (see FIG. 20) relative to the surface processing layer 140 to be smaller than the contact angle of the first charge transport layer solution 150a (see FIG. 20) relative to the pixel defining layer 120. In one embodiment, the surface processing layer 140 may include a conductive primer that causes the contact angle of the first charge transport layer solution 150a (see FIG. 20) relative to the surface processing layer 140 to be 20 degrees or less.

**[0092]** Thus, when the first charge transport layer solution 150a (see FIG. 20) is ejected onto the surface processing layer 140 inside the opening OP of the pixel defining layer 120 in each pixel PX using an inkjet print method or nozzle print method, the first charge transport layer solution 150a (see FIG. 20) may be stably confined within the corresponding pixel PX, without spreading to the exposed top surface of the pixel defining layer 120 due to high wettability of the first charge transport layer solution 150a (see FIG. 20) relative to the surface processing layer 140. The first charge transport layer 150(see FIG. 21) may be uniformly formed on the surface processing layer 140.

**[0093]** Referring to FIGS. 20 and 21, the first charge transport layer 150 is formed on the surface processing layer 140. For example, referring to FIG. 20, the first charge transport layer solution 150a is ejected from an ejection device 10 into the opening OP of the pixel defining layer 120 using an inkjet print method or nozzle print method. Here, the ejection pressure and ejection speed of the ejection device 10 may generate a large force F with which the first charge transport layer solution 150a spreads beyond the surface processing layer 140. As a result, the first charge transport layer solution 150a may spread from an edge of the surface processing layer 140.

**[0094]** However, the grooves g1 that form roughness at the edge of the surface processing layer 140 may increase resistance to the force F. The increased resistance may prevent the first charge transport layer solution 150a from spreading beyond the surface processing layer 140, thus confining the first charge transport layer solution 150a in the grooves g1.

**[0095]** Referring to FIG. 21, the first charge transport layer solution 150a is dried to form the first charge transport layer 150, which forms pinning points P with the surface processing layer 140, on the surface processing layer 140. The first charge transport layer 150 may be conformally formed along the surface processing layer 140 having the grooves g1.

**[0096]** Referring to FIGS. 22 and 23, a light-emitting layer 160 is formed on the first charge transport layer 150. For example, referring to FIG. 22, a light-emitting layer solution 160a is ejected from an ejection device 20 into the opening OP of the pixel defining layer 120 using an inkjet print method or nozzle print method. Here, the light-emitting layer solution 160a may contain the same solvent as the first charge transport layer solution 150a of FIG. 20. In this case, the first charge transport layer 150 may become lyophilic to the light-emitting layer solution 160a, and the light-emitting layer solution 160a, when ejected onto the first charge transport layer 150, may be prevented from spreading beyond the first charge transport layer 150 to the exposed top surface of the pixel defining layer 120.

**[0097]** Referring to FIG. 23, the light-emitting layer solution 160a is dried to form the light-emitting layer 160 on the first charge transport layer 150.

**[0098]** Referring to FIG. 24, a second transport layer 170, a second charge injection layer 180, and a second electrode 190 are formed on the light-emitting layer 160. The second charge transport layer 170, the second charge injection layer 180 and the second electrode 190 may be formed, for example, by a deposition process.

**[0099]** The method of manufacturing a light-emitting display device according to the current embodiment may further include placing an encapsulation substrate on the second electrode 190. In addition, the method of manufacturing a light-emitting display device according to the current embodiment may include placing a spacer between the second electrode 190 and the encapsulation

substrate. Various methods may be used for placing the encapsulation substrate and the spacer.

**[0100]** Embodiments of the present invention provide at least one of the following advantages. A light-emitting display device according to one embodiment includes a surface processing layer having roughness formed on an edge thereof by a plurality of grooves. The surface processing layer having roughness may prevent a first charge transport layer solution ejected onto the surface processing layer using an inkjet print method or a nozzle print method from spreading beyond the surface processing layer. Accordingly, this may prevent the first charge transport layer solution from spreading toward an adjacent pixel which emits light of a different color and thus unwantedly forming a first charge transport layer up to part of the adjacent pixel.

**[0101]** Therefore, the light-emitting display device according to at least one embodiment may prevent a light-emitting layer from being formed on the first charge transport layer up to part of the adjacent pixel, thereby avoiding a situation where light-emitting layers emitting light of different colors overlap each other in adjacent pixels. As a result, this can prevent degradation of display quality due to the display of an unwanted mixed color on the light-emitting display device being driven.

**[0102]** Example embodiments have been disclosed herein, and although specific terms are employed, they are used and are to be interpreted in a generic and descriptive sense only and not for purpose of limitation. In some instances, as would be apparent to one of skill in the art as of the filing of the present application, features, characteristics, and/or elements described in connection with a particular embodiment may be used singly or in combination with features, characteristics, and/or elements described in connection with other embodiments unless otherwise indicated.

## Claims

### 1. A light-emitting display device (100), comprising:

a substrate (105) including a plurality of pixels (PX);  
 a first electrode (110) on the substrate (105) for each pixel (PX);  
 a pixel defining layer (120) on the substrate (105) to define each pixel (PX), the pixel defining layer (120) having an opening (OP) to expose the first electrode (110);  
 a charge injection layer (130) on the first electrode (110);  
 a surface processing layer (140) on the charge injection layer (130), the surface processing layer (140) extending from inside the opening (OP) of the pixel defining layer (120) to a top surface of the pixel defining layer (120), the surface processing layer (140) including a plurality of

grooves (g1) in a portion extending on the top surface of the pixel defining layer (120);  
 a charge transport layer (150) on the surface processing layer (140);  
 a light-emitting layer (160) on the charge transport layer (150); and  
 a second electrode (190) on the light-emitting layer (160).

### 2. The device (100) as claimed in claim 1, wherein:

the grooves (g1) are on at least one side of the opening (OP) along a first direction (D1), and the first direction (D1) is toward an adjacent pixel (PX) which emits light of a different color from a color of light emitted from a pixel (PX) in which the surface processing layer (140) is formed.

### 3. The device (100) as claimed in claim 2, wherein the grooves (g1) are shaped substantially as straight lines that extend along a second direction (D2) crossing the first direction (D2).

### 4. The device (100) as claimed in claim 2, wherein the grooves (g1) have widths (w1, w2, w3) that increase in a direction toward the adjacent pixel (PX).

### 5. The device (100) as claimed in claim 2, wherein the grooves (g1) have substantially equal widths.

### 6. The device (100) as claimed in claim 2, wherein the grooves (g3) have an oblique shape between the first direction (D1) and a second direction (D2) crossing the first direction (D1).

### 7. The device (100) as claimed in claim 2, wherein the grooves (g2) have a lattice shape.

### 8. The device (100) as claimed in one of the previous claims, wherein:

the pixels (PX) include pixels (PX) arranged in a row and that emit light of a same color, and the surface processing layer (140) is formed in units of the pixels (PX).

### 9. The device (100) as claimed in one of the previous claims, wherein the charge transport layer (150) is conformally formed along the surface processing layer (140).

### 10. A method for manufacturing a light-emitting display device (100), the method comprising:

forming a first electrode (110) for each of a plurality of pixels (PX) on a substrate (105);  
 forming a pixel defining layer (120) on the substrate (105) to define each of the pixels (PX),

the pixel defining layer (120) having an opening to expose the first electrode (110) of each of the pixels (PX);

forming a charge injection layer (130) on the first electrode (110);

forming a surface processing layer (140) on the charge injection layer (130), the surface processing layer (140) extending from inside the opening (OP) of the pixel defining layer (120) to a top surface of the pixel defining layer (120) and having a plurality of grooves (g1) in a portion extending on the top surface of the pixel defining layer (120);

forming a charge transport layer (150) on the surface processing layer (140);

forming a light-emitting layer (160) on the charge transport layer (150); and

forming a second electrode (190) on the light-emitting layer (160).

11. The method as claimed in claim 10, wherein forming the charge transport layer (150) includes providing a charge transport layer solution (150a) into the opening (OP) of the pixel defining layer (120) and drying the charge transport layer solution (150a), and wherein forming the light-emitting layer (160) includes providing a light-emitting layer solution (160a) into the opening (OP) of the pixel defining layer (120) and drying the light-emitting layer solution (160a).

12. The method as claimed in claim 11, wherein the charge transport layer solution (150a) and the light-emitting layer solution (160a) include a same solvent.

13. The method as claimed in one of claims 10 to 12, wherein the charge injection layer (130) and the light-emitting layer (160) are formed using an inkjet print method or a nozzle print method.

14. The method as claimed in one of the previous claims, wherein:

the grooves (g1) are on at least one side of the opening (OP) along a first direction (D1), and the first direction (D1) is toward an adjacent pixel (PX) which emits light of a different color from a color of light emitted from a pixel (PX) in which the surface processing layer (140) is formed.

15. The method as claimed in claim 10, wherein the charge transport layer (150) is conformally formed along the surface processing layer (140).

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FIG. 1

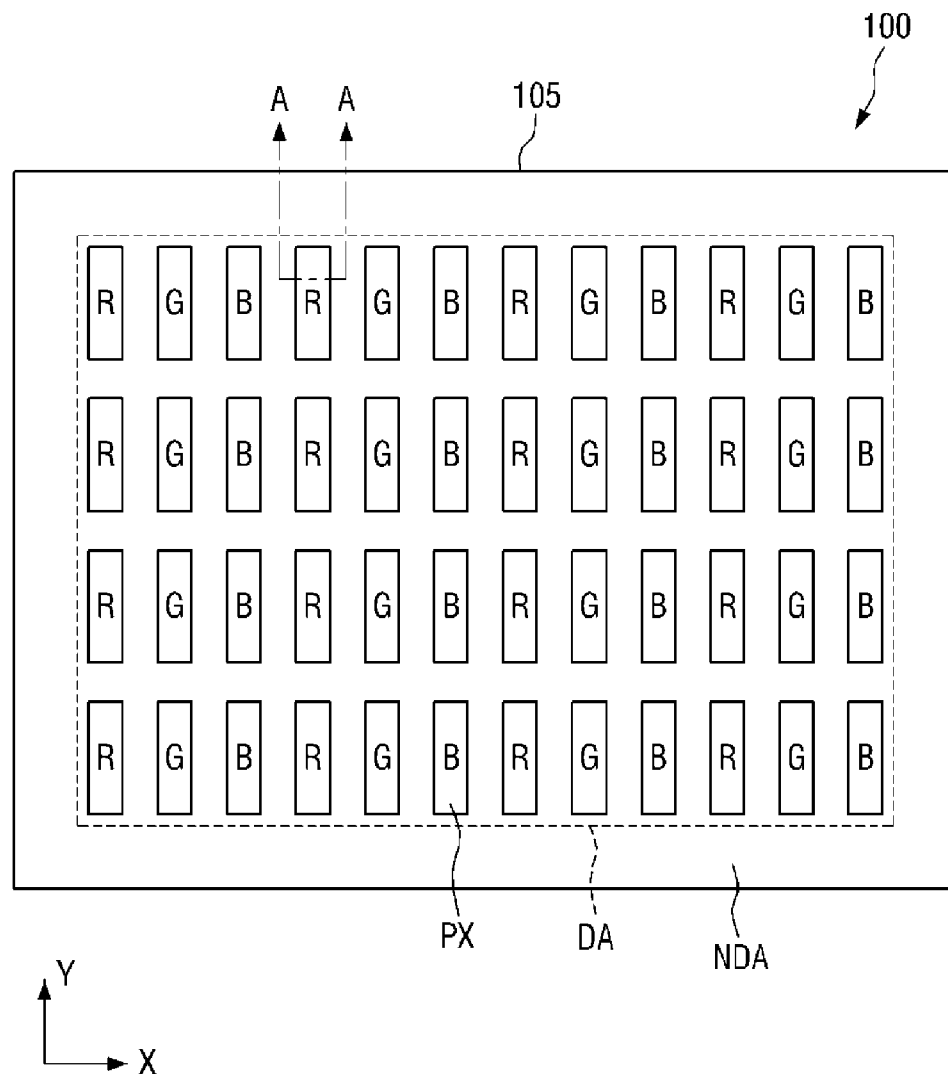


FIG. 2

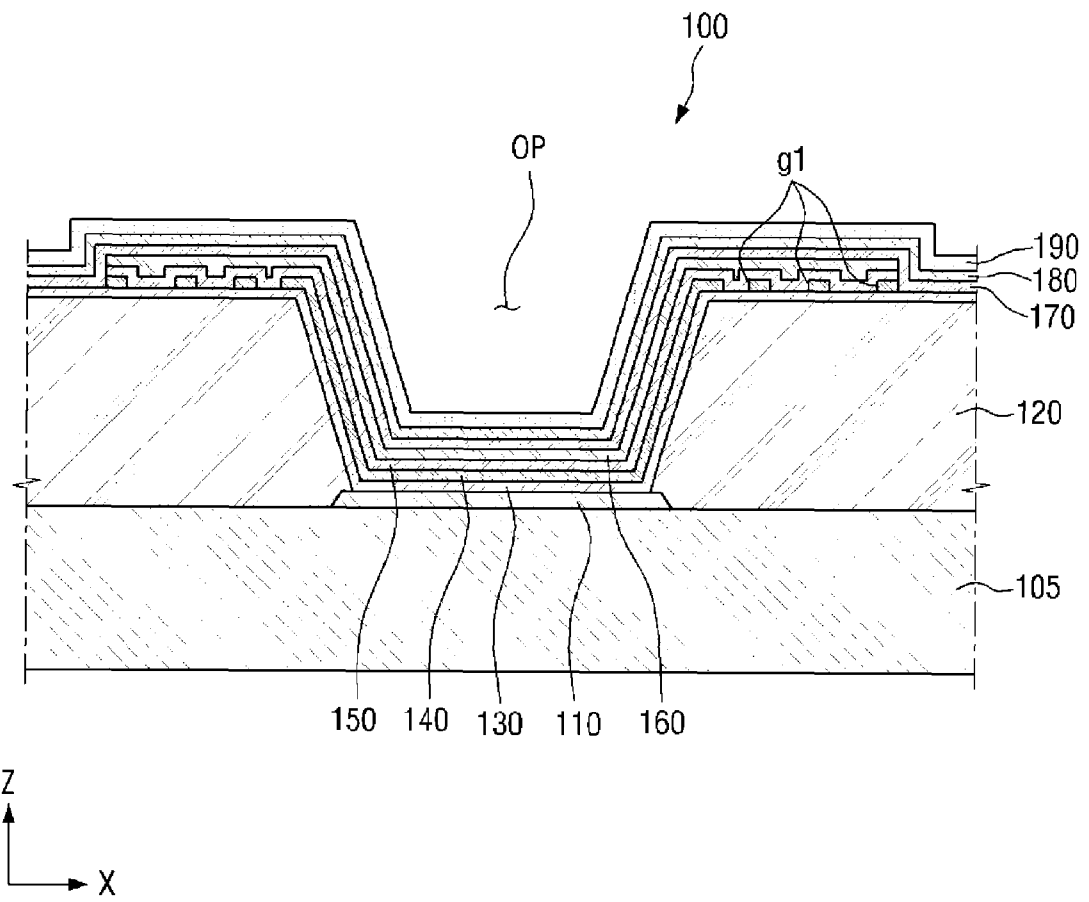


FIG. 3

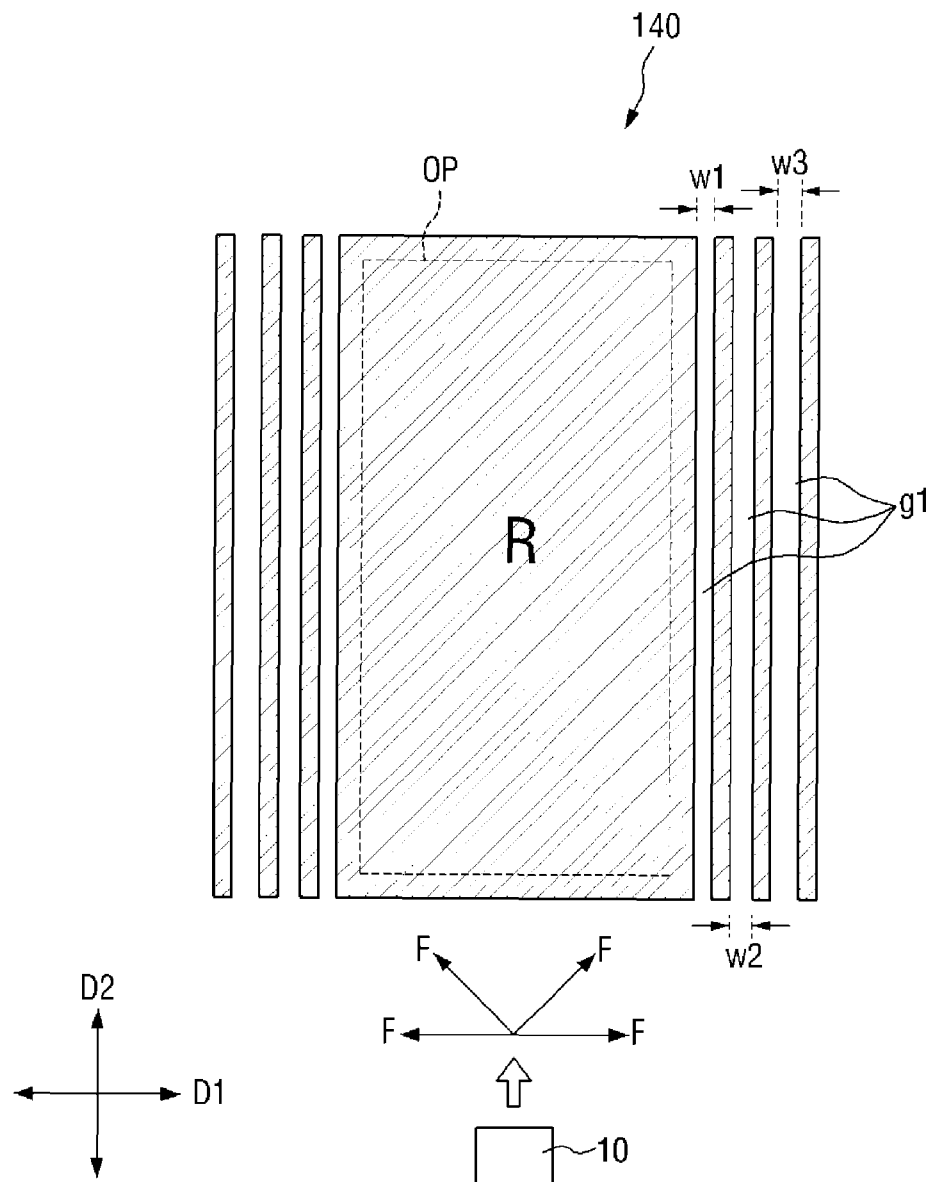


FIG. 4

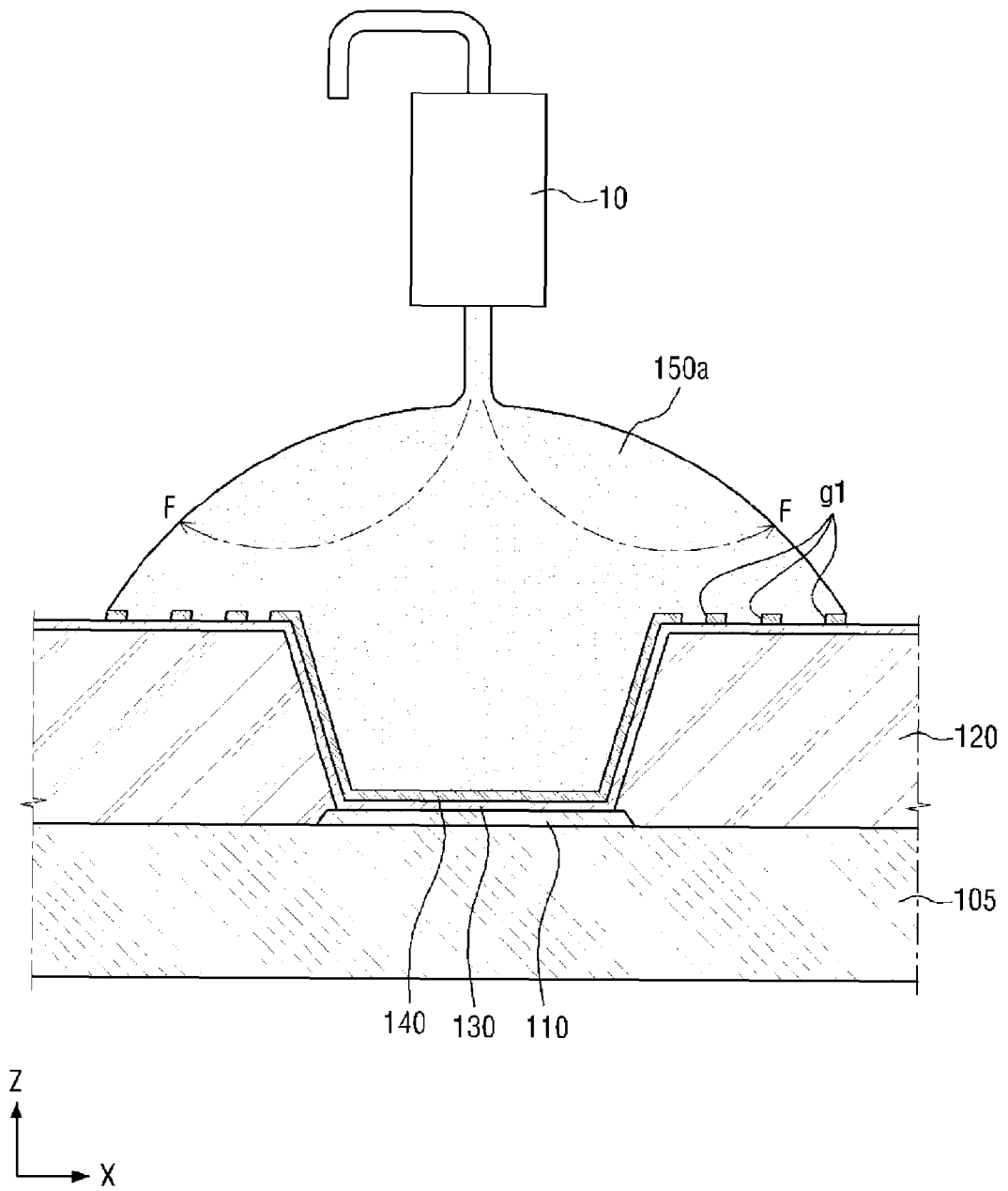


FIG. 5

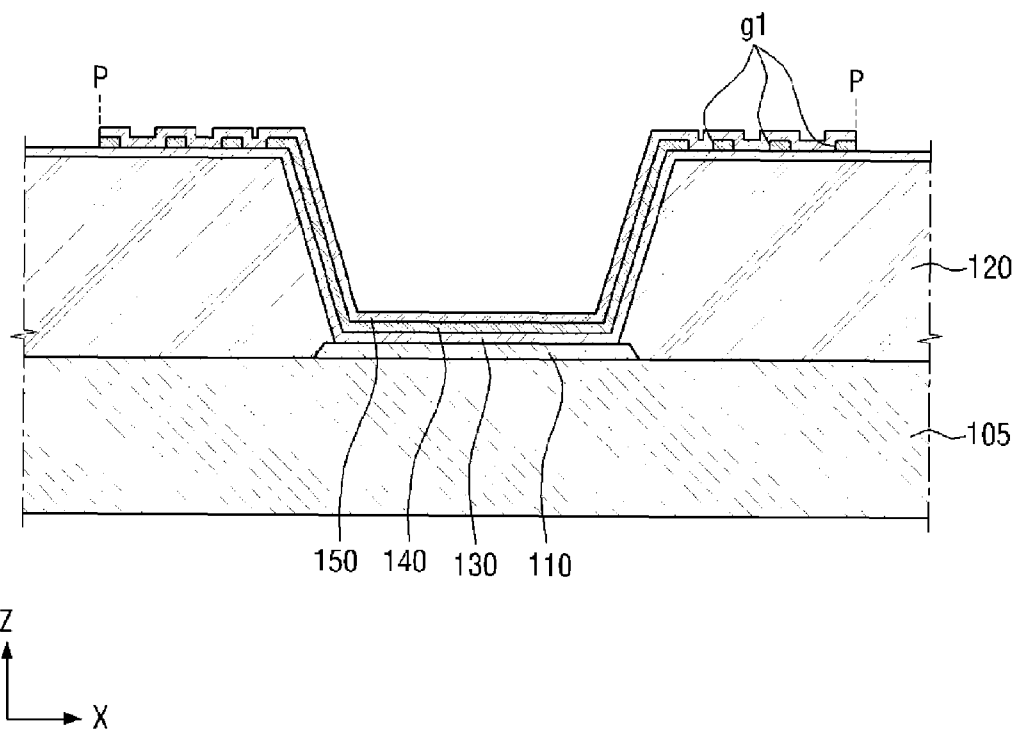


FIG. 6

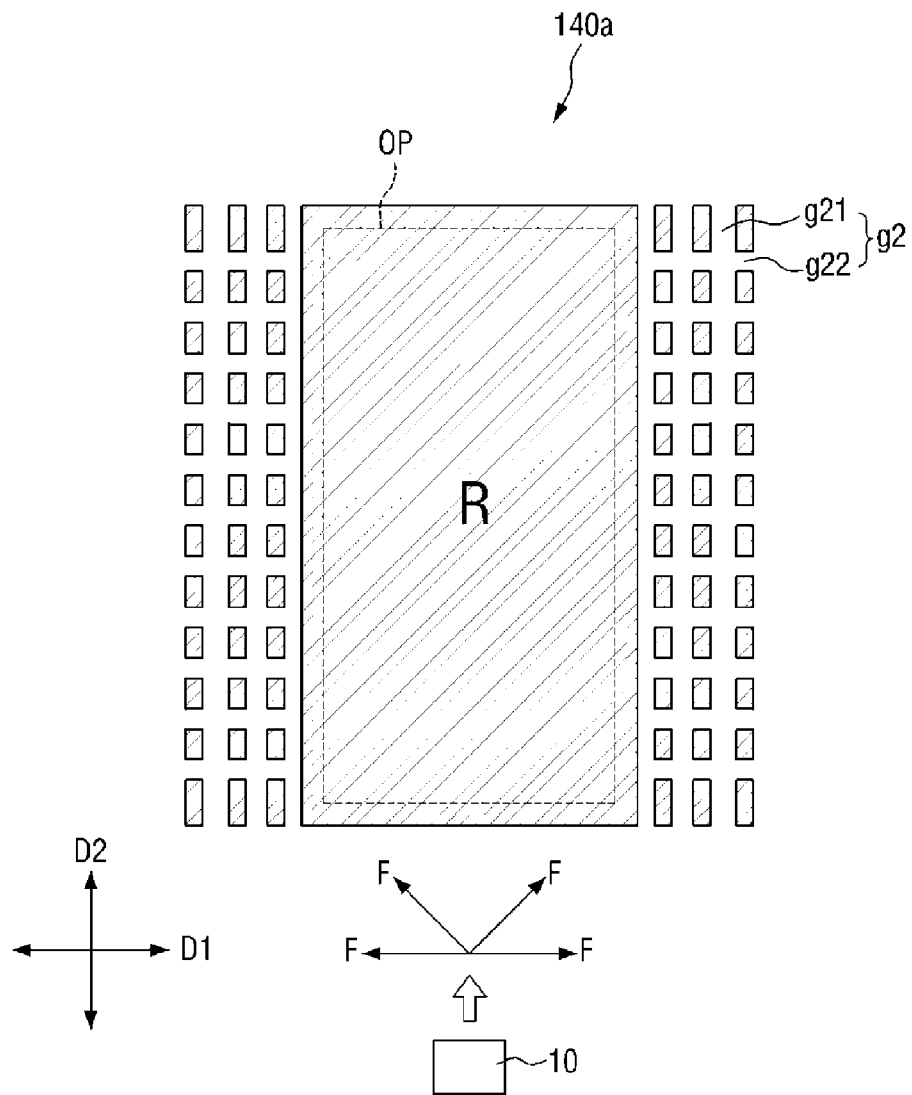


FIG. 7

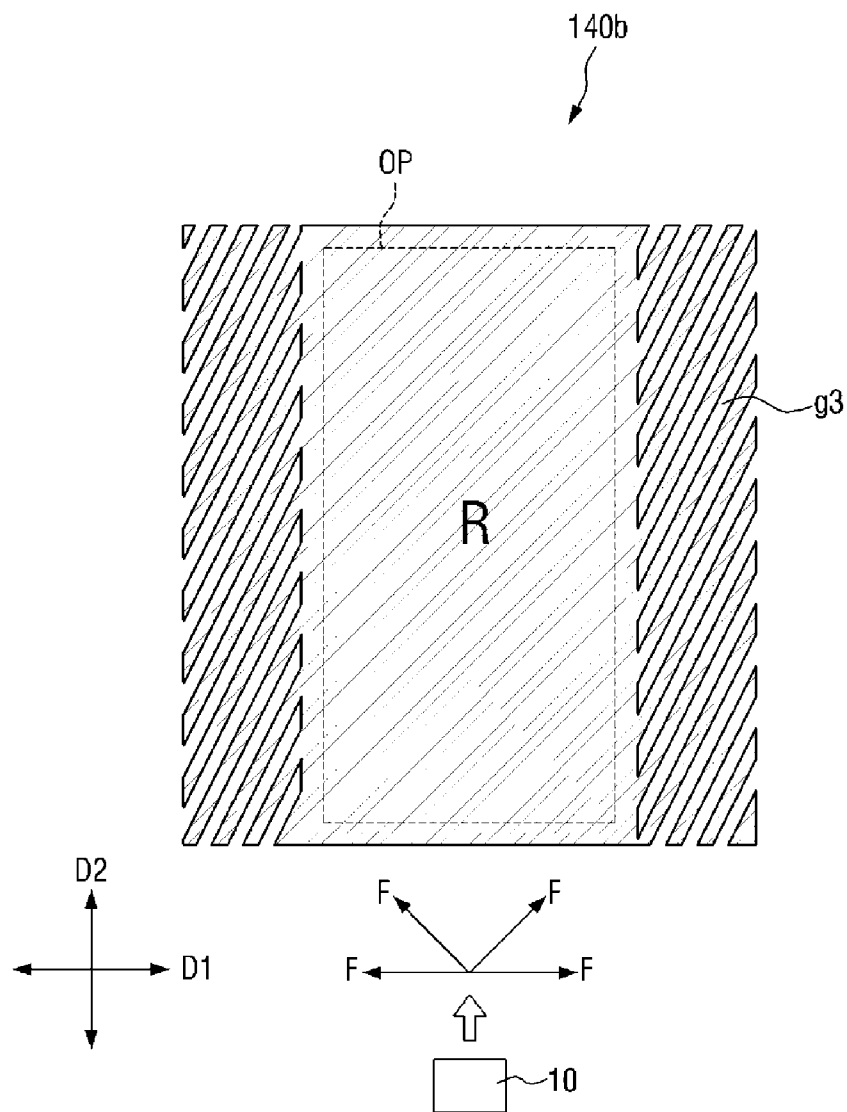


FIG. 8

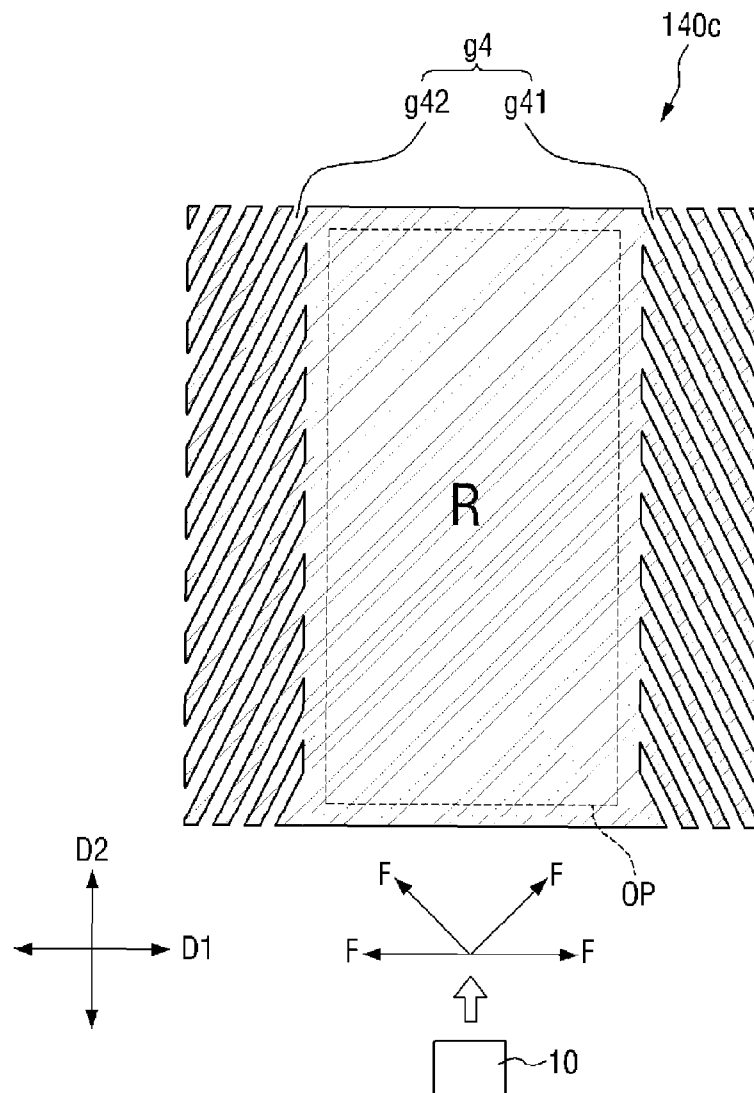


FIG. 9

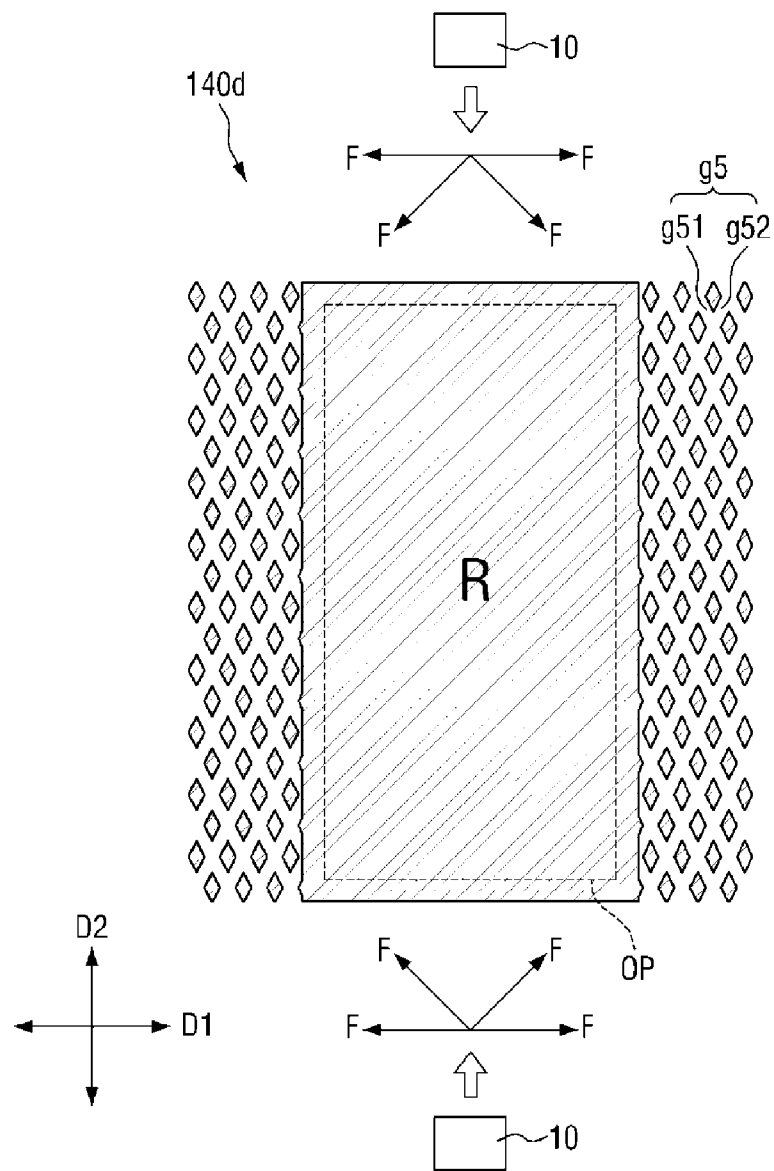


FIG. 10

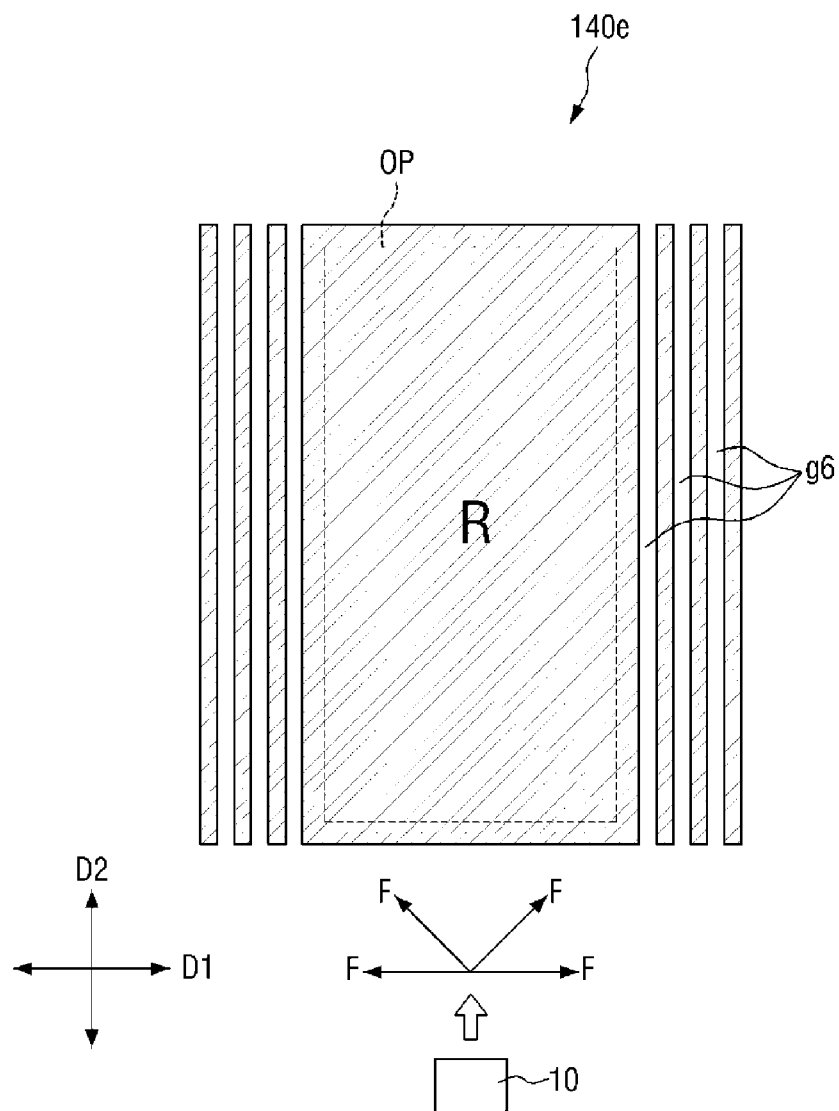


FIG. 11

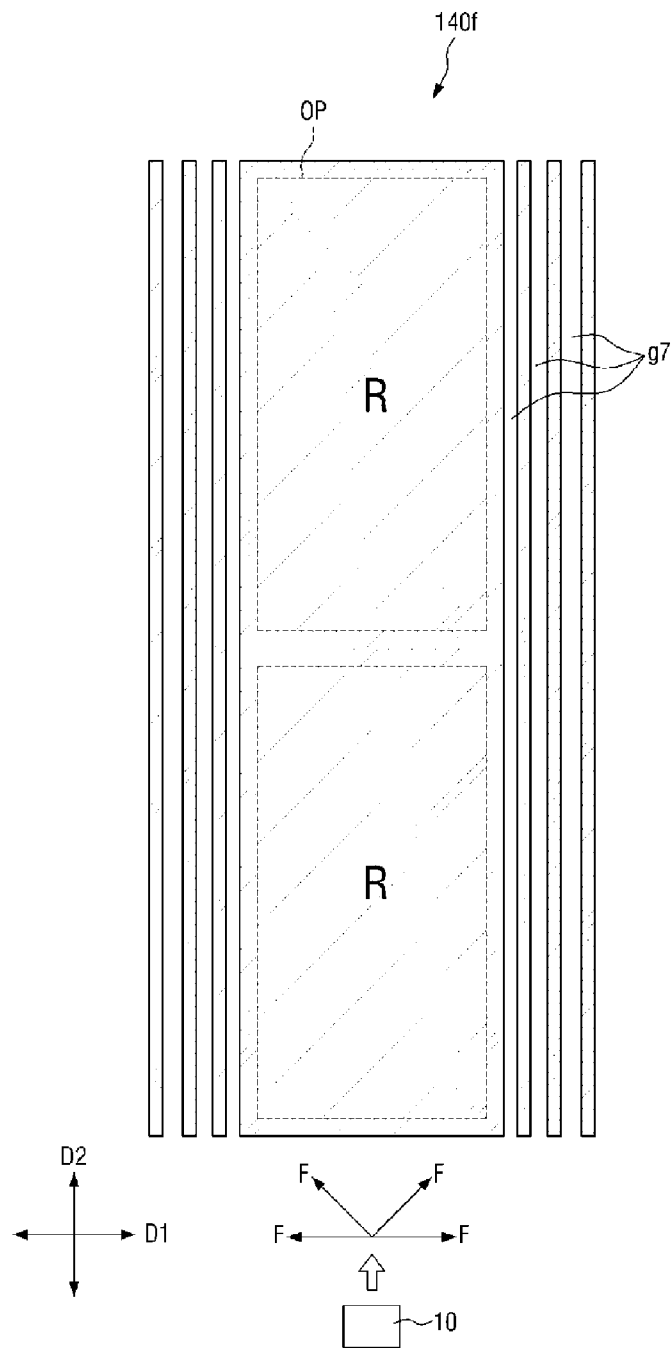


FIG. 12

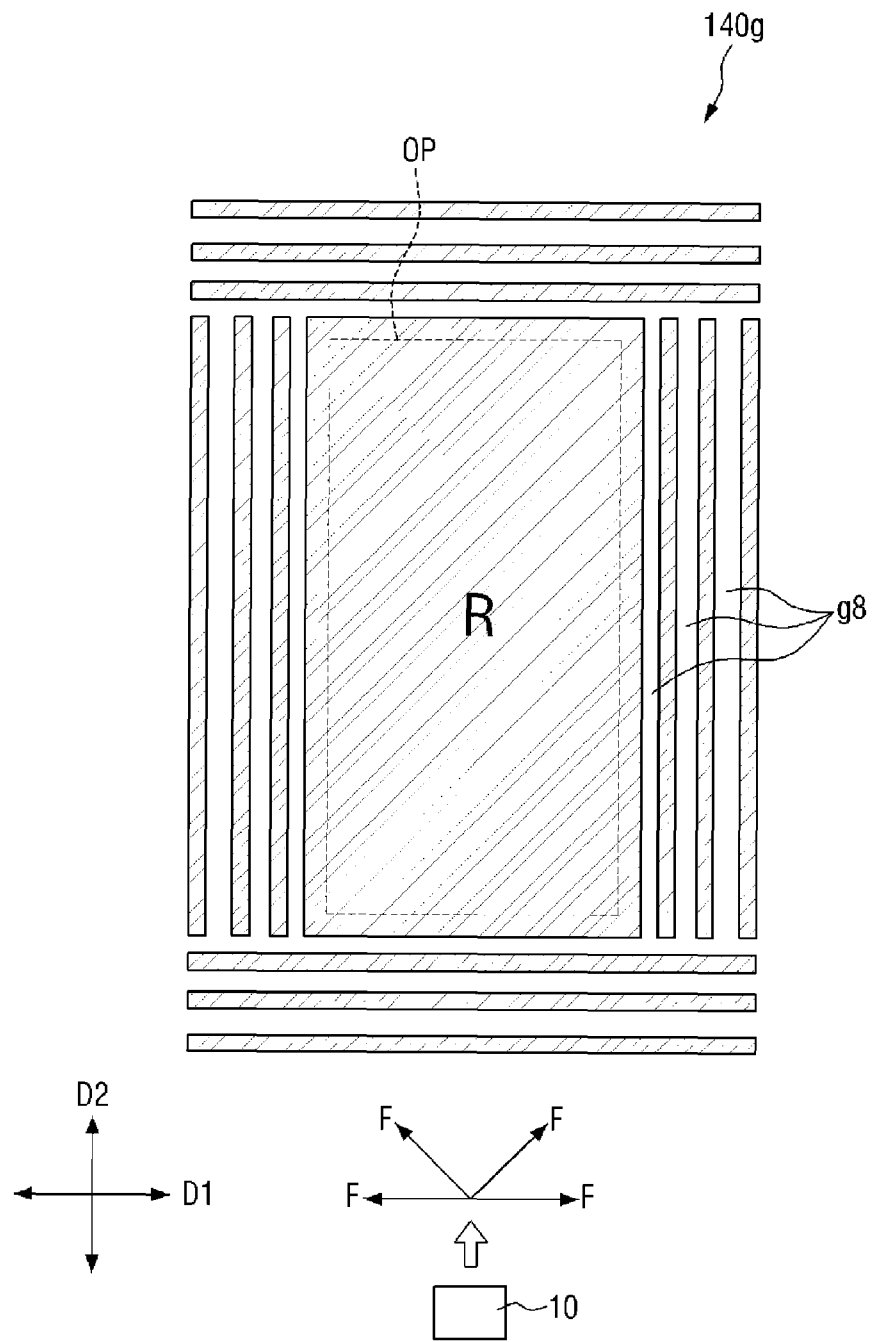


FIG. 13

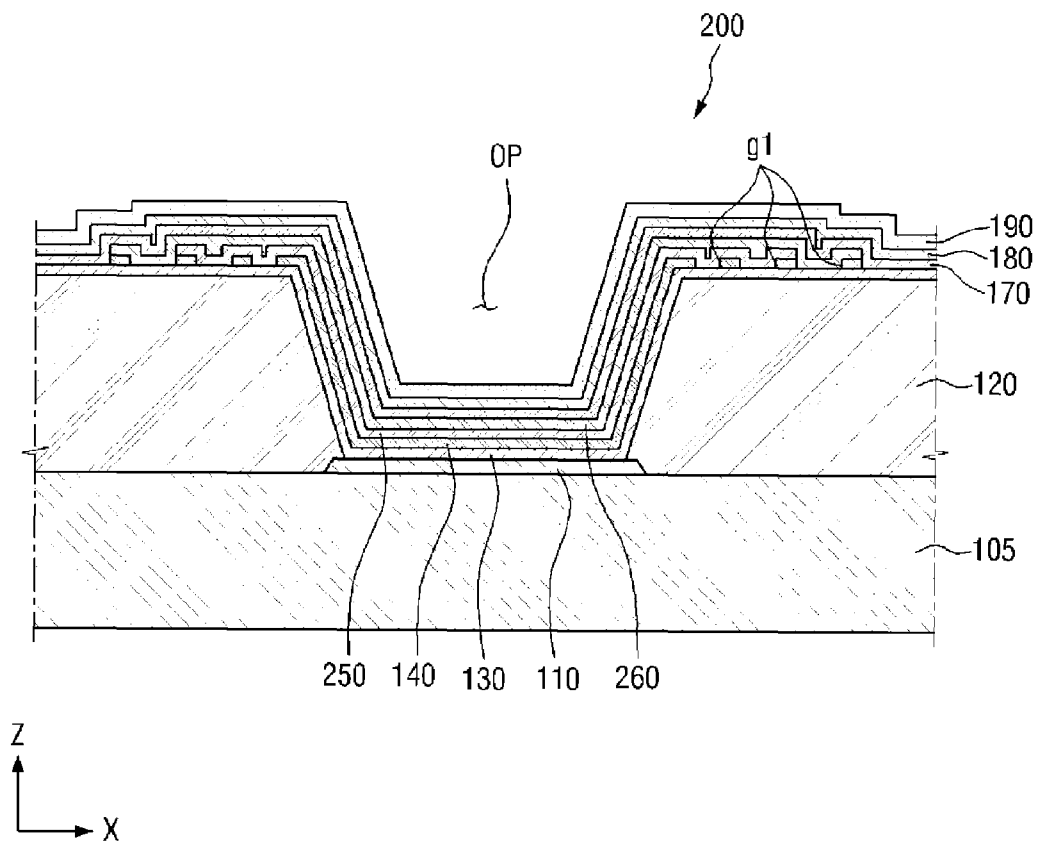


FIG. 14

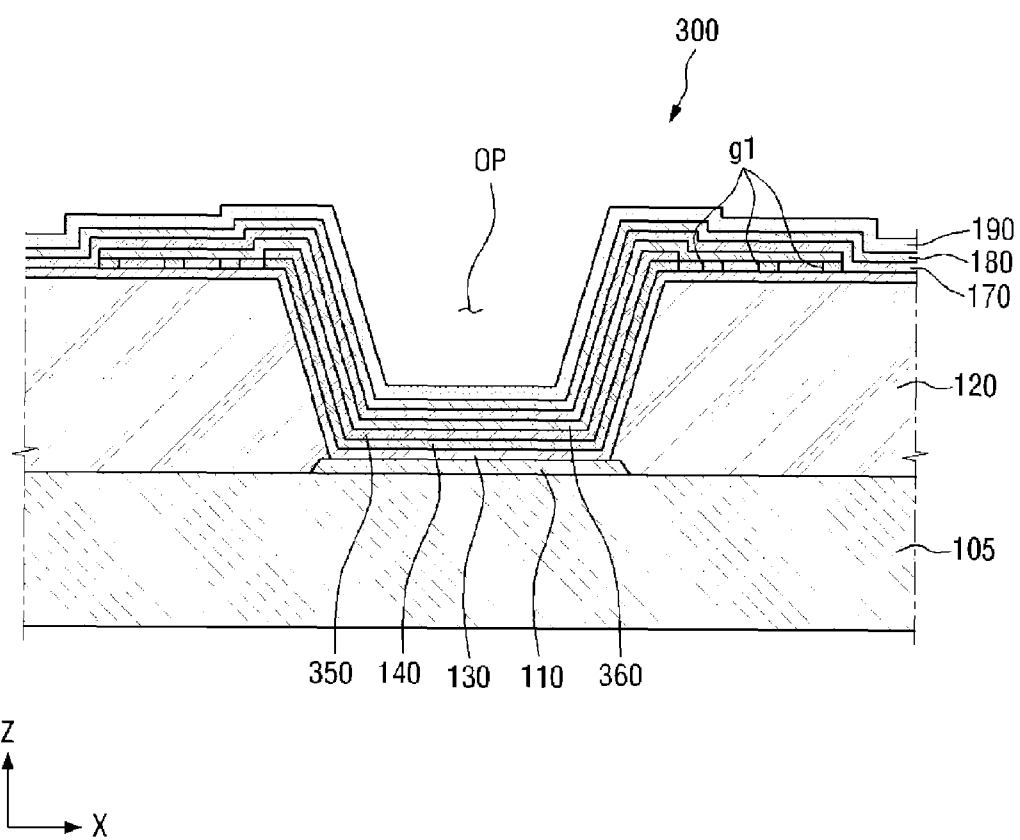


FIG. 15

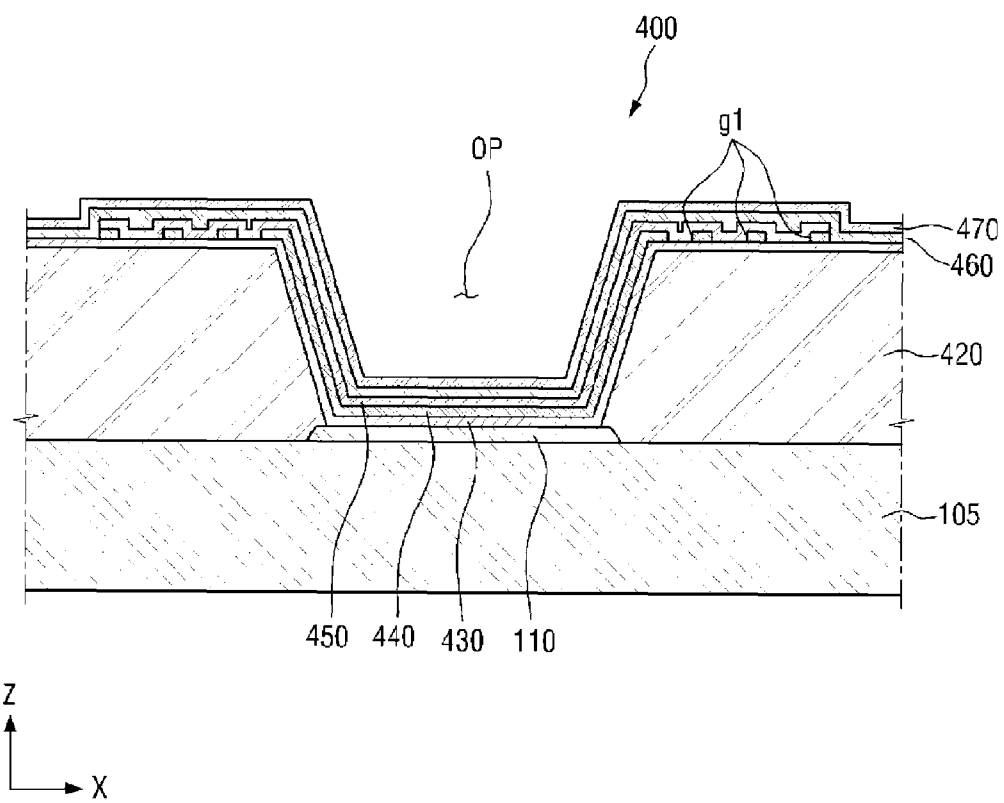


FIG. 16

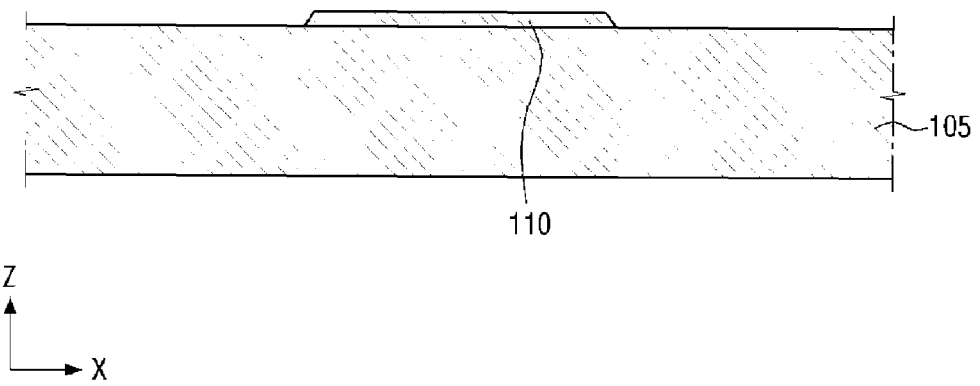


FIG. 17

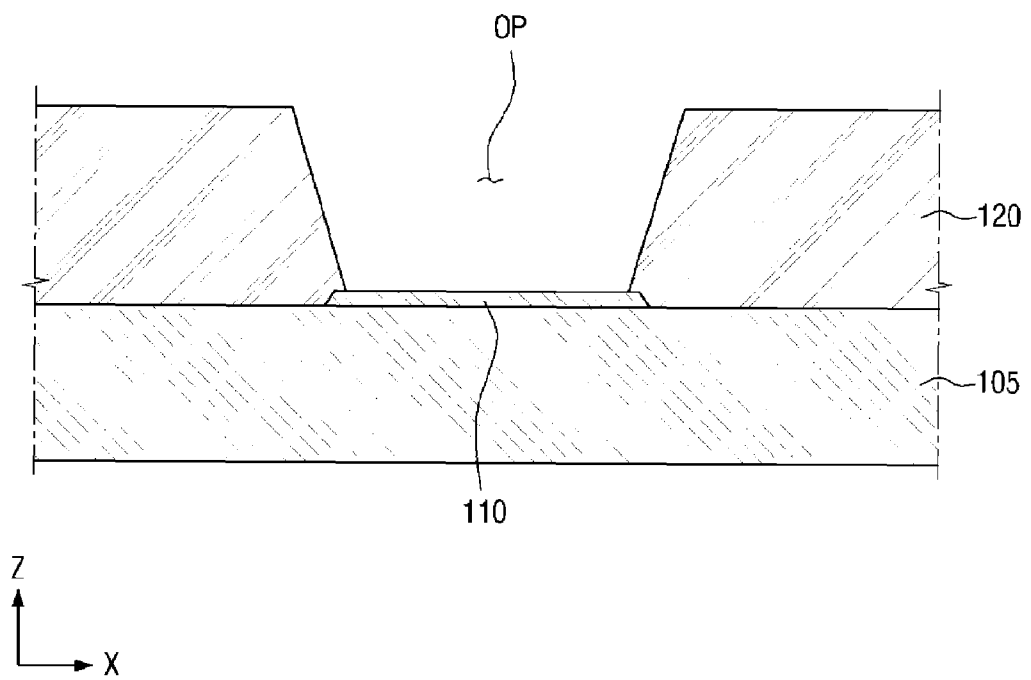


FIG. 18

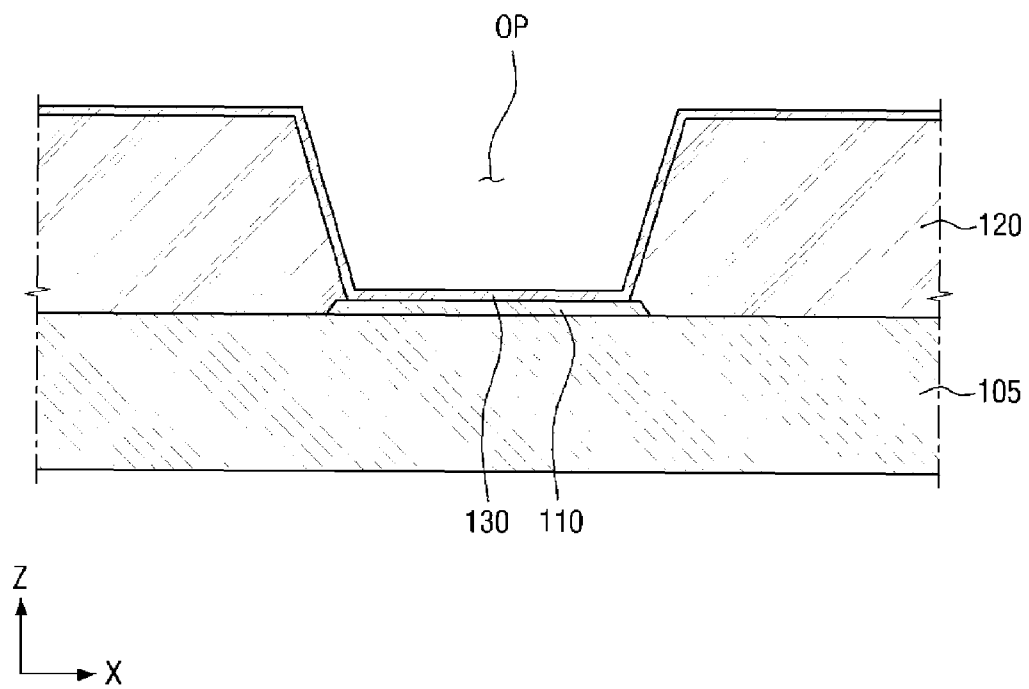


FIG. 19

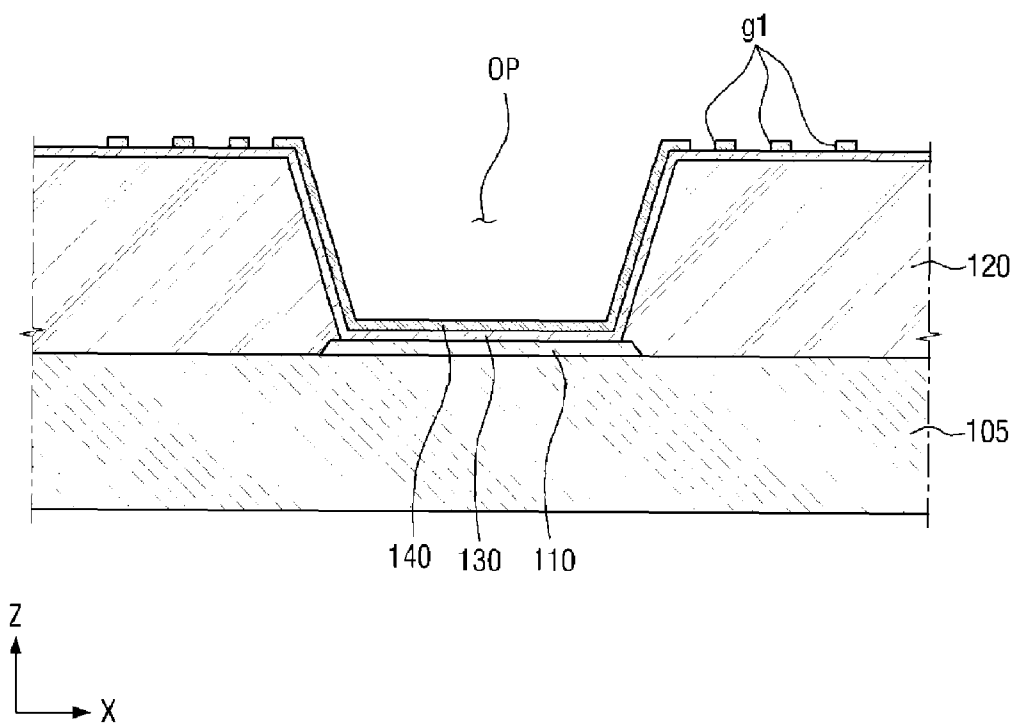


FIG. 20

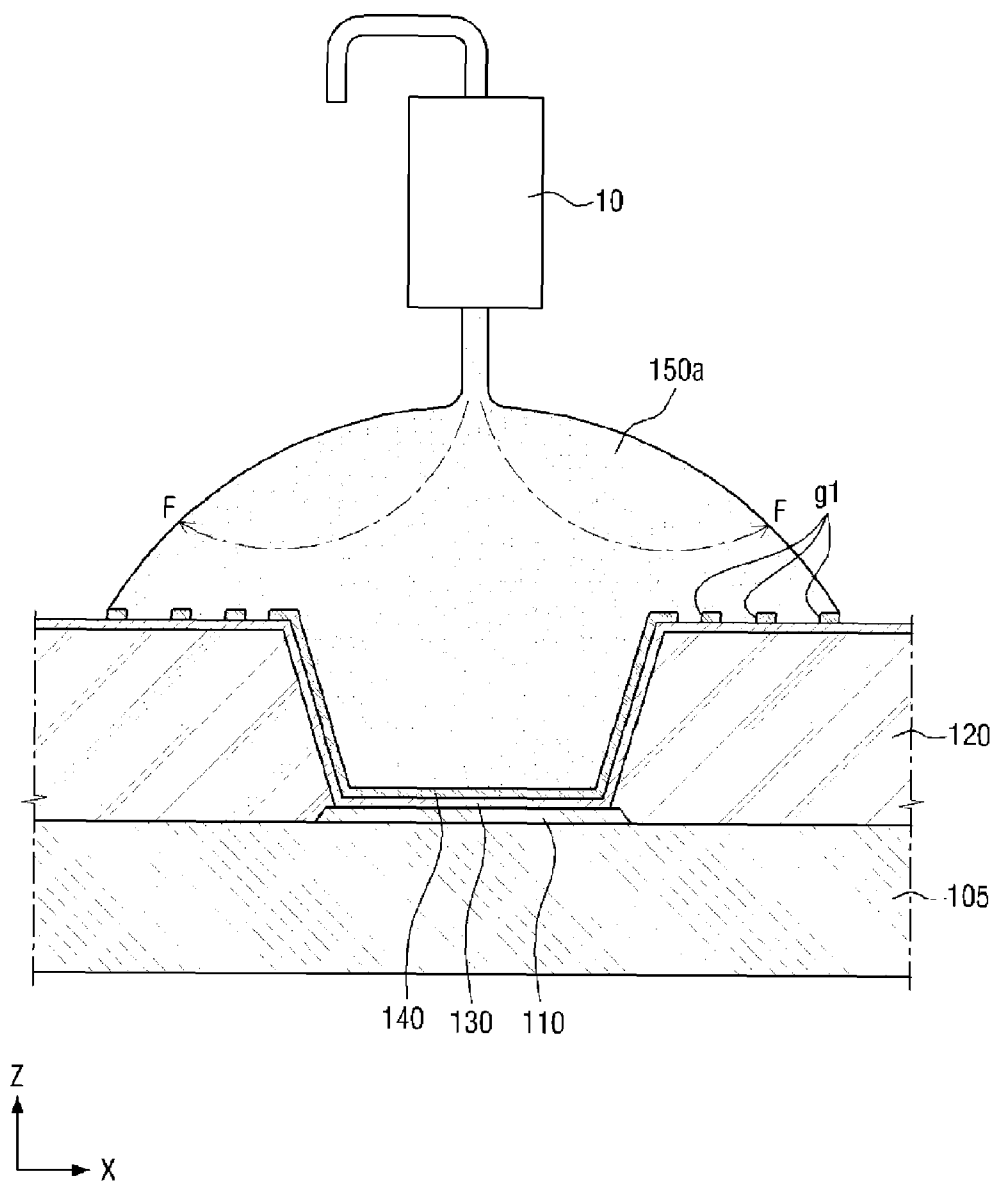


FIG. 21

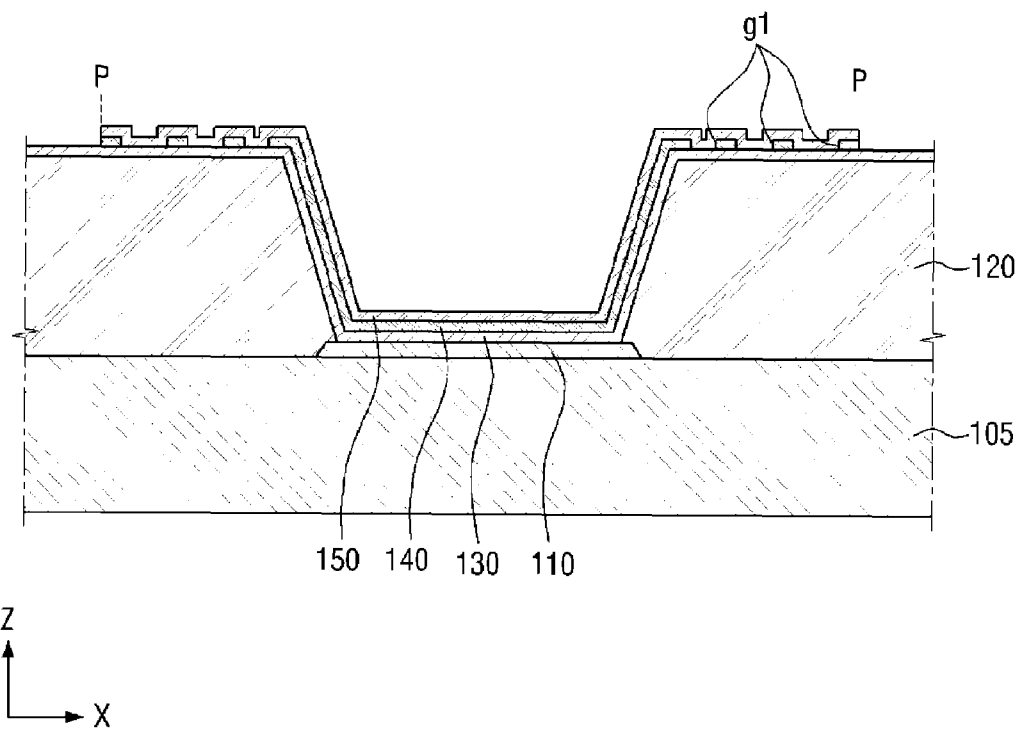


FIG. 22

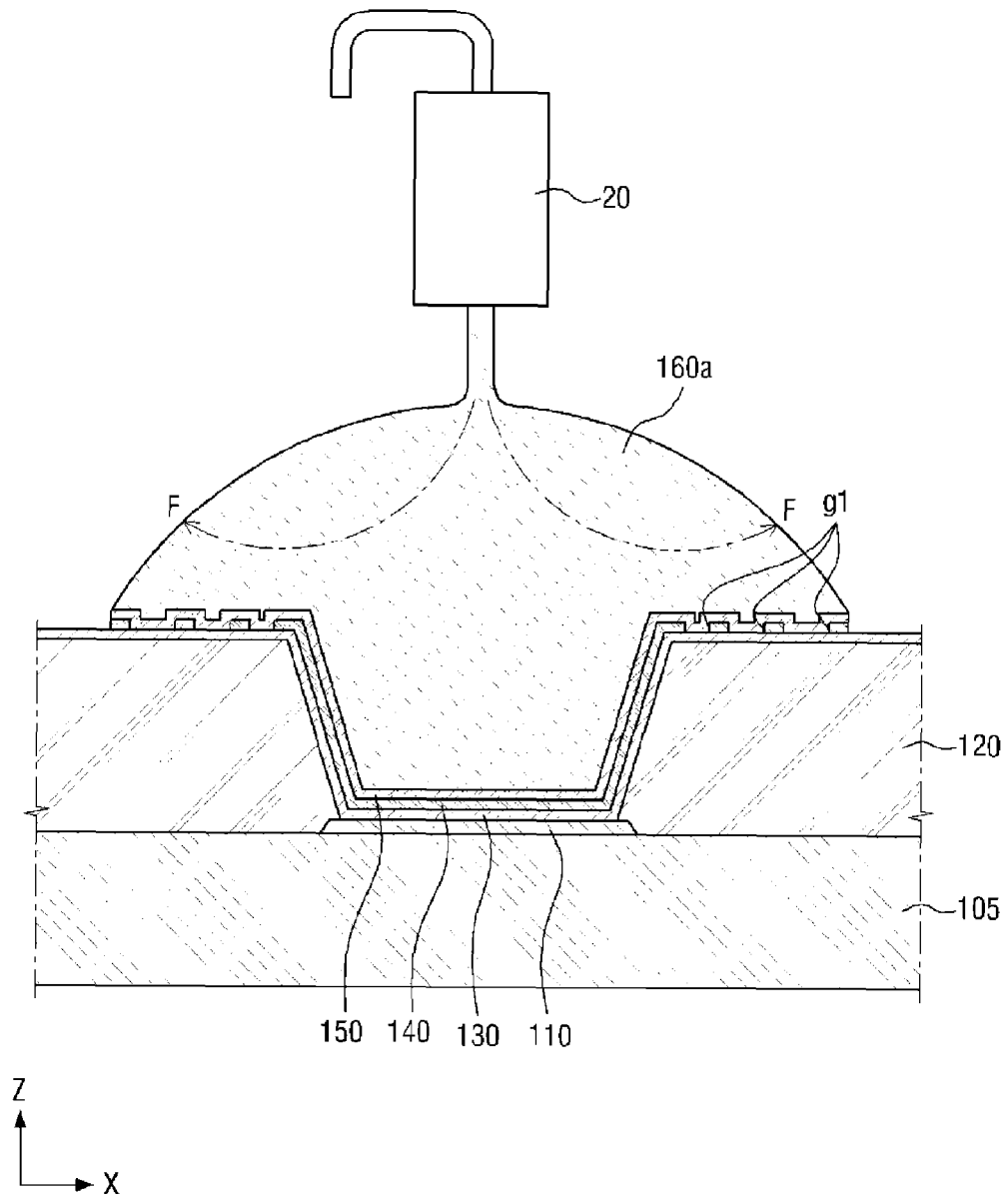


FIG. 23

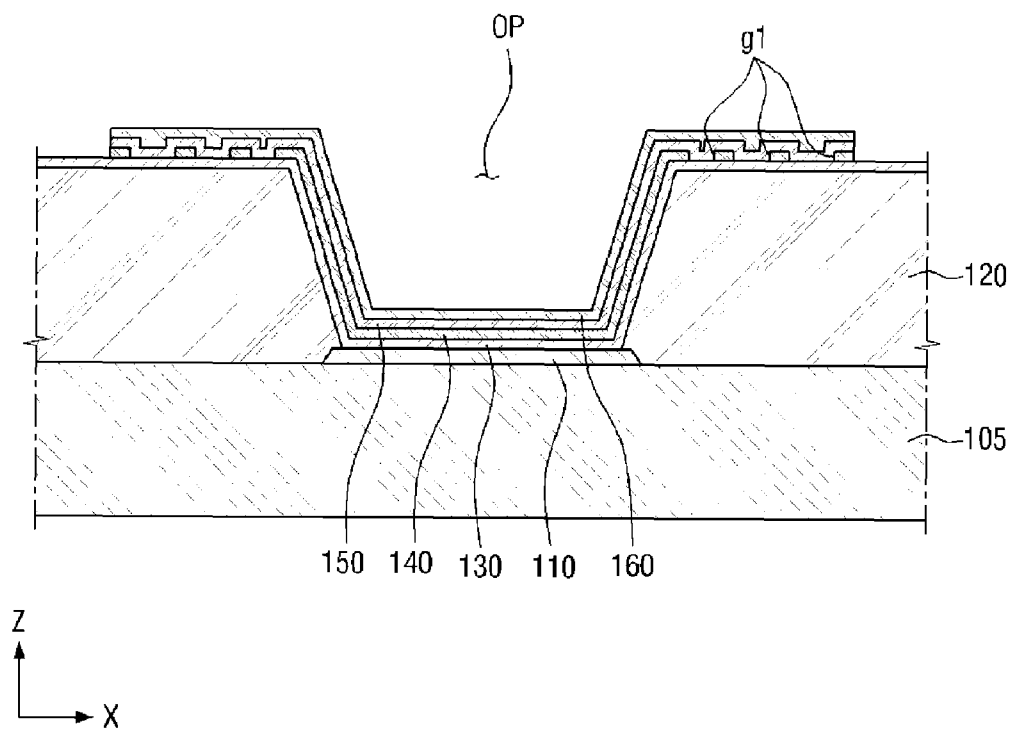
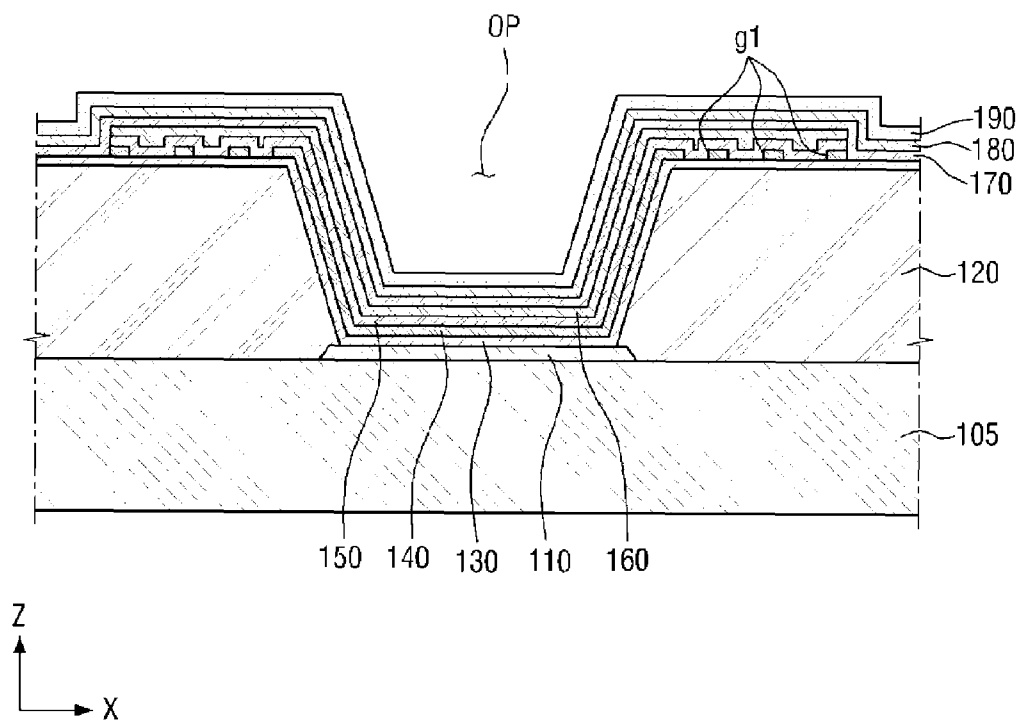


FIG. 24





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Application Number  
EP 15 16 5415

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| DOCUMENTS CONSIDERED TO BE RELEVANT                                                                                                                                                                                                                    |                                                                                                                                                                                                                                                                                  |                                                                                                                                                                                                                                                                              |                                             |
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| Category                                                                                                                                                                                                                                               | Citation of document with indication, where appropriate, of relevant passages                                                                                                                                                                                                    | Relevant to claim                                                                                                                                                                                                                                                            | CLASSIFICATION OF THE APPLICATION (IPC)     |
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| The present search report has been drawn up for all claims                                                                                                                                                                                             |                                                                                                                                                                                                                                                                                  |                                                                                                                                                                                                                                                                              |                                             |
| Place of search<br>The Hague                                                                                                                                                                                                                           |                                                                                                                                                                                                                                                                                  | Date of completion of the search<br>18 January 2016                                                                                                                                                                                                                          | Examiner<br>Faou, Marylène                  |
| CATEGORY OF CITED DOCUMENTS<br>X : particularly relevant if taken alone<br>Y : particularly relevant if combined with another document of the same category<br>A : technological background<br>O : non-written disclosure<br>P : intermediate document |                                                                                                                                                                                                                                                                                  | T : theory or principle underlying the invention<br>E : earlier patent document, but published on, or after the filing date<br>D : document cited in the application<br>L : document cited for other reasons<br>& : member of the same patent family, corresponding document |                                             |

EPO FORM 1503 03.82 (P04C01)

**ANNEX TO THE EUROPEAN SEARCH REPORT  
ON EUROPEAN PATENT APPLICATION NO.**

EP 15 16 5415

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This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on  
The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

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| 专利名称(译)        | 发光显示装置及其制造方法                                                                                                                                                                      |         |            |
| 公开(公告)号        | <a href="#">EP2988332A1</a>                                                                                                                                                       | 公开(公告)日 | 2016-02-24 |
| 申请号            | EP2015165415                                                                                                                                                                      | 申请日     | 2015-04-28 |
| [标]申请(专利权)人(译) | 三星显示有限公司                                                                                                                                                                          |         |            |
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| 当前申请(专利权)人(译)  | 三星DISPLAY CO. , LTD.                                                                                                                                                              |         |            |
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| 发明人            | KIM, GEUN TAK                                                                                                                                                                     |         |            |
| IPC分类号         | H01L27/32 H01L51/56 H01L51/50                                                                                                                                                     |         |            |
| CPC分类号         | H01L51/5008 H01L27/3211 H01L27/3218 H01L27/3246 H01L27/3262 H01L51/0004 H01L51/0005 H01L51/5012 H01L51/5056 H01L51/5072 H01L51/5088 H01L51/5092 H01L51/5206 H01L51/5221 H01L51/56 |         |            |
| 优先权            | 1020140109588 2014-08-22 KR                                                                                                                                                       |         |            |
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#### 摘要(译)

一种发光显示装置，包括具有多个像素 (PX) 的基板 (105)。对于每个像素，在基板 (105) 上提供第一电极 (110)，并且像素限定层 (120) 限定每个像素 (PX)。像素限定层 (120) 具有开口以暴露第一电极 (110)。电荷注入层 (130) 位于第一电极 (110) 上，表面处理层 (140) 位于电荷注入层 (130) 上。表面处理层 (140) 从像素限定层 (120) 的开口内部延伸到像素限定层 (120) 的顶表面。表面处理层 (140) 包括在像素限定层 (120) 的顶表面上延伸的部分中的多个凹槽。电荷传输层 (150) 位于表面处理层 (140) 上，发光层 (160) 位于电荷传输层 (150) 上，第二电极 (190) 位于发光层 (160) 上。

FIG. 2

