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(54) **DC-DC converter and organic light emitting display using the same**

DC/DC-Wandler und organische lichtemittierende Anzeige, die diesen Wandler einsetzt

Convertisseur CC-CC et affichage électroluminescent organique l'utilisant

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Description

[0001] The present invention relates to a DC-DC converter and an organic light emitting display using the same.

[0002] Recently, various flat panel displays having less weight and volume than cathode ray tubes (CRTs) have been developed. Flat panel displays include liquid crystal displays, field emission displays, plasma display devices, organic light emitting displays, etc.

[0003] Among others, organic light emitting displays generate images using organic light emitting diodes (OLEDs) that generate light by the recombination of electrons and holes generated corresponding to the flow of current.

[0004] Organic light emitting displays have various advantages, e.g., excellent color reproducibility and slimness so that their applications expand to various markets such as cellular phones, PDAs, MP3s, etc.

[0005] FIG. 1 is a circuit diagram showing a pixel in a conventional organic light emitting display. Referring to FIG. 1, the pixel is coupled to a data line Dm and a scan line Sn and includes a first transistor T1, a second transistor T2, a capacitor Cst, and an organic light emitting diode OLED.

[0006] A source of the first transistor T1 is coupled to a first power supply ELVDD, a drain of the first transistor T1 is coupled to an anode electrode of the organic light emitting diode OLED, and a gate of the first transistor T1 is coupled to a first node N1. A source of the second transistor T2 is coupled to the data line Dm, a drain of the second transistor T2 is coupled to the first node N1, and a gate of the second transistor T2 is coupled to the scan line Sn. A first electrode of the capacitor Cst is coupled to the first power supply ELVDD, and a second electrode of the capacitor Cst is coupled to the first node N1. An anode electrode of the organic light emitting diode OLED is coupled to the drain of the first transistor T1 and a cathode electrode of the organic light emitting diode OLED is coupled to a second power supply ELVSS.

[0007] The voltage at the first node N1 of the pixel as described above is determined according to a data signal transferred through the data line Dm, and the first transistor T1 of the pixel drives a current from the first power supply ELVDD to the second power supply ELVSS according to the voltage of the first node N1. The organic light emitting diode OLED emits light according to the operation of the pixel, and a current having a magnitude represented by the following equation 1 flows through the organic light emitting diode OLED.

[Equation 1]

$$I_{oled} = \frac{\beta}{2} (V_{GS} - V_{th})^2 = \frac{\beta}{2} (V_{data} - ELVDD - V_{th})^2$$

[0008] Here, I_{oled} represents the current flowing through the organic light emitting diode, V_{GS} represents the voltage between the gate and source of the first transistor T1, V_{th} represents the threshold voltage of the first transistor T1, V_{data} represents the voltage of the data signal, and ELVDD represents the voltage of the first power supply.

[0009] The organic light emitting display having the pixel as described above operates by receiving a power supply from a battery. Therefore, in order to extend a use time of the organic light emitting display, the battery may be desired to have a larger capacity. If the capacity of the battery becomes larger, the voltage of the power supply output from the battery may become higher.

[0010] The first voltage ELVDD and the second voltage ELVSS transferred to the pixel are generated by receiving the power supply from a DC-DC converter that includes a booster circuit and an inverter circuit. However, in a conventional booster circuit, the boosting range of the voltage has been limited. For example, in a case where the first power supply ELVDD has a voltage of 4.6V, if the input voltage is between 2.9V and 4.2V, the booster circuit generates a voltage of 4.6V. However, if the input voltage is 4.2V or higher, the booster circuit may generate a voltage higher than 4.6V.

[0011] Therefore, in the related art, the first power supply ELVDD has a voltage of 4.6V or higher, and thus a battery having a large capacity cannot be adopted.

[0012] US2007/0273306 discloses an LED lighting apparatus including a boosting circuit and an inverted boosting circuit controlled by a dual PWM control IC. WO2008/155600 discloses a switched-mode power converter. EP 1 951 005 and US2005/231133 disclose an organic light emitting display having a DC-DC converter.

[0013] According to the invention, there is provided a DC-DC converter according to claim 1 and an organic light emitting display using the converter according to claim 7.

[0014] One aspect of the present invention provides a DC-DC converter that generates voltage of a first power supply so that current flows through an organic light emitting diode irrespective of a power supply voltage from a battery, and an organic light emitting display using the same.

[0015] With the DC-DC converter and the organic light emitting display using the same according to exemplary embodiments of the present invention, the voltage from the power supply output from the battery can be set to be high, making it possible to utilize a high-capacity battery. Therefore, the use time of the organic light emitting display can be extended.

[0016] The accompanying drawings, together with the specification illustrate exemplary embodiments of the present invention, and, together with the description, serve to explain the principles of the present invention.

FIG. 1 is a circuit diagram showing a conventional pixel circuit adapted for an organic light emitting display;
FIG. 2 is a block diagram of an organic light emitting display according to an exemplary embodiment of the present invention;
FIG. 3 is a block diagram of a DC-DC converter according to an exemplary embodiment of the present invention; and
FIG. 4 is a circuit diagram showing further details of the DC-DC converter of FIG. 3.

[0017] Hereinafter, certain exemplary embodiments according to the present invention will be described with reference to the accompanying drawings. Here, when a first element is described as being coupled to a second element, the first element may be directly coupled to the second element or indirectly coupled to the second element via a third element. Further, some of the elements that are not essential to the complete understanding of the invention are omitted for clarity. Also, like reference numerals refer to like elements throughout.

[0018] FIG. 2 is a block diagram of an organic light emitting display according to an exemplary embodiment of the present invention. Referring to FIG. 2, the organic light emitting display includes a display region 100, a data driver 200, a scan driver 300, a DC-DC converter 400, and a battery 500.

[0019] The display region 100 includes a plurality of pixels 101, wherein each pixel 101 includes an organic light emitting diode for emitting light corresponding to a flow of current. The display region 100 includes n scan lines S1, S2, ..., Sn-1 and Sn extending in a row direction for transferring scan signals, and m data lines D1, D2, ..., Dm-1 and Dm extending in a column direction for transferring data signals.

[0020] The display region 100 is driven by receiving a first voltage ELVDD and a second voltage ELVSS from the DC-DC converter 400. Therefore, the display region 100 generates currents to flow through the organic light emitting diodes in accordance with the scan signals, the data signals, the first voltage ELVDD, and the second voltage ELVSS to emit light, thereby displaying an image.

[0021] The data driver 200 generates data signals in accordance with image signals having red, blue, and green components. The data driver 200 applies the data signals to the display region 100 through the data lines D1, D2, ..., Dm-1, and Dm.

[0022] The scan driver 300 is coupled to the scan lines S1, S2, ..., Sn-1, and Sn to transfer the scan signals that it generates to specific rows of the display region 100. The pixels 101, input with the scan signals, receive the data signals output from the data driver 200, and thus, the voltages corresponding to the data signals are transferred to the pixels 101.

[0023] The DC-DC converter 400 receives a power supply from the battery 500 to generate the first voltage ELVDD and the second voltage ELVSS. The DC-DC converter 400 includes a booster circuit and an inverter circuit, wherein the booster circuit boosts the voltage of the power supply to generate the first voltage ELVDD and the inverter circuit inverts the voltage of the power supply to generate the second voltage ELVSS.

[0024] A conventional booster circuit can boost a low voltage to a high voltage but cannot reduce a high voltage to a low voltage. Therefore, when the voltage of the power supply output from the battery 500 is higher than the first voltage ELVDD generated from the booster circuit, such a booster circuit cannot be used. Therefore, in order that the booster circuit can also be utilized with a high-capacity battery having a power supply with a relatively high voltage, the booster circuit according to an exemplary embodiment of the present invention is operated in the following cases: a case where the voltage of the power supply output from the battery is lower than the voltage of the first power supply ELVDD; and a case where the voltage of the power supply output from the battery is higher than the voltage of the first power supply ELVDD.

[0025] Therefore, the booster circuit generates the first voltage ELVDD having substantially the same voltage, substantially irrespective of the voltage of the power supply. The operation of the booster circuit according to an exemplary embodiment of the invention will be described in detail with reference to FIGS. 3 and 4.

[0026] The voltage of the second power supply ELVSS generated by the inverter circuit is a voltage that allows the organic light emitting diode to be driven in a saturation region, wherein the saturation region may be changed according to the materials utilized for an organic film of the organic light emitting diode and the properties of the transistor. Therefore, in order that a desired image can be sufficiently expressed even under unfavorable conditions, the voltage of the second power supply ELVSS is designed to have a voltage level margin of about 2 to 3V when designing the organic light emitting display.

[0027] The battery 500 holds a charge corresponding to the power supply and transfers the charged power to the DC-DC converter 400, thereby allowing the DC-DC converter 400 to generate the first voltage ELVDD and the second voltage ELVSS.

[0028] FIG. 3 is a block diagram showing a DC-DC converter according to an exemplary embodiment of the present invention. Referring to FIG. 3, the DC-DC converter 400 includes a voltage detecting unit 410, a booster circuit 420, an inverter circuit 430, and a pulse width modulation (PWM) controller 440.

[0029] The voltage detecting unit 410 receives and measures the voltage of the power supply.

[0030] The booster circuit 420 boosts the voltage of the power supply output from the battery 500 to generate the first voltage ELVDD.

[0031] The inverter circuit 430 inverts the voltage of the power supply output from the battery 500 to generate the second voltage ELVSS.

[0032] The PWM controller 440 outputs a control signal whose pulse width is varied to control the booster circuit 420 and the inverter circuit 430, thereby generating the first voltage ELVDD and the second voltage ELVSS.

[0033] However, when the voltage from the power supply detected by the voltage detecting unit 410 is higher than a reference voltage (e.g., a predetermined voltage), the PWM controller 440 outputs the control signal that controls the voltage of the power supply to be lower than the voltage of the first power supply ELVDD by dropping the voltage from the power supply. In other words, the PWM controller 440 controls the pulse width of the control signal in accordance with the voltage of the power supply.

[0034] The booster circuit 420 performs different operations when the voltage detected by the voltage detecting unit 410 is higher than the voltage of the first power supply ELVDD as opposed to when the voltage detected by the voltage detecting unit 410 is lower than the voltage of the first power supply ELVDD, so that the booster circuit can operate normally.

[0035] First, when the voltage from the power supply detected by the voltage detecting unit 410 is higher than the voltage of the first power supply ELVDD, the booster circuit 420 reduces the voltage from the power supply to be lower than the first voltage ELVDD and then boosts the reduced voltage using the control signal. At this time, the reduced voltage is lower than the voltage from the first power supply so that the remaining portions of the booster circuit 420 can be operated normally.

[0036] When the voltage of the power supply detected by the voltage detecting unit 410 is lower than the voltage of the first power supply ELVDD, the booster circuit 420 can be operated normally. That is, the booster circuit 420 can boost the voltage from the power supply using the control signal.

[0037] Therefore, the booster circuit 420 can generate the voltage from the first power supply ELVDD, substantially irrespective of the voltage from the power supply.

[0038] The inverter circuit 430 receives and inverts the power supply output from the battery and then makes the absolute value of the voltage high, thereby generating the second voltage ELVSS.

[0039] FIG. 4 is a circuit diagram showing an example of the DC-DC converter of FIG. 3. With reference to FIG. 4, the description that follows will make reference to the voltage of the power supply generally output from the battery 500 and the voltage of the first power supply ELVDD. Referring to FIG. 4, the DC-DC converter 400 includes a capacitor C that is coupled between a power input terminal and the battery 500 to charge the voltage of the power supply, a first inductor L1 (e.g., a coil) having one terminal coupled to the capacitor C and the other terminal coupled to a first node N1, a voltage detecting unit 410 that is coupled to the capacitor C to measure the voltage of the power supply, a PWM controller 440 that outputs control signals whose pulse widths are controlled corresponding to a voltage sensing signal output from the voltage detecting unit 410, a first switching device M1 having a first electrode coupled to the first node N1, a second electrode coupled to a ground, and a gate that receives a first control signal output from the PWM controller 440, a second switching device M2 having a first electrode coupled to the first node N1, a second electrode coupled to a second node N2, and a gate that receives a second control signal output from the PWM controller 440, a third switching device M3 having a first electrode coupled to the first node N1, a second electrode coupled to a third node N3, and a gate that receives a third control signal output from the PWM controller 440, a second inductor L2 (e.g., a coil) having one terminal coupled to the third node N3 and the other terminal coupled to the ground, and a diode D1 having a cathode electrode coupled to the third node N3. Also, the first to third switching devices M1-M3 are MOSFET devices, wherein if the first electrode is the source, the second electrode is the drain, and if the first electrode is the drain, the second electrode is the source. In the illustrated embodiment, the first switching device M1 and the third switching device M3 are n-channel devices, and the second switching device M2 is a p-channel device. However, the present invention is not limited thereto.

[0040] The voltage detecting unit 410 includes a comparator 411, a pair of resistors 412 and 413 forming a voltage divider, and a reference voltage source 414. The comparator 411 receives a reference voltage from the reference voltage source 414 in its inverting input. The reference voltage source may be coupled to the first power supply ELVDD, or may be a separate voltage source. The resistors 412 and 413 divide the power supply voltage from the battery 500, and the divided voltage is received at the non-inverting input of the comparator 411. Thus, the comparator compares the divided voltage with that of the reference voltage, and if the divided voltage, which corresponds to the power supply voltage, is higher than the reference voltage, the comparator sends an output signal having a high level to the PWM controller 440. On the other hand, if the divided voltage is lower than the reference voltage, the comparator sends an output signal having a low level to the PWM controller 440.

[0041] According to an exemplary embodiment, the booster circuit is adapted to boost the voltage of the power supply, which may be in a range from 2.9 to 4.2V, to generate the voltage of the first power supply ELVDD to have a voltage of 4.6V. Hereinafter, the operation of the DC-DC converter 400 will be described according to two cases: a case where

the voltage of the power supply output from the battery 500 is 2.9 to 4.2V, and a case where the voltage of the power supply output from the battery 500 is in a range of about 4.2V to 4.8V.

[0042] First, the case where the voltage of the power supply output from the battery 500 is 2.9 to 4.2V will be described. If the first switching device M1 is turned on and the second switching device M2 is turned off by the PWM controller 440, the power supply output from the battery 500 flows to the ground through the first switching device M1. When a current flows through the first inductor L1 to the first switching device M1, energy is stored in the first inductor L1 in the form of a first voltage that interferes with the current flowing through the first inductor L1. When the first switching device M1 is turned off, the second switching device M2 is turned on so that the energy stored in the first inductor L1 is transferred as a voltage through the second switching device M2 to the second node N2. At this time, the magnitude of the first voltage of the first inductor L1 is controlled according to the pulse width of the first control signal output from the PWM controller 440 for switching the first switching device M1. Therefore, as the pulse width of the first control signal is controlled, the voltage output to the second node N2 is controlled (e.g., it has a predetermined magnitude).

[0043] The case where the voltage of the power supply output from the battery 500 is in the range of about 4.2V to 4.8V will now be described. The second switching device M2 is always turned off by the PWM controller 440. According to an exemplary embodiment, the second switching device M2 has a parasitic body diode coupled between its source and its drain. Thus, when the second switching device M2 is turned off, the second switching device M2 has a connection in the form of a diode whose anode electrode is coupled to the first node N1 and cathode electrode is coupled to the second node N2. The diode formed by the second switching device M2 has a forward-bias voltage of about 0.7V. Here, when the first switching device M1 is turned on, the second switching device M2 that is diode-connected becomes biased in a reverse direction and current flows from the first node N1 to the ground direction through the first switching device M1, thereby generating the first voltage from the first inductor L1. When the first switching device M1 is turned off, the second switching device M2 becomes biased in a forward direction so that energy stored in the first inductor L1 is output as a voltage to the second node N2, thereby facilitating the generation of an ELVDD voltage of 4.6V.

[0044] The third switching device M3 performs turn-on and turn-off operations in accordance with the third control signal from the PWM controller 440. By the turn-on and turn-off operations of the third switching device M3, a second voltage is generated across the second inductor L2. The second voltage is generated in a direction tending to prevent a current from flowing through the second inductor L2, so that a power having a negative voltage is output by the second inductor L2 through the third node N3. Therefore, the second voltage ELVSS is generated and output.

[0045] The diode D1 illustrated outside the DC-DC converter 400 reduces or prevents an inflow of current from the output terminal of the second power supply ELVSS.

[0046] While the present invention has been described in connection with certain exemplary embodiments, it is to be understood that the invention is not limited to the disclosed embodiments, but, on the contrary, is intended to cover various modifications and equivalent arrangements included within the scope of the appended claims.

Claims

1. A DC-DC converter, comprising:

a voltage detector (410) for detecting a voltage from a battery (500);
 a first circuit comprising a boost converter (420) arranged to receive the voltage from the battery and to generate and output a first voltage (ELVDD); and
 a pulse width modulation PWM controller (440) coupled to the voltage detector (410), the PWM controller being configured to control the first circuit to boost the voltage from the battery when the battery voltage is lower than a reference voltage and to reduce the voltage from the battery when the detected battery voltage is higher than a reference voltage;
 wherein the first circuit comprises first and second switching devices (M_1 , M_2) controlled by the PWM controller,
characterised by:

a second circuit (430) arranged to receive and invert the voltage from the battery and to generate and output a second voltage (ELVSS); and
 wherein the PWM controller is arranged, in the event that the detected battery voltage is higher than the reference voltage, to turn the second switching device (M_2) off such that the second switching device functions as a diode device, and to switch the first switching device (M_1) on and off to respectively reverse-bias and forward-bias the second switching device (M_2) to provide the first voltage.

2. The DC-DC converter as claimed in claim 1, wherein the PWM controller is configured to output a control signal for controlling the first circuit (420) and the second circuit (430), and to control a pulse width of the control signal in

accordance with the voltage from the battery.

3. The DC-DC converter as claimed in claim 2, wherein the first circuit (420) comprises:

5 a first inductor (L_1) having a first terminal coupled to the battery (500);
 a first switch (M_1) having a turn-on or turn-off state determined in accordance with the control signal, a first
 electrode coupled to a second terminal of the inductor and a second electrode coupled to a ground such that
 the first inductor is charged with a first voltage in accordance with the turn-on or turn-off state; and
 10 a second switch (M_2) having a turn-on or turn-off state determined in accordance with the control signal, a first
 electrode coupled to the second terminal of the inductor and a second electrode coupled to an output terminal
 of the booster circuit.

4. The DC-DC converter as claimed in claim 3, wherein when the second switch is turned off, it is configured to function
 as a diode such that the voltage of the battery drops by a forward-bias voltage of the diode.

15 5. The DC-DC converter as claimed in claim 3, wherein the second switch is adapted to reduce the voltage of the
 battery in accordance with a resistance between the first and second electrodes of the second switch in the turn-
 on state.

20 6. The DC-DC converter as claimed in claim 3, 4 or 5, wherein the second circuit (430) comprises:

a third switch (M_3) having a turn-on or a turn-off state determined in accordance with the control signal, a first
 electrode coupled to the second terminal of the inductor (L_1), the third switch for switching the battery transferred
 through the first inductor;

25 a second inductor (L_2) for charging a second voltage utilizing the voltage of the battery (500) transferred through
 the first inductor by transferring or blocking the voltage of the battery according to a switching operation of the
 third switch, wherein a second electrode of the third switch is coupled to a first terminal of the second inductor
 (L_2) and the second terminal of the second inductor (L_2) is connected to ground; and

30 a diode (D_1) coupled to the output terminal of the second circuit to pass the voltage charged in the second
 inductor to the battery, and to block voltage from the battery from being transferred to the second inductor.

7. An organic light emitting display, comprising:

35 a display region for displaying an image corresponding to data signals, scan signals, a first voltage and a second
 voltage;

a data driver for generating and outputting the data signals;

a scan driver for generating and outputting the scan signals; and

a DC-DC converter according to any one of the preceding claims for generating and outputting the first voltage
 and the second voltage.

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Patentansprüche

1. DC/DC-Wandler, umfassend:

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einen Spannungsdetektor (410) zum Detektieren einer Spannung von einer Batterie (500);
 eine erste Schaltung, die einen Hochsetzsteller (420) umfasst, der eingerichtet ist, um die Spannung von der
 Batterie zu empfangen und eine erste Spannung (ELVDD) zu generieren und auszugeben; und
 einen Pulsweitenmodulations- PWM-Controller (440), der mit dem Spannungsdetektor (410) gekoppelt ist, wobei
 50 der PWM-Controller konfiguriert ist, um die erste Schaltung zu steuern, um die Spannung von der Batterie zu
 verstärken, wenn die Batteriespannung niedriger als eine Referenzspannung ist, und um die Spannung von
 der Batterie zu reduzieren, wenn die detektierte Batteriespannung höher als eine Referenzspannung ist;
 wobei die erste Schaltung erste und zweite Schaltvorrichtungen (M_1 , M_2) umfasst, die von dem PWM-Controller
 gesteuert werden,

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gekennzeichnet durch:

eine zweite Schaltung (430), die eingerichtet ist, um die Spannung von der Batterie zu empfangen und
 umzukehren und eine zweite Spannung (ELVSS) zu generieren und auszugeben; und

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wobei der PWM-Controller eingerichtet ist, um für den Fall, dass die detektierte Batteriespannung höher als die Referenzspannung ist, die zweite Schaltvorrichtung (M_2) derart auszuschalten, dass die zweite Schaltvorrichtung als Diodenvorrichtung funktioniert, und um die erste Schaltvorrichtung (M_1) ein- und auszuschalten, um die zweite Schaltvorrichtung (M_2) jeweils in Sperrichtung und Durchlassrichtung vorzuspannen, um die erste Spannung bereitzustellen.

2. DC/DC-Wandler nach Anspruch 1, wobei der PWM-Controller konfiguriert ist, um ein Steuersignal auszugeben, um die erste Schaltung (420) und die zweite Schaltung (430) zu steuern, und um eine Pulsweite des Steuersignals gemäß der Spannung von der Batterie zu steuern.

3. DC/DC-Wandler nach Anspruch 2, wobei die erste Schaltung (420) Folgendes umfasst:

eine erste Drosselspule (L_1), die eine erste Klemme aufweist, die mit der Batterie (500) gekoppelt ist; einen ersten Schalter (M_1), der einen Einschalt- oder Ausschalt-Zustand aufweist, der gemäß dem Steuersignal bestimmt wird, eine erste Elektrode, die mit einer zweiten Klemme der Drosselspule gekoppelt ist, und eine zweite Elektrode, die mit Masse gekoppelt ist, so dass die erste Drosselspule mit einer ersten Spannung gemäß einem Einschalt- oder Ausschaltzustand geladen wird; und einen zweiten Schalter (M_2), der einen Einschalt- oder Ausschalt-Zustand aufweist, der gemäß dem Steuersignal bestimmt wird, eine erste Elektrode, die mit der zweiten Klemme der Drosselspule gekoppelt ist, und eine zweite Elektrode, die mit einer Ausgangsklemme der Booster-Schaltung gekoppelt ist.

4. DC/DC-Wandler nach Anspruch 3, wobei dieser konfiguriert ist, wenn der zweite Schalter ausgeschaltet ist, um als Diode zu funktionieren, so dass die Spannung der Batterie auf eine Vorspannung in Durchlassrichtung der Diode abfällt.

5. DC/DC-Wandler nach Anspruch 3, wobei der zweite Schalter geeignet ist, um die Spannung der Batterie gemäß einem Widerstand zwischen den ersten und zweiten Elektroden des zweiten Schalters im Einschalt-Zustand zu reduzieren.

6. DC/DC-Wandler nach Anspruch 3, 4 oder 5, wobei die zweite Schaltung (430) Folgendes umfasst:

einen dritten Schalter (M_3), der einen Einschalt- oder einen Ausschalt-Zustand aufweist, der gemäß dem Steuersignal bestimmt wird, wobei eine erste Elektrode mit der zweiten Klemme der Drosselspule (L_1) gekoppelt ist, wobei der dritte Schalter zum Umschalten der Batterie, die über die erste Drosselspule übertragen wird, gedacht ist; eine zweite Drosselspule (L_2) zum Laden einer zweiten Spannung unter Verwendung der Spannung der Batterie (500), die über die erste Drosselspule übertragen wird, indem sie die Spannung der Batterie gemäß einem Schaltvorgang des dritten Schalters überträgt oder blockiert, wobei eine zweite Elektrode des dritten Schalters mit einer ersten Klemme der zweiten Drosselspule (L_2) gekoppelt ist und die zweite Klemme der zweiten Drosselspule (L_2) an Masse angeschlossen ist; und eine Diode (D_1), die mit der Ausgangsklemme der zweiten Schaltung gekoppelt ist, um die Spannung, die in der zweiten Drosselspule geladen wird, an die Batterie weiterzugeben und die Spannung von der Batterie daran zu hindern, an die zweite Drosselspule weitergegeben zu werden.

7. Organische lichtemittierende Anzeige, umfassend:

einen Anzeigebereich zum Anzeigen eines Bildes, das Datensignalen, Abtastsignalen, einer ersten Spannung und einer zweiten Spannung entspricht; einen Datentreiber zum Generieren und Ausgeben der Datensignale; einen Abtasttreiber zum Generieren und Ausgeben der Abtastsignale; und einen DC/DC-Wandler nach einem der vorhergehenden Ansprüche zum Generieren und Ausgeben der ersten Spannung und der zweiten Spannung.

Revendications

1. Convertisseur continu-continu, comprenant :

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un détecteur de tension (410) pour détecter une tension d'une batterie (500) ;
un premier circuit comprenant un convertisseur élévateur (420) agencé pour recevoir la tension de la batterie et pour générer et délivrer une première tension (ELVDD) ; et
un contrôleur de modulation de durée d'impulsion PWM (440) couplé au détecteur de tension (410), le contrôleur de PWM étant configuré pour commander le premier circuit pour amplifier la tension de la batterie lorsque la tension de la batterie est inférieure à une tension de référence et pour réduire la tension de la batterie lorsque la tension de la batterie détectée est supérieure à une tension de référence ;
dans lequel le premier circuit comprend des premier et deuxième dispositifs de commutation (M1, M2) commandés par le contrôleur de PWM,

caractérisé par :

un deuxième circuit (430) agencé pour recevoir et inverser la tension de la batterie et pour générer et délivrer une deuxième tension (ELVSS) ; et
dans lequel le contrôleur de PWM est agencé, dans le cas où la tension de batterie détectée est supérieure à la tension de référence, pour ouvrir le deuxième dispositif de commutation (M2) de sorte que le deuxième dispositif de commutation agisse en tant que dispositif de diode, et pour fermer et ouvrir le premier dispositif de commutation (M1) pour, respectivement, polariser en inverse et polariser en direct le deuxième dispositif de commutation (M2) pour fournir la première tension.

2. Convertisseur continu-continu selon la revendication 1, dans lequel le contrôleur de PWM est configuré pour délivrer un signal de commande pour commander le premier circuit (420) et le deuxième circuit (430), et pour commander une durée d'impulsion du signal de commande conformément à la tension de la batterie.

3. Convertisseur continu-continu selon la revendication 2, dans lequel le premier circuit (420) comprend :

une première inductance (L1) ayant une première borne couplée à la batterie (500) ;
un premier commutateur (M1) ayant un état fermé ou ouvert déterminé conformément au signal de commande, une première électrode couplée à une deuxième borne de l'inductance et une deuxième électrode couplée à une masse de sorte que la première inductance soit chargée avec une première tension conformément à l'état fermé ou ouvert ; et
un deuxième commutateur (M2) ayant un état fermé ou ouvert déterminé conformément au signal de commande, une première électrode couplée à la deuxième borne de l'inductance et une deuxième électrode couplée à une borne de sortie du circuit amplificateur.

4. Convertisseur continu-continu selon la revendication 3, dans lequel, lorsque le deuxième commutateur est ouvert, il est configuré pour fonctionner en tant que diode de sorte que la tension de la batterie chute d'une tension de polarisation directe de la diode.

5. Convertisseur continu-continu selon la revendication 3, dans lequel le deuxième commutateur est conçu pour réduire la tension de la batterie conformément à une résistance entre les première et deuxième électrodes du deuxième commutateur dans l'état fermé.

6. Convertisseur continu-continu selon la revendication 3, 4 ou 5, dans lequel le deuxième circuit (430) comprend :

un troisième commutateur (M3) ayant un état fermé ou ouvert déterminé conformément au signal de commande, une première électrode couplée à la deuxième borne de l'inductance (L1), le troisième commutateur servant à commuter la tension de batterie transférée par l'intermédiaire de la première inductance ;
une deuxième inductance (L2) pour charger une deuxième tension en utilisant la tension de la batterie (500) transférée par l'intermédiaire de la première inductance en transférant ou bloquant la tension de la batterie conformément à une opération de commutation du troisième commutateur, dans lequel une deuxième électrode du troisième commutateur est couplée à une première borne de la deuxième inductance (L2) et la deuxième borne de la deuxième inductance (L2) est connectée à la masse ; et
une diode (D1) couplée à la borne de sortie du deuxième circuit pour faire passer la tension chargée dans la deuxième inductance vers la batterie, et pour empêcher que la tension de la batterie soit transférée à la deuxième inductance.

7. Afficheur électroluminescent organique, comprenant :

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une région d'affichage pour afficher une image correspondant à des signaux de données, des signaux de balayage, une première tension et une deuxième tension ;
un dispositif de commande de données pour générer et délivrer les signaux de données ;
un dispositif de commande de balayage pour générer et délivrer les signaux de balayage ; et
5 un convertisseur continu-continu selon l'une quelconque des revendications précédentes pour générer et délivrer la première tension et la deuxième tension.

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FIG. 1

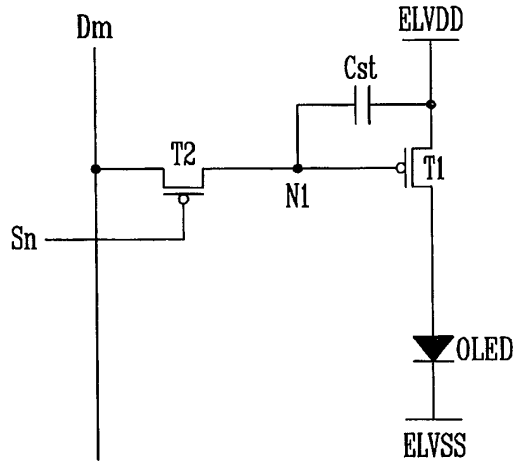
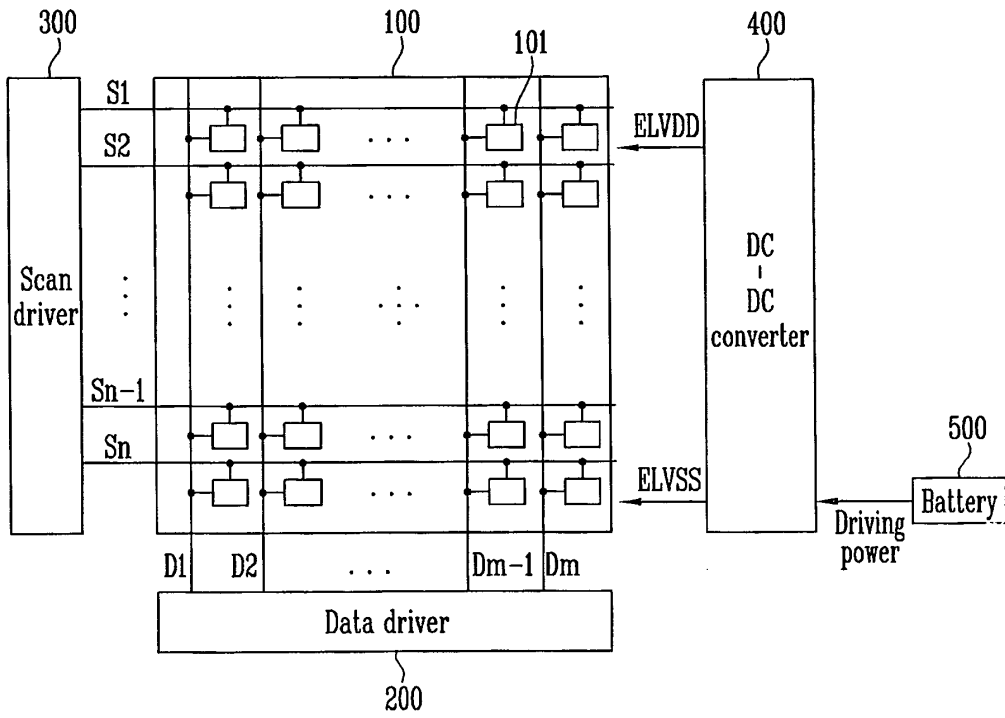
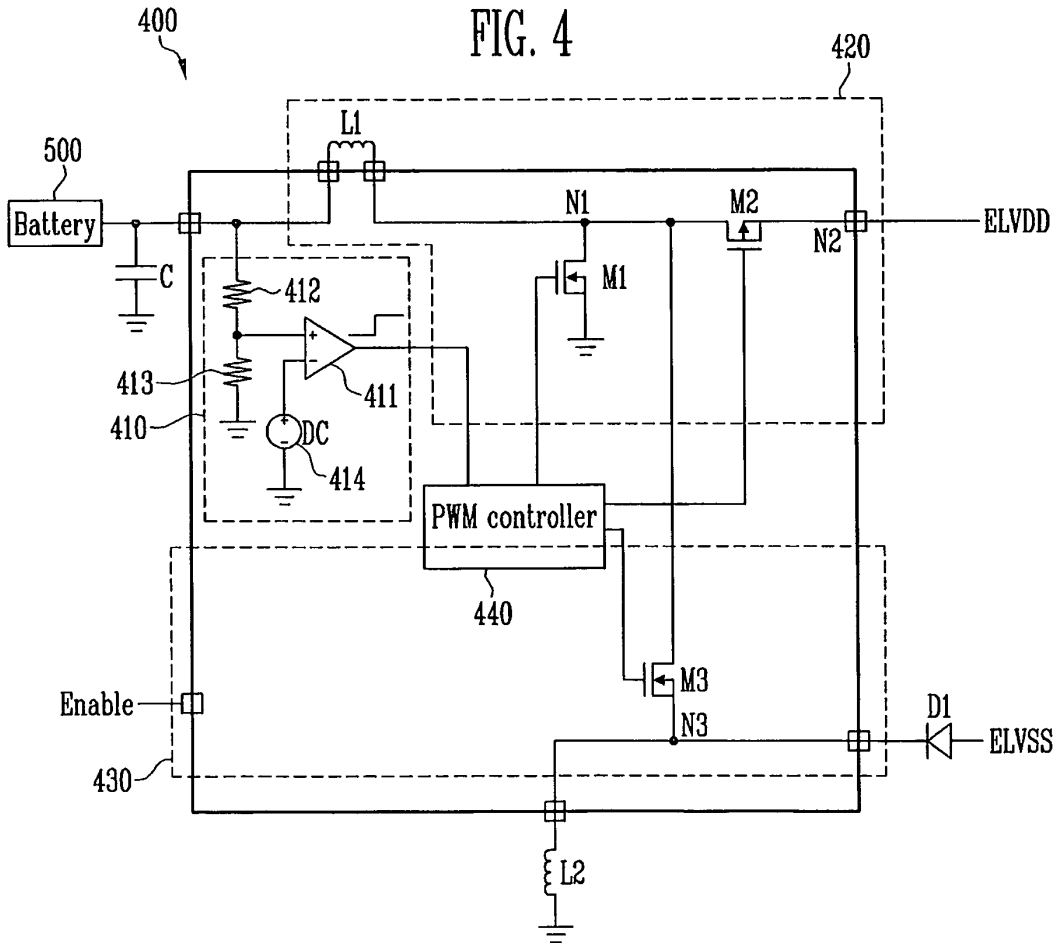
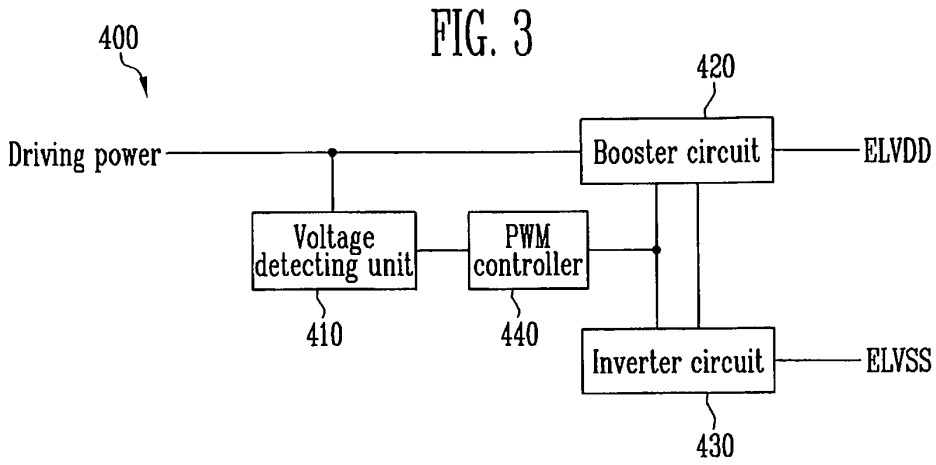


FIG. 2





REFERENCES CITED IN THE DESCRIPTION

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Patent documents cited in the description

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- WO 2008155600 A [0012]
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专利名称(译)	DC-DC转换器和使用其的有机发光显示器		
公开(公告)号	EP2237252B1	公开(公告)日	2015-11-04
申请号	EP2010151590	申请日	2010-01-25
[标]申请(专利权)人(译)	三星显示有限公司		
申请(专利权)人(译)	三星移动显示器有限公司.		
当前申请(专利权)人(译)	三星DISPLAY CO. , LTD.		
[标]发明人	PARK SUNG CHEON YUN SU YEON		
发明人	PARK, SUNG-CHEON YUN, SU-YEON		
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CPC分类号	H02M3/1588 G09G3/3225 G09G2300/0842 G09G2330/021 H02M2001/0045 H02M2001/009 Y02B70/1466 Y10T307/406		
优先权	1020090016730 2009-02-27 KR		
其他公开文献	EP2237252A1		
外部链接	Espacenet		

摘要(译)

DC-DC转换器产生用于驱动有机发光显示器中的像素的第一功率和第二功率，使得第一功率和第二功率的电压基本上与来自电源或电池的电压无关。电压检测器检测来自电源的电压，并且升压电路和逆变器电路分别升压和反转来自电源的电压，以分别为像素产生和输出第一和第二电源。PWM控制器控制升压电路和逆变器电路以控制第一电力和第二电力的电压。当来自电压检测器检测到的电源的电压高于参考电压时，升压电路适于将来自电源的电压降低到低于第一电源的电压。

[Equation 1]

$$I_{oled} = \frac{\beta}{2} (V_{GS} - V_{th})^2 = \frac{\beta}{2} (V_{data} - ELVDD - V_{th})^2$$