



(12) **EUROPEAN PATENT APPLICATION**
published in accordance with Art. 153(4) EPC

(43) Date of publication:
10.05.2017 Bulletin 2017/19

(51) Int Cl.:
H01L 27/15^(2006.01) H01L 21/77^(2017.01)

(21) Application number: **14882776.9**

(86) International application number:
PCT/CN2014/090390

(22) Date of filing: **05.11.2014**

(87) International publication number:
WO 2016/000374 (07.01.2016 Gazette 2016/01)

(84) Designated Contracting States:
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR
Designated Extension States:
BA ME

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(30) Priority: **01.07.2014 CN 201410309285**

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(54) **LIGHT-EMITTING DIODE DISPLAY PANEL AND MANUFACTURING METHOD THEREFOR, AND DISPLAY DEVICE**

(57) A light-emitting diode (LED) display panel, a manufacturing method thereof and a display device are disclosed. The LED display panel comprises a thin-film transistor (TFT) backplane, a light-emitting structure and an overcoat (OC) layer (10). The OC layer (10) is disposed between the TFT backplane and the light-emitting structure and configured to cover gate lines (21) and data

lines (22) in a display area of the LED display panel. The manufacturing process of the LED display panel adds the process of the OC layer (10) and hence can effectively avoid the scratches onto the TFT backplane caused by an evaporation mask plate when the light-emitting structure is formed and prevent the caused electrostatic problem.

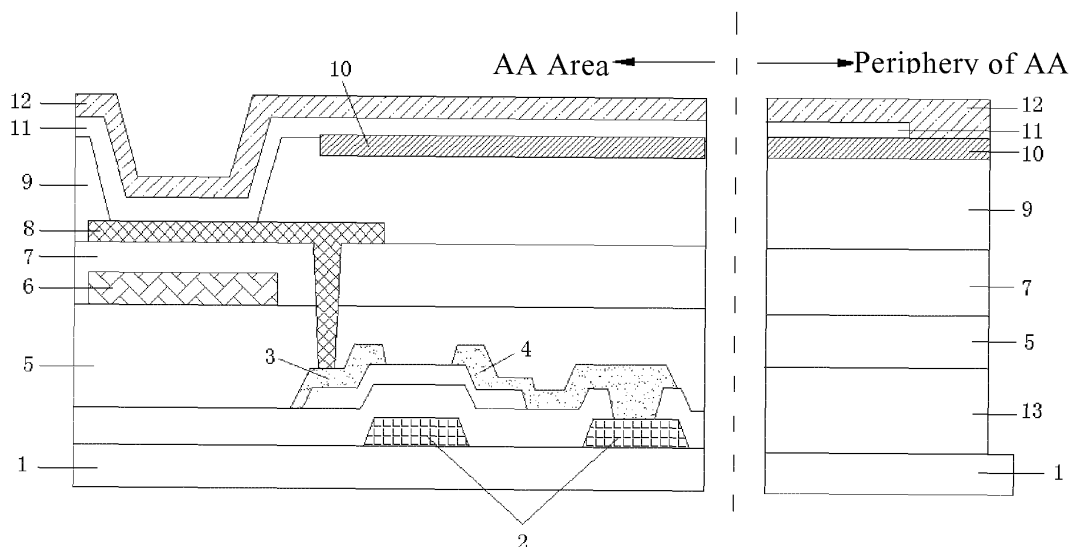


FIG. 1

Description

TECHNICAL FIELD

[0001] At least one embodiment of the present invention relates to a light-emitting diode (LED) display panel, a manufacturing method thereof and a display device comprising the same.

BACKGROUND

[0002] Compared with liquid crystal displays (LCDs) dominating the market currently, LED displays have the advantages of simple process, wide viewing angle, rapid response speed, high brightness, high contrast, bright colors, wide operating range, light weight, low thickness and low power consumption.

SUMMARY

[0003] At least one embodiment of the present invention provides a light-emitting diode (LED) display panel, a manufacturing method thereof and a display device to solve the technical problems of the LED display panel: the scratches onto a thin-film transistor (TFT) backplane caused by an evaporation mask plate when a light-emitting structure is formed and the caused electrostatic problem.

[0004] At least one embodiment of the present invention provides an LED display panel, which comprises a thin film transistor (TFT) backplane, a light-emitting structure and an overcoat (OC) layer. The OC layer is disposed between the TFT backplane and the light-emitting structure and configured to cover gate lines and data lines in a display area of the LED display panel.

[0005] At least one embodiment of the present invention further provides a display device, which comprises the foregoing LED display panel.

[0006] At least one embodiment of the present invention further provides a method for manufacturing an LED display panel. The LED display panel comprises a TFT backplane and a light-emitting structure. The manufacturing method comprises: forming the TFT backplane; forming an OC layer configured to cover gate lines and data lines in a display area of the LED display panel; and forming one layer in the light-emitting structure on the OC layer.

BRIEF DESCRIPTION OF THE DRAWINGS

[0007] Simple description will be given below to the accompanying drawings of the embodiments to provide a more clear understanding of the technical proposals of the embodiments of the present invention. Obviously, the drawings described below only involve some embodiments of the present invention but are not intended to limit the present invention.

FIG. 1 is a schematic sectional view of an LED display panel provided by the embodiment of the present invention;

FIG. 2 is a plan view of the LED display panel provided by the embodiment of the present invention; and

FIG. 3 is a flowchart of a method for manufacturing an LED display panel, provided by the present invention.

Reference numbers:

[0008]

1. base substrate; 2. gate electrode; 3. drain electrode; 4. source electrode; 5. passivation layer; 6. color filter (CF) layer; 7. resin layer; 8. anode; 9. luminous area defining layer; 10. overcoat layer; 11. EML; 12. cathode; 13. circuit on periphery of display area; 21. gate line; 22. data line.

DETAILED DESCRIPTION

[0009] For more clear understanding of the objectives, technical proposals and advantages of the embodiments of the present invention, clear and complete description will be given below to the technical proposals of the embodiments of the present invention with reference to the accompanying drawings of the embodiments of the present invention. Obviously, the preferred embodiments are only partial embodiments of the present invention but not all the embodiments. All the other embodiments obtained by those skilled in the art without creative efforts on the basis of the embodiments of the present invention illustrated shall fall within the scope of protection of the present invention.

[0010] The inventor of the application has found that: in the manufacturing process of an LED display panel, a mask plate and a thin film transistor (TFT) backplane must be tightly attached to each other when a light-emission layer is evaporated by mask evaporation; and if the edge of the mask plate has defects, the TFT backplane will be scratched during alignment, and hence undesirable phenomena such as short circuit and static electricity may be caused. Moreover, as the manufacturing process of the light-emission layer is very complex and multiple processes such as evaporation and chemical vapor deposition (CVD) are required, strong static electricity tends to be produced to damage the circuit.

[0011] At least one embodiment of the present invention provides an LED display panel at first. The LED display panel may be an organic light-emitting diode (OLED) display panel. The schematic sectional view of the display panel is as shown in FIG. 1. The plan view of the display panel is as shown in FIG. 2.

[0012] The LED display panel comprises a TFT backplane, a light-emitting structure and an overcoat (OC) layer 10 which is disposed between the TFT backplane

and the light-emitting structure. The OC layer 10 is configured to cover gate lines and data lines in a display area (AA area) of the LED display panel. For instance, as shown in FIG. 2, the OC layer 10 is provided in a grid structure for covering gate lines 21 and data lines 22 in the display area. The word "covering" refers to shielding in the vertical direction.

[0013] In the embodiment of the present invention, the TFT backplane may include a base substrate 1, gate electrodes 2, drain electrodes 3, source electrodes 4, a passivation layer 5 and the like, and the light-emitting structure includes an anode 8, a light-emission layer 11 and a cathode 12. As the step of forming the OC layer 10 may be executed before the mask evaporation of the light-emission layer 11, at least one part of the OC layer 10 makes contact with the light-emission layer 11 in the light-emitting structure. For instance, at least one part of the OC layer 10 makes contact with one side of the light-emission layer 11 in the light-emitting structure facing the TFT backplane. Therefore, the OC layer 10 can effectively avoid the scratches onto the line areas in the display area of the TFT backplane caused by the mask plate during the process of mask evaporation for the light-emission layer 11 and prevent the problems such as short circuit and static electricity caused by this.

[0014] In one embodiment, the OC layer 10 is also configured to cover the position of a circuit 13 on the periphery of the display area of the LED display panel. For instance, as shown in FIG. 2, the OC layer 10 is provided with a rectangular frame structure for covering the peripheral circuit on the periphery of the display area. The circuit 13 on the periphery of the display area includes a drive circuit, etc. Thus, the OC layer 10 can effectively avoid the scratches onto the peripheral circuit of the display area of the TFT backplane caused by a mask plate during the process of mask evaporation for the light-emission layer 11 and prevent the problems such as short circuit and static electricity caused by this. The material of the OC layer is not limited herein as long as the material has good hardness and can prevent the TFT backplane from being scratched by the mask plate.

[0015] In addition, the process of forming the cathode 12 may also adopt a mask evaporation method, and the step of forming the OC layer 10 may also be executed before the mask evaporation for the cathode 12, so that at least one part of the OC layer 10 may make contact with one side of the light-emission layer 11 in the light-emitting structure facing the cathode 12. Thus, the OC layer 10 can effectively avoid the scratches onto the light-emission layer 11 caused by a mask plate during the process of mask evaporation for the cathode 12.

[0016] In at least one embodiment, the OC layer 10 may be made from a conductive material and is electrically connected with the cathode 12. For instance, the OC layer 10 may be made from a metal material or a transparent conductive material such as ITO. ITO has good hardness after annealing crystallization and is suitable to prepare the OC layer just like a metal material.

In this case, the OC layer 10 is connected in parallel with the cathode 12 and not only has the function of electrostatic shielding but also can reduce the internal resistance (IR) drop of the cathode 12. When the electric conductivity of the OC layer 10 is better, the IR drop of the cathode 12 is lower, and hence the light-emitting area can be appropriately increased.

[0017] The LED display panel as shown in FIG. 1 adopts a bottom-emission mode. As the luminous area defining layer 9 has defined the range of a luminous area and light will run through the TFT backplane, no black matrix (BM) is required in the bottom-emission mode due to the characteristics of the structure.

[0018] When top-emission mode is adopted, the cathode 12 is made from a transparent material. If the OC layer 10 is also made from a transparent conductive material, e.g., made from ITO, a black matrix must be formed subsequently to block light for TFTs. At this point, the LED display panel further comprises BMs which are configured to cover gate lines and data lines in a display area of the LED display panel.

[0019] If the OC layer 10 is made from a metallic material, because metal is light-tight, the OC layer 10 can achieve the light shielding effect of the TFTs simultaneously, so that the TFTs are not affected by light illumination. At this point, the subsequent manufacturing process of the BMs can be saved, and hence the cost can be reduced.

[0020] Taking the bottom-emission mode as shown in FIG. 1 as an example, the LED display panel provided by at least one embodiment of the present invention may comprise: a base substrate 1; a TFT array including gate electrodes 2, drain electrodes 3 and source electrodes 4 formed on the base substrate 1; a passivation layer 5, a color filter (CF) layer 6 and a resin layer 7 formed on the TFT array in sequence, in which the resin layer 7 is a transparent resin layer; and an anode 8 of the light-emitting structure, a luminous area defining layer 9, an OC layer 10, a light-emission layer 11 of the light-emitting structure, and a cathode 12 of the light-emitting structure formed on the resin layer 7 in sequence.

[0021] As described above, the embodiment of the present invention is also applicable to a top-emission mode LED display panel. No further description will be given here.

[0022] In the embodiment of the present invention, the process of the OC layer is added in the manufacturing process of the LED display panel, so that the scratches onto the TFT backplane caused by the evaporation mask when the light-emitting structure is formed and the electrostatic discharge caused by this can be effectively prevented. When the OC layer is conductive, the OC layer is connected with the cathode of the light-emitting structure of the display panel and the IR drop of the cathode can also be reduced. In addition, when the top-emission mode is adopted, as an opaque material such as metal is adopted to prepare the OC layer, the TFTs are protected from being affected by illumination. Thus, the sub-

sequent manufacturing process of BMs can be saved, and hence the cost can be reduced.

[0023] At least one embodiment of the present invention further provides a display device, which comprises the LED display panel provided by the embodiment of the present invention. The display device may be: any product or component with display function such as a mobile phone, a tablet PC, a TV, a display, a notebook computer, a digital picture frame and a navigator.

[0024] At least one embodiment of the present invention further provides a method for manufacturing an LED display panel (e.g., an OLED display panel). The LED display panel comprises a TFT backplane and a light-emitting structure. The manufacturing method comprises: forming the TFT backplane; forming an OC layer which is configured to cover gate lines and data lines in a display area of the LED display panel; and forming one layer in the light-emitting structure on the OC layer (namely one side of the OC layer away from the TFT backplane, as shown in FIG. 1).

[0025] In one embodiment, the OC layer may also be configured to cover a circuit on the periphery of the display area of the LED display panel.

[0026] The word "one layer" may be a light-emission layer or a cathode in the light-emitting structure. The layer is generally required to be prepared by a process of mask evaporation. Compared with the processes such as photolithography, the operation of the process of mask evaporation is relatively simple, but the mask plate must be tightly attached to a substrate. The OC layer has the function of preventing the TFT backplane or the light-emission layer from being scratched by the mask plate during the process of mask evaporation and avoids the problems such as short circuit and electrostatic discharge caused by the scratches.

[0027] In at least one embodiment, the manufacturing method further comprises the step of annealing the OC layer between the step of forming the OC layer and the step of forming one layer in the light-emitting structure on the OC layer.

[0028] Taking the structure as shown in FIG. 1 as an example, in at least one embodiment, the step of forming one layer in the light-emitting structure on the OC layer 10 includes: forming a light-emission layer 11 of the light-emitting structure on the OC layer 10 by mask evaporation.

[0029] In one embodiment, before the step of forming the light-emission layer 11 of the light-emitting structure by mask evaporation, the manufacturing method further comprises: forming an anode 8 of the light-emitting structure on the TFT backplane (namely one side of the TFT backplane away from the base substrate 1); and forming a luminous area defining layer 9, so that one part of the luminous area defining layer 9 is formed on the anode 8 and one part of the OC layer 10 is formed on the luminous area defining layer 9.

[0030] In one embodiment, after the step of forming the light-emission layer 11 of the light-emitting structure

by mask evaporation, the manufacturing method further comprises: forming a cathode 12 of the light-emitting structure. At this point, the OC layer 10 is made from a conductive material and electrically connected with the cathode 12. The electrical connection between the OC layer 10 and the cathode 12 not only can have the function of electrostatic shielding but also can reduce the IR drop of the cathode 12. When the electric conductivity of the OC layer 10 is better, the IR drop of the cathode 12 is lower, and hence the light-emitting area can be appropriately increased.

[0031] As described above, in the top-emission mode, if the OC layer is made from a transparent conductive material, the method for manufacturing the LED display panel further comprises the step of forming BMs.

[0032] Taking the step of forming the light-emission layer in the light-emitting structure on the OC layer as an example, as shown in FIG. 3 which is a flowchart of the method for manufacturing the LED display panel, provided by the embodiment of the present invention, the method may comprise the following steps S1 to S6. Each of the following steps is described below.

S1: providing a base substrate.

S2: forming a TFT array including gate electrodes, drain electrodes and source electrodes on the base substrate.

S3: forming a passivation layer, a CF layer and a resin layer on the TFT array in sequence, in which the resin layer may be a transparent resin layer.

S4: forming an anode of the light-emitting structure and a luminous area defining layer on the resin layer.

S5: forming an OC layer on the luminous area defining layer, in which the OC layer is configured to cover gate lines and data lines in a display area of the LED display panel. In one example, the OC layer may also be configured to cover a circuit on the periphery of the display area of the LED display panel.

S6: forming a light-emission layer of the light-emitting structure and a cathode of the light-emitting structure on the OC layer. For instance, the OC layer is made from a conductive material and electrically connected with the cathode. For instance, the light-emission layer of the light-emitting structure may be an organic light emission layer.

[0033] In the embodiment of the present invention, the process of the OC layer is added in the manufacturing process of the OLED display panel, so that the scratches onto the TFT backplane caused by the evaporation mask when the light-emitting structure is formed and the electrostatic discharge caused by this can be effectively prevented. When the OC layer is conductive, the OC layer is connected with the cathode of the light-emitting structure of the display panel, and hence the IR drop of the cathode can also be reduced.

[0034] In the top-emission mode, the cathode is made from a transparent material. If the OC layer is also made

from a transparent conductive material, e.g., made from ITO, BMs must be formed subsequently to block light for TFTs. At this point, the OLED display panel further comprises BMs which are configured to cover gate lines and data lines in a display area of the OLED display panel.

[0035] If the OC layer is made from a metallic material, because metal is light-tight, the OC layer can achieve the light shield effect of the TFTs simultaneously, so that the TFTs are not affected by illumination. At this point, the subsequent manufacturing process of BMs can be saved, and hence the cost can be reduced.

[0036] The foregoing is only the preferred embodiments of the present invention and not intended to limit the scope of protection of the present invention. The scope of protection of the present invention should be defined by the appended claims.

[0037] The application claims priority to the Chinese patent application No. 201410309285.7, filed July 1, 2014, the disclosure of which is entirely incorporated herein by reference as part of the application.

Claims

1. A light-emitting diode (LED) display panel, comprising a thin-film transistor (TFT) backplane, a light-emitting structure, and an overcoat (OC) layer, wherein the OC layer is disposed between the TFT backplane and the light-emitting structure and configured to cover gate lines and data lines in a display area of the LED display panel.
2. The LED display panel according to claim 1, wherein the OC layer is also configured to cover a circuit on a periphery of the display area of the LED display panel.
3. The LED display panel according to claim 1 or 2, wherein at least one part of the OC layer makes contact with a light emission layer in the light-emitting structure.
4. The LED display panel according to any one of claims 1 to 3, wherein the light-emitting structure includes a cathode; and the OC layer is made from a conductive material and electrically connected with the cathode.
5. The LED display panel according to claim 4, wherein the OC layer is made from a transparent conductive material.
6. The LED display panel according to claim 5, wherein the LED display panel adopts a top-emission mode and further comprises a black matrix.
7. The LED display panel according to claim 5 or 6, wherein the OC layer is made from indium tin oxide (ITO).
8. The LED display panel according to claim 4, wherein the OC layer is made from metal.
9. A display device, comprising the LED display panel according to any one of claims 1 to 8.
10. A method for manufacturing an LED display panel, the LED display panel comprising a TFT backplane and a light-emitting structure, the manufacturing method comprising:
 - forming the TFT backplane;
 - forming an OC layer configured to cover gate lines and data lines in a display area of the LED display panel; and
 - forming one layer in the light-emitting structure on the OC layer.
11. The method for manufacturing the LED display panel according to claim 10, wherein the OC layer is also configured to cover a circuit on a periphery of the display area of the LED display panel.
12. The method for manufacturing the LED display panel according to claim 10 or 11, between the step of forming the OC layer and the step of forming one layer in the light-emitting structure on the OC layer, further comprising:
 - annealing the OC layer.
13. The method for manufacturing the LED display panel according to any one of claims 10 to 12, wherein the step of forming one layer in the light-emitting structure on the OC layer includes: forming a light-emission layer of the light-emitting structure on the OC layer by mask evaporation.
14. The method for manufacturing the LED display panel according to claim 13, before the step of forming the light-emission layer of the light-emitting structure, further comprising:
 - forming an anode of the light-emitting structure on the TFT backplane; and
 - forming a luminous area defining layer, wherein one part of the luminous area defining layer is formed on the anode, and one part of the OC layer is formed on the luminous area defining layer.
15. The method for manufacturing the LED display panel according to claim 13 or 14, after the step of forming the light-emission layer of the light-emitting structure, further comprising:

forming a cathode of the light-emitting structure,
wherein the OC layer is made from a conductive
material and electrically connected with the
cathode.

5

- 16.** The method for manufacturing the LED display panel
according to claim 15, wherein the OC layer is made
from a transparent conductive material; and the
method for manufacturing the LED display panel fur-
ther comprises:

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forming a black matrix.

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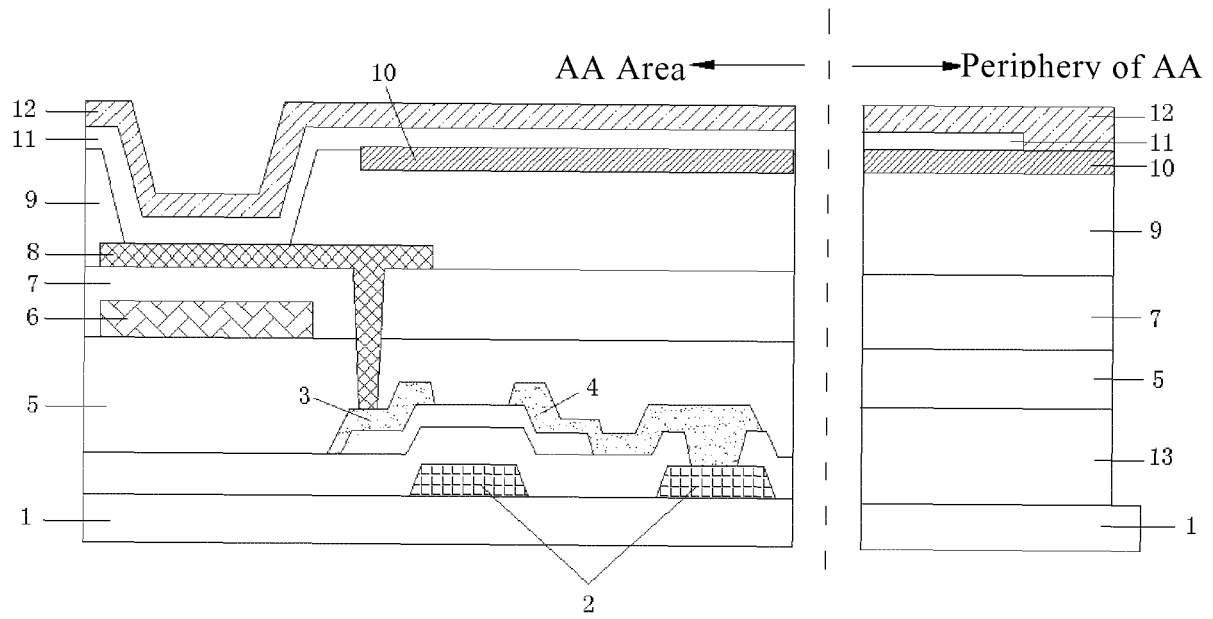


FIG. 1

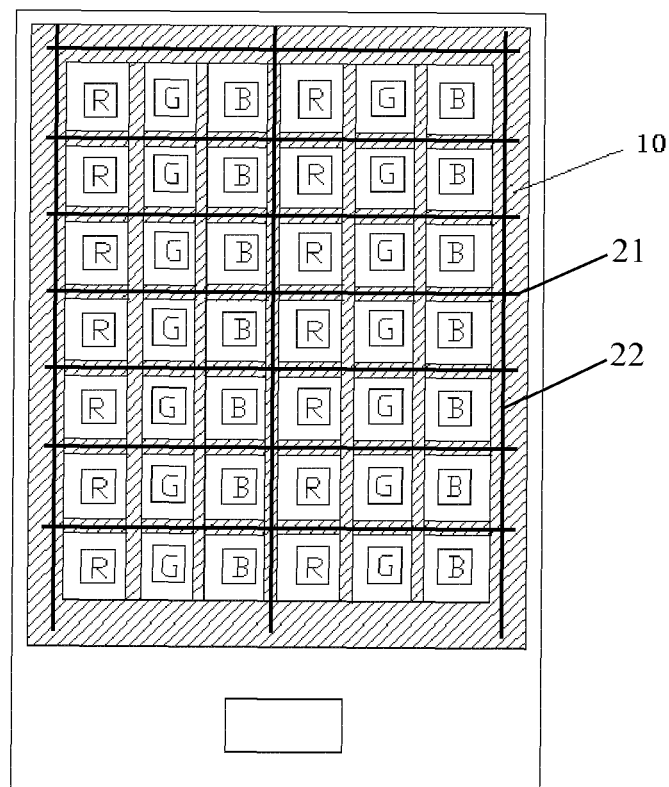


FIG. 2

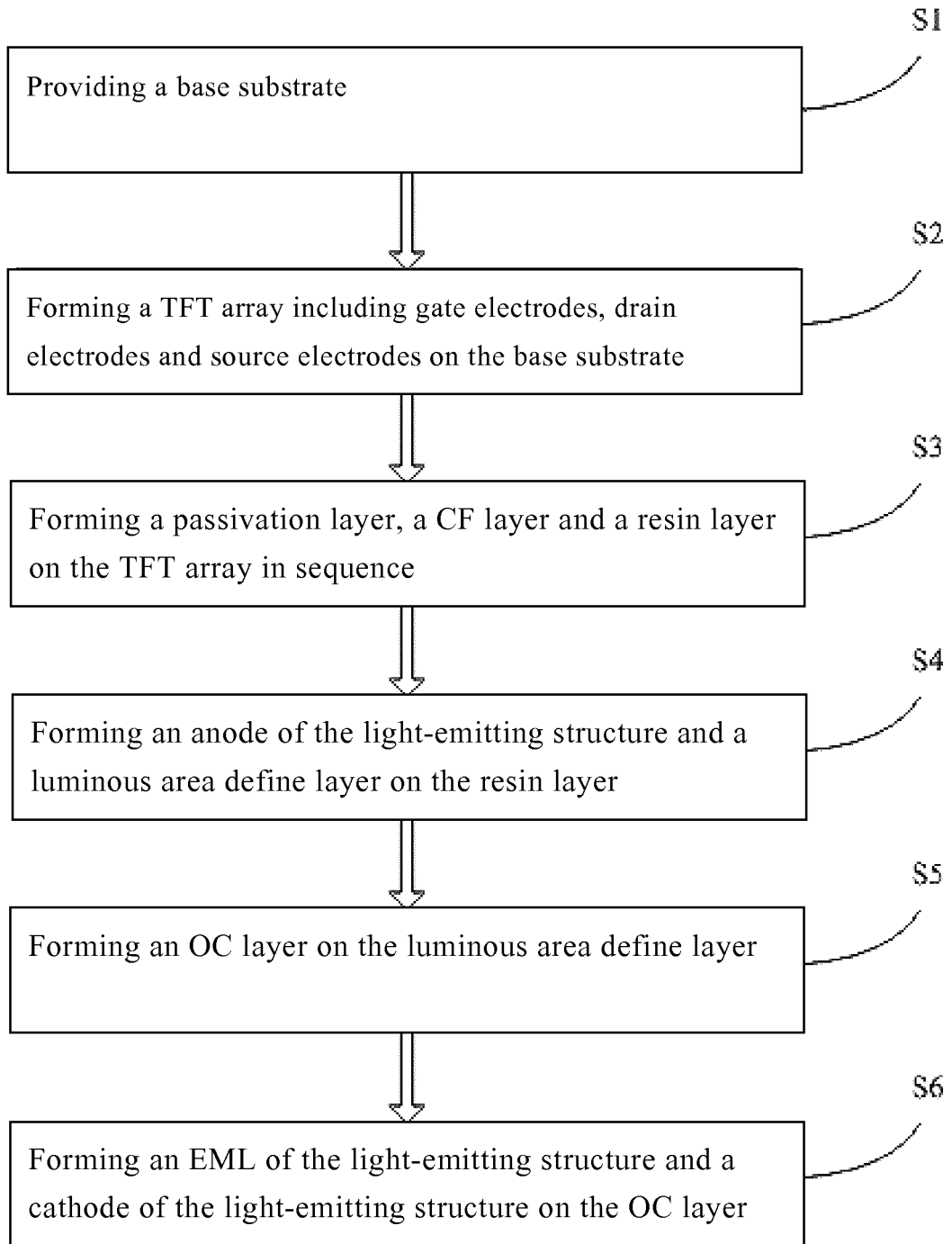


FIG. 3

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2014/090390

A. CLASSIFICATION OF SUBJECT MATTER

H01L 27/15 (2006.01) i; H01L 21/77 (2006.01) i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

CNABS; DWPI: light-emitting, light, gate, data, protect

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	CN 102376896 A (SAMSUNG MOBILE DISPLAY CO., LTD.), 14 March 2012 (14.03.2012), description, paragraphs [0046]-[0063], and figures 2A-21	1-2, 9-13
PX	CN 104112758 A (BOE TECHNOLOGY GROUP CO., LTD.), 22 October 2014 (22.10.2014), the whole document	1-16
A	US 2014042397 A1 (LG DISPLAY CO., LTD.), 13 February 2014 (13.02.2014), the whole document	1-16
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A	CN 1893092 A (SAMSUNG ELECTRONICS CO., LTD.), 10 January 2007 (10.01.2007), the whole document	1-16
A	US 2009179550 A1 (SAMSUNG ELECTRONICS CO., LTD.), 16 July 2009 (16.07.2009), the whole document	1-16
A	CN 1779990 A (SAMSUNG SDI CO., LTD.), 31 May 2006 (31.05.2006), the whole document	1-16

☐ Further documents are listed in the continuation of Box C.☒ See patent family annex.

* Special categories of cited documents:	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"A" document defining the general state of the art which is not considered to be of particular relevance	
"E" earlier application or patent but published on or after the international filing date	"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
"O" document referring to an oral disclosure, use, exhibition or other means	
"P" document published prior to the international filing date but later than the priority date claimed	"&" document member of the same patent family

Date of the actual completion of the international search 24 March 2015 (24.03.2015)	Date of mailing of the international search report 03 April 2015 (03.04.2015)
Name and mailing address of the ISA/CN: State Intellectual Property Office of the P. R. China No. 6, Xitucheng Road, Jimenqiao Haidian District, Beijing 100088, China Facsimile No.: (86-10) 62019451	Authorized officer CHAI, Chunying Telephone No.: (86-10) 62419122

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/CN2014/090390

Patent Documents referred in the Report	Publication Date	Patent Family	Publication Date
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This list of references cited by the applicant is for the reader's convenience only. It does not form part of the European patent document. Even though great care has been taken in compiling the references, errors or omissions cannot be excluded and the EPO disclaims all liability in this regard.

Patent documents cited in the description

- CN 201410309285 [0037]

专利名称(译)	发光二极管显示面板及其制造方法和显示装置		
公开(公告)号	EP3166148A1	公开(公告)日	2017-05-10
申请号	EP2014882776	申请日	2014-11-05
[标]申请(专利权)人(译)	京东方科技集团股份有限公司		
申请(专利权)人(译)	京东方科技集团股份有限公司.		
当前申请(专利权)人(译)	京东方科技集团股份有限公司.		
[标]发明人	LI YONGQIAN		
发明人	LI, YONGQIAN		
IPC分类号	H01L27/15 H01L21/77		
CPC分类号	H01L27/3246 H01L27/3258 H01L27/3272 H01L51/5228 H01L21/77 H01L27/124 H01L27/1262 H01L27/15 H01L33/42 H01L33/44		
优先权	201410309285.7 2014-07-01 CN		
其他公开文献	EP3166148A4		
外部链接	Espacenet		

摘要(译)

公开了一种发光二极管 (LED) 显示面板, 其制造方法和显示装置。LED显示面板包括薄膜晶体管 (TFT) 背板, 发光结构和外涂层 (OC) (10)。OC层 (10) 设置在TFT背板和发光结构之间, 并且被配置为覆盖LED显示面板的显示区域中的栅极线 (21) 和数据线 (22)。LED显示面板的制造工艺增加了OC层 (10) 的工艺, 因此可以有效地避免在形成发光结构时由蒸发掩模板引起的TFT背板上的划痕, 并防止引起的静电问题。

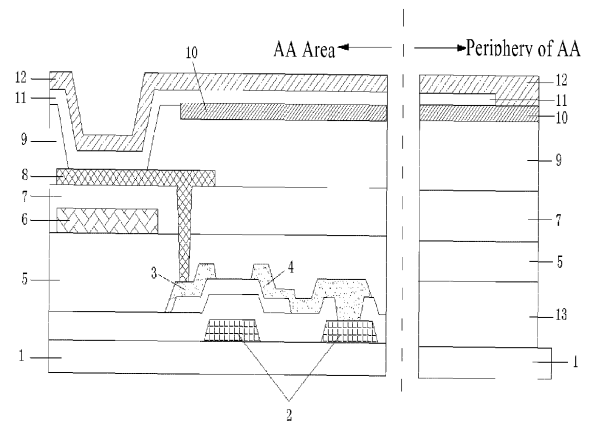


FIG. 1