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(54) **OLED ARRAY SUBSTRATE AND METHOD FOR MANUFACTURING SAME, AND OLED DISPLAY PANEL**

(57) The present disclosure provides an OLED array substrate, a manufacturing method thereof, and an OLED display panel. The OLED array substrate comprises a first electrode layer, a pixel definition layer, an organic material functional layer, and a second electrode layer arranged successively in a light exit direction. The organic material functional layer comprises light-emitting

regions that emit light of different colors. The pixel definition layer comprises pixel regions corresponding to the light-emitting regions, and spacing regions arranged between adjacent pixel regions. Each spacing region is configured to electrically isolate adjacent light-emitting regions.

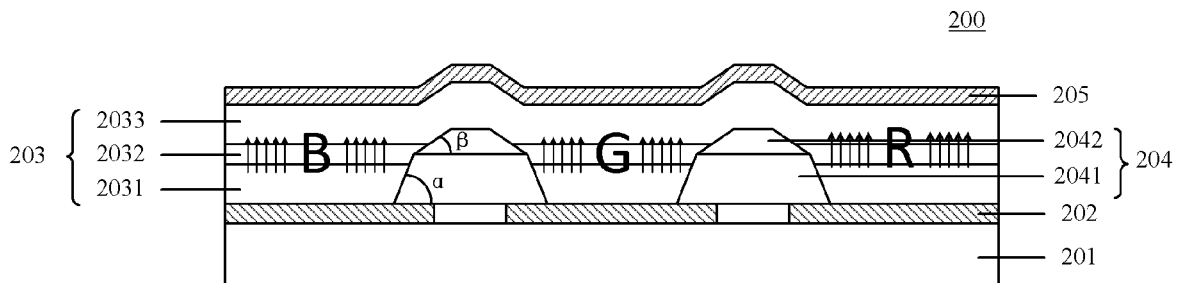


Fig.2

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Description

RELATED APPLICATION

[0001] The present application claims the benefit of Chinese Patent Application No. 201610609987.6, filed on July 29, 2016, the entire disclosure of which is incorporated herein by reference.

FIELD

[0002] The present disclosure generally relates to the field of display, and more particularly to an organic light-emitting diode (OLED) array substrate, an OLED display panel comprising such an OLED array substrate, and a manufacturing method of such an OLED array substrate.

BACKGROUND

[0003] Organic light-emitting diode (OLED) display technology is a technology that realizes display using reversible color change generated by an organic semiconductor material under the driving of a current. An OLED display has advantages of being ultra-light and ultra-thin, having high brightness, large viewing angle, low voltage, low power consumption, fast response and high resolution, being anti-seismic and bendable, having low cost and simple process, using fewer raw materials, having high luminous efficiency and wide temperature range, and the like, and is hence considered to be the most promising new-generation display technology.

SUMMARY

[0004] It is an objective of the present disclosure to provide an improved OLED array substrate, an OLED display panel comprising such an OLED array substrate, and a manufacturing method of such an OLED array substrate.

[0005] According to one aspect of the present disclosure, there is provided an OLED array substrate. The OLED array substrate comprises a first electrode layer, a pixel definition layer, an organic material functional layer, and a second electrode layer arranged successively in a light exit direction. The organic material functional layer comprises light-emitting regions that emit light of different colors. The pixel definition layer comprises pixel regions corresponding to the light-emitting regions, and spacing regions arranged between adjacent pixel regions. Each spacing region is configured to electrically isolate adjacent light-emitting regions.

[0006] In the OLED array substrate proposed in the present disclosure, by employing the spacing regions that electrically isolate adjacent light-emitting regions, upon displaying a monochromatic image by only illuminating light-emitting regions of a certain color, it is possible to prevent adjacent light-emitting regions of different colors from being also illuminated weakly, which results

in abnormal display. As a result, display effect of the display device can be improved.

[0007] In some embodiments, the organic material functional layer comprises a first functional layer, a light-emitting layer and a second functional layer arranged successively in the light exit direction, and each spacing region comprises a first spacing portion and a second spacing portion arranged successively in the light exit direction. The first spacing portion is configured to separate portions of at least the first functional layer corresponding to the respective light-emitting regions from each other.

[0008] In such an OLED array substrate, the adjacent light-emitting regions are electrically isolated from each other, as long as the portions of the first functional layer corresponding to the respective light-emitting regions are separated from each other. Therefore, as compared to an implementation of separating portions of the first functional layer, the light-emitting layer and the second functional layer corresponding to the respective light-emitting regions from each other, the second functional layer corresponding to the respective light-emitting regions can be integrally formed. Consequently, the manufacturing process of such an OLED array substrate is simpler and the cost thereof is also lower.

[0009] In some embodiments, an angle between a side wall of the first spacing portion and a lower surface of the first spacing portion is larger than an angle between a side wall of the second spacing portion and a lower surface of the second spacing portion.

[0010] In such an OLED array substrate, by making the angle between the side wall of the first spacing portion and the lower surface thereof larger than the angle between the side wall of the second spacing portion and the lower surface thereof, a corner can be formed at the joint between the side wall of the first spacing portion and the side wall of the second spacing portion, such that the separated portions of the first functional layer corresponding to the respective light-emitting regions can be formed simultaneously. Consequently, the manufacturing process of the OLED array substrate is simpler and the cost thereof also is lower.

[0011] In some embodiments, the angle between the side wall of the first spacing portion and the lower surface of the first spacing portion is no less than 60°, and the angle between the side wall of the second spacing portion and the lower surface of the second spacing portion is no more than 30°.

[0012] In some embodiments, the lateral etching rate of the material of the first spacing portion is greater than that of the material of the second spacing portion. Under the same etching conditions, the greater the lateral etching rate of the material is, the larger the formed angle between the side wall and the lower surface will be.

[0013] In some embodiments, the width of an upper surface of the first spacing portion is no more than the width of the lower surface of the second spacing portion.

[0014] In such an OLED array substrate, by making

the width of the upper surface of the first spacing portion no more than the width of the lower surface of the second spacing portion, a notch can be formed at the interface between the first spacing portion and the second spacing portion, such that the separated portions of at least the first functional layer corresponding to the respective light-emitting regions can be formed simultaneously. Consequently, the manufacturing process of the OLED array substrate is simpler and the cost thereof is also lower.

[0015] In some embodiments, the thickness of the first spacing portion is greater than the thickness of the first functional layer.

[0016] In such an OLED array substrate, by making the thickness of the first spacing portion greater than the thickness of the first functional layer, separated portions of the first functional layer corresponding to the respective light-emitting regions can be formed simultaneously between the first spacing portions. Consequently, the manufacturing process of the OLED array substrate is simpler and the cost thereof is also lower.

[0017] In some embodiments, the first functional layer comprises at least one of a hole injection layer, a hole transport layer and an electron barrier layer, and the second functional layer comprises at least one of a hole barrier layer, an electron transport layer and an electron injection layer. The first electrode layer is an anode layer, and the second electrode layer is a cathode layer.

[0018] In some embodiments, the light-emitting layer comprises an organic light-emitting layer and an optical compensation layer arranged between the organic light-emitting layer and the first functional layer. The optical compensation layer is configured to adjust the effective optical cavity length in the organic material functional layer so that light of different colors is emitted from the organic material functional layer at maximum light intensity.

[0019] In some embodiments, the first electrode layer is a reflective electrode layer, and the second electrode layer is a transparent electrode layer.

[0020] According to another aspect of the present disclosure, there is provided an OLED display panel comprising any of the OLED array substrates described above.

[0021] According to a further aspect of the present disclosure, there is provided a method of manufacturing an OLED array substrate. The method comprises successively forming a first electrode layer, a pixel definition layer, an organic material functional layer, and a second electrode layer. The organic material functional layer comprises light-emitting regions that emit light of different colors. The pixel definition layer comprises pixel regions corresponding to the light-emitting regions, and spacing regions arranged between adjacent pixel regions. Each spacing region is configured to electrically isolate adjacent light-emitting regions.

[0022] In some embodiments, forming the organic material functional layer comprises successively forming a first functional layer, a light-emitting layer, and a second functional layer. Each spacing region comprises a first

spacing portion and a second spacing portion arranged successively in a light exit direction. The first spacing portion is configured to separate portions of at least the first functional layer corresponding to the respective light-emitting regions from each other.

[0023] It is to be noted that all the aspects of the present disclosure have similar or identical example implementations and benefits, which are not described here for simplicity.

[0024] These and other aspects of the present disclosure will be apparent from and will be elucidated with reference to the embodiments described hereinafter.

BRIEF DESCRIPTION OF DRAWINGS

[0025]

Fig 1 schematically illustrates a sectional view of an existing OLED array substrate.

Fig 2 schematically illustrates a sectional view of an OLED array substrate according to embodiments of the present disclosure.

Fig 3 schematically illustrates a sectional view of an OLED array substrate according to embodiments of the present disclosure.

DETAILED DESCRIPTION

[0026] Exemplary embodiments of the present disclosure will be described in detail below with reference to the accompanying drawings. The drawings are schematic and not drawn to scale, which are merely illustrative of the embodiments of the present disclosure and are not intended to limit the scope of the present disclosure. In the drawings, the same reference numerals denote the same or similar parts. In order to clarify the technical solution of the present disclosure, the process steps and device structures well known in the art are omitted here.

[0027] Hereinafter, specific examples of a touch panel, a display device, and a manufacturing method of a touch panel according to embodiments of the present disclosure will be described by way of example with reference to the accompanying drawings. The drawings are schematic and not drawn to scale, which are merely illustrative of the embodiments of the present disclosure and are not intended to limit the scope of the present disclosure.

[0028] For ease of description, spatially relative terms such as "under", "below", "lower", "above", "upper" and the like may be used herein to describe the relationship between one element or component and another element or component as shown in the drawings. It is to be understood that the spatially relative terms are intended to summarize different orientations of devices in use or operation other than those shown in the drawings. For example, if a device in the drawings is turned over, the elements described as being "under" or "below" other

elements or components will be "above" other elements or components. In this way, the exemplary term "below" can cover two orientations of "under" and "above". The device may take other orientations (which is rotated 90 degrees or in other orientations), and the spatially relative descriptors used here are interpreted accordingly.

[0029] Hereinafter, the present disclosure will be described based on an example of RGB pixels including a red (R) light-emitting region, a blue (B) light-emitting region, and a green (G) light-emitting region. However, as will be appreciated by those skilled in the art, the present disclosure is not limited to the RGB pixel structure but may equally be applied to pixel structures including light-emitting regions of other number and types.

[0030] Fig 1 schematically illustrates a sectional view of an existing OLED array substrate. As shown in Fig 1, an OLED array substrate 100 comprises a substrate 101, a first electrode layer 102, a pixel definition layer 104, an organic material functional layer 103, and a second electrode layer 105 arranged successively in a light exit direction. The organic material functional layer 103 comprises a light-emitting region B that emits blue light, a light-emitting region G that emits green light, and a light-emitting region R that emits red light, and comprises a first functional layer, a light-emitting layer and a second functional layer arranged successively. The pixel definition layer 104 comprises pixel regions corresponding to the light-emitting regions B, G, R, and spacing regions arranged between adjacent pixel regions. In the OLED array substrate shown in Fig 1, except that the light-emitting layer is vapor deposited using a fine metal mask to form portions of the light-emitting layer corresponding to the respective light-emitting regions B, G, R, the first functional layer, the second functional layer and the second electrode layer 105 are all formed by vapor deposition using a common mask.

[0031] Therefore, the portions of the first functional layer, the second functional layer, and the second electrode layer corresponding to the respective light-emitting regions B, G, R are all connected together. As a result, as shown in Fig 1, in the OLED array substrate, there exist both a longitudinal current perpendicular to the OLED array substrate and a lateral current parallel to the OLED array substrate. Upon displaying a monochromatic image, this lateral current causes two other light-emitting regions to be also illuminated weakly, resulting in abnormal display.

[0032] Fig 2 illustrates a sectional view of an OLED array substrate according to an embodiment of the present disclosure. As shown in Fig 2, an OLED array substrate 200 comprises a substrate 201, a first electrode layer 202, a pixel definition layer, an organic material functional layer 203, and a second electrode layer 205 arranged successively in a light exit direction. The organic material functional layer 203 comprises light-emitting regions B, G, R that emit light of different colors. The pixel definition layer comprises pixel regions corresponding to the light-emitting regions B, G, R, and spacing regions

204 arranged between adjacent pixel regions. Each spacing region 204 is configured to electrically isolate the adjacent light-emitting regions B, G, R.

[0033] It is to be noted that the pixel definition layer has a mesh structure. The meshes of the mesh structure correspond to the pixel regions of the pixel definition layer, while the edges of the meshes correspond to the spacing regions 204.

[0034] In the OLED array substrate proposed in the present disclosure, by employing the spacing regions that electrically isolate adjacent light-emitting regions, upon displaying a monochromatic (e.g. green) image by only illuminating a light-emitting region of a certain color (e.g. the light-emitting region G that emits green light), it is possible to prevent adjacent light-emitting regions of different colors (e.g. the light-emitting region B that emits blue light and the light-emitting region R that emits red light) from being also illuminated weakly, which results in abnormal display. As a result, the display effect of the display device can be improved.

[0035] As shown in Fig 2, the organic material functional layer 203 comprises a first functional layer 2031, a light-emitting layer 2032, and a second functional layer 2033 arranged successively in the light exit direction, and each spacing region 204 comprises a first spacing portion 2041 and a second spacing portion 2042 arranged successively in the light exit direction. The first spacing portion 2041 is configured to separate portions of the first functional layer 2031 corresponding to the respective light-emitting regions from each other. Optionally, the first spacing portion 2041 further separates portions of the light-emitting layer 2032 corresponding to the respective light-emitting regions from each other.

[0036] In such an OLED array substrate, the adjacent light-emitting regions are electrically isolated from each other, as long as the portions of the first functional layer corresponding to the respective light-emitting regions are separated from each other. Therefore, as compared to an implementation of separating portions of the first functional layer, the light-emitting layer and the second functional layer corresponding to the respective light-emitting regions from each other, the second functional layer 2033 corresponding to the respective light-emitting regions can be integrally formed. Consequently, the manufacturing process of such an OLED array substrate is simpler and the cost thereof is also lower.

[0037] As schematically shown in Fig 2, the angle α between a side wall of the first spacing portion 2041 and a lower surface of the first spacing portion 2041 is larger than the angle β between a side wall of the second spacing portion 2042 and a lower surface of the second spacing portion 2042.

[0038] In such an OLED array substrate, by making the angle α between the side wall of the first spacing portion 2041 and the lower surface thereof larger than the angle β between the side wall of the second spacing portion 2042 and the lower surface thereof, a corner can be formed at the joint between the side wall of the first

spacing portion 2041 and the side wall of the second spacing portion 2042, such that the separated portions of the first functional layer 2031 corresponding to the respective light-emitting regions can be formed simultaneously. Consequently, the manufacturing process of the OLED array substrate is simpler and the cost thereof also is lower.

[0039] Optionally, the angle α between the side wall of the first spacing portion 2041 and the lower surface of the first spacing portion 2041 is no less than 60° , and the angle β between the side wall of the second spacing portion 2042 and the lower surface of the second spacing portion 2042 is no more than 30° .

[0040] Optionally, the lateral etching rate of the material of the first spacing portion 2041 is greater than the lateral etching rate of the material of the second spacing portion 2042. Under the same etching conditions, the greater the lateral etching rate of the material is, the larger the angle formed between the side wall and the lower surface will be.

[0041] Fig 3 illustrates a sectional view of an OLED array substrate according to another embodiment of the present disclosure. An OLED array substrate 300 comprises a substrate 301, a first electrode layer 302, a pixel definition layer, an organic material functional layer 303, and a second electrode layer 305 arranged successively in a light exit direction. The organic material functional layer 303 comprises light-emitting regions B, G, R that emit light of different colors. The pixel definition layer comprises pixel regions corresponding to the light-emitting regions B, G, R, and spacing regions 304 arranged between adjacent pixel regions. Each spacing region 304 is configured to electrically isolate adjacent light-emitting regions B, G, R.

[0042] As shown in Fig 3, the organic material functional layer 303 comprises a first functional layer 3031, a light-emitting layer 3032, and a second functional layer 3033 arranged successively in the light exit direction, and each spacing region 304 comprises a first spacing portion 3041 and a second spacing portion 3042 arranged successively in the light exit direction. The first spacing portion 3041 is configured to separate portions of the first functional layer 3031 corresponding to the respective light-emitting regions from each other. Optionally, the first spacing portion 3041 further separates portions of the light-emitting layer 3032 corresponding to the respective light-emitting regions from each other. Unlike the OLED array substrate 200 shown in Fig 2, in the OLED array substrate 300, the width of an upper surface of the first spacing portion 3041 is no more than the width of the lower surface of the second spacing portion 3042.

[0043] In such an OLED array substrate, by making the width of the upper surface of the first spacing portion 3041 no more than the width of the lower surface of the second spacing portion 3042, a notch can be formed at the interface between the first spacing portion 3041 and the second spacing portion 3042, such that the separated portions of at least the first functional layer 3031 corre-

sponding to the respective light-emitting regions can be formed simultaneously. Consequently, the manufacturing process of the OLED array substrate is simpler and the cost thereof is also lower.

[0044] In the OLED array substrate 300, since the notch formed at the interface between the first spacing portion 3041 and the second spacing portion 3042 ensures that the portions of the first functional layer 3031 are separated from each other, the angle α between the side wall of the first spacing portion 3041 and the lower surface of the first spacing portion 3041 may be larger than, smaller than, even equal to the angle β between the side wall of the second spacing portion 3042 and the lower surface of the second spacing portion 3042.

[0045] Optionally, as shown in Figs 2 and 3, the thicknesses of the first spacing portions 2041, 3041 are greater than the thicknesses of the first functional layers 2031, 3031.

[0046] In such an OLED array substrate, by making the thickness of the first spacing portion greater than the thickness of the first functional layer, the separated portions of the first functional layer corresponding to the respective light-emitting regions can be formed simultaneously between the first spacing portions. Consequently, the manufacturing process of the OLED array substrate is simpler and the cost thereof is also lower.

[0047] It is to be noted that, although the first spacing portion and the second spacing portion are schematically illustrated as a regular trapezoid in the drawings, in an alternative embodiment, the first spacing portion and the second spacing portion may take any suitable shape. For example, the first spacing portion and/or the second spacing portion may be in the shape of a rectangle, an inverted trapezoid, or the like.

[0048] In the aforesaid embodiments, the first functional layer may comprise at least one of a hole injection layer, a hole transport layer and an electron barrier layer, and the second functional layer may comprise at least one of a hole barrier layer, an electron transport layer and an electron injection layer. The first electrode layer is an anode layer, and the second electrode layer is a cathode layer.

[0049] In the aforesaid embodiments, the first electrode layer may be a reflective electrode layer, and the second electrode layer may be a transparent electrode layer.

[0050] In the aforesaid embodiments, the light-emitting layer may comprise an organic light-emitting layer and an optical compensation layer arranged between the organic light-emitting layer and the first functional layer. The optical compensation layer is configured to adjust the effective optical cavity length in the organic material functional layer so that light of different colors is emitted from the organic material functional layer at maximum light intensity.

[0051] The present disclosure further provides an OLED display panel comprising any of the OLED array substrates described above.

[0052] In addition, the present disclosure provides a method of manufacturing an OLED array substrate. The method comprises successively forming a first electrode layer, a pixel definition layer, an organic material functional layer, and a second electrode layer. The organic material functional layer comprises light-emitting regions that emit light of different colors. The pixel definition layer comprises pixel regions corresponding to the light-emitting regions and spacing regions arranged between adjacent pixel regions. Each spacing region is configured to electrically isolate adjacent light-emitting regions.

[0053] In the above manufacturing method, forming the organic material functional layer may comprise successively forming a first functional layer, a light-emitting layer, and a second functional layer. Each spacing region comprises a first spacing portion and a second spacing portion arranged successively in a light exit direction. The first spacing portion is configured to separate portions of at least the first functional layer corresponding to the respective light-emitting regions from each other.

[0054] In such a manufacturing method, the first spacing portion and the second spacing portion may be formed in the same layer. Alternatively, the first spacing portion and the second spacing portion may be formed in different layers.

[0055] The concept of the present disclosure can be widely applied to any system having display function, including a desktop computer, a laptop computer, a mobile phone, a tablet computer, and the like. In addition, although several embodiments have been described in detail above, other modifications are possible. For example, components may be added to the described system or removed from the described system. Other embodiments may be within the scope of the present disclosure. Numerous variations and modifications may be made by those skilled in the art without departing from the spirit and scope of the present disclosure in view of the teachings of the present disclosure.

Claims

1. An OLED array substrate comprising a first electrode layer, a pixel definition layer, an organic material functional layer, and a second electrode layer arranged successively in a light exit direction, wherein the organic material functional layer comprises light-emitting regions being configured to emit light of different colors, the pixel definition layer comprises pixel regions corresponding to the light-emitting regions and spacing regions arranged between adjacent pixel regions, and each spacing region is configured to electrically isolate adjacent light-emitting regions.
2. The array substrate according to claim 1, wherein the organic material functional layer comprises a first

functional layer, a light-emitting layer, and a second functional layer arranged successively in the light exit direction, and each spacing region comprises a first spacing portion and a second spacing portion arranged successively in the light exit light, the first spacing portion being configured to separate portions of at least the first functional layer corresponding to respective light-emitting regions from each other.

3. The array substrate according to claim 2, wherein an angle between a side wall of the first spacing portion and a lower surface of the first spacing portion is larger than an angle between a side wall of the second spacing portion and a lower surface of the second spacing portion.
4. The array substrate according to claim 3, wherein the angle between the side wall of the first spacing portion and the lower surface of the first spacing portion is no less than 60 °, and the angle between the side wall of the second spacing portion and the lower surface of the second spacing portion is no more than 30 °.
5. The array substrate according to claim 3, wherein a lateral etching rate of a material of the first spacing portion is greater than a lateral etching rate of a material of the second spacing portion.
6. The array substrate according to claim 2, wherein a width of an upper surface of the first spacing portion is no more than a width of the lower surface of the second spacing portion.
7. The array substrate according to claim 2, wherein a thickness of the first spacing portion is greater than a thickness of the first functional layer.
8. The array substrate according to claim 2, wherein the first functional layer comprises at least one of a hole injection layer, a hole transport layer and an electron barrier layer, the second functional layer comprises at least one of a hole barrier layer, an electron transport layer and an electron injection layer, the first electrode layer is an anode layer, and the second electrode layer is a cathode layer.
9. The array substrate according to claim 2, wherein the light-emitting layer comprises an organic light-emitting layer and an optical compensation layer arranged between the organic light-emitting layer and the first functional layer.
10. The array substrate according to claim 1, wherein the first electrode layer is a reflective electrode layer, and the second electrode layer is a transparent electrode layer.

11. An OLED display panel comprising an OLED array substrate according to any one of claims 1 to 10.
12. A manufacturing method of an OLED array substrate, comprising: 5
- forming a first electrode layer, a pixel definition layer, an organic material functional layer and a second electrode layer successively, 10
- wherein 10
- the organic material functional layer comprises light-emitting regions being configured to emit light of different colors, 10
- the pixel definition layer comprises pixel regions corresponding to the light-emitting regions and spacing regions arranged between adjacent pixel regions, and each spacing region is configured to electrically isolate adjacent light-emitting regions. 15
- 20
13. The manufacturing method according to claim 12, wherein forming the organic material functional layer comprises forming a first functional layer, a light-emitting layer and a second functional layer successively, and each spacing region comprises a first spacing portion and a second spacing portion arranged successively in a light exit direction, the first spacing portion being configured to separate portions of at least the first functional layer corresponding to respective light-emitting regions from each other. 25
- 30
14. The manufacturing method according to claim 13, wherein the first spacing portion and the second spacing portion are formed in a same layer. 35
15. The manufacturing method according to claim 13, wherein the first spacing portion and the second spacing portion are formed in different layers. 40

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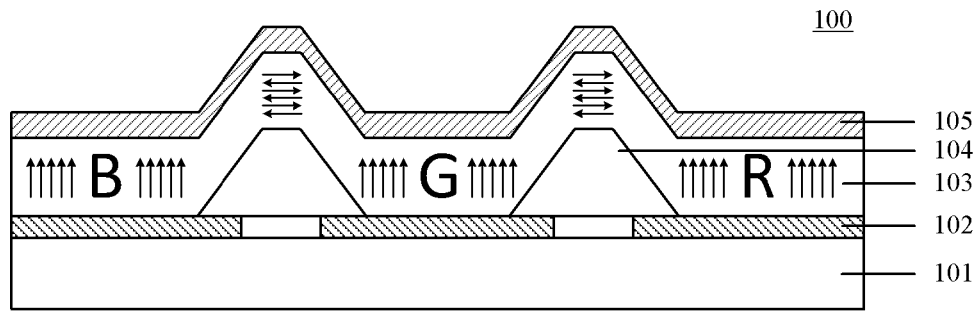


Fig.1

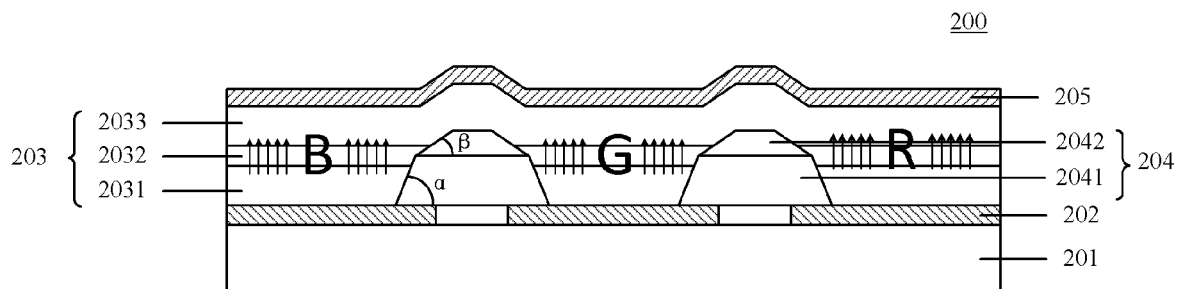


Fig.2

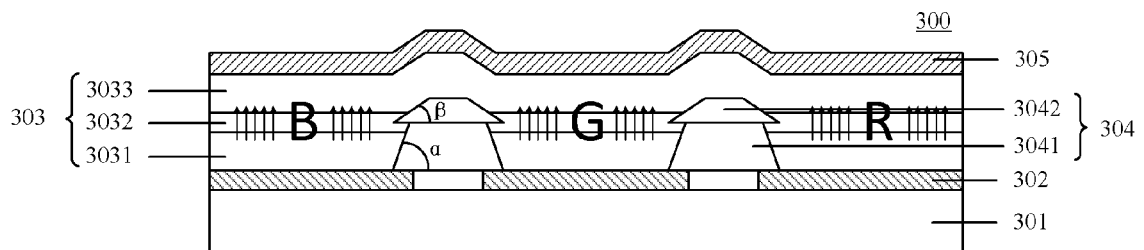


Fig.3

INTERNATIONAL SEARCH REPORT

International application No.
PCT/CN2017/076584

A. CLASSIFICATION OF SUBJECT MATTER

H01L 27/32 (2006.01) i; H01L 51/56 (2006.01) i
According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

CNABS, CPRSABS, DWPI: OLED, PDL, light emit+, +luminesc+, +luminous, EL, organic, isolate+, segregrat+, separate+, bank+,
defin+, spacer?, partition, pillar?, second, another, addition+, sub, subjoin+, optics compensation, angle, dam, limitation

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
PX	CN 106158914 A (BOE TECHNOLOGY GROUP CO., LTD. et al.) 23 November 2016 (23.11.2016) the whole document	1-15
X	US 2008230772 A1 (KAMIURA et al.) 25 September 2008 (25.09.2008) description, paragraphs [0032]-[0068], and figures 1-6F	1-3, 5-8, 10-13, 15
Y	US 2008230772 A1 (KAMIURA et al.) 25 September 2008 (25.09.2008) description, paragraphs [0032]-[0068], and figures 1-6F	4, 9, 14
Y	JP 2010282899 A (PANASONIC CORP.) 16 December 2010 (16.12.2010) description, paragraph [0071], and figures 2 and 3	4, 14
Y	CN 104538559 A (BEIJING VISIONOX TECHNOLOGY CO., LTD. et al.) 22 April 2015 (22.04.2015) description, paragraph [0040], and figure 3	9

☒ Further documents are listed in the continuation of Box C.☒ See patent family annex.

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"A" document defining the general state of the art which is not considered to be of particular relevance	
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"O" document referring to an oral disclosure, use, exhibition or other means	
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Date of the actual completion of the international search 11 May 2017	Date of mailing of the international search report 12 June 2017
Name and mailing address of the ISA State Intellectual Property Office of the P. R. China No. 6, Xitucheng Road, Jimenqiao Haidian District, Beijing 100088, China Facsimile No. (86-10) 62019451	Authorized officer TIAN, Shufeng Telephone No. (86-10) 62089264

Form PCT/ISA/210 (second sheet) (July 2009)

INTERNATIONAL SEARCH REPORT

International application No.
PCT/CN2017/076584

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	CN 105742311 A (KUNSHAN GOVISIONOX OPTOELECTRONICS CO., LTD.) 06 July 2016 (06.07.2016) description, paragraph [0003], and figures 1 and 2	1-3, 6-8, 10-14
X	CN 102017158 A (CAMBRIDGE DISPLAY TECHNOLOGY CO., LTD. et al.) 13 April 2011 (13.04.2011) description, paragraphs [0092]-[0132], and figures 7, 8A-8D, 10 and 11	1, 2, 6-8, 10-13, 15
X	CN 101552234 A (E.I. DU PONT DE NEMOURS AND CO.) 07 October 2009 (07.10.2009) description, page 5, line 8 to page 11, line 15, and figures 1-3	1-3, 6-8, 10-14
X	US 6339288 B1 (QIAN et al.) 15 January 2002 (15.01.2002) description, column 9, line 1 to column 24, line 16, and figures 1-26	1, 2, 6-8, 10-14
X	US 2012080694 A1 (YOSHIDA et al.) 05 April 2012 (05.04.2012) description, paragraphs [0061]-[0063], and figure 4	1-3, 6-8, 10-14
X	JP 2007220656 A (TOPPAN PRINTING CO., LTD.) 30 August 2007 (30.08.2007) description, paragraphs [0030]-[0033], and figure 1	1, 2, 6-8, 10-13, 15

Form PCT/ISA/210 (continuation of second sheet) (July 2009)

INTERNATIONAL SEARCH REPORT Information on patent family members

International application No.
PCT/CN2017/076584

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专利名称(译)	oled阵列基板，IT和OLED显示面板的生产过程		
公开(公告)号	EP3493263A4	公开(公告)日	2020-03-25
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[标]申请(专利权)人(译)	京东方科技集团股份有限公司 合肥京东方光电科技有限公司		
申请(专利权)人(译)	京东方科技集团股份有限公司. 合肥京东方光电科技有限公司.		
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CPC分类号	H01L27/3244 H01L51/56 H01L27/3211 H01L27/3246 H01L27/3295 H01L27/32 H01L51/5218 H01L51/5234 H01L51/5265 H01L2227/323		
优先权	201610609987.6 2016-07-29 CN		
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摘要(译)

本公开提供了一种OLED阵列基板，其制造方法和OLED显示面板。

OLED阵列基板包括沿光出射方向连续布置的第一电极层，像素限定层，有机材料功能层和第二电极层。有机材料功能层包括发出不同颜色的光的发光区域。像素限定层包括与发光区域相对应的像素区域以及布置在相邻像素区域之间的间隔区域。每个间隔区域被配置为电隔离相邻的发光区域。