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(54) **PIXEL STRUCTURE, MANUFACTURING METHOD THEREOF, PIXEL DISPLAY METHOD AND ARRAY SUBSTRATE**

(57) The present invention discloses a pixel structure and a preparation method thereof, a pixel display method and an array substrate, so as to solve the problems that the life time of the QLED display device of the existing quantum dot electroluminescent QLED display device is short and the difficulty for preparation of the QLED display device is great. The pixel structure comprises: a thin film transistor TFT for controlling a Micro-Electro-Mechanical

System MEMS switch; the Micro-Electro-Mechanical System MEMS switch being used for controlling transmission amount of outgoing light of a quantum dot light emitting diode QLED device; the quantum dot light emitting diode QLED device being a top emission type for emitting light constantly based on a constant light emitting driving signal.

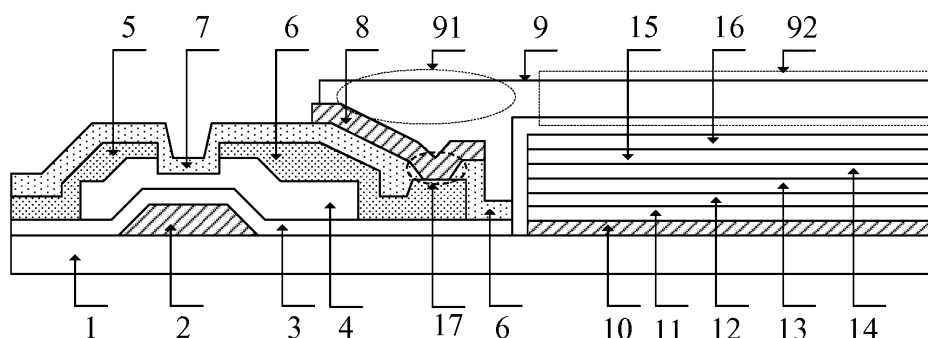


Fig. 1

Description

FIELD OF THE INVENTION

[0001] The present invention relates to the field of display technology, particularly to a pixel structure and a preparation method thereof, a pixel display method and an array substrate.

BACKGROUND OF THE INVENTION

[0002] The quantum dot (QD) may also be called nanocrystalline, it is a nanoparticle consisting of II-VI family or III-V family elements, since electrons and holes are confined by quanta, successive energy band structures become discrete energy level structures having molecular properties, which can emit fluorescent light after being excited, the luminescent spectrum thereof can be controlled by changing the size of the quantum dot, the fluorescent intensity and stability are both good, which is a perfect electroluminescent material. There is a great variety of quantum dots, which are represented by CdS, CdSe, CdTe, ZnO, ZnS, ZnSe, ZnTe of II-VI family and GaAs, GaP, GaSb, HgS, HgSe, HgTe, InAs, InP, InSb, AlAs, AlP, AlSb of III-V family.

[0003] At present, the quantum dot as a display material has been widely used in the field of display, for example, quantum dot light emitting display (QLED) manufactured by using quantum dots as the luminescent material. The structure used by the existing QLED display is similar as the organic light emitting display (OLED), which controls the cathode of the QLED display device in the QLED display through the on-off action of the TFT, so as to control the QLED display device to emit light or not emit light, i.e., controlling flicker of the QLED display device. However, the QLED display of the prior art has the following problems: the existing QLED display controls the frequent flicker of the QLED display device through constant change of the driving current, hence, it may result in unbalance of injection of charges (i.e., the number of injected electrons and holes is unbalanced), repetition of this process for a long time may result in chemical dissociation of the quantum dots (e.g., oxidation/deoxidization) thereby property change may occur, such that the life time of the QLED display device is reduced; moreover, the way of controlling the cathode of the QLED display device in the QLED display through on-off action of the TFT to realize change of the driving current so as to control flicker of the QLED display device has the problem that the current control is unstable, which requires a complex compensation circuit to compensate the current, hence, the difficulty for preparation of the QLED display device of the prior art is great.

SUMMARY OF THE INVENTION

[0004] The object of the present invention is to provide a pixel structure and a preparation method thereof, a pixel

display method and an array substrate, so as to solve the problems that the life time of the existing QLED display device is short and the difficulty of the preparation process is great.

[0005] The embodiment of the present invention provides a pixel structure, comprising:

a thin film transistor (TFT) for controlling a Micro-Electro-Mechanical System (MEMS) switch;
the Micro-Electro-Mechanical System (MEMS) switch being used for controlling transmission amount of outgoing light of a quantum dot light emitting diode (QLED) device;
the quantum dot light emitting diode (QLED) device being a top emission type for emitting light constantly based on a constant light emitting driving signal.

[0006] In the embodiment of the present invention, the MEMS switch is added in the pixel structure, the TFT controls the working state of the MEMS switch so as to control transmission amount of outgoing light of the QLED device.

[0007] Optionally, the drain electrode of the TFT is electrically connected with the MEMS switch, the TFT, after being turned on, controls the MEMS switch based on a signal received by a source electrode of itself. In this embodiment, the working state of the MEMS switch is changed or adjusted based on the on/off state of the TFT and the received signal, thereby controlling transmission amount of outgoing light of the QLED device.

[0008] Optionally, the MEMS switch comprises a connecting portion and a switch portion, the connecting portion being electrically connected with the drain electrode of the TFT, the switch portion being arranged at a side in a light emission direction of the QLED device to control light amount of the outgoing light of the QLED device. In this embodiment, the switch portion of the MEMS switch is arranged in the light emission direction of the QLED device, such that it can control the light amount of the outgoing light of the QLED device based on its switching amplitude.

[0009] Optionally, the pixel structure further comprises light shielding bars, the light shielding bars being located below an edge of a side of the MEMS switch close to the QLED device. In the embodiment of the present invention, the light shielding bars can shield light leakage of the MEMS switch and avoid display abnormality.

[0010] Optionally, the pixel structure further comprises a first conductive structure through which first conductive structure the MEMS switch is electrically connected with the drain electrode of the TFT.

[0011] Optionally, an insulating layer is arranged between the drain electrode of the TFT and the connecting portion of the MEMS switch, the first conductive structure is electrically connected with the drain electrode of the TFT through a first via hole on the insulating layer. In the embodiment of the present invention, the first via hole formed on the insulating layer enables the first conductive

structure and the drain electrode to realize electrical connection.

[0012] Optionally, the light shielding bars and the first conductive structure are arranged in the same layer and are insulated from each other, materials of the first conductive structure and the light shielding bars are metal materials. In the embodiment of the present invention, the light shielding bars and the first conductive structure are arranged in the same layer, which can save working processes in preparation.

[0013] The beneficial effect of the embodiment of the present invention is as follows: when the display device works, the TFT controls the working state of the MEMS switch, so as to control transmission amount of outgoing light of the QLED device, avoid frequent flicker of the QLED device, and increase the life time of the QLED device.

[0014] The embodiment of the present invention provides an array substrate comprising a plurality of pixel units arranged in an array, each of the pixel units adopts a pixel structure provided by the above embodiments.

[0015] The beneficial effect of the embodiment of the present invention is as follows: when the display device works, the QLED device is enabled to emit light constantly, the TFT controls the working state of the MEMS switch, so as to control transmission amount of outgoing light of the QLED device, avoid frequent flicker of the QLED device, and increase the life time of the QLED device.

[0016] The embodiment of the present invention provides a method for preparing a pixel structure, comprising:

forming, on a substrate, a thin film transistor (TFT) and a top emission type quantum dot light emitting diode (QLED) device adjacent to the TFT;
forming, on the substrate on which the above step is performed, a Micro-Electro-Mechanical System (MEMS) switch comprising a connecting portion and a switch portion, enabling the connecting portion of the MEMS switch to be electrically connected with a drain electrode of the TFT, and enabling the switch portion of the MEMS to be formed above the QLED.

[0017] Optionally, after forming the TFT on the substrate, it further comprises:

forming an insulating layer comprising a first via hole over the TFT, and forming, on the substrate, a first region spaced apart from the TFT; wherein the position of the first via hole corresponds to the position of the drain electrode of the TFT.

[0018] Optionally, it further comprises forming a first conductive structure electrically connected with the drain electrode of the TFT over the insulating layer; enabling the connecting portion of the MEMS switch to be electrically connected with the drain electrode of the TFT is specifically: enabling the connecting portion of the

MEMS switch to be electrically connected with the drain electrode of the TFT through the first conductive structure.

[0019] Optionally, before forming a MEMS switch comprising a connecting portion and a switch portion, it further comprises:

forming light shielding bars above the QLED, the light shielding bars being located below an edge of a side of the MEMS switch close to the QLED device.

[0020] The beneficial effect of the embodiment of the present invention is as follows: the MEMS switch is added in the pixel structure, the MEMS switch is electrically connected with the drain electrode of the TFT; when the display device works, the QLED device is enabled to emit light constantly, the TFT controls the working state of the MEMS switch, so as to control transmission amount of outgoing light of the QLED device, avoid frequent flicker of the QLED device, and increase the life time of the QLED device.

[0021] The embodiment of the present invention provides a pixel display method, comprising:

a quantum dot light emitting diode (QLED) device emitting light constantly based on driving of a constant light emitting driving signal;
a thin film transistor (TFT) controlling a working state of a Micro-Electro-Mechanical System (MEMS) switch, to enable the MEMS switch to control transmission amount of outgoing light of the QLED device, so as to realize pixel display.

[0022] Optionally, the TFT controlling the working state of the MEMS switch to enable the MEMS switch to control transmission amount of outgoing light of the QLED device comprises:

transmitting an image data signal received by a source electrode of the TFT to the connecting portion of the MEMS switch via the drain electrode of the TFT and the first conductive structure successively, to enable the switch portion of the MEMS switch to open a switching amplitude matching with the image data signal, so as to control light amount of outgoing light of the QLED device.

[0023] The beneficial effect of the embodiment of the present invention is as follows: the MEMS switch is added in the pixel structure, the MEMS switch is electrically connected with the drain electrode of the TFT; when the display device works, the QLED device is enabled to emit light constantly, the TFT controls the working state of the MEMS switch, so as to control transmission amount of outgoing light of the QLED device, avoid frequent flicker of the QLED device, and increase the life time of the QLED device.

BRIEF DESCRIPTION OF THE DRAWINGS

[0024]

Fig. 1 is a schematic view of a sectional structure of a first pixel structure provided by the embodiment of the present invention;

Fig. 2 is a schematic view of a sectional structure of a second pixel structure provided by the embodiment of the present invention;

Fig. 3 is a schematic view of a sectional structure of a third pixel structure provided by the embodiment of the present invention;

Fig. 4 is a schematic view of a sectional structure of a fourth pixel structure provided by the embodiment of the present invention;

Fig. 5 is a schematic view of a sectional structure of a fifth pixel structure provided by the embodiment of the present invention;

Fig. 6 is a schematic view of a sectional structure of a sixth pixel structure provided by the embodiment of the present invention;

Fig. 7 is a schematic view of light shielding bars and color resistances provided by the embodiment of the present invention;

Fig. 8 is a flow chart of a method for preparing a pixel structure provided by the embodiment of the present invention;

Fig. 9 is a flow chart of a pixel display method provided by the embodiment of the present invention.

Reference signs:

[0025] Substrate 1; gate electrode 2; gate insulating layer 3; active layer 4; source electrode 5; drain electrode 6; insulating layer 7; passivation layer 71; first conductive structure 8; MEMS switch 9; anode 10; hole injection layer 11; hole transport layer 12; QLED light emitting layer 13; electron transport layer 14; electron injection layer 15; cathode 16; first via hole 17; light shielding bar 18; first electrode 19; second electrode 20.

DETAILED DESCRIPTION OF THE INVENTION

[0026] Next, the implementing process of the embodiments of the present invention will be explained in detail with reference to the drawings of the description. It should be noted that the same or similar signs represent the same or similar elements or elements with the same or similar functions all through. The following embodiments described with reference to the drawings are exemplary, which are only used for explanations of the present invention, while cannot be understood as limitations to the present invention.

[0027] In order to avoid frequent flicker of the QLED device in the pixel structure and increase the life time of the QLED device, the embodiment of the present invention provides a pixel structure, comprising: a thin film transistor (TFT) for controlling a Micro-Electro-Mechanical System (MEMS) switch; the Micro-Electro-Mechanical System (MEMS) switch being used for controlling transmission amount of outgoing light of a quantum dot light emitting diode (QLED) device; the quantum dot light emitting diode (QLED) device being a top emission type for emitting light constantly based on a constant light emitting driving signal. It should be noted that "constant light emitting" in the embodiment of the present invention is not limited to absolute constant light emitting, nor means that the QLED device is in the bright state all the time and not turned off, the "constant" here is respect to the flicker light emitting mode of the QLED device, the intensity of the driving signal is relatively constant when the pixel is driven to emit light, it is not necessary to realize different gray scale display of the pixel through the strength of the driving signal. The QLED device adapted for "constant light emitting" provides a MEMS switch that can control transmission amount of outgoing light of the quantum dot QLED device, the MEMS switch can, based on the control of the TFT, not transmit the outgoing light of the QLED device, or transmit part of the outgoing light of the QLED device, or transmit all outgoing light of the QLED device, such that the pixel structure can provide the same function of performing different gray scale display as flicker light emitting of the QLED device when the QLED device is in the state of "constant light emitting", in this embodiment, the MEMS switch can be an adjustable optical grating or light valve, the optical grating or light valve can control the width of the opened gap by the TFT so as to control the amount of the outgoing light. The TFT can be a bottom gate type, a top gate type or other structures, the TFT at least one of the source electrode and the drain electrode electrically connected with the MEMS switch, so that the TFT drives the open state or open amplitude of the MEMS switch based on the strength of the driving signal so as to adjust the amount of light transmission.

[0028] Optionally, the drain electrode of the TFT is electrically connected with the MEMS switch, the TFT, after being turned on, controls the MEMS switch based on a signal received by a source electrode of itself. In this embodiment, the MEMS switch is electrically connected with the drain electrode of the TFT, thereby realizing control of the MEMS switch by the TFT. Certainly, for different types of TFT, the source electrode of the TFT can also be electrically connected with the MEMS switch, since the TFT can generally be turned on bidirectionally, it does not influence the implementation of the present invention either the source electrode or the drain electrode of the TFT being electrically connected with the MEMS switch, which will not be repeated here.

[0029] Optionally, the MEMS switch comprises a connecting portion and a switch portion, the connecting portion being electrically connected with the drain electrode of the TFT, the switch portion being arranged at a side in a light emission direction of the QLED device to control light amount of the outgoing light of the QLED device. In

this embodiment, the switch portion of the MEMS switch is arranged in the light emission direction of the QLED device, such that it can control the light amount of the outgoing light of the QLED device based on its switching amplitude.

[0030] Optionally, the pixel structure further comprises light shielding bars, the light shielding bars being located below an edge of a side of the MEMS switch close to the QLED device. In the embodiment of the present invention, the light shielding bars can shield light leakage of the MEMS switch and avoid display abnormality.

[0031] Optionally, the pixel structure further comprises a first conductive structure through which first conductive structure the MEMS switch is electrically connected with the drain electrode of the TFT.

[0032] Optionally, an insulating layer is arranged between the drain electrode of the TFT and the connecting portion of the MEMS switch, the first conductive structure is electrically connected with the drain electrode of the TFT through a first via hole on the insulating layer. In the embodiment of the present invention, the first via hole formed on the insulating layer enables the first conductive structure and the drain electrode to realize electrical connection. It should be noted that for TFTs of different structures, the insulating layer may be one of a passivation layer, a gate insulating layer or a planarization layer or a combination thereof.

[0033] Optionally, the light shielding bars and the first conductive structure are arranged in the same layer and are insulated from each other, materials of the first conductive structure and the light shielding bars are metal materials. In the embodiment of the present invention, the light shielding bars and the first conductive structure are arranged in the same layer, which can save working processes in preparation.

[0034] In the embodiment of the present invention, the pixel structure has a "constant light emitting" QLED device, a MEMS switch controlled by the TFT, the TFT can control the working state of the MEMS switch based on the strength of the driving signal, so that the MEMS switch transmits the outgoing light of the QLED device that meets the requirement and controls the transmission amount of the outgoing light of the QLED device.

[0035] In order to describe the pixel structure provided by the embodiment of the present invention more clearly, the pixel structure as shown in Fig. 1 is explained as follows:

Embodiment 1

[0036] Referring to Fig. 1, the pixel structure comprises a thin film transistor (TFT), a quantum dot light emitting diode (QLED) device spaced apart from the TFT, the thin film transistor comprises a gate electrode 2, a gate insulating layer 3, an active layer 4, a source electrode 5, a drain electrode 6 and an insulating layer 7 formed on a substrate 1 successively, a first via hole 17 is formed on the insulating layer 7, the first via hole 17 corresponds

to at least part of the drain electrode 6 of the TFT. In the embodiment of the present invention, the TFT is a bottom gate type structure, the insulating layer 7 is a passivation layer or a combination of a passivation layer and a planarization layer.

[0037] The pixel structure further comprises a first conductive structure 8 formed over the insulating layer 7, the first conductive structure 8 is electrically connected with the drain electrode 6, the first conductive structure 8 is electrically connected with the drain electrode 6 of the TFT through the first via hole 17.

[0038] A MEMS switch 9 is formed over the first conductive structure 8, the MEMS switch 9 is electrically connected with the first conductive structure 8; the MEMS switch 9 adjusts the switching amplitude based on the control of on-off of the TFT, so as to control transmission amount of the outgoing light of the QLED device. For example, when the TFT is turned on, the MEMS switch 9 opens a certain amplitude, so that the light of the QLED device penetrates the opened MEMS switch 9 for display, the open degree of the MEMS switch 9 can be determined by the parameter of the signal received by the TFT e.g., the image data signal, for example, if the display brightness to which the image data signal corresponds is high, the open amplitude of the MEMS switch 9 is large, if the display brightness to which the image data signal corresponds is low, the open amplitude of the MEMS switch 9 is small; when the TFT is turned off, the MEMS switch 9 is closed, so that the light of the QLED device cannot penetrate the MEMS switch 9 for display; certainly, it may also take values based on boundary, i.e., enabling the MEMS switch 9 to have only two states, i.e., open and close states, corresponding to the two states, enabling the QLED device to have two states of transmitting light and not transmitting light. It should be noted that the first conductive structure 8 may be made from a metal material or an ITO, it is fabricated using metal in an optional embodiment, so as to achieve the effect of having a good conductivity and signal transfer characteristic. In the embodiment of the present invention, the pixel structure is provided with a MEMS switch 9, the TFT switch controls switching of the MEMS switch 9, so as to control display of the QLED device.

[0039] Optionally, the MEMS switch 9 comprises a connecting portion 91 and a switch portion 92; the connecting portion 91 covers the first conductive structure 8 at the position of the first via hole 17, the switch portion 92 is arranged at a side in the light emission direction of the QLED device. In the embodiment of the present invention, since the MEMS switch 9 can change the switching amplitude of the switching state, the MEMS switch 9, after shielding the QLED device, can control whether the QLED device emits light based on its switching state, and control the transmission amount of the outgoing light of the QLED device based on the switching amplitude, so as to realize display; in an optional embodiment, the connecting portion 91 of the MEMS switch 9 covers the first conductive structure 8 at the position of the first via hole

17, i.e., the area of the connecting portion 91 is greater than the area of the first conductive structure 8, thus it can avoid the first conductive structure 8 from being corrupted and damaged during the subsequent fabricating process.

[0040] Optionally, the QLED device comprises an anode 10, a hole injection layer 11, a hole transport layer 12, a QLED light emitting layer 13, an electron transport layer 14, an electron injection layer 15 and a cathode 16 formed on the substrate 1 successively. It should be noted that the QLED device in this embodiment is of a top emission type, the cathode 16 of the QLED device is of a transparent material, the light is transmitted by the cathode 16 of the QLED device and arrives at the switching portion 92 of the MEMS switch 9. In an additional embodiment, the QLED device may also be an inverted bottom emission type QLED device, so as to be adapted for different use requirements.

[0041] It should be noted that the specific structure of the switch portion 92 of the MEMS switch 9 can be a grating valve, or other adjustable light transmissive structures with unlimited shapes such as a grid; in an optional embodiment, the gratings of the grating valve may be arranged in various directions, including angles formed with the TFT from the source electrode to the drain electrode direction between 0 to 90 degrees (referring to the drawings, i.e., substantially 0 degree in the case as shown in Fig. 1, substantially 90 degrees in the case as shown in Fig. 2), such as the switch portion 92 in the sectional schematic view of the pixel structure as shown in Fig. 2.

[0042] In an optional embodiment, in order to avoid the MEMS switch 9 from generating light leakage at the edge when shielding the QLED device, light shielding bars can also be added on the basis of the pixel structure as shown in Fig. 1. The pixel structure as shown in Fig. 3 differs from the pixel structure as shown in Fig. 1 in that: light shielding bars 18 are arranged at the edge of the cathode 16 of the QLED device, the material of the light shielding bars 18 may be any material with the light shielding function, e.g., metal, black matrix material, or other resin materials in which light shielding substances are added; in an optional embodiment, the light shielding bars 18 and the first conductive structure 8 are arranged in the same layer and are insulated from each other with, specifically, the light shielding bars 18 and the first conductive structure 8 are formed in the same patterning process using the same material, thus the patterning process such as one exposure can be saved, which reduces the cost and optimizes the fabricating process. Fig. 7 shows a schematic view of the light shielding bars 18 and the color resistances, the color resistances include for example R, G, B color resistances, or combination of other colors such as R, G, B W, certainly, the R, G, B color resistances are not limited to the arrangement manner as shown in Fig. 7, various arrangement and layout can be performed based on needs; in an optional embodiment, the light shielding bars 18 are connected together on a row or a

column of pixels for shielding light leakage at the edge of the MEMS switch 9; in another preferred embodiment, the light shielding bars 18 are not limited only to shield light to one edge of the pixel, for example, when the shape of the pixel is substantially a rectangle, the light shielding bars 18 can be arranged at any position of the edge of the pixel for shielding light, preferably, the light shielding bars 18 are at least arranged at the edge of a side of the pixel opposite to the TFT, the light shielding effect of which is better. In a further preferred embodiment, the light shielding bars 18 are also electrically connected with a cathode signal source for providing an electrical signal for the cathode, thereby improving the luminous effect of the QLED device.

[0043] The beneficial effect of the embodiment of the present invention is as follows: a MEMS switch is added in the pixel structure, the MEMS switch is electrically connected with the first conductive structure, the first conductive structure is electrically connected with the drain electrode of the TFT through the first via hole, thereby enabling the MEMS switch to be electrically connected with the drain electrode of the TFT; when the display device works, the QLED device emits light constantly, the TFT controls the light emission amount of the outgoing light of the QLED device by controlling the switching amplitude of the MEMS switch, so as to avoid frequent flicker of the QLED device and increase the life time of the QLED device; The display of the QLED display device is controlled through switching of the MEMS switch, hence, the current compensation is not needed to adjust the brightness of the QLED display device, the complex current compensation circuit can be omitted, so that the preparation difficulty of the QLED device is reduced.

Embodiment II

[0044] Referring to Fig. 4, another pixel structure is provided, the pixel structure comprising a thin film transistor (TFT), a quantum dot light emitting diode (QLED) device spaced apart from the TFT, the thin film transistor comprises an ohmic contact layer, an active layer 4, a gate insulating layer 3, a gate electrode 2, a passivation layer 71 and a source-drain electrode metal layer formed on a substrate 1 successively; The ohmic contact layer comprises a first electrode 19 and a second electrode 20, the source-drain metal layer comprises a source electrode 5 and a drain electrode 6. The TFT is a top gate type structure, compared with the above bottom gate type TFT, this embodiment is not a simple transformation and replacement from the bottom gate type to the top gate type, this optional embodiment uses a TFT of the top gate type structure which enables the connecting portion 91 to contact with the drain electrode 6 directly when fabricating the MEMS switch, which simplifies the fabricating process, avoids fabricating steps and reduces the cost.

[0045] The pixel structure further comprises a MEMS switch 9 electrically connected with the drain electrode

6, the MEMS switch 9 comprising a connecting portion and a switch portion, the connecting portion 91 being electrically connected with the drain electrode of the TFT switch, the switch portion 92 shielding the QLED device. It should be noted that in the embodiment of the present invention, the insulating layer over the TFT is omitted. The connecting portion 91 of the MEMS switch 9 is in direct contact with the drain electrode 6 of the TFT switch to realize electrical connection. The MEMS switch 9 adjusts the switching amplitude based on the on-off control of the TFT, so as to control the transmission amount of outgoing light of the QLED device. For example, when the TFT is turned on, the MEMS switch 9 opens a certain amplitude, so that the light of the QLED device penetrates the opened MEMS switch 9 for display, the open amplitude of the MEMS switch 9 can be determined by the parameter of the image data signal, for example, if the display brightness to which the image data signal corresponds is high, the open amplitude of the MEMS switch 9 is large, if the display brightness to which the image data signal corresponds is low, the open amplitude of the MEMS switch 9 is small; when the TFT is turned off, the MEMS switch 9 is closed, so that the light of the QLED device cannot penetrate the MEMS switch 9 for display; certainly, it may also take values based on boundary, i.e., enabling the MEMS switch 9 to have only two states, i.e., maximum amplitude and close states, corresponding to the two states, enabling the outgoing light of the QLED device to have a maximum transmission amount and not transmit light.

[0046] Optionally, the QLED device comprises an anode 10, a hole injection layer 11, a hole transport layer 12, a QLED light emitting layer 13, an electron transport layer 14, an electron injection layer 15 and a cathode 16 formed on the substrate 1 successively. It should be noted that the QLED device is of a top emission type, the cathode 16 of the QLED device is of a transparent material, the light is transmitted by the cathode 16 of the QLED device and arrives at the switch portion 92 of the MEMS switch 9.

[0047] It should be noted that the specific structure of the switch portion 92 of the MEMS switch 9 can be a grating valve, or other adjustable light transmissive structures with unlimited shapes such as a grid; in an optional embodiment, the gratings of the grating valve may be arranged in various directions, including angles formed with the TFT from the source electrode to the drain electrode direction between 0 to 90 degrees, such as the switch portion 92 in the sectional schematic view of the pixel structure as shown in Fig. 5.

[0048] In an optional embodiment, in order to avoid the MEMS switch 9 from generating light leakage at the edge when shielding the QLED device, light shielding bars can also be added on the basis of the pixel structure as shown in Fig. 1. The pixel structure as shown in Fig. 6 differs from the pixel structure as shown in Fig. 4 in that: light shielding bars 18 are arranged at the edge of the cathode 16 of the QLED device, the light shielding bars 18 are

located below the switch portion 92 of the MEMS switch 9, the material of the light shielding bars 18 may be any material with the light shielding function, e.g., metal, black matrix material, or other resin materials in which light shielding substances are added. Fig. 7 shows a schematic view of the light shielding bars 18 and the color resistances, the color resistances include for example R, G, B color resistances, or combination of other colors such as R, G, B W, certainly, the R, G, B color resistances are not limited to the arrangement manner as shown in Fig. 7, various arrangement and layout can be performed based on needs, for example, the color resistances to which the R, G, B, W sub-pixels that belong to the same pixel are arranged in a 2x2 matrix; in an optional embodiment, the light shielding bars 18 are connected together on a row or a column of pixels for shielding light leakage at the edge of the MEMS switch 9; in another preferred embodiment, the light shielding bars 18 are not limited only to shield light to one edge of the pixel, for example, when the shape of the pixel is substantially a rectangle, the light shielding bars 18 can be arranged at any position of the edge of the pixel for shielding light, preferably, the light shielding bars 18 are at least arranged at the edge of a side of the pixel opposite to the TFT, the light shielding effect of which is better. In a further preferred embodiment, the light shielding bars 18 are also electrically connected with a cathode signal source for providing an electrical signal for the cathode, thereby improving the luminous effect of the QLED device.

[0049] The beneficial effect of the embodiment of the present invention is as follows: a MEMS switch is added in the pixel structure, the MEMS switch is electrically connected with the first conductive structure; when the display device works, the QLED device emits light constantly, the TFT controls the light emission amount of the outgoing light of the QLED device by controlling the switching amplitude of the MEMS switch, so as to avoid frequent flicker of the QLED device and increase the life time of the QLED device; The display of the QLED display device is controlled through switching of the MEMS switch, hence, the current compensation is not needed to adjust the brightness of the QLED display device, the complex current compensation circuit can be omitted, so that the preparation difficulty of the QLED device is reduced.

Embodiment III

[0050] The embodiment of the present invention provides an array substrate comprising a plurality of pixel units arranged in an array, each pixel unit adopts a pixel structure provided by the above embodiment.

[0051] Optionally, for the consideration of simplifying the fabricating process and saving the working procedure, the QLED of each column of pixel can be made as a whole, i.e., according to the corresponding arrangement manner of the R, G, B color resistances as shown in Fig. 7.

[0052] The beneficial effect of the embodiment of the

present invention is as follows: a MEMS switch is added in the pixel structure, the MEMS switch is electrically connected with the first conductive structure; when the display device works, the QLED device emits light constantly, the TFT controls the light emission amount of the outgoing light of the QLED device by controlling the switching amplitude of the MEMS switch, so as to avoid frequent flicker of the QLED device and increase the life time of the QLED device; The display of the QLED display device is controlled through switching of the MEMS switch, hence, the current compensation is not needed to adjust the brightness of the QLED display device, the complex current compensation circuit can be omitted, so that the preparation difficulty of the QLED device is reduced.

Embodiment IV

[0053] Referring to Fig. 8, the embodiment of the present invention provides a method for preparing a pixel structure, comprising:

Step 801, forming, on a substrate, a thin film transistor (TFT), and a top emission type quantum dot light emitting diode (QLED) device adjacent to the TFT.

[0054] The TFT may be a bottom gate type, a top gate type or other structure. For example, a first TFT structure is provided, this TFT is a bottom gate type, comprising a gate metal layer, a gate insulating layer, an active layer, a source-drain metal layer formed on the substrate successively; for another example, a second TFT structure is provided, this TFT is a top gate type, comprising a source-drain metal layer, an active layer, a gate insulating layer and a gate metal layer formed on the substrate successively; for a further example, a third TFT structure is provided, the TFT is a top gate type, comprising an ohmic contact layer, an active layer, a gate insulating layer, a gate metal layer, a passivation layer and a source-drain metal layer formed on the substrate successively, such a structure takes the ohmic contact layer as the medium for contact of the source-drain metal layer and the active layer, hence, it can improve the TFT property effectively and reduce parasitic capacitance between the source-drain metal layer and the gate metal layer. The QLED device may comprise an anode, a hole injection layer, a hole transport layer, a QLED light emitting layer, an electron injection layer, an electron transport layer and a cathode formed on the substrate successively. The specific preparation method is slightly different based on different structures of the TFT.

[0055] After forming the TFT on the substrate, it generally comprises:

Step 802, forming an insulating layer comprising a first via hole over the TFT, and forming on the substrate a first region depart from the TFT; wherein the position of the first via hole corresponds to the posi-

tion of the drain electrode of the TFT. As for the first TFT structure and the second TFT structure, the insulating layer may be one of a passivation layer and a planarization layer or a combination thereof generally. As for the third TFT structure, the insulating layer may be a planarization layer generally. It should be noted that for the third TFT structure, since the source-drain metal layer of the TFT is at the uppermost, the source electrode and the drain electrode of the TFT are connected with the ohmic contact layer through the via hole, hence, the subsequently prepared MEMS switch can be electrically connected with the drain electrode of the TFT directly, thereby omitting the insulating layer.

[0056] In the case of considering a better conductivity or reducing the preparation difficulty, optionally, for the preparation of the pixel structure using the first TFT, a first conductive structure electrically connected with the drain electrode of the TFT can be formed over the insulating layer, which enables the connecting portion of the MEMS switch to be electrically connected with the drain electrode of the TFT through the first conductive structure, such that when the connecting portion of the MEMS switch is prepared subsequently, it does not have to consider extending into the layer where the drain electrode of the TFT locates, which reduces the preparation difficulty and also has a better conductive performance.

[0057] Since the subsequently prepared MEMS switch is not absolutely airtight, the phenomenon of light leakage may exist at the side of the MEMS switch, for example, the side of the MEMS switch away from the TFT. Therefore, optionally, it further comprises forming light shielding bars above the QLED, the light shielding bars are located below the edge of a side of the MEMS switch close to the QLED device.

[0058] Step 803, forming, on the substrate on which the above step is performed, a Micro-Electro-Mechanical System (MEMS) switch comprising a connecting portion and a switch portion, enabling the connecting portion of the MEMS switch to be electrically connected with a drain electrode of the TFT, and enabling the switch portion of the MEMS to be formed above the QLED.

[0059] The beneficial effect of the embodiment of the present invention is as follows: a MEMS switch is added in the pixel structure, the MEMS switch is electrically connected with the first conductive structure, the first conductive structure is electrically connected with the drain electrode of the TFT through the first via hole, thereby enabling the MEMS switch to be electrically connected with the drain electrode of the TFT; when the display device works, the QLED device emits light constantly, the TFT controls the light emission amount of the outgoing light of the QLED device by controlling the switching amplitude of the MEMS switch, so as to avoid frequent flicker of the QLED device and increase the life time of the QLED device; The display of the QLED display device is controlled through switching of the MEMS switch,

hence, the current compensation is not needed to adjust the brightness of the QLED display device, the complex current compensation circuit can be omitted, so that the preparation difficulty of the QLED device is reduced.

Embodiment V

[0060] Referring to Fig. 9, the embodiment of the present invention provides a pixel display method, comprising:

Step 901, a quantum dot light emitting diode (QLED) device emitting light constantly based on driving of a constant light emitting driving signal.

Step 902, a thin film transistor (TFT) controlling a working state of a Micro-Electro-Mechanical System (MEMS) switch, to enable the MEMS switch to control transmission amount of outgoing light of the QLED device, so as to realize pixel display.

[0061] Optionally, in step 902, the TFT controlling the working state of the MEMS switch to enable the MEMS switch to control transmission amount of outgoing light of the QLED device comprises:

transmitting an image data signal received by a source electrode of the TFT to the connecting portion of the MEMS switch via the drain electrode of the TFT and the first conductive structure successively, to enable the switch portion of the MEMS switch to open a switching amplitude matching with the image data signal, so as to control light amount of outgoing light of the QLED device.

[0062] The beneficial effect of the embodiment of the present invention is as follows: a MEMS switch is added in the pixel structure, the MEMS switch is electrically connected with the first conductive structure; when the display device works, the QLED device emits light constantly, the TFT controls the working state of the MEMS switch, to control transmission amount of the outgoing light of the QLED device, so as to avoid frequent flicker of the QLED device and increase the life time of the QLED device.

[0063] Apparently, the skilled person in the art can make various amendments and variations to the embodiments of the present application without departing from the spirit and scope of the present application. In this way, provided that these modifications and variations of the present application belong to the scopes of the claims of the present application and the equivalent technologies thereof, the present application also intends to cover these modifications and variations.

Claims

1. A pixel structure, **characterized in that**, comprising:

a thin film transistor TFT for controlling a Micro-Electro-Mechanical System MEMS switch; the Micro-Electro-Mechanical System MEMS switch being used for controlling transmission amount of outgoing light of a quantum dot light emitting diode QLED device; the quantum dot light emitting diode QLED device being a top emission type for emitting light constantly based on a constant light emitting driving signal.

2. The pixel structure as claimed in claim 1, **characterized in that** a drain electrode of the TFT is electrically connected with the MEMS switch, the TFT, after being turned on, controls the MEMS switch based on a signal received by a source electrode of itself.

3. The pixel structure as claimed in claim 2, **characterized in that** the MEMS switch comprises a connecting portion and a switch portion, the connecting portion being electrically connected with the drain electrode of the TFT, the switch portion being arranged at a side in a light emission direction of the QLED device to control light amount of the outgoing light of the QLED device.

4. The pixel structure as claimed in claim 3, **characterized in that** the pixel structure further comprises light shielding bars, the light shielding bars being located below an edge of a side of the MEMS switch close to the QLED device.

5. The pixel structure as claimed in claim 4, **characterized in that** the pixel structure further comprises a first conductive structure through which the MEMS switch is electrically connected with the drain electrode of the TFT.

6. The pixel structure as claimed in claim 5, **characterized in that** an insulating layer is arranged between the drain electrode of the TFT and the connecting portion of the MEMS switch, the first conductive structure is electrically connected with the drain electrode of the TFT through a first via hole on the insulating layer.

7. The pixel structure as claimed in claim 6, **characterized in that** the light shielding bars and the first conductive structure are arranged in the same layer and are insulated from each other, materials of the first conductive structure and the light shielding bars are metal materials.

8. An array substrate comprising a plurality of pixel units arranged in an array, **characterized in that** each of the pixel units adopts a pixel structure as claimed in any one of claims 1 to 7.

9. A method for preparing a pixel structure, **characterized in that** comprising:

forming, on a substrate, a thin film transistor TFT and a top emission type quantum dot light emitting diode QLED device adjacent to the TFT;
forming, on the substrate on which the above step is performed, a Micro-Electro-Mechanical System MEMS switch comprising a connecting portion and a switch portion, enabling the connecting portion of the MEMS switch to be electrically connected with a drain electrode of the TFT, and enabling the switch portion of the MEMS to be formed above the QLED.

10. The method as claimed in claim 9, **characterized in that** after forming the TFT on the substrate, further comprising:

forming an insulating layer comprising a first via hole over the TFT, and forming, on the substrate, a first region spaced apart from the TFT; wherein position of the first via hole corresponds to position of the drain electrode of the TFT.

11. The method as claimed in claim 10, **characterized in that**, further comprising forming a first conductive structure electrically connected with the drain electrode of the TFT over the insulating layer; enabling the connecting portion of the MEMS switch to be electrically connected with the drain electrode of the TFT is specifically: enabling the connecting portion of the MEMS switch to be electrically connected with the drain electrode of the TFT through the first conductive structure.

12. The method as claimed in claim 11, **characterized in that**, before forming a MEMS switch comprising a connecting portion and a switch portion, further comprising:

forming light shielding bars above the QLED, the light shielding bars being located below an edge of a side of the MEMS switch close to the QLED device.

13. A pixel display method, **characterized in that**, comprising:

a quantum dot light emitting diode QLED device emitting light constantly based on driving of a constant light emitting driving signal;
a thin film transistor TFT controlling a working state of a Micro-Electro-Mechanical System MEMS switch, to enable the MEMS switch to control transmission amount of outgoing light of the QLED device, so as to realize pixel display.

14. The method as claimed in claim 13, **characterized in that** the TFT controlling the working state of the MEMS switch to enable the MEMS switch to control transmission amount of outgoing light of the QLED device comprises:

transmitting an image data signal received by a source electrode of the TFT to the connecting portion of the MEMS switch via the drain electrode of the TFT and the first conductive structure successively, to enable the switch portion of the MEMS switch to open a switching amplitude matching with the image data signal, so as to control light amount of outgoing light of the QLED device.

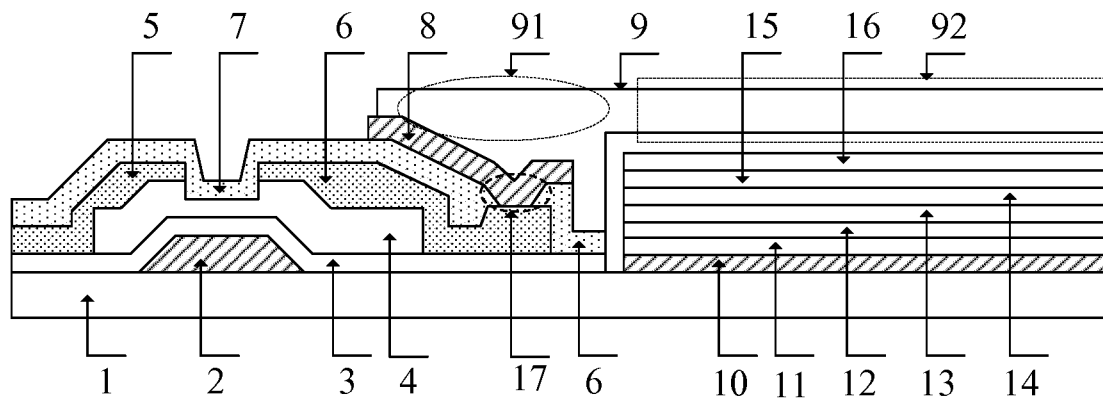


Fig. 1

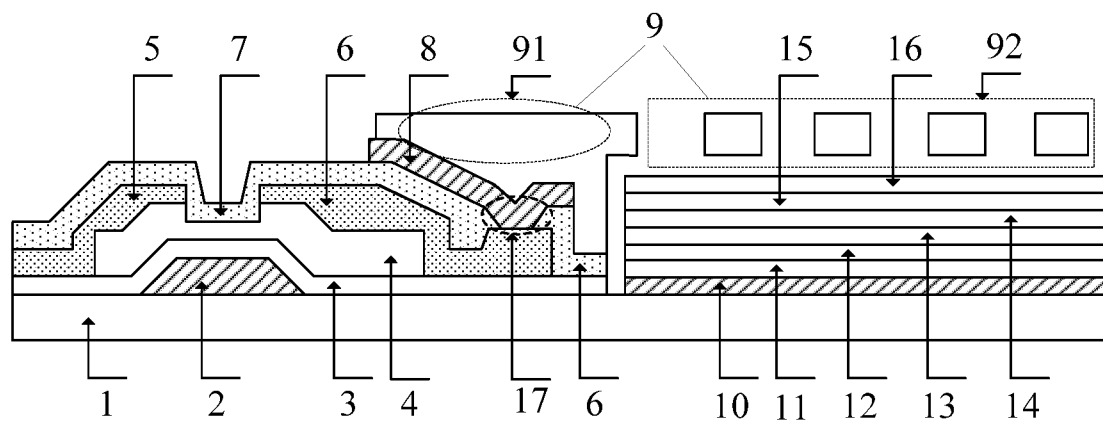


Fig. 2

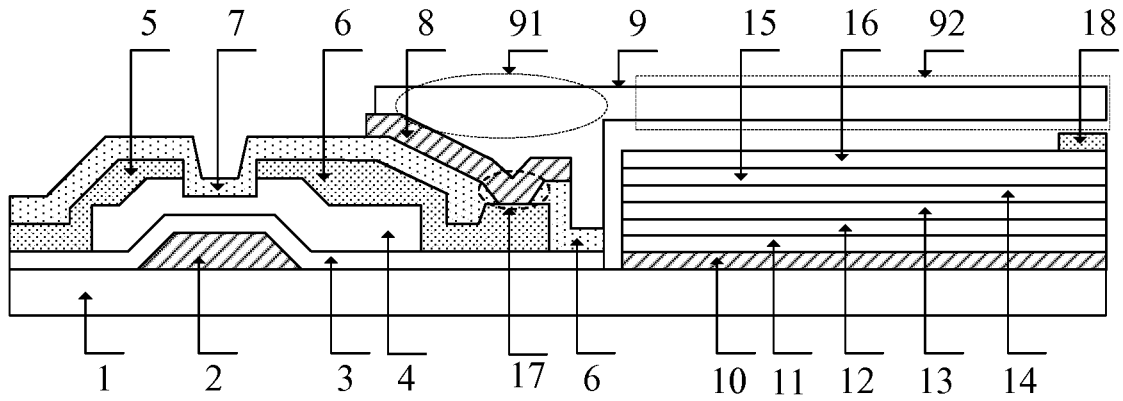


Fig. 3

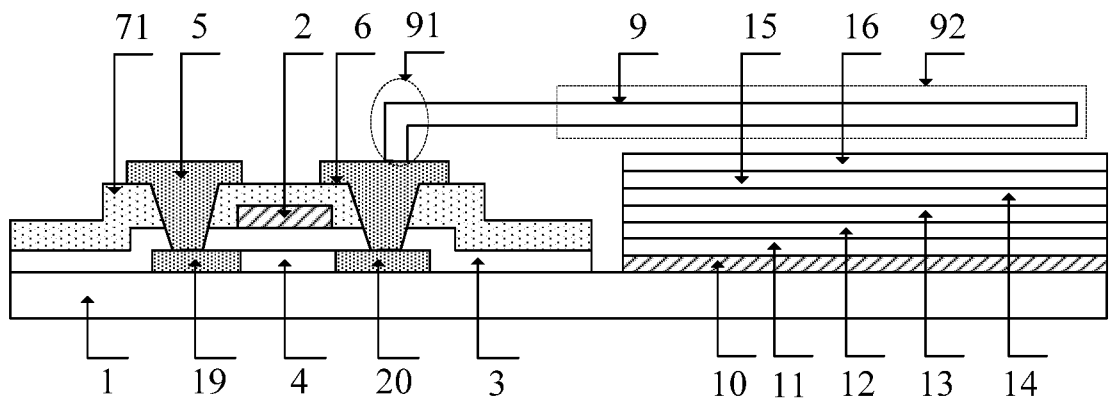


Fig. 4

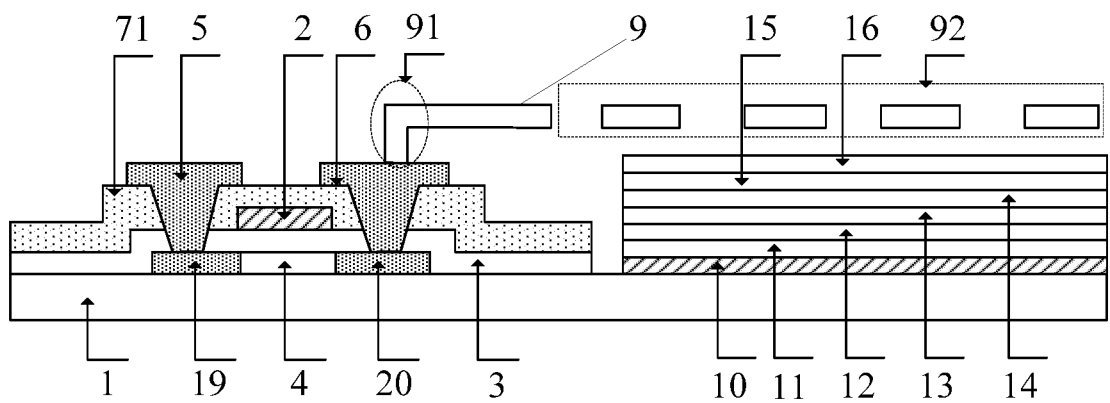


Fig. 5

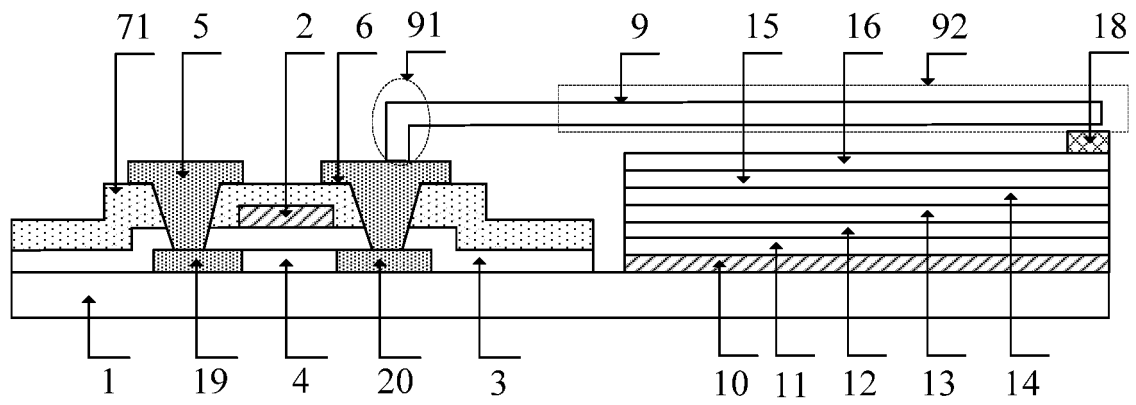


Fig. 6

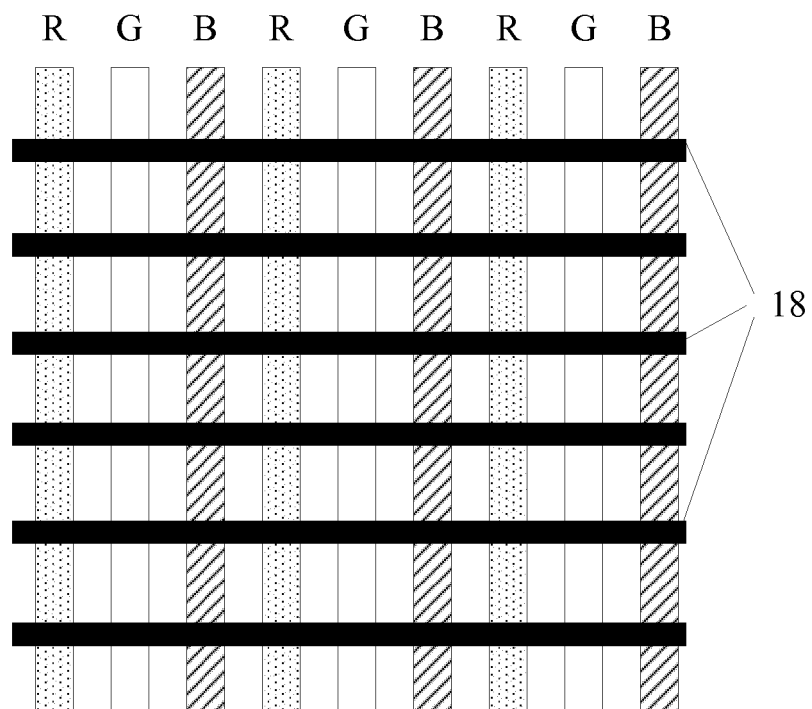


Fig. 7

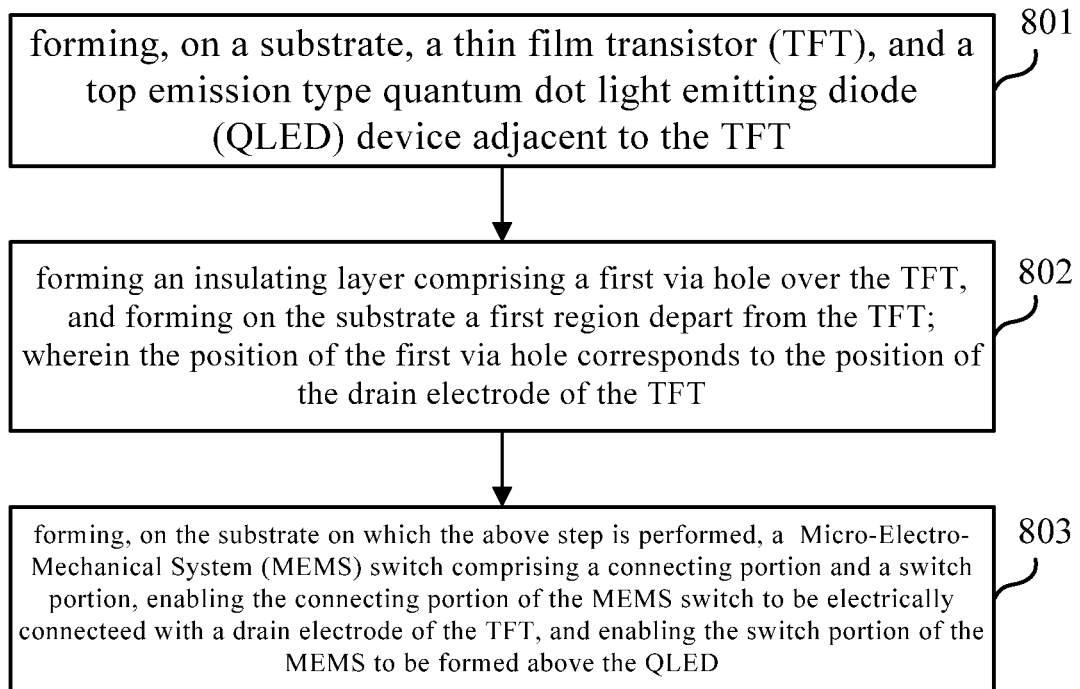


Fig. 8

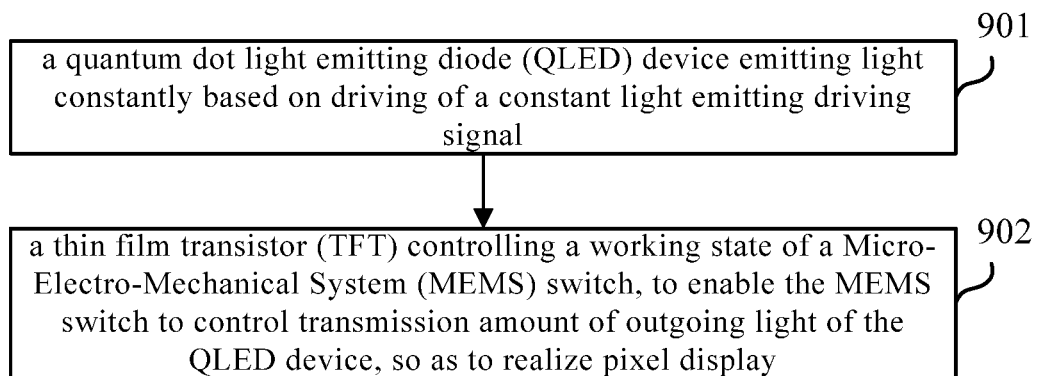


Fig. 9

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2015/076559

A. CLASSIFICATION OF SUBJECT MATTER

G09G 3/32 (2006.01) i; H01L 27/32 (2006.01) i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

G09G, H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

CNPAT, CNKI, EPODOC, WPI: MEMS, QLED, TFT, pixel?, array+, microelectromechanical w system, micro w electro w mechanical w system?, quantum w dots, light w emitting w diode?, thin w film w transistor

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
PX	CN 104464632 A (BOE TECHNOLOGY GROUP CO., LTD.), 25 March 2015 (25.03.2015), description, paragraphs [0006]-[0033], and claims 1-14	1-14
E	CN 204288767 U (BOE TECHNOLOGY GROUP CO., LTD.), 22 April 2015 (22.04.2015), claims 1-8	1-8
A	US 2011157676 A1 (SAMSUNG ELECTRONICS CO., LTD.), 30 June 2011 (30.06.2011), description, paragraphs [0023]-[0037], and figures 1-2	1-14
A	CN 101749602 A (AU OPTRONICS CORP.), 23 June 2010 (23.06.2010), the whole document	1-14
A	CN 102280546 A (LG DISPLAY CO., LTD. et al.), 14 December 2011 (14.12.2011), the whole document	1-14
A	CN 202855736 U (BOE TECHNOLOGY GROUP CO., LTD.), 03 April 2013 (03.04.2013), the whole document	1-14

☐ Further documents are listed in the continuation of Box C.
 ☒ See patent family annex.

* Special categories of cited documents:	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"A" document defining the general state of the art which is not considered to be of particular relevance	"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
"E" earlier application or patent but published on or after the international filing date	"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"&" document member of the same patent family
"O" document referring to an oral disclosure, use, exhibition or other means	
"P" document published prior to the international filing date but later than the priority date claimed	

Date of the actual completion of the international search 26 August 2015 (26.08.2015)	Date of mailing of the international search report 09 September 2015 (09.09.2015)
Name and mailing address of the ISA/CN: State Intellectual Property Office of the P. R. China No. 6, Xitucheng Road, Jimenqiao Haidian District, Beijing 100088, China Facsimile No.: (86-10) 62019451	Authorized officer QUAN, Yujun Telephone No.: (86-10) 61648437

INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.

PCT/CN2015/076559

Patent Documents referred in the Report	Publication Date	Patent Family	Publication Date
CN 104464632 A	25 March 2015	None	
CN 204288767 U	22 April 2015	None	
US 2011157676 A1	30 June 2011	KR 20110077912 A	07 July 2011
		US 8896902 B2	25 November 2014
CN 101749602 A	23 June 2010	None	
CN 102280546 A	14 December 2011	US 2011291071 A1	01 December 2011
		KR 20110129121 A	01 December 2011
		KR 101274068 B1	12 June 2013
		US 8552416 B2	08 October 2013
CN 202855736 U	03 April 2013	None	

Form PCT/ISA/210 (patent family annex) (July 2009)

专利名称(译)	像素结构，其制造方法，像素显示方法和阵列基板		
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申请号	EP2015795097	申请日	2015-04-14
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当前申请(专利权)人(译)	京东方科技集团股份有限公司.		
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发明人	ZHANG, BIN CAO, ZHANFENG KONG, XIANGCHUN YAO, QI GAO, JINCHENG LI, ZHENGLIANG HE, XIAOLONG		
IPC分类号	G09G3/32 H01L27/32		
CPC分类号	G09G3/32 G02B26/02 G09G3/3433 G09G2320/0646 H01L27/1214 H01L27/32		
优先权	201410818586.2 2014-12-24 CN		
其他公开文献	EP3239969A4		
外部链接	Espacenet		

摘要(译)

本发明公开了一种像素结构及其制备方法，像素显示方法和阵列基板，以解决现有量子点电致发光QLED显示装置的QLED显示装置寿命短的问题。制备QLED显示装置的难度很大。像素结构包括：薄膜晶体管TFT，用于控制微机电系统MEMS开关;微机电系统MEMS开关用于控制量子点发光二极管QLED器件的出射光透射量;量子点发光二极管QLED器件是用于基于恒定发光驱动信号恒定发光的顶部发光型。

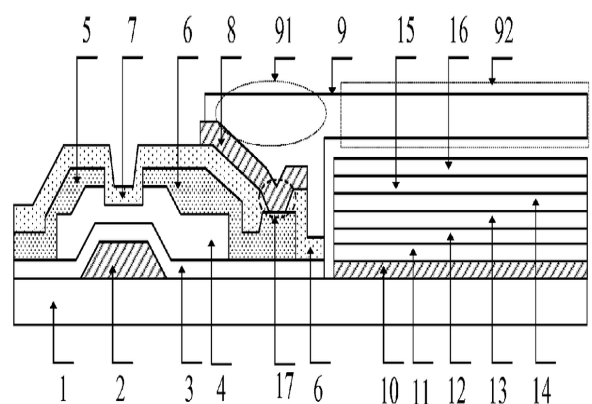


Fig. 1