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(54) **AMOLED PIXEL DRIVE CIRCUIT, METHOD AND DISPLAY DEVICE**

(57) The present disclosure provides an AMOLED pixel driving circuit, method and a display device. The AMOLED pixel driving circuit is for driving an organic light-emitting diode (OLED) and includes: a charge storage unit configured to be charged in a data writing stage and be discharged in a pixel lighting stage to light up the OLED; a data writing unit configured to write a data current in the data writing stage; a light-emitting control unit configured to enable a connection between the charge storage unit and the OLED in the pixel lighting stage. The AMOLED pixel driving circuit further includes a current amplification unit configured to, in the data writing stage, amplify the data current and charge the charge storage unit with the amplified data current.

rent in the data writing stage; a light-emitting control unit configured to control to enable a connection between the charge storage unit and the OLED in the pixel lighting stage. The AMOLED pixel driving circuit further includes a current amplification unit configured to, in the data writing stage, amplify the data current and charge the charge storage unit with the amplified data current.

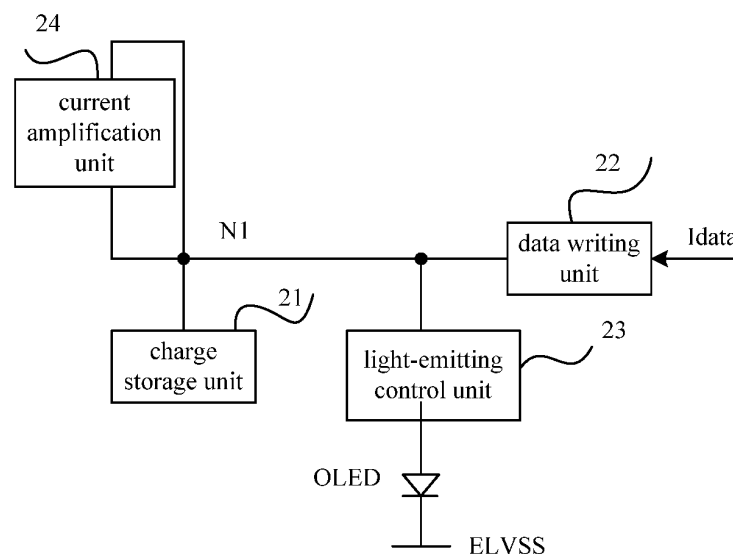


Fig. 2

Description

CROSS-REFERENCE TO RELATED APPLICATION

[0001] The present application claims a priority of the Chinese patent application No. 201410158960.0 filed on April 18, 2014, which is incorporated herein by reference in its entirety.

TECHNICAL FIELD

[0002] The present disclosure relates to the field of display technology, in particular to an active matrix/organic light Emitting diode (AMOLED) pixel driving circuit, method and a display device.

BACKGROUND

[0003] A current-mode AMOLED pixel driving circuit directly uses a current signal to drive a pixel circuit. Brightness of an OLED is directly proportional to a value of the driving current, and a multi-level gray scale display can be realized.

[0004] As shown in Fig. 1, an existing current-mode AMOLED pixel driving circuit includes a driving transistor TP1, a data writing transistor TP2 and a storage unit; the TP1 and TP2 are PMOS transistors. The storage unit includes a storage capacitor C and a resistor R which are connected in parallel with each other. In a data writing stage, a data-writing control signal Gate received in a gate electrode of the TP2 is of low level, the TP2 turns on, Idata passes through the TP2 and charges the storage capacitor C. In a pixel lighting stage, a light-emitting control signal EM received in a gate electrode of the TP1 is of low level, the TP1 turns on, the storage capacitor C is discharged to light up the OLED. A cathode of the OLED is coupled with a low-level output terminal ELVSS of a driving power supply. The existing current-mode AMOLED pixel driving circuit requires a driving current of a certain value to drive the OLED, and since a charging amount of the storage capacitor in the data writing stage is fixed and the charging amount cannot be adjusted in this stage, thus the gray scale level cannot be adjusted.

SUMMARY

[0005] A main object of the present disclosure is to provide an AMOLED pixel driving circuit, method and a display device, which can drive an OLED with a very small data current and can adjust a gray scale level by adjusting a value of a current flowing through the OLED in a pixel lighting stage.

[0006] In order to achieve the above object, the present disclosure provides a pixel driving circuit for driving an organic light emitting diode (OLED), including: a charge storage unit configured to be charged in a data writing stage and be discharged in a pixel lighting stage to light up the OLED; a data writing unit configured to write a

data current in the data writing stage; a light-emitting control unit configured to control to enable a connection between the charge storage unit and the OLED in the pixel lighting stage; and a current amplification unit configured to, in the data writing stage, amplify the data current and charge the charge storage unit with the amplified data current.

[0007] During implementation, the current amplification unit amplifies the data current during an entire period of time of the data writing stage and charge the charge storage unit with the amplified data current.

[0008] During implementation, the current amplification unit charges the charge storage unit with the amplified data current during a part of the period of time of the data writing stage according to requirements of gray scale level.

[0009] During implementation, the data writing stage includes a current amplification stage and a direct charging stage; in the current amplification stage, the current amplification unit amplifies the data current and the data writing unit uses the amplified data current to charge the charge storage unit; in the direct charging stage, the data writing unit is further configured to directly charge the charge storage unit with the data current.

[0010] During implementation, a connection point between the data writing unit and the charge storage unit is a writing node;

the current amplification unit includes a current amplification control module and a proportional current mirror; the current amplification control module is configured to enable a connection between the writing node and a current input terminal of the proportional current mirror during an entire or a part of the period of time of the data writing stage;

a current output terminal of the proportional current mirror is coupled with the writing node; and

the proportional current mirror is configured to amplify the data current.

[0011] During implementation, the data writing unit includes a data writing transistor, a gate electrode of the data writing transistor receives a data-writing control signal, a first electrode of the data writing transistor receives the data current and a second electrode of the data writing transistor is coupled with the writing node.

[0012] The current amplification control module includes:

an amplification control transistor, a base electrode of the amplification control transistor is coupled with the writing node and a first electrode of the amplification control transistor receives a current amplification control signal;

a writing control transistor, a gate electrode of the writing control transistor receives the data-writing control signal, a first electrode of the writing control transistor is coupled with the current input terminal of the proportional current mirror, and a second electrode of the writing control transistor is coupled with

a second electrode of the amplification control transistor.

[0013] During implementation, a timing sequence of the current amplification control signal is same as or different from a timing sequence of the data-writing control signal.

[0014] During implementation, the data writing transistor and the writing control transistor are PMOS transistors.

[0015] During implementation, one or both of the data writing transistor and the writing control transistor are NMOS transistors.

[0016] During implementation, the proportional current mirror includes an input branch and an output branch; the input branch includes:

a first PMOS transistor, a first electrode of the first PMOS transistor is coupled with a high-level output terminal of a driving power supply; and

a first NMOS transistor, a gate electrode of the first NMOS transistor is coupled with a first electrode of the first NMOS transistor, the first electrode of the first NMOS transistor is coupled with a low-level output terminal of the driving power supply, and a second electrode of the first NMOS transistor is coupled with a second electrode of the first PMOS transistor; the second electrode of the first NMOS transistor is the current input terminal of the proportional current mirror;

the output branch includes:

a second PMOS transistor, a gate electrode of the second PMOS transistor is coupled with a gate electrode of the first PMOS transistor, a first electrode of the second PMOS transistor is coupled with the high-level output terminal of the driving power supply, and a second electrode of the second PMOS transistor is coupled with the gate electrode of the second PMOS transistor; and

a second NMOS transistor, a gate electrode of the second NMOS transistor is coupled with the gate electrode of the first NMOS transistor, a first electrode of the second NMOS transistor is the current output terminal, and a second electrode of the second NMOS transistor is coupled with the second electrode of the second PMOS transistor.

[0017] During implementation, a width to length ratio of the second PMOS transistor is equal to a width to length ratio of the second NMOS transistor; a width to length ratio of the first PMOS transistor is equal to a width to length ratio of the first NMOS transistor; the width to length ratio of the second PMOS transistor is K times of the width to length ratio of the first PMOS transistor, K is greater than 1.

[0018] During implementation, the light-emitting control unit includes a driving transistor; the driving transistor has a gate electrode for receiving a light-emitting control signal, a first electrode coupled with the writing node and a second electrode coupled with an anode of the OLED; a cathode of the OLED is coupled with the low-level output terminal of the driving power supply.

[0019] During implementation, the charge storage unit includes a storage capacitor and a resistor which are connected in parallel with each other; the storage capacitor has one terminal coupled with the writing node and the other terminal coupled with the low-level output terminal of the driving power supply.

[0020] The present disclosure further provides a pixel driving method including:

in a data writing stage, writing by a data writing unit, a data current;

in the data writing stage, amplifying by a current amplification unit, the data current, and charging a charge storage unit with the amplified data current; in a pixel lighting stage, controlling by a light-emitting control unit, to enable a connection between the charge storage unit and an OLED, and discharging the charge storage unit to light up the OLED.

[0021] During implementation, the data writing stage includes a current amplification stage and a direct charging stage;

in the current amplification stage, the current amplification unit amplifies the data current and the data writing unit uses the amplified data current to charge the charge storage unit;

in the direct charging stage, the data writing unit directly charges the charge storage unit with the data current.

[0022] The present disclosure further provides a display device including an OLED and the above pixel driving circuit; the pixel driving circuit is configured to drive the OLED.

[0023] As compared with the related art, the pixel driving circuit, method and the display device of the present disclosure use the current amplification unit in the data writing stage to amplify the data current written by the data writing unit, charges the charge storage unit with the amplified data current, and discharges the charge storage unit in the pixel lighting stage to light up the OLED, so that the OLED may be driven by a very small data current; and a value of a current flowing through the OLED in the pixel lighting stage may be adjusted by controlling a period of time during which the current amplification unit amplifies the data current, so that a gray scale level may be adjusted.

BRIEF DESCRIPTION OF THE DRAWINGS

[0024]

Fig. 1 is a circuit diagram of an existing current-mode

AMOLED pixel driving circuit;

Fig. 2 is a block diagram of an AMOLED pixel driving circuit according to one embodiment of the present disclosure;

Fig. 3 is a block diagram of an AMOLED pixel driving circuit according to another embodiment of the present disclosure;

Fig. 4 is a block diagram of an AMOLED pixel driving circuit according to still another embodiment of the present disclosure;

Fig. 5 is a circuit diagram of an AMOLED pixel driving circuit according to yet another embodiment of the present disclosure;

Fig. 6 is a diagram showing a timing sequence of working signals of the AMOLED pixel driving circuit according to the embodiment shown in Fig. 5.

DETAILED DESCRIPTION

[0025] The technical solutions of embodiments of the present disclosure will be described hereinafter in a clear and complete manner in conjunction with drawings of the embodiments of the present disclosure. Obviously, the described embodiments are merely some rather than all of, the embodiments of the present disclosure. Based on these embodiments of the present disclosure, a person skilled in the art may obtain other embodiments without creative work, which also fall within the scope of the present disclosure.

[0026] Transistors adopted in all embodiments of the present disclosure may be thin film transistors, field effect transistors, or other devices having same characteristics. In embodiments of the present disclosure, in order to distinguish two electrodes of a transistor in addition to a gate electrode, one electrode of the two is referred to as "source electrode" and the other electrode is referred to as "drain electrode".

[0027] As shown in Fig. 2, an AMOLED pixel driving circuit according to one embodiment of the present disclosure is configured to drive an organic light-emitting diode (OLED) and includes:

a charge storage unit 21 configured to be charged in a data writing stage and discharged in a pixel lighting stage to light up the OLED;
a data writing unit 22 configured to write a data current I_{data} in the data writing stage;
a light-emitting control unit 23 configured to control to enable a connection between the charge storage unit 21 and the OLED in the pixel lighting stage; and
a current amplification unit 24 configured to, in the data writing stage, amplify the data current I_{data} and charge the charge storage unit 21 with the amplified data current I_{data} .

[0028] The AMOLED pixel driving circuit according to one embodiment of the present disclosure is a current-mode AMOLED pixel driving circuit, uses the current am-

plification unit to amplify the data current written by the data writing unit in the data writing stage, charges the charge storage unit with the amplified data current, and discharges the charge storage unit in the pixel lighting stage to light up the OLED, so that the OLED may be driven by a very small data current. A value of a current flowing through the OLED in the pixel lighting stage may be adjusted by controlling a period of time during which the current amplification unit amplifies the data current, so that a gray scale level may be adjusted.

[0029] In the above embodiment, the current amplification unit may amplify the data current during an entire period of time of the data writing stage and charge the charge storage unit with the amplified data current. According to requirements of gray scale level, the current amplification unit may charge the charge storage unit with the amplified data current during a part of the period of time of the data writing stage.

[0030] Specifically, when the current amplification unit amplifies the data current during a part of the period of time of the data writing stage, the data writing stage may be divided into a current amplification stage and a direct charging stage.

[0031] The current amplification unit amplifies the data current in the current amplification stage and the data writing unit uses the amplified data current to charge the charge storage unit. The data writing unit is further configured to directly charge the charge storage unit with the data current in the direct charging stage.

[0032] Specifically, as shown in Fig. 3, a connection point between the data writing unit 22 and the charge storage unit 21 is a writing node N1.

[0033] The current amplification unit includes a current amplification control module 241 and a proportional current mirror 242.

[0034] The current amplification control module 241 is configured to enable a connection between the writing node N1 and a current input terminal IN of the proportional current mirror 242 during the entire or part of the period of time of the data writing stage.

[0035] A current output terminal of the proportional current mirror 242 is coupled with the writing node N1.

[0036] The proportional current mirror 242 is configured to amplify the data current I_{data} .

[0037] In the embodiment shown in Fig. 3, the proportional current mirror is adopted to amplify the data current I_{data} . Since the proportional current mirror may amplify current and is not affected by process and temperature, thus stability of a display screen is further ensured.

[0038] Specifically, as shown in Fig. 4, the data writing unit includes a data writing transistor T1, a gate electrode thereof receives a data-writing control signal Gate, a first electrode thereof receives the data current I_{data} and a second electrode thereof is connected with the writing node N1.

[0039] The current amplification control module 241 includes:

an amplification control transistor TC, a base electrode of TC is coupled with the writing node N1 and a first electrode of TC receives a current amplification control signal Gate1;

a writing control transistor TIC, a gate electrode of TIC receives the data-writing control signal Gate, a first electrode of TIC is coupled with the current input terminal IN of the proportional current mirror 242, and a second electrode of TIC is coupled with a second electrode of the amplification control triode TC.

[0040] In the embodiment shown in Fig. 4, the TI and TIC are p-channel metal oxide semiconductor field effect transistors (PMOS transistors). In actual operation, one or both of the TI and TIC may employ an NMOS transistor, which accordingly requires a simple adjustment of pin connections and control signals.

[0041] In the embodiment shown in Fig. 4, a timing sequence of Gate1 may be same as a timing sequence of Gate; then, the current amplification control module enables the connection between the writing node N1 and the current input terminal IN of the proportional current mirror 242 during the entire period of time of the data writing stage.

[0042] The timing sequence of Gate1 may also be different from the timing sequence of Gate, that is, when Gate controls the TC and TIC to turn on, Gate1 controls the TC to turn on during a part of the period of time, and Gate1 controls the TC to turn off in the rest of the period of time, the current amplification control module enable the connection between the writing node N1 and the current input terminal IN of the proportional current mirror 242 during the part of the period of time of the data writing stage.

[0043] Specifically, as shown in Fig. 5, the proportional current mirror includes an input branch and an output branch.

[0044] The input branch includes:

a first PMOS transistor TP1, a first electrode of TP1 is coupled with a high-level output terminal ELVDD of the driving power supply; and

a first NMOS transistor TN1, a gate electrode of TN1 is coupled with a first electrode of the first NMOS transistor TN1, the first electrode of TN1 is coupled with the low-level output terminal ELVSS of the driving power supply, and a second electrode of TN1 is coupled with a second electrode of the first PMOS transistor TP1;

the second electrode of the first NMOS transistor TN1 is the current input terminal IN of the proportional current mirror.

[0045] The output branch includes:

a second PMOS transistor TP2, a gate electrode of TP2 is coupled with the gate electrode of the first PMOS transistor TP1, a first electrode of TP2 is coupled

with the high-level output terminal ELVDD of the driving power supply, and a second electrode of TP2 is coupled with the gate electrode of the second PMOS transistor TP2; and

a second NMOS transistor TN2, a gate electrode of TN2 is coupled with the gate electrode of the first NMOS transistor TN1, a first electrode of TN2 is the current output terminal of the proportional current mirror, and a second electrode of TN2 is coupled with the second electrode of the second PMOS transistor TP2.

[0046] The charge storage unit includes a storage capacitor C and a resistor R which are connected in parallel with each other.

[0047] One terminal of the storage capacitor C is coupled with the writing node N1, and the other terminal of the storage capacitor C is coupled with the low-level output terminal ELVSS of the driving power supply.

[0048] The light-emitting control unit includes a driving transistor DTFT, a gate electrode of DTFT receives the light-emitting control signal EM, a first electrode of DTFT is coupled with the writing node N1, and a second electrode of DTFT is coupled with an anode of the OLED.

[0049] The cathode of the OLED is coupled with the low-level output terminal ELVSS of the driving power supply.

[0050] In the embodiment shown in Fig. 5, the DTFT is a PMOS transistor; in actual operation, the DTFT may also employ an NMOS transistor. A point N2 is a node coupled with the gate electrode of the TP1, and a point N3 is a node coupled with the gate electrode of the TN1.

[0051] In the embodiment shown in Fig. 5, a width to length ratio of the second PMOS transistor TP2 is equal to a width to length ratio of the second NMOS transistor TN2; a width to length ratio of the first PMOS transistor TP1 is equal to a width to length ratio of the first NMOS transistor TN1.

[0052] The width to length ratio of the second PMOS transistor TP2 is K times of the width to length ratio of the first PMOS transistor TP1, K is greater than 1, then $I_{out} = K I_{ref}$, where I_{out} represents an output current of the proportional current mirror, I_{ref} represents an input current of the proportional current mirror.

[0053] As shown in Fig. 6, the timing sequence of the Gate is same as the timing sequence of the Gate1. That is, in the data writing stage, the current amplification unit amplifies the data current and uses the amplified data current to charge the charge storage unit. When the AMOLED pixel driving circuit shown in Fig. 5 works, in the data writing stage T1, Gate and Gate1 are low-level signals, EM is a high-level signal, TI, TIC and TC turn on, and DTFT turns off; I_{data} is first input in the base electrode of the TC through the TI; the TC performs a first amplification of the I_{data} , the input current I_{ref} of the proportional current mirror is $N \times I_{data}$, the output current I_{out} of the proportional current mirror is $K \times N \times I_{data}$, I_{out} is for charging the storage capacitor C, where N is an

amplification factor of the TC.

[0054] In the data writing stage T1, a suitable bias voltage is applied at the N2 point and the N3 point, so that all of TP1, TP2, TN1 and TN work in the saturation region, at this time, Iref and Iout are almost irrelevant to ELVDD, which may avoid flashing caused by instable current resulting from voltage fluctuation of the power supply, and ensure the stability of a display screen.

[0055] In the pixel lighting stage T2, Gate and Gate1 are high-level signals, EM is a low-level signal, TI, TIC and TC turn off, DTFT turns on, and the storage capacitor is discharged to light up the OLED.

[0056] In actual operation, a period of time during which the TC turns on may be controlled by adjusting the timing sequence of Gate1, and then Iout is controlled, and the gray scale level may be adjusted.

[0057] In the AMOLED pixel driving circuit of the embodiment shown in Fig. 5, the number of transistors is large, the AMOLED pixel driving circuit is more suitable for use in top-emitting; but since the output current of the proportional current mirror is only related to a width to length ratio of the MOS transistor, thus, the MOS transistor may be made in small size and will not occupy much space, and may be applied in the OLED-on-silicon (silicon-based organic light emitting diode) micro display technology based on monocrystalline silicon substrates.

[0058] The present disclosure further provides an AMOLED pixel driving method applied in the above AMOLED pixel driving circuit, includes:

in a data writing stage, writing, by a data writing unit, a data current;

in the data writing stage, amplifying, by a current amplification unit, the data current, and charging a charge storage unit with the amplified data current; in a pixel lighting stage, controlling, by an light-emitting control unit, to enable a connection between the charge storage unit and an OLED, and discharging the charge storage unit to light up the OLED.

[0059] Specifically, when the current amplification unit amplifies the data current during a part of the period of time of the data writing stage, the data writing stage may include a current amplification stage and a direct charging stage.

[0060] In the current amplification stage, the current amplification unit amplifies the data current and the data writing unit uses the amplified data current to charge the charge storage unit.

[0061] In the direct charging stage, the data writing unit directly charges the charge storage unit with the data current.

[0062] The present disclosure further provides a display device, which includes an OLED and the above AMOLED pixel driving circuit configured to drive the OLED.

[0063] The above are merely optional embodiments of the present disclosure. It should be appreciated that, a

person skilled in the art may make further improvements and modifications without departing from the principle of the present disclosure, and these improvements and modifications shall also fall within the scope of the present disclosure.

Claims

1. A pixel driving circuit for driving an organic light-emitting diode (OLED), comprising:
 - a charge storage unit configured to be charged in a data writing stage and be discharged in a pixel lighting stage to light up the OLED;
 - a data writing unit configured to write a data current in the data writing stage;
 - a light-emitting control unit configured to control to enable a connection between the charge storage unit and the OLED in the pixel lighting stage; and
 - a current amplification unit configured to, in the data writing stage, amplify the data current and charge the charge storage unit with the amplified data current.
2. The pixel driving circuit according to claim 1, wherein the current amplification unit amplifies the data current during an entire period of time of the data writing stage and charge the charge storage unit with the amplified data current.
3. The pixel driving circuit according to claim 1, wherein the current amplification unit charges the charge storage unit with the amplified data current during a part of the period of time of the data writing stage according to requirements of gray scale level.
4. The pixel driving circuit according to claim 3, wherein the data writing stage comprises a current amplification stage and a direct charging stage; in the current amplification stage, the current amplification unit amplifies the data current and the data writing unit uses the amplified data current to charge the charge storage unit; in the direct charging stage, the data writing unit is further configured to directly charge the charge storage unit with the data current.
5. The pixel driving circuit according to any one of claims 1 to 4, wherein a connection point between the data writing unit and the charge storage unit is a writing node; the current amplification unit comprises a current amplification control module and a proportional current mirror; the current amplification control module is configured to enable a connection between the writing node and a current input terminal of the proportional

current mirror during an entire or a part of the period of time of the data writing stage;
a current output terminal of the proportional current mirror is coupled with the writing node; and
the proportional current mirror is configured to amplify the data current.

6. The pixel driving circuit according to claim 5, wherein the data writing unit comprises a data writing transistor, a gate electrode of the data writing transistor receives a data-writing control signal, a first electrode of the data writing transistor receives the data current and a second electrode of the data writing transistor is coupled with the writing node.

7. The pixel driving circuit according to claim 6, wherein the current amplification control module comprises:

an amplification control transistor, a base electrode of the amplification control transistor is coupled with the writing node and a first electrode of the amplification control transistor receives a current amplification control signal;
a writing control transistor, a gate electrode of the writing control transistor receives the data-writing control signal, a first electrode of the writing control transistor is coupled with the current input terminal of the proportional current mirror, and a second electrode of the writing control transistor is coupled with a second electrode of the amplification control transistor.

8. The pixel driving circuit according to claim 7, wherein a timing sequence of the current amplification control signal is same as or different from a timing sequence of the data-writing control signal.

9. The pixel driving circuit according to claim 7, wherein the data writing transistor and the writing control transistor are PMOS transistors.

10. The pixel driving circuit according to claim 7, wherein one or both of the data writing transistor and the writing control transistor are NMOS transistors.

11. The pixel driving circuit according to any one of claims 5 to 10, wherein the proportional current mirror comprises an input branch and an output branch; the input branch comprises:

a first PMOS transistor, a first electrode of the first PMOS transistor is coupled with a high-level output terminal of a driving power supply; and
a first NMOS transistor, a gate electrode of the first NMOS transistor is coupled with a first electrode of the first NMOS transistor, the first electrode of the first NMOS transistor is coupled with a low-level output terminal of the driving power

supply, and a second electrode of the first NMOS transistor is coupled with a second electrode of the first PMOS transistor;
the second electrode of the first NMOS transistor is the current input terminal of the proportional current mirror;
the output branch comprises:

a second PMOS transistor, a gate electrode of the second PMOS transistor is coupled with a gate electrode of the first PMOS transistor, a first electrode of the second PMOS transistor is coupled with the high-level output terminal of the driving power supply, and a second electrode of the second PMOS transistor is coupled with the gate electrode of the second PMOS transistor; and
a second NMOS transistor, a gate electrode of the second NMOS transistor is coupled with the gate electrode of the first NMOS transistor, a first electrode of the second NMOS transistor is the current output terminal, and a second electrode of the second NMOS transistor is coupled with the second electrode of the second PMOS transistor.

12. The pixel driving circuit according to claim 11, wherein a width to length ratio of the second PMOS transistor is equal to a width to length ratio of the second NMOS transistor; a width to length ratio of the first PMOS transistor is equal to a width to length ratio of the first NMOS transistor;
the width to length ratio of the second PMOS transistor is K times of the width to length ratio of the first PMOS transistor, K is greater than 1.

13. The pixel driving circuit according to any one of claims 1 to 12, wherein the light-emitting control unit comprises a driving transistor;
the driving transistor has a gate electrode for receiving a light-emitting control signal, a first electrode coupled with the writing node and a second electrode coupled with an anode of the OLED;
a cathode of the OLED is coupled with the low-level output terminal of the driving power supply.

14. The pixel driving circuit according to any one of claims 1 to 13, wherein the charge storage unit comprises a storage capacitor and a resistor which are connected in parallel with each other;
the storage capacitor has one terminal coupled with the writing node and the other terminal coupled with the low-level output terminal of the driving power supply.

15. A pixel driving method comprising:

in a data writing stage, writing, by a data writing

unit, a data current;
in the data writing stage, amplifying, by a current
amplification unit, the data current, and charging
a charge storage unit with the amplified data cur- 5
rent;
in a pixel lighting stage, controlling, by a light-
emitting control unit, to enable a connection be-
tween the charge storage unit and an OLED,
and discharging the charge storage unit to light 10
up the OLED.

16. The pixel driving method according to claim 15,
wherein
the data writing stage comprises a current amplifi- 15
cation stage and a direct charging stage; in the cur-
rent amplification stage, the current amplification unit
amplifies the data current and the data writing unit
uses the amplified data current to charge the charge
storage unit;
in the direct charging stage, the data writing unit di- 20
rectly charges the charge storage unit with the data
current.
17. A display device comprising an OLED and the pixel
driving circuit according to any one of claims 1 to 14; 25
wherein the pixel driving circuit is configured to drive
the OLED.

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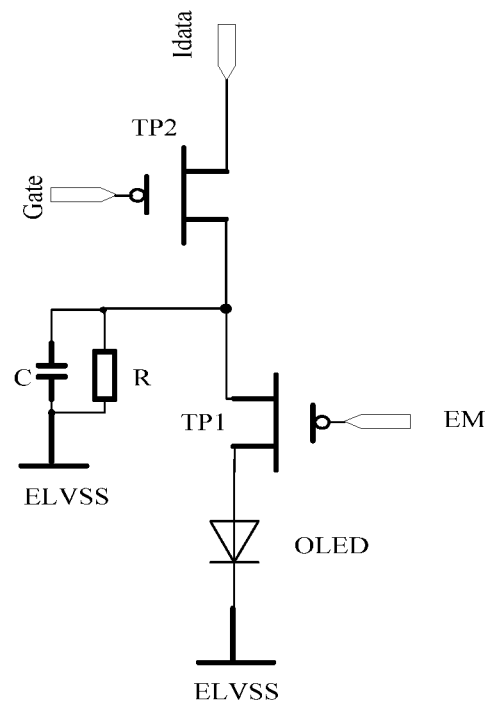


Fig. 1

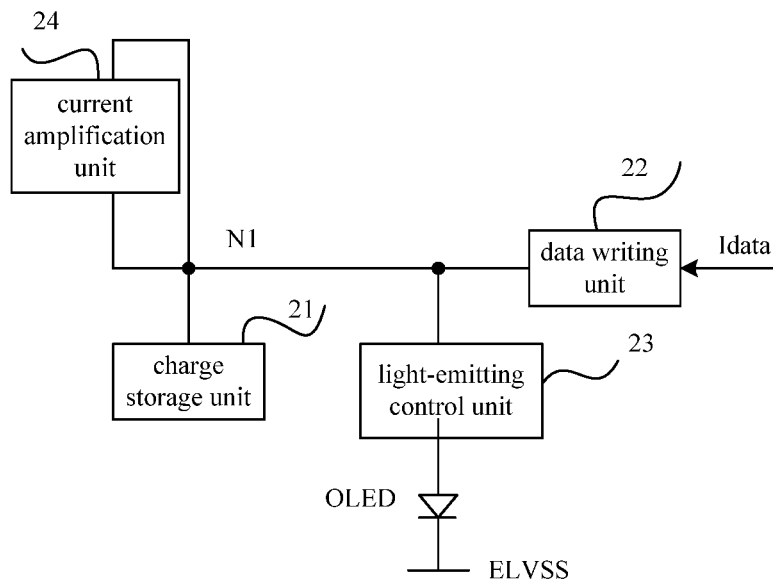


Fig. 2

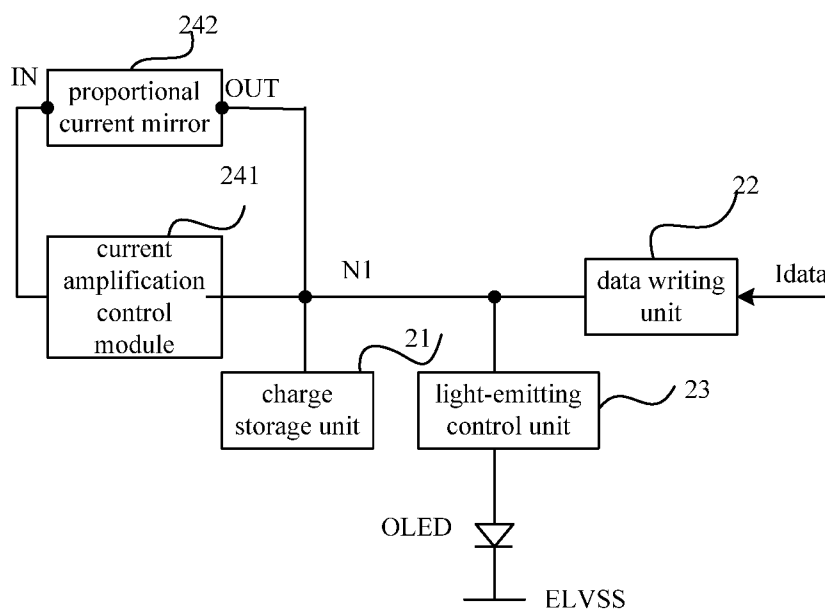


Fig. 3

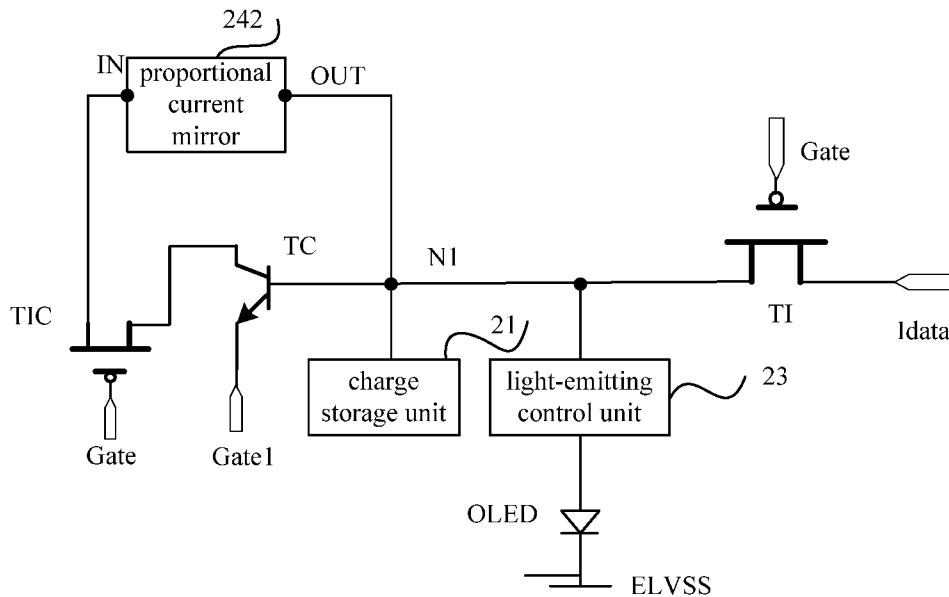


Fig. 4

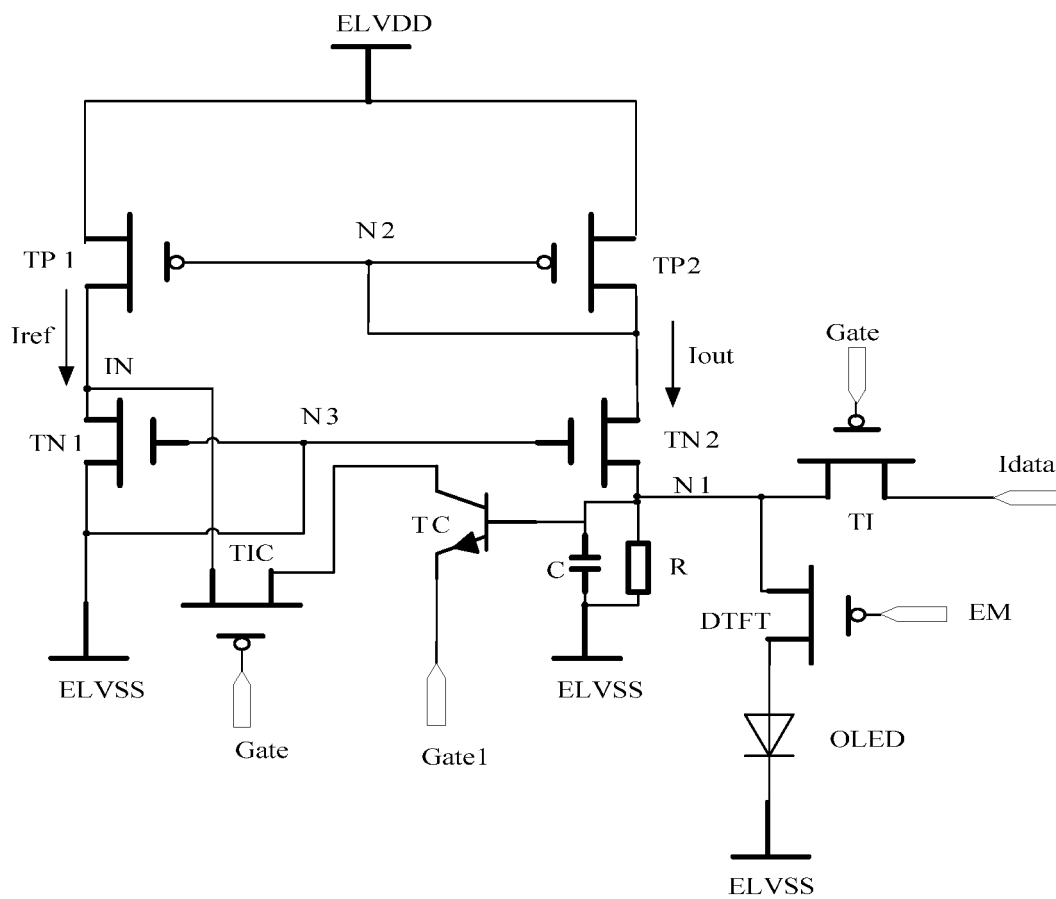


Fig. 5

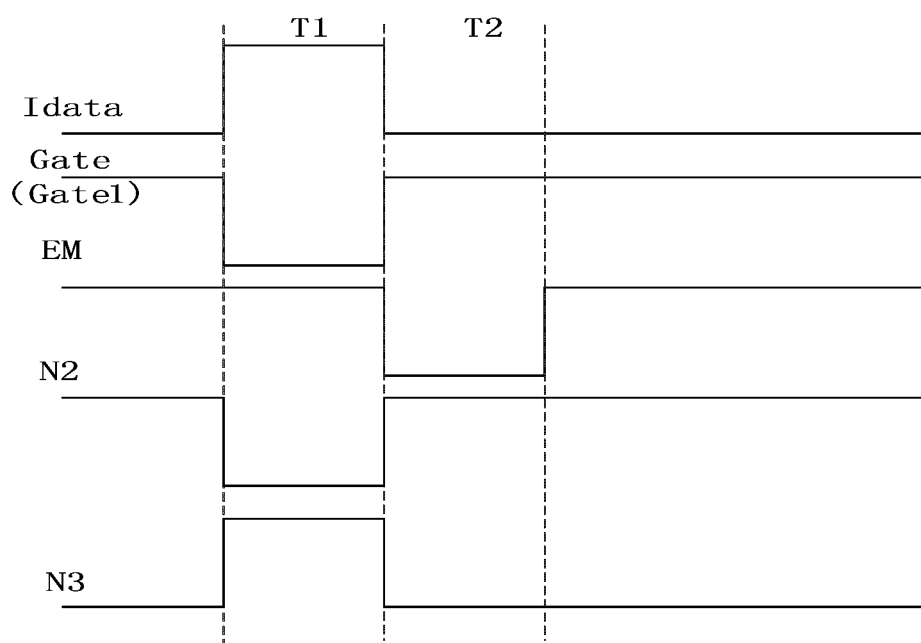


Fig. 6

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2014/085277

A. CLASSIFICATION OF SUBJECT MATTER

G09G 3/32 (2006.01) i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

G09G 3/-

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

CNPAT, CNKI, EPODOC, WPI: organic light emitting diode, pixel, driv+, OLED, storage, capacitance, current, amplificat+

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
PX	CN 103956138 A (BOE TECHNOLOGY GROUP CO., LTD.), 30 July 2014 (30.07.2014), claims 1-10, description, paragraphs 45-98, and figures 1-6	1-17
PX	CN 203773914 U (BOE TECHNOLOGY GROUP CO., LTD.), 13 August 2014 (13.08.2014), claims 1-7, description, paragraphs 41-87, and figures 1-6	1-17
X	CN 101097680 A (LG. PHILIPS LCD CO., LTD.), 02 January 2008 (02.01.2008), description, pages 5-14, and figures 1-11	1-4, 15-17
A	CN 201549184 U (SICHUAN CCO DISPLAY TECHNOLOGY CO., LTD.), 11 August 2010 (11.08.2010), the whole document	1-17
A	CN 203311819 U (BOE TECHNOLOGY GROUP CO., LTD.), 27 November 2013 (27.11.2013), the whole document	1-17
A	US 2005/0225251 A1 (TOPPOLY OPTOELECTRONICS CORP.), 13 October 2005 (13.10.2005), the whole document	1-17

☐ Further documents are listed in the continuation of Box C.☒ See patent family annex.

* Special categories of cited documents:

“A” document defining the general state of the art which is not considered to be of particular relevance

“E” earlier application or patent but published on or after the international filing date

“L” document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

“O” document referring to an oral disclosure, use, exhibition or other means

“P” document published prior to the international filing date but later than the priority date claimed

“T” later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

“X” document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

“Y” document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

“&” document member of the same patent family

Date of the actual completion of the international search
12 December 2014 (12.12.2014)Date of mailing of the international search report
05 January 2015 (05.01.2015)Name and mailing address of the ISA/CN:
State Intellectual Property Office of the P. R. China
No. 6, Xitucheng Road, Jimenqiao
Haidian District, Beijing 100088, China
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INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.

PCT/CN2014/085277

Patent Documents referred in the Report	Publication Date	Patent Family	Publication Date
CN 103956138 A	30 July 2014	None	
CN 203773914 U	13 August 2014	None	
CN 101097680 A	02 January 2008	US 7889160 B2	15 February 2011
		CN 100570689 C	16 December 2009
		JP 2008015451 A	24 January 2008
		US 2008001857 A1	03 January 2008
		KR 20080002011 A	04 January 2008
		JP 4477617 B2	09 June 2010
		KR 101202040 B1	16 November 2012
CN 201549184 U	11 August 2010	None	
CN 203311819 U	27 November 2013	None	
US 2005/0225251 A1	13 October 2005	None	

Form PCT/ISA/210 (patent family annex) (July 2009)

REFERENCES CITED IN THE DESCRIPTION

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Patent documents cited in the description

- CN 201410158960 [0001]

专利名称(译)	Amoled像素驱动电路，方法和显示设备		
公开(公告)号	EP3133586A4	公开(公告)日	2017-12-13
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[标]申请(专利权)人(译)	京东方科技集团股份有限公司		
申请(专利权)人(译)	京东方科技集团股份有限公司.		
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IPC分类号	G09G3/32 G09G3/3233		
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优先权	201410158960.0 2014-04-18 CN		
其他公开文献	EP3133586A1		
外部链接	Espacenet		

摘要(译)

本公开提供了一种AMOLED像素驱动电路，方法和显示装置。AMOLED像素驱动电路用于驱动有机发光二极管（OLED），并且包括：电荷存储单元，被配置为在数据写入级中充电并且在像素发光级中放电以点亮OLED；数据写入单元，被配置为在数据写入阶段写入数据电流；发光控制单元，被配置为控制以在像素发光级中实现电荷存储单元和OLED之间的连接。AMOLED像素驱动电路还包括电流放大单元，被配置为在数据写入阶段放大数据电流并用放大的数据电流对电荷存储单元充电。