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(54) **OLED PIXEL CIRCUIT, DISPLAY DEVICE AND CONTROL METHOD**

(57) The present disclosure provides an OLED pixel circuit, a display device and a control method. The OLED pixel circuit includes: an OLED; a driving transistor; a first switching unit configured to connect a data signal output end and a gate electrode of the driving transistor; a second switching unit configured to connect a power signal output end and a source electrode of the driving transistor; a compensation circuit connected to the gate electrode of the driving transistor and configured to maintain a voltage of the gate electrode of the driving transistor during a light-emitting period, so as to enable a current flowing through the OLED to be irrelevant to a threshold voltage V_{th} of the driving transistor; and a reference signal generation module configured to generate, based on a current threshold voltage of the driving transistor, a reference signal to be used by the compensation circuit, wherein a voltage of the reference signal and the threshold voltage meet at least one of validation conditions capable of validating the compensation circuit.

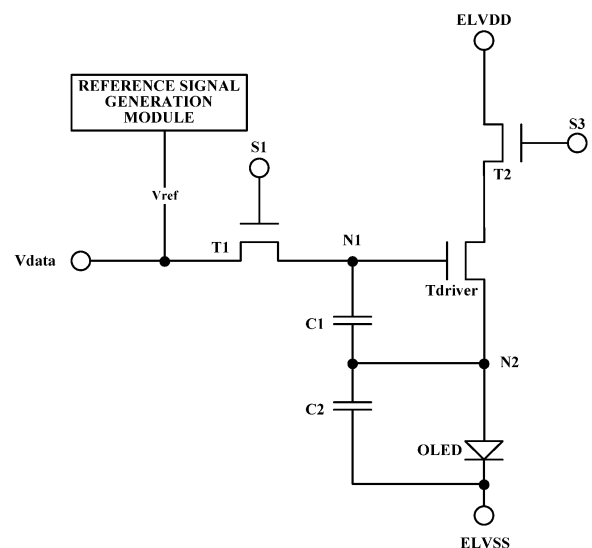


FIG. 2

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Description

CROSS REFERENCE OF RELATED APPLICATION

[0001] The present application claims a priority of Chinese patent application No. 201510232424.5 filed on May 08, 2015, the disclosure of which is incorporated herein by reference in its entirety.

TECHNICAL FIELD

[0002] The present disclosure relates to the field of display technology, in particular to an organic light-emitting diode (OLED) pixel circuit, a display device and a control method.

BACKGROUND

[0003] OLEDs have become important light-emitting elements in a new flat panel display device because they have advantages of self-luminescence, high contrast, wide color gamut, simple manufacturing process, low power consumption, enabling flexible display and etc.

[0004] In a pixel of an OLED display panel, each of sub-pixels includes a driving transistor. In an OLED pixel circuit, current flowing through the OLED is controlled by a data signal V_{data} and affected by a threshold voltage V_{th} of the driving transistor.

[0005] Due to a fact that features such as threshold voltages and mobilities of thin film transistors (TFTs) in respective pixel circuits are different, the driving transistors in respective OLED pixel circuits may have different performance parameters accordingly. As a result, the currents flowing through respective OLEDs may be affected by different shifts of the threshold voltages V_{th} of the driving transistors, and thus be different, so that brightness uniformity and brightness consistence of the OLED display device are adversely affected, thereby display quality of the OLED display device is degraded.

[0006] Therefore, a compensation circuit is provided for each pixel circuit. The compensation circuit is connected to a gate electrode of the driving transistor, and is configured to maintain a voltage of the gate electrode of the driving transistor during a light-emitting period. Therefore, it is able for a current flowing through the OLED to be irrelevant to a threshold voltage V_{th} of the driving transistor, and eliminate the effect on the brightness uniformity and the brightness consistence of the OLED display device by the shifts of the threshold voltages.

[0007] However, in the related art, all of the compensation circuits have limited compensation ranges. When the threshold voltage shifts beyond the compensation range, the compensation circuit is invalidated. In a product including a plurality of OLED pixels, the voltage of the driving transistor in each of the OLED pixels has a unique initial value and a unique variation. Thus, when identical compensation circuits are provided for the plu-

ality of OLED pixels respectively, it is possible that some of the compensation circuits are validated for their corresponding OLED pixels, while the other compensation circuits are invalidated for their corresponding OLED pixels. Consequently, the brightness uniformity and the brightness consistence of the OLED display device may be significantly affected.

SUMMARY

[0008] An object of the present disclosure is to provide an OLED pixel circuit, a display device and a method for controlling the OLED pixel unit, so as to enlarge the compensation range of the compensation circuit.

[0009] In one aspect, the present disclosure provides in some embodiments an OLED pixel circuit including:

an OLED;

a driving transistor, wherein a drain electrode of the driving transistor is connected to the OLED;

a first switching unit configured to connect a data signal output end and a gate electrode of the driving transistor;

a second switching unit configured to connect a power signal output end and a source electrode of the driving transistor; and

a compensation circuit connected to the gate electrode of the driving transistor and configured to maintain a voltage of the gate electrode of the driving transistor during a light-emitting period, so as to enable a current flowing through the OLED to be irrelevant to a threshold voltage V_{th} of the driving transistor,

the pixel circuit further includes:

a reference signal generation module configured to generate, based on a current threshold voltage of the driving transistor, a reference signal to be used by the compensation circuit, wherein a voltage of the reference signal and the threshold voltage meet at least one of validation conditions capable of validating the compensation circuit.

[0010] Alternatively, one column of OLED pixels share one reference signal generation module which includes:

a determination unit, configured to select a target driving circuit from driving circuits corresponding to the column of OLED pixels, wherein the reference signal is to be used by the target driving circuit; and a signal generation unit, configured to generate, based on a current threshold voltage of the driving transistor of the target driving circuit, a reference signal to be used by the compensation circuit of the target driving circuit, wherein a voltage of the reference signal generated by the signal generation unit and the current threshold voltage of the driving tran-

sistor of the target driving circuit meet at least one of validation conditions capable of validating the compensation circuit of the target driving circuit.

[0011] Alternatively, a first electrode of the OLED is connected to the driving transistor, a second electrode of the OLED is connected to the ground, and the compensation circuit includes:

a first capacitor, wherein an end of the first capacitor is connected to the gate electrode of the driving transistor, and the other end of the first capacitor is connected to the drain electrode of the driving transistor; and

a second capacitor, wherein an end of the second capacitor is connected to the drain electrode of the driving transistor, and the other end of the second capacitor is connected to the second electrode of the OLED,

during a reset period, the first switching unit is configured to be turned on and output the reference signal to the gate electrode of the driving transistor, and the second switching unit is configured to be turned on and output a first power signal to the source electrode of the driving transistor;

during a compensation period, the first switching unit is configured to be turned on and output the reference signal to the gate electrode of the driving transistor, and the second switching unit is configured to be turned on and output a second power signal to the source electrode of the driving transistor, wherein a voltage of the first power signal is lower than a voltage of the second power signal;

during a writing period, the first switching unit is configured to be turned on and output a data signal to the gate electrode of the driving transistor, and the second switching unit is configured to be turned off; and

during the light-emitting period, the first switching unit is configured to be turned off, and the second switching unit is configured to be turned on and output the second power signal to the source electrode of the driving transistor.

[0012] Alternatively, the conditions includes:

$$A-B+a(C-A)<D;$$

and/or

$$E<A-B,$$

where

A indicates a voltage value of the reference signal;

B indicates the threshold voltage of the driving transistor;

C indicates a voltage value of the data signal;

D indicates a threshold voltage of the OLED;

E indicates a voltage value of the first power signal; and

a = a capacitance value of the first capacitor/(the capacitance value of the first capacitor + a capacitance value of the second capacitor). In the above OLED pixel circuit,

the first switching unit is a thin film transistor (TFT), a source electrode of which is connected to a data line, a drain electrode of which is connected to the gate electrode of the driving transistor, a gate electrode of which is connected to an output end of a first control signal, and which is configured to be turned on when the first control signal is effective, wherein the first control signal is effective during the reset period, the compensation period and the writing period; and

the second switching unit is a TFT, a source electrode of which is connected to the power signal output end, a drain electrode of which is connected to the source electrode of the driving transistor, a gate electrode of which is connected to an output end of a second control signal, and which is configured to be turned on when the second control signal is effective, wherein the second control signal is effective during the reset period, the compensation period and the light-emitting period.

[0013] Alternatively, the signal generation unit is configured to:

during the reset period and the compensation period corresponding to the target driving circuit, generate, based on a current threshold voltage of the driving transistor of the target driving circuit, and output the reference signal to be used by the compensation circuit of the target driving circuit, wherein the voltage of the reference signal generated by the signal generation unit and the current threshold voltage of the driving transistor of the target driving circuit meet at least one of validation conditions capable of validating the compensation circuit of the target driving circuit.

[0014] Alternatively, the reference signal generation module further includes:

a third switching unit configured to connect the signal generation unit and the data line and output the reference signal generated by the signal generation unit to the data line during the reset period and the compensation period.

[0015] Alternatively, the third switching unit is a TFT, a source electrode of which is connected to the signal

generation unit, a drain electrode of which is connected to the data line, a gate electrode of which is connected to the third control signal output end, and which is configured to be turned on when the third control signal is effective. The third control signal is effective during the reset period and the compensation period.

[0016] Alternatively, the reference signal generation module further includes a third switching unit configured to connect the signal generation unit and the data line and output the reference signal generated by the signal generation unit to the data line during the reset period and the compensation period. Moreover, the pixel circuit further includes a fourth switching unit configured to connect a data driving chip and the data line and output the data signal generated by the data driving chip to the data line during the writing period.

[0017] Alternatively, the third switching unit is a TFT, a source electrode of which is connected to the signal generation unit, a drain electrode of which is connected to the data line, a gate electrode of which is connected to an output end of a third control signal, and which is configured to be turned on when the third control signal is effective. The third control signal is effective during the reset period and the compensation period. The fourth switching unit is a TFT, a source electrode of which is connected to the data driving chip, a drain electrode of which is connected to the data line, a gate electrode of which is connected to an output end of a fourth control signal, and which is configured to be turned on when the fourth control signal is effective. The fourth control signal is effective during the writing period.

[0018] In another aspect, the present disclosure provides in some embodiments a display device including the above OLED pixel circuit.

[0019] In yet another aspect, the present disclosure provides in some embodiments a method for controlling the OLED pixel circuit, wherein the OLED pixel circuit includes:

an OLED;
a driving transistor; and
a compensation circuit configured to maintain a voltage of a gate electrode of the driving transistor during a light-emitting period, so as to enable a current flowing through the OLED to be irrelevant to a threshold voltage V_{th} of the driving transistor, and
the method includes a reference signal generation step of:

generating, based on a current threshold voltage of the driving transistor, a reference signal to be used by the compensation circuit, wherein a voltage of the reference signal and the threshold voltage meet at least one of validation conditions capable of validating the compensation circuit.

[0020] Alternatively, one column of OLED pixels share

one reference signal generation module, and the reference signal generation step includes steps of:

selecting a target driving circuit from driving circuits corresponding to the column of OLED pixels, wherein the reference signal is to be used by the target driving circuit; and

generating, based on a current threshold voltage of the driving transistor of the target driving circuit, a reference signal to be used by the compensation circuit of the target driving circuit, wherein a voltage of the generated reference signal and the current threshold voltage of the driving transistor of the target driving circuit meet at least one of validation conditions capable of validating the compensation circuit of the target driving circuit.

[0021] Alternatively, a first electrode of the OLED is connected to the driving transistor, a second electrode of the OLED is connected to the ground, and the compensation circuit includes:

a first capacitor, wherein an end of the first capacitor is connected to the gate electrode of the driving transistor, and the other end of the first capacitor is connected to the drain electrode of the driving transistor; and

a second capacitor, wherein an end of the second capacitor is connected to the drain electrode of the driving transistor, and the other end of the second capacitor is connected to the second electrode of the OLED,
the method further includes steps of:

during a reset period, turning on the first switching unit and outputting the reference signal to the gate electrode of the driving transistor, and turning on the second switching unit and outputting a first power signal to the source electrode of the driving transistor;

during a compensation period, turning on the first switching unit and outputting the reference signal to the gate electrode of the driving transistor, and turning on the second switching unit and outputting a second power signal to the source electrode of the driving transistor, wherein a voltage of the first power signal is lower than a voltage of the second power signal;

during a writing period, turning on the first switching unit and outputting a data signal to the gate electrode of the driving transistor, and turning off the second switching unit; and
during the light-emitting period, turning off the first switching unit, and turning on the second switching unit and outputting the second power signal to the source electrode of the driving transistor.

[0022] Alternatively, the conditions includes:

$$A-B+a(C-A)<D;$$

and/or

$$E<A-B,$$

where

A indicates a voltage value of the reference signal;
B indicates the threshold voltage of the driving transistor;
C indicates a voltage value of the data signal;
D indicates a threshold voltage of the OLED;
E indicates a voltage value of the first power signal;
and
a= a capacitance value of the first capacitor/(the capacitance value of the first capacitor + a capacitance value of the second capacitor).

[0023] In the embodiments of the present disclosure, the reference signal is generated based on a current threshold voltage of the driving transistor, thereby when the threshold voltage changes, the reference voltage changes accordingly. In other words, the reference voltage may change according to the change of the threshold voltage, so as to enable the conditions of validating the compensation circuit to be met even when the threshold voltage changes, and enlarge the compensation range of the compensation circuit.

BRIEF DESCRIPTION OF THE DRAWINGS

[0024]

Fig.1 illustrates an OLED pixel circuit according to an embodiment of the present disclosure;
Fig.2 illustrates another OLED pixel circuit according to an embodiment of the present disclosure;
Fig.3 illustrates a signal timing sequence of the pixel circuit as shown in Fig.2;
Fig.4 illustrates a connection between a signal generation unit and a data line in the OLED pixel circuit as shown in Fig.2;
Fig.5 illustrates a connection between the signal generation unit as well as a data driving chip and the data line in the OLED pixel circuit as shown in Fig.2;
Fig.6 illustrates yet another OLED pixel circuit according to an embodiment of the present disclosure;
and
Fig.7 illustrates a signal timing sequence of the pixel circuit as shown in Fig.6.

DETAILED DESCRIPTION

[0025] In order to make the objects, the technical solutions and the advantages of the present disclosure more apparent, some technical solutions of the present disclosure will be described hereinafter in a clear and complete manner in conjunction with the drawings and embodiments. Obviously, the following embodiments are merely a part of, rather than all of, the embodiments of the present disclosure, and based on these embodiments, a person skilled in the art may obtain the other embodiments, which also fall within the scope of the present disclosure.

[0026] Unless otherwise defined, any technical or scientific term used herein shall have the common meaning understood by a person of ordinary skills. Such words as "first" and "second" used in the specification and claims are merely used to differentiate different components rather than to represent any order, number or importance. Similarly, such words as "one" or "a" are merely used to represent the existence of at least one member, rather than to limit the number thereof. Such words as "connect" or "connected to" may include electrical connection, direct or indirect, rather than to be limited to physical or mechanical connection. Such words as "on", "under", "left" and "right" are merely used to represent relative position relationship, and when an absolute position of the object is changed, the relative position relationship will be changed too.

[0027] According to the OLED pixel circuit, the display device and the method for controlling the OLED pixel circuit in the embodiments of the present disclosure, a unique reference signal is generated based on each particular driving transistor, and the reference signal is used by the compensation circuit to enable the compensation circuit to be validated, so as to enlarge the compensation range of the compensation circuit.

[0028] As mentioned above, in the related art, all of the compensation circuits have limited compensation ranges. When the threshold voltage shifts beyond the compensation range, the compensation circuit is invalidated. In a product including a plurality of OLED pixels, the voltage of the driving transistor in each of the OLED pixels has a unique initial value and a unique variation. Thus, when identical compensation circuits are provided for the plurality of OLED pixels respectively, it is possible that some of the compensation circuits are validated for their corresponding OLED pixels, while the other compensation circuits are invalidated for their corresponding OLED pixels. As a consequence, the brightness uniformity and the brightness consistence of the OLED display device may be significantly affected.

[0029] There are two conventional solutions for the above problem:

1. providing a compensation circuit with a larger compensation range; and

2. providing a driving transistor with more stable performance.

[0030] It can be seen from above that the conventional solutions are focusing on improving the quality of hardware elements to validate the compensation circuit, which is common knowledge for designing the OLED driving circuit.

[0031] During the process of implementing the present disclosure with efforts and creative works, the inventor of the present disclosure finds out that, although various compensation circuits have been designed for the threshold voltage V_{th} of the driving transistor, none of such compensation circuits can operate properly unless one or more limiting conditions are met. Furthermore, at least some of the limiting conditions are relevant to both the threshold voltage V_{th} and the reference signal V_{ref} , which limit the compensation range of the compensation circuit.

[0032] In other words, the conventional compensation circuit cannot operate properly unless the threshold voltage V_{th} and the reference voltage V_{ref} meet a particular requirement. However, in the conventional driving circuit, the reference voltage V_{ref} is constant, thereby the corresponding threshold voltage V_{th} is limited. Thus, it is possible that the above limiting conditions are no longer met when the threshold voltage V_{th} changes, so that the compensation circuit is invalidated.

[0033] In contrast, the present disclosure solves the problem of limited compensation range of the conventional compensation circuit from a viewpoint of signal design. As shown in Fig. 1, in the embodiment of the present disclosure, it is provided an OLED pixel circuit including:

an OLED;
a driving transistor Tdriver, wherein a drain electrode of the driving transistor is connected to the OLED;
a first switching unit T1 configured to connect a data signal output end and a gate electrode of the driving transistor Tdriver;
a second switching unit T2 configured to connect a power signal output end and a source electrode of the driving transistor Tdriver; and
a compensation circuit connected to the gate electrode of the driving transistor and configured to maintain a voltage of the gate electrode of the driving transistor during a light-emitting period, so as to enable a current flowing through the OLED to be irrelevant to a threshold voltage V_{th} of the driving transistor,
wherein the pixel circuit further includes:

a reference signal generation module configured to generate, based on a current threshold voltage of the driving transistor, a reference signal to be used by the compensation circuit, wherein a voltage of the reference signal and the threshold voltage meet at least one of vali-

dition conditions capable of validating the compensation circuit.

[0034] In this embodiment of the present disclosure, the reference signal is generated based on the current threshold voltage of the driving transistor. Thus, when the threshold voltage is changed, the reference voltage is changed accordingly. In other words, the reference voltage may change according to the change of the threshold voltage, so as to enable the conditions of validating the compensation circuit to be met even when the threshold voltage changes, and enlarge the compensation range of the compensation circuit.

[0035] In addition, the present disclosure solves the problem of limited compensation range of the conventional compensation circuit and obtains the solutions from a viewpoint of signal design instead of a viewpoint of just circuit design.

[0036] In the embodiments of the present disclosure, the reference signal generation module generates the reference signal based on factors such as the threshold voltage, and the threshold voltage may be obtained in one of many modes. In the following, two of the modes are briefly explained.

Mode 1

[0037] A detecting circuit is arranged in each driving circuit and configured to detect the current threshold voltage of the driving transistor.

[0038] Due to a fact that the threshold voltage of the driving transistor shifts insignificantly during a short period of time, the detecting frequency of the detecting circuit may be determined based on the requirement. For example, the threshold voltage is detected every time the driving circuit is turned on. In this case, the driving circuit operates based on this threshold voltage until it is turned off. Alternatively, the threshold voltage of the driving transistor may be detected and updated by the detecting circuit at regular interval of time, e.g. 1 hour or 2 hours, which is not particularly defined herein.

Mode 2

[0039] A diagram or a table representing a relation between the threshold voltage and a length of operation time of the driving transistor may be obtained in advance, and then the length of the operation time of the driving transistor is recorded in real time.

[0040] When the reference signal generation module needs to generate the reference signal, it determines the current threshold voltage of the driving transistor based on the length of the operation time, and generates the reference signal based on the current threshold voltage of the driving transistor.

[0041] The above are merely two examples of the modes for obtaining the current threshold voltage of the driving transistor, which are not particularly defined here-

in.

[0042] Since the conventional OLED display panel includes a plurality of unique OLED pixels, one reference signal generation module may be arranged for each of the OLED pixels.

[0043] However, a circuit with such arrangement is complicated. In addition, a whole display panel may be adversely affected no matter whether the reference signal generation modules are arranged at a display region or a non-display region of the display panel. In particular, when the reference signal generation modules are arranged at the display region, the aperture ratio of the display panel has to be reduced; in contrast, when the reference signal generation modules are arranged at the non-display region, a bezel of the display panel has to be widened, which is against a trend of slim product.

[0044] An existing display panel is scanned line by line, and thus the reference voltage is used by each of the pixels in an identical column during different period of time.

[0045] As a result, in contrast to a solution of arranging one reference signal generation module for each of the OLED pixels which has disadvantages such as high cost, the present disclosure provides in some embodiment an alternative solution. In this solution, a reference signal generation module is arranged for each column of OLED pixels, i.e. one column of OLED pixels share one reference signal generation module. Thus, the reference signal generation module may generate and output voltages of reference signals for corresponding OLED pixels during different periods.

[0046] In the above mode, one column of OLED pixels share one reference signal generation module, and the reference signal generation module includes:

a determination unit, configured to select a target driving circuit from driving circuits corresponding to the column of OLED pixels, wherein the reference signal is to be used by the target driving circuit; and a signal generation unit, configured to generate, based on a current threshold voltage of the driving transistor of the target driving circuit, a reference signal to be used by the compensation circuit of the target driving circuit. A voltage of the reference signal generated by the signal generation unit and the current threshold voltage of the driving transistor of the target driving circuit meet at least one of validation conditions capable of validating the compensation circuit of the target driving circuit

[0047] In the embodiments of the present disclosure, the determination unit determines the driving circuit currently requiring the reference signal in real time, determines the particular threshold voltage V_{th} of the driving transistor in the driving circuit, and then generates the corresponding reference signal based on the particular threshold voltage V_{th} . Here, the corresponding reference signal meets the validation conditions capable of validating

ing the compensation circuit of the target driving circuit. In this embodiment, the reference signal generation unit is reused by the pixels in one column based on the fact that the pixels in one column uses the reference signals during different periods of time, so that the pixels in one column merely require one reference signal generation module. As a result, the number of the hardware elements in the circuit is significantly reduced, thereby the cost for manufacturing the OLED pixel circuit is reduced.

[0048] Furthermore, when the reference signal generation modules are arranged at the display region, compared with the solution that a reference signal generation module is arranged for each OLED pixels, the aperture ratio of the display panel may be increased in the present solution in which one column of OLED pixels share one reference signal generation module. On the other hand, when the reference signal generation modules are arranged at the non-display region, compared with the solution that a reference signal generation module is arranged for each OLED pixels, an area occupied by the reference signal generation modules on the non-display region may be reduced in the present solution in which one column of OLED pixels share one reference signal generation module, so as to facilitate to narrow the bezel of the display panel.

[0049] In the related art, the reference signal is output to the corresponding compensation circuit via an independent signal transmission line under the control of an independent transistor. As a result, both the number of the transistors and the number of the data lines are large.

[0050] The present disclosure provides in some embodiments an OLED pixel circuit for reducing the number of the transistors and the number of the data lines. In the OLED pixel circuit, the reference signals and the data signals are transmitted by the data lines in a time-sharing manner under the control of one transistor, so that both the number of the TFTs and the number of the data transmission lines are reduced.

[0051] As shown in Fig.2, a first electrode of the OLED is connected to the driving transistor Tdriver, a second electrode of the OLED is connected to the ground ELVSS, and the compensation circuit includes:

a first capacitor C1, wherein one end N1 of the first capacitor C1 is connected to the gate electrode of the driving transistor, and the other end N2 of the first capacitor C1 is connected to the drain electrode of the driving transistor; and

a second capacitor C2, wherein one end of the second capacitor C2 is connected to the drain electrode of the driving transistor, and the other end of the second capacitor C2 is connected to the second electrode of the OLED.

[0052] The ground ELVSS is another voltage source being different from an output end of a power source, and configured to cooperate with an output end of the power source to drive the OLED to emit light.

[0053] In the above embodiment, during a reset period, the first switching unit T1 is configured to be turned on and output the reference signal to the gate electrode of the driving transistor, and the second switching unit T2 is configured to be turned on and output a first power signal to the source electrode of the driving transistor; during a compensation period, the first switching unit T1 is configured to be turned on and output the reference signal to the gate electrode of the driving transistor, and the second switching unit T2 is configured to be turned on and output a second power signal to the source electrode of the driving transistor, wherein a voltage of the first power signal is lower than a voltage of the second power signal;

during a writing period, the first switching unit T1 is configured to be turned on and output a data signal to the gate electrode of the driving transistor, and the second switching unit T2 is configured to be turned off; and during the light-emitting period, the first switching unit T1 is configured to be turned off, and the second switching unit T2 is configured to be turned on and output the second power signal to the source electrode of the driving transistor.

[0054] Fig.3 illustrates the timing sequence of the above driving circuit.

[0055] As shown in Figs.2 and 3, the above circuit operates as follows.

[0056] The above 3T2C circuit generally operates in four periods, i.e. the reset period, the compensation period, the writing period and the light-emitting period.

[0057] During the reset period, all TFTs are turned on, the reference signal is written to the node N1, the signal of a previous time frame is cleared, and the first power signal is written to the node N2 by the transistor T2.

[0058] During the compensation period, all TFTs are turned on, the power signal changes from being of a low level to being of a high level, and the power signal is progressively written to the node N2 by the transistor T2 and the transistor Tdriver. During this period, a voltage at the node N2 is progressively recharged to be equal to a difference between the reference voltage and the threshold voltage Vth (T1), and the transistor Tdriver is turned off, so that the compensation process ends.

[0059] In other words, during the above compensation period, it is necessary to eliminate the effect on the capacitor C1 by the data signal of the previous time frame. Thus, a reference signal is inputted to the node N1, and the node N2 is cleared by the first voltage signal being of a low level. During the compensation period, the second source signal is progressively written to the node N2 by the transistor T2 and the transistor Tdriver, and the voltage at the node N2 is increased to be equal to a difference between the reference voltage and the threshold voltage Vth (T1) by the second power signal.

[0060] Thus, the voltage of the first power signal written to the node N2 by the transistor T2 during the reset period is less than the voltage of the second power signal written to the node N2 by the transistor T2 during the compen-

sation period. In the related art, the signal inputted by ELVDD generally has a constant value. In the present disclosure, the voltage of the signal inputted to the node N2 changes during different periods according to a change of an amplitude of the signal inputted by ELVDD, so as to reduce the number of the signal transmission lines.

[0061] During the writing period, the transistor T2 is turned off, both the transistor T1 and the transistor Tdriver are turned on, and the data signal Vdata is written to the node N1 by the transistor T1. At this point, the node N2 is in a floating state, and the voltage at the node N2 changes according to the change of the voltage at the node N1. As a result, the voltage at the node N2 is increased according to an increase of the voltage at the node N1 during this period.

[0062] During the light-emitting period, the transistor T1 is turned off, both the transistor T2 and the transistor Tdriver are turned on, and then a circuit is formed by the second power source, the transistor T2, the transistor Tdriver, the OLED and the ground ELVSS so as to drive the OLED to emit light.

[0063] During the light-emitting period, the node N2 is reconnected to the ELVDD by the transistor T2, and the voltage may change. At this point, the node N1 is in a floating state, and the voltage at the node N1 is increased in proportion to an increase of the voltage at the node N2. Thus, the increased voltage at the node N1 includes the threshold voltage of the driving transistor, so as to enable the current flowing through the OLED to be irrelevant to a threshold voltage Vth of the driving transistor.

[0064] In the above arrangement, the reference signal generation module generates the reference signal based on the validation conditions and the current threshold voltage of the transistor Tdriver, so that the voltage of the reference signal and the threshold voltage always meet validation conditions capable of validating the compensation circuit.

[0065] Furthermore, in the above arrangement, both the reference signal and the data signal are controlled by one transistor T1, and transmitted by one signal transmission line (i.e. the data line). As a result, both the number of the data transmission lines and the number of the transistors are reduced, the circuit is simplified, and the product cost is reduced.

[0066] The validation conditions capable of validating the compensation circuit in the arrangement as shown in Fig.2 are explained as follows.

[0067] In the 3T2C pixel circuit as shown in Fig.2, the validation conditions capable of validating the compensation circuit may include:

$$A-B+a(C-A)<D;$$

and/or

$$E < A - B,$$

where

A indicates a voltage value of the reference signal;
 B indicates the threshold voltage of the driving transistor;
 C indicates a voltage value of the data signal;
 D indicates a threshold voltage of the OLED;
 E indicates a voltage value of the first power signal;
 and
 a is a proportional coefficient and equals to $C1/(C1+C2)$.

[0068] It should be appreciated that, when the compensation circuit is required to implement the compensation in any given situation, the compensation circuit has to meet many conditions, and a failure of meeting any of these conditions may lead to the validation range to be narrowed. Thus, the compensation range of the compensation circuit may be enlarged when each validation condition is satisfied.

[0069] As a result, in the embodiments of the present disclosure, the compensation range may be enlarged when the voltage of the reference signal and the threshold voltage meet at least one of validation conditions capable of validating the compensation circuit. In an embodiment, all of the validation conditions relevant to the threshold voltage may be met, so as to enable the validation of the compensation circuit to be met irrespective of any shift of the threshold voltage.

[0070] As shown in Figs.2 and 3, the first switching unit is a thin film transistor (TFT), a source electrode of which is connected to a data line, a drain electrode of which is connected to the gate electrode of the driving transistor, a gate electrode of which is connected to an output end of a first control signal S1, and which is configured to be turned on when the first control signal is effective, wherein the first control signal is effective during the reset period, the compensation period and the writing period; and the second switching unit is a TFT, a source electrode of which is connected to the power signal output end, a drain electrode of which is connected to the source electrode of the driving transistor, a gate electrode of which is connected to an output end of a second control signal S3, and which is configured to be turned on when the second control signal is effective, wherein the second control signal is effective during the reset period, the compensation period and the light-emitting period.

[0071] In an embodiment, as shown in Fig.2, the reference signal generation module may output the reference signal to the data line in many modes, which are illustrated as follows.

[0072] In Mode 1, the output end is directly connected to the data line, and the reference signal generation module merely generates the reference signal during the re-

set period and the compensation period.

[0073] As shown in Fig.3, during the reset period and the compensation period, a combination of the reference signal and the data signal is transmitted via the data line.

5 A null signal is outputted by the data driving chip. Then a combination of the reference signal and the null signal is the reference signal. During the writing period, the reference signal generation module outputs the null signal to the data line, thereby a combination of the null signal and the data signal is the data signal. During the light-emitting period, S1 is turned off, and none of the signals transmitted on the data line may affect the pixel circuit.

[0074] Thus, the reference signals of the data signals may be transmitted in the time-sharing manner in the Mode 1.

[0075] In other words, in the Mode 1, during the reset period and the compensation period corresponding to the target driving circuit, the signal generate unit generate, based on a current threshold voltage of the driving transistor of the target driving circuit, and output the reference signal to be used by the compensation circuit of the target driving circuit. Here, a voltage of the reference signal generated by the signal generation unit and the current threshold voltage of the driving transistor of the target driving circuit meet at least one of validation conditions capable of validating the compensation circuit of the target driving circuit.

[0076] The above Mode 1 has a simple structure, but requires the reference signal generation module to generate and output the reference signal in a precise time.

[0077] Alternatively, a Mode 2 may be adopted to reduce the cost. In Mode 2, one or more TFTs are added, so that the timing for outputting the signal generated by the reference signal generation module to the data line is controlled by the TFT. At this point, in the pixel, the added TFT are turned on during the reset period and the compensation period, and are turned off during other time periods.

[0078] In the above embodiment, as shown in Fig.4, the reference signal generation module further includes:

45 a third switching unit T3 configured to connect the signal generation unit and the data line and output the reference signal generated by the signal generation unit to the data line during the reset period and the compensation period.

[0079] As shown in Fig.4, the third switching unit T3 is a TFT, a source electrode of which is connected to the signal generation unit, a drain electrode of which is connected to the data line, a gate electrode of which is connected to an output end of a third control signal, and which is configured to be turned on when the third control signal is effective. The third control signal is effective during the reset period and the compensation period.

[0080] In the above embodiment, during the reset period and the compensation period, the null signal may be outputted by the data driving chip with some noises,

which may interfere the reference signal. In a further embodiment, a TFT is added to reduce such noises. As shown in Fig.5, the reference signal generation module further includes a third switching unit T3 configured to connect the signal generation unit and the data line and output the reference signal generated by the signal generation unit to the data line during the reset period and the compensation period. The pixel circuit further includes a fourth switching unit T4 configured to connect a data driving chip and the data line and output the data signal generated by the data driving chip to the data line during the writing period.

[0081] As shown in Fig.5, the third switching unit is a TFT, a source electrode of which is connected to the signal generation unit, a drain electrode of which is connected to the data line, a gate electrode of which is connected to an output end of the third control signal, and which is configured to be turned on when the third control signal is effective. The third control signal is effective during the reset period and the compensation period. The fourth switching unit is a TFT, a source electrode of which is connected to the data driving chip, a drain electrode of which is connected to the data line, a gate electrode of which is connected to an output end of a fourth control signal, and which is configured to be turned on when the fourth control signal is effective. The fourth control signal is effective during the writing period.

[0082] The OLED pixel circuit has been described by taking the specific 3T2C pixel circuit as an example. However, the present disclosure is not limited to such embodiments, and the OLED pixel circuit may be implemented by another pixel circuit, such as the 4T2C pixel circuit which is shown in Fig.6 and corresponds to the timing sequence of Fig.7.

[0083] As shown in Figs.6 and 7, the 4T2C circuit operates as follows.

[0084] The above 4T2C circuit generally operates in four periods, i.e. the reset period, the compensation period, the writing period and the light-emitting period.

[0085] During the reset period, all TFTs are turned on, the reference signal is written to the node N10, the signal of a previous time frame is cleared, and the signal Vsus is written to the node N20 by the transistor T30. The signal Vsus represents a low voltage which is less than the voltage of the reference signal written to the node N10.

[0086] During the compensation period, both the transistor T10 and the transistor T20 are turned on, and the transistor T30 are turned off. The power signal is progressively written to the node N20 by the transistor T20 and the transistor Tdriver, the voltage at the node N20 is recharged to be equal to a difference between the reference voltage and the threshold voltage Vth (T10), and the transistor Tdriver is turned off, so that the compensation process ends.

[0087] During the writing period, both the transistor T20 and the transistor T30 are turned off, both the transistor T10 and the transistor Tdriver are turned on, and the data signal Vdata is written to the node N10 by the transistor

T10.

[0088] During the light-emitting period, both the transistor T10 and the transistor T30 are turned off, both the transistor T20 and the transistor Tdriver are turned on, and then a circuit is formed by the power source, the transistor T20, the transistor Tdriver, the OLED and the ground ELVSS so as to drive the OLED to emit light.

[0089] During the light-emitting period, the node N20 is reconnected to ELVDD by the transistor T20, and the voltage thereof may be changed. At this time, the node N10 is in a floating state, and the voltage at the node N10 is increased in proportion to an increase of the voltage at the node N20. Thus, the increased voltage at the node N10 includes the threshold voltage of the driving transistor, so as to enable the current flowing through the OLED to be irrelevant to a threshold voltage Vth of the driving transistor.

[0090] As can be seen from above, the validation conditions relevant to the reference voltage may include:

$$A > F;$$

and

$$A - B + a(C - A) < D;$$

where

A indicates a voltage value of the reference signal;
B indicates the threshold voltage of the driving transistor;

C indicates a voltage value of the data signal;

D indicates a threshold voltage of the OLED;

F indicates the voltage Vsus.

a is a proportional coefficient and equals to $C10/(C10+C20)$.

[0091] The embodiments of the present disclosure are described by taking N-type transistors as an example. However, it should be appreciated that the present disclosure is not limited to the above embodiments, and each of the N-type transistors may be replaced by a N-type TFT or a complementary metal oxide semiconductor (CMOS) transistor based on a corresponding timing sequence. When a P-type transistor functioning as a switch is used to replace the N-type transistor, the timing sequence is merely required to be amended by changing the original high levels to the low levels and changing the original low levels to the high levels. When the driving transistor is replaced, the location of the OLED and the design of the power signal should be changed accordingly, which is known for a person skilled in the art and thus is omitted herein.

[0092] In another aspect, the present disclosure provides in some embodiments a display device including

the above OLED pixel circuit.

[0093] The display device may be an electronic paper, an OLED panel, a mobile phone, a tablet computer, a television, a monitor, a notebook computer, a digital picture frame, a navigator or any other product or member having a display function.

[0094] In yet another aspect, the present disclosure provides in some embodiments a method for controlling the OLED pixel circuit, wherein the OLED pixel circuit includes:

an OLED;
a driving transistor; and
a compensation circuit configured to maintain a voltage of a gate electrode of the driving transistor during a light-emitting period, so as to enable a current flowing through the OLED to be irrelevant to a threshold voltage V_{th} of the driving transistor,
the method includes
a reference signal generation step of generating, based on a current threshold voltage of the driving transistor, a reference signal to be used by the compensation circuit, wherein a voltage of the reference signal and the threshold voltage meet at least one of validation conditions capable of validating the compensation circuit.

[0095] When one column of OLED pixels share one reference signal generation module, the reference signal generation module includes:

selecting a target driving circuit from driving circuits corresponding to the column of OLED pixels, wherein the reference signal is to be used by the target driving circuit; and
generating, based on a current threshold voltage of the driving transistor of the target driving circuit, a reference signal to be used by the compensation circuit of the target driving circuit, wherein a voltage of the generated reference signal and the current threshold voltage of the driving transistor of the target driving circuit meet at least one of validation conditions capable of validating the compensation circuit of the target driving circuit.

[0096] In an embodiment, a first electrode of the OLED is connected to the driving transistor, a second electrode of the OLED is connected to the ground, and the compensation circuit includes: a first capacitor, wherein an end of the first capacitor is connected to the gate electrode of the driving transistor, and the other end of the first capacitor is connected to the drain electrode of the driving transistor; and a second capacitor, wherein an end of the second capacitor is connected to the drain electrode of the driving transistor, and the other end of the second capacitor is connected to the second electrode of the OLED, and the method further includes steps of:

during a reset period, turning on the first switching unit and outputting the reference signal to the gate electrode of the driving transistor, and turning on the second switching unit and outputting a first power signal to the source electrode of the driving transistor;

during a compensation period, turning on the first switching unit and outputting the reference signal to the gate electrode of the driving transistor, and turning on the second switching unit and outputting a second power signal to the source electrode of the driving transistor, wherein a voltage of the first power signal is lower than a voltage of the second power signal;

during a writing period, turning on the first switching unit and outputting a data signal to the gate electrode of the driving transistor, and turning off the second switching unit; and

during the light-emitting period, turning off the first switching unit, and turning on the second switching unit and outputting the second power signal to the source electrode of the driving transistor.

[0097] The above are merely the preferred embodiments of the present disclosure. It should be noted that, a person skilled in the art may make improvements and modifications without departing from the principle of the present disclosure, and these improvements and modifications shall also fall within the scope of the present disclosure.

Claims

1. An organic light-emitting diode (OLED) pixel circuit, comprising:

an OLED;
a driving transistor, wherein a drain electrode of the driving transistor is connected to the OLED;
a first switching unit configured to connect a data signal output end and a gate electrode of the driving transistor;
a second switching unit configured to connect a power signal output end and a source electrode of the driving transistor; and
a compensation circuit connected to the gate electrode of the driving transistor and configured to maintain a voltage of the gate electrode of the driving transistor during a light-emitting period, so as to enable a current flowing through the OLED to be irrelevant to a threshold voltage V_{th} of the driving transistor,
wherein the pixel circuit further comprises:

a reference signal generation module configured to generate, based on a current threshold voltage of the driving transistor, a

- reference signal to be used by the compensation circuit, wherein a voltage of the reference signal and the threshold voltage meet at least one of validation conditions capable of validating the compensation circuit. 5
2. The OLED pixel circuit according to claim 1, wherein one column of OLED pixels share one reference signal generation module which comprises: 10
- a determination unit, configured to select a target driving circuit from driving circuits corresponding to the column of OLED pixels, wherein the reference signal is to be used by the target driving circuit; and 15
- a signal generation unit, configured to generate, based on a current threshold voltage of the driving transistor of the target driving circuit, a reference signal to be used by the compensation circuit of the target driving circuit, wherein a voltage of the reference signal generated by the signal generation unit and the current threshold voltage of the driving transistor of the target driving circuit meet at least one of validation conditions capable of validating the compensation circuit of the target driving circuit. 20 25
3. The OLED pixel circuit according to claim 2, wherein a first electrode of the OLED is connected to the driving transistor, a second electrode of the OLED is connected to the ground, and the compensation circuit comprises: 30
- a first capacitor, wherein an end of the first capacitor is connected to the gate electrode of the driving transistor, and the other end of the first capacitor is connected to the drain electrode of the driving transistor; and 35
- a second capacitor, wherein an end of the second capacitor is connected to the drain electrode of the driving transistor, and the other end of the second capacitor is connected to the second electrode of the OLED. 40
4. The OLED pixel circuit according to claim 3, wherein during a reset period, the first switching unit is configured to be turned on and output the reference signal to the gate electrode of the driving transistor, and the second switching unit is configured to be turned on and output a first power signal to the source electrode of the driving transistor; 45
- during a compensation period, the first switching unit is configured to be turned on and output the reference signal to the gate electrode of the driving transistor, and the second switching unit is configured to be turned on and output a second power signal to the source electrode of the driving transistor, wherein 50 55

a voltage of the first power signal is lower than a voltage of the second power signal;

during a writing period, the first switching unit is configured to be turned on and output a data signal to the gate electrode of the driving transistor, and the second switching unit is configured to be turned off; and

during the light-emitting period, the first switching unit is configured to be turned off, and the second switching unit is configured to be turned on and output the second power signal to the source electrode of the driving transistor.

5. The OLED pixel circuit according to claim 4, wherein the conditions comprises:

$$A-B+a(C-A)<D;$$

and/or

$$E<A-B,$$

where

A indicates a voltage value of the reference signal;

B indicates the threshold voltage of the driving transistor;

C indicates a voltage value of the data signal;

D indicates a threshold voltage of the OLED;

E indicates a voltage value of the first power signal; and

a = a capacitance value of the first capacitor/(the capacitance value of the first capacitor + a capacitance value of the second capacitor).

6. The OLED pixel circuit according to claim 4, wherein the first switching unit is a thin film transistor (TFT), a source electrode of which is connected to a data line, a drain electrode of which is connected to the gate electrode of the driving transistor, a gate electrode of which is connected to an output end of a first control signal, and which is configured to be turned on when the first control signal is effective, wherein the first control signal is effective during the reset period, the compensation period and the writing period; and 45
- the second switching unit is a TFT, a source electrode of which is connected to the power signal output end, a drain electrode of which is connected to the source electrode of the driving transistor, a gate electrode of which is connected to an output end of a second control signal, and which is configured to be turned on when the second control signal is effective, wherein the second control signal is effective 50 55

during the reset period, the compensation period and the light-emitting period.

7. The OLED pixel circuit according to claim 6, wherein the signal generation unit is configured to:

during the reset period and the compensation period corresponding to the target driving circuit, generate, based on a current threshold voltage of the driving transistor of the target driving circuit, and output the reference signal to be used by the compensation circuit of the target driving circuit, wherein the voltage of the reference signal generated by the signal generation unit and the current threshold voltage of the driving transistor of the target driving circuit meet at least one of validation conditions capable of validating the compensation circuit of the target driving circuit.

8. The OLED pixel circuit according to claim 6, wherein the reference signal generation module further comprises:

a third switching unit configured to connect the signal generation unit and the data line and output the reference signal generated by the signal generation unit to the data line during the reset period and the compensation period.

9. The OLED pixel circuit according to claim 8, wherein the third switching unit is a TFT, a source electrode of which is connected to the signal generation unit, a drain electrode of which is connected to the data line, a gate electrode of which is connected to an output end of a third control signal, and which is configured to be turned on when the third control signal is effective, wherein the third control signal is effective during the reset period and the compensation period.

10. The OLED pixel circuit according to claim 6, wherein the reference signal generation module further comprises: a third switching unit configured to connect the signal generation unit and the data line and output the reference signal generated by the signal generation unit to the data line during the reset period and the compensation period; and the pixel circuit further comprises: a fourth switching unit configured to connect a data driving chip and the data line and output the data signal generated by the data driving chip to the data line during the writing period.

11. The OLED pixel circuit according to claim 10, wherein the third switching unit is a TFT, a source electrode of which is connected to the signal generation unit,

a drain electrode of which is connected to the data line, a gate electrode of which is connected to an output end of a third control signal, and which is configured to be turned on when the third control signal is effective, wherein the third control signal is effective during the reset period and the compensation period; and

the fourth switching unit is a TFT, a source electrode of which is connected to the data driving chip, a drain electrode of which is connected to the data line, a gate electrode of which is connected to an output end of a fourth control signal, and which is configured to be turned on when the fourth control signal is effective, wherein the fourth control signal is effective during the writing period.

12. A display device comprising the OLED pixel circuit according to any one of claims 1-11.

13. A method for controlling an organic light-emitting diode (OLED) pixel circuit, wherein the OLED pixel circuit comprises:

an OLED;

a driving transistor; and

a compensation circuit configured to maintain a voltage of a gate electrode of the driving transistor during a light-emitting period, so as to enable a current flowing through the OLED to be irrelevant to a threshold voltage V_{th} of the driving transistor, wherein the method comprises a reference signal generation step of:

generating, based on a current threshold voltage of the driving transistor, a reference signal to be used by the compensation circuit, wherein a voltage of the reference signal and the threshold voltage meet at least one of validation conditions capable of validating the compensation circuit.

14. The method according to 13, wherein one column of OLED pixels share one reference signal generation module, and the reference signal generation step comprises steps of:

selecting a target driving circuit from driving circuits corresponding to the column of OLED pixels, wherein the reference signal is to be used by the target driving circuit; and generating, based on a current threshold voltage of the driving transistor of the target driving circuit, a reference signal to be used by the compensation circuit of the target driving circuit, wherein a voltage of the generated reference signal and the current threshold voltage of the driving transistor of the target driving circuit meet

at least one of validation conditions capable of validating the compensation circuit of the target driving circuit.

15. The method according to 13, wherein a first electrode of the OLED is connected to the driving transistor, a second electrode of the OLED is connected to the ground, and the compensation circuit comprises:

a first capacitor, wherein an end of the first capacitor is connected to the gate electrode of the driving transistor, and the other end of the first capacitor is connected to the drain electrode of the driving transistor; and
a second capacitor, wherein an end of the second capacitor is connected to the drain electrode of the driving transistor, and the other end of the second capacitor is connected to the second electrode of the OLED,
the method further comprises steps of:

during a reset period, turning on the first switching unit and outputting the reference signal to the gate electrode of the driving transistor, and turning on the second switching unit and outputting a first power signal to the source electrode of the driving transistor;
during a compensation period, turning on the first switching unit and outputting the reference signal to the gate electrode of the driving transistor, and turning on the second switching unit and outputting a second power signal to the source electrode of the driving transistor, wherein a voltage of the first power signal is lower than a voltage of the second power signal;
during a writing period, turning on the first switching unit and outputting a data signal to the gate electrode of the driving transistor, and turning off the second switching unit; and
during the light-emitting period, turning off the first switching unit, and turning on the second switching unit and outputting the second power signal to the source electrode of the driving transistor.

16. The method according to 13, wherein the conditions comprises:

$$A - B + a(C - A) < D;$$

and/or

$$E < A - B,$$

where

A indicates a voltage value of the reference signal;
B indicates the threshold voltage of the driving transistor;
C indicates a voltage value of the data signal;
D indicates a threshold voltage of the OLED;
E indicates a voltage value of the first power signal; and
 $a = \text{a capacitance value of the first capacitor} / (\text{the capacitance value of the first capacitor} + \text{a capacitance value of the second capacitor})$.

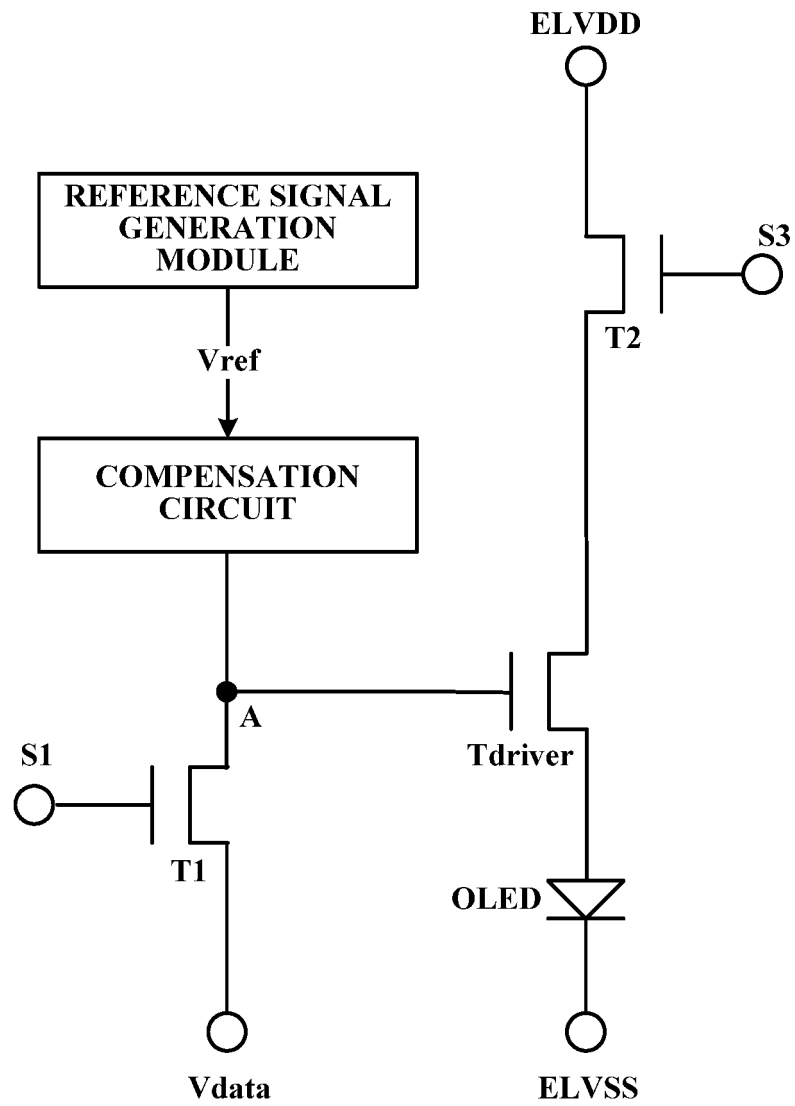


FIG. 1

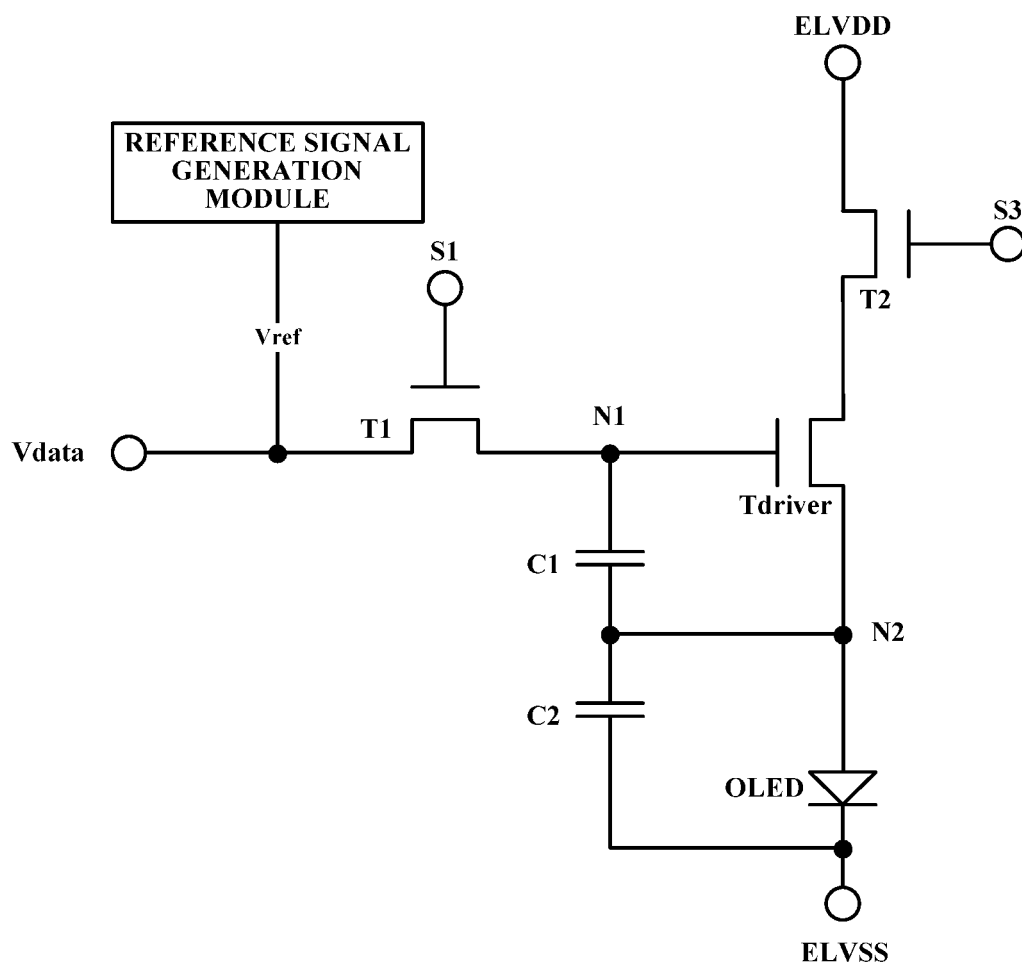


FIG. 2

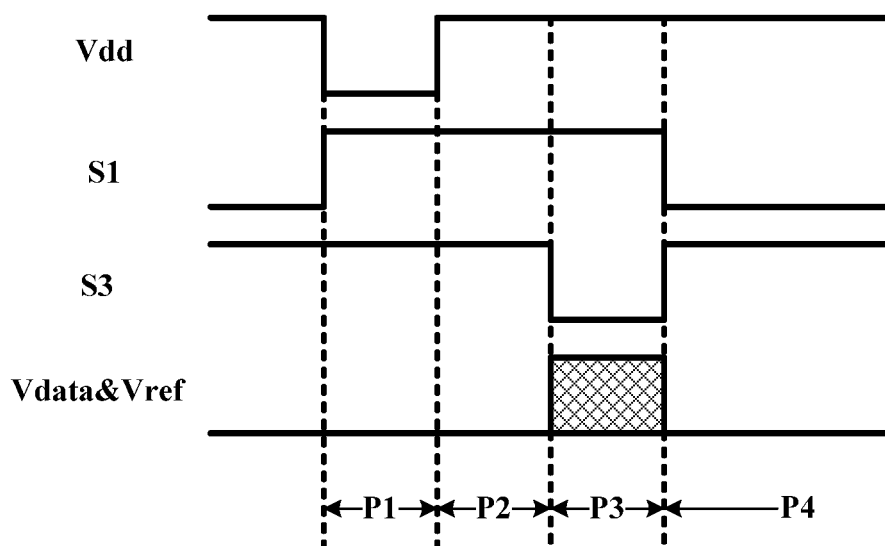


FIG. 3

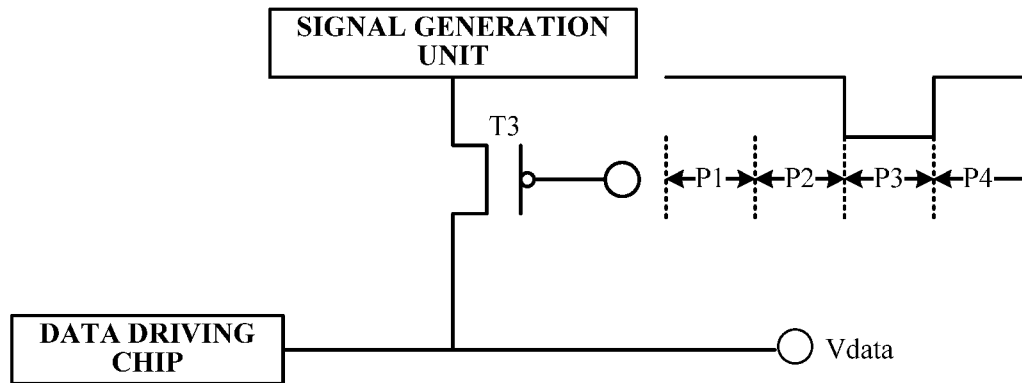


FIG. 4

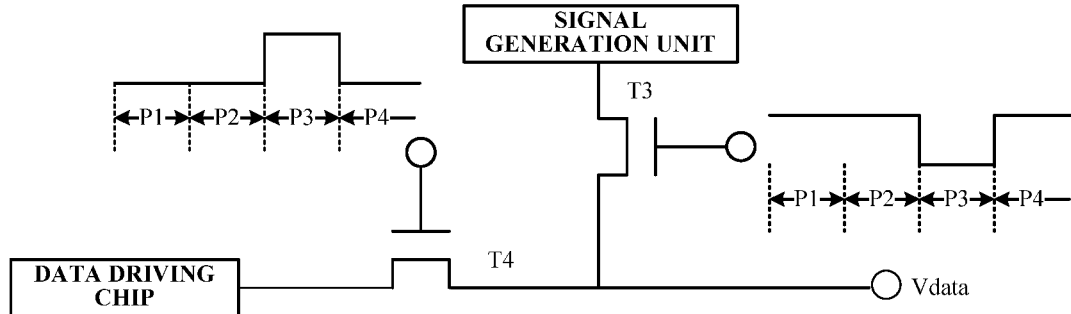


FIG. 5

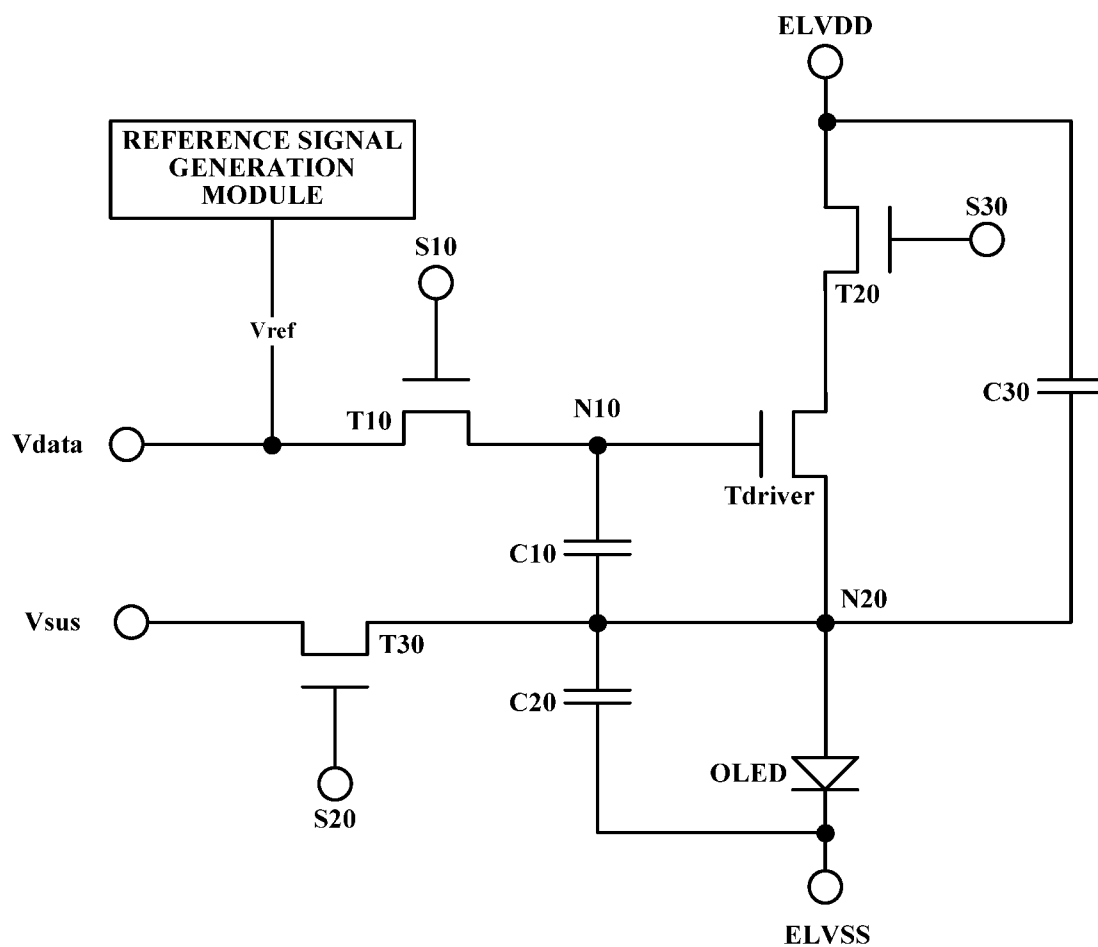


FIG. 6

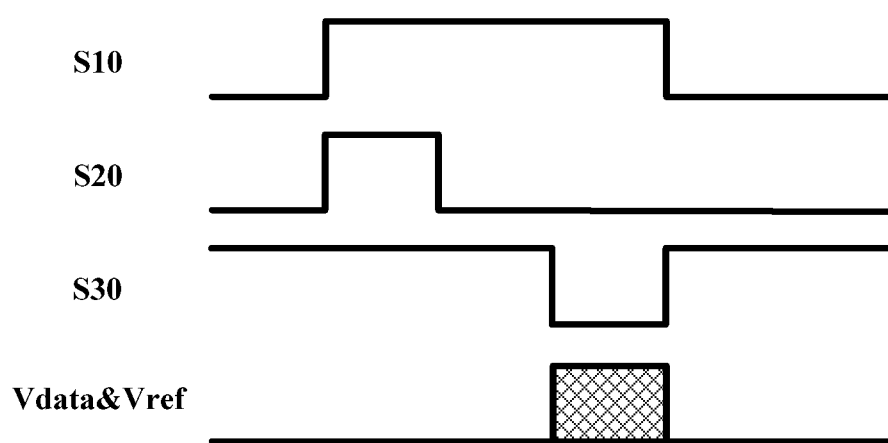


FIG. 7

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2015/092198

A. CLASSIFICATION OF SUBJECT MATTER

G09G 3/32 (2006.01) i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

G09G3

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
 CNPAT, CNABS, CNTXT: light emitting diode, electroluminescence, ?led, reference, standard, voltage, signal, threshold value, drift, offset, character, according to, depend on, following, generate, change, transistor, switch, +tft, +fet, mos+, third, three, capacitance, store, second, two

VEN, WOTXT, EPTXT, USTXT: ?led, le, light w emit+ w diode?, reference, voltage, signal, threshold, drift, character+, depend+, acoord+, produc+, form+, generat+, chang+, var+, alter+, transistor?, switch+, +tft?, +fet?, mod+, third+, three, capacit+, stor+, two, second+

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 2014174905 A1 (SHARP K.K.), 30 October 2014 (30.10.2014), description, pages 12-54, and figures 1-32	1-2, 12-14
Y	WO 2014174905 A1 (SHARP K.K.), 30 October 2014 (30.10.2014), description, pages 12-54, and figures 1-32	3-11, 15-16
Y	CN 103915061 A (LG DISPLAY CO., LTD.), 09 July 2014 (09.07.2014), description, paragraphs 0032-0056, and figures 1-4B	3-11, 15-16
Y	CN 102945654 A (AU OPTRONICS CORP.), 27 February 2013 (27.02.2013), description, pages 3-8, and figures 1-7	3-11, 15-16
PX	CN 104778925 A (BOE TECHNOLOGY GROUP CO., LTD.), 15 July 2015 (15.07.2015), claims 1-16, description, pages 5-11, and figures 1-7	1-16
A	CN 101430859 A (SONY CORPORATION), 13 May 2009 (13.05.2009), the whole document	1-16

☒ Further documents are listed in the continuation of Box C.☒ See patent family annex.

* Special categories of cited documents:

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"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

18 January 2016 (18.01.2016)

Date of mailing of the international search report

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Name and mailing address of the ISA/CN:
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INTERNATIONAL SEARCH REPORT

International application No.
PCT/CN2015/092198

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 20090284515 A1 (TOSHIBA MATSUSHITA DISPLAY TECHNOLOGY CO., LTD.), 19 November 2009 (19.11.2009), the whole document	1-16

Form PCT/ISA/210 (continuation of second sheet) (July 2009)

INTERNATIONAL SEARCH REPORT
 Information on patent family members

International application No.

PCT/CN2015/092198

Patent Documents referred in the Report	Publication Date	Patent Family	Publication Date
WO 2014174905 A1	30 October 2014	None	
CN 103915061 A	09 July 2014	US 2014184665 A1	03 July 2014
		KR 20140086467 A	08 July 2014
		GB 2510481 A	06 August 2014
		DE 102013114348 A1	03 July 2014
		GB 2510481 B	16 September 2015
CN 102945654 A	27 February 2013	TW 201415442 A	16 April 2014
		TW I460706 B	11 November 2014
CN 104778925 A	15 July 2015	None	
CN 101430859 A	13 May 2009	CN 101430859 B	22 December 2010
		US 2009079727 A1	26 March 2009
		JP 4530017 B2	25 August 2010
		JP 2009080252 A	16 April 2009
US 20090284515 A1	19 November 2009	JP 2010107763 A	13 May 2010
		JP 2009276669 A	26 November 2009

Form PCT/ISA/210 (patent family annex) (July 2009)

REFERENCES CITED IN THE DESCRIPTION

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Patent documents cited in the description

- CN 201510232424 [0001]

专利名称(译)	OLED像素电路，显示装置和控制方法		
公开(公告)号	EP3296983A4	公开(公告)日	2018-12-12
申请号	EP2015851606	申请日	2015-10-19
[标]申请(专利权)人(译)	京东方科技集团股份有限公司		
申请(专利权)人(译)	京东方科技集团股份有限公司.		
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IPC分类号	G09G3/32 G09G3/3225		
CPC分类号	G09G3/325 G09G3/32 G09G3/3233 G09G3/3291 G09G2300/0819 G09G2300/0852 G09G2300/0861 G09G2300/0866 G09G2300/0876 G09G2320/0233 G09G2320/045 G09G2330/021 G09G2330/08		
代理机构(译)	LAVOIX		
优先权	201510232424.5 2015-05-08 CN		
其他公开文献	EP3296983A1		
外部链接	Espacenet		

摘要(译)

本公开提供了OLED像素电路，显示装置和控制方法。OLED像素电路包括：OLED；一个驱动晶体管；第一开关单元，用于连接数据信号输出端和驱动晶体管的栅极；第二开关单元，用于连接所述驱动晶体管的功率信号输出端和源极；补偿电路，连接至驱动晶体管的栅极，用以在发光时段内维持驱动晶体管的栅极电压，以使流过OLED的电流与阈值电压无关驱动晶体管的 V_{th} ；参考信号产生模块，用于根据所述驱动晶体管的电流阈值电压，产生所述补偿电路使用的参考信号，所述参考信号的电压和所述阈值电压满足至少一个验证条件能够验证补偿电路。