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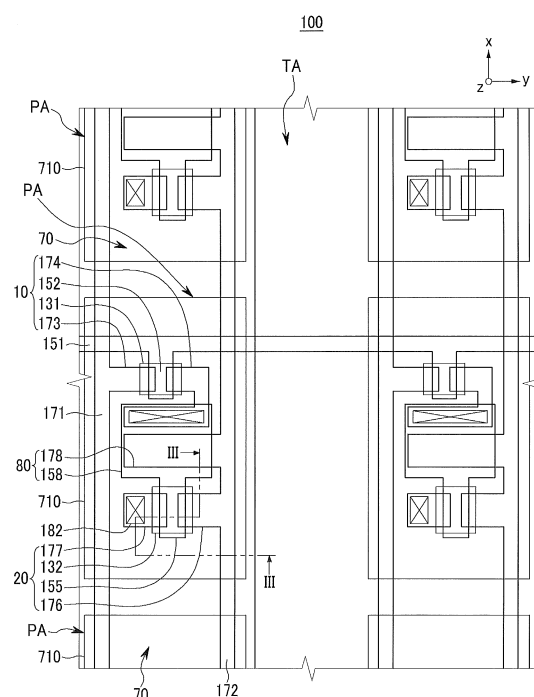
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(54) **Organic light emitting diode display**

(57) An organic light emitting diode display including a plurality of pixel areas and a transparent area interposed between the plurality of pixel areas, the display includes a substrate member, thin film transistors and capacitor elements on the substrate member, the thin film transistor and the capacitor elements overlapping with the pixel areas, a gate line, a data line, and a common power supply line on the substrate member, the gate line, the data line, and the common power supply line overlapping with the pixel areas and the transparent area, and being connected to corresponding ones of the thin film transistors and/or the capacitor elements, and pixel electrodes on the substrate member, the pixel electrodes overlapping with all of the thin film transistors and capacitor elements, and with respective portions of the gate line, the data line, and the common power supply line that overlap with the pixel areas.

FIG. 2





EUROPEAN SEARCH REPORT

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Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
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The present search report has been drawn up for all claims			
Place of search Munich		Date of completion of the search 14 March 2014	Examiner Bernabé Prieto, A
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
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一种有机发光二极管显示器，包括多个像素区域和介于所述多个像素区域之间的透明区域，所述显示器包括基板构件，薄膜晶体管和基板构件上的电容器元件，薄膜晶体管和电容器元件与基板构件上的像素区域，栅极线，数据线和公共电源线重叠，栅极线，数据线和公共电源线与像素区域和透明区域重叠，以及连接到相应的薄膜晶体管和/或电容器元件，以及基板构件上的像素电极，像素电极与所有薄膜晶体管和电容器元件重叠，并与栅极线的各个部分重叠，数据线和与像素区域重叠的公共电源线。

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