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(54) **Organic light emitting display and method of fabricating the same**

(57) An organic light emitting display and a method of fabricating the same, in which the resistance of a cathode electrode is reduced by applying a conductive ink material on the cathode electrode. A pixel defining layer

is formed on a substrate, with a recess or depression in a non-light emitting region of the pixel defining layer. An electrode on the substrate includes a portion within the recess of the pixel defining layer. The conductive ink is in the portion of the electrode in the recess.

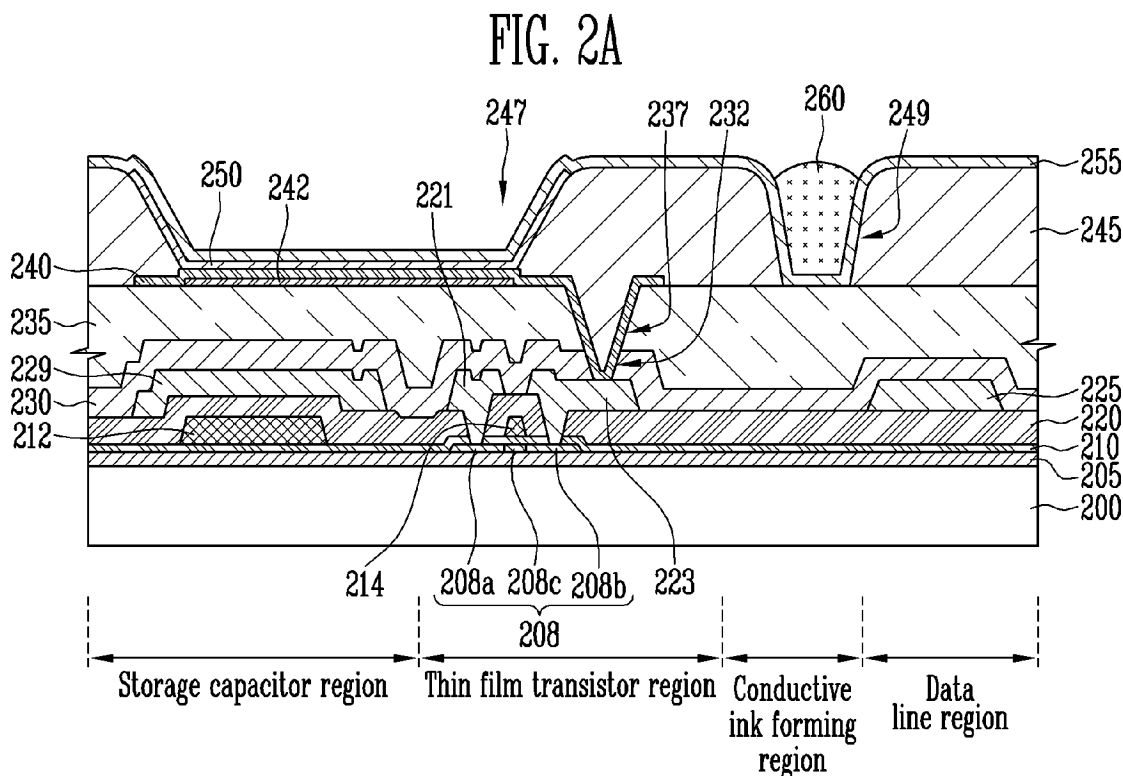
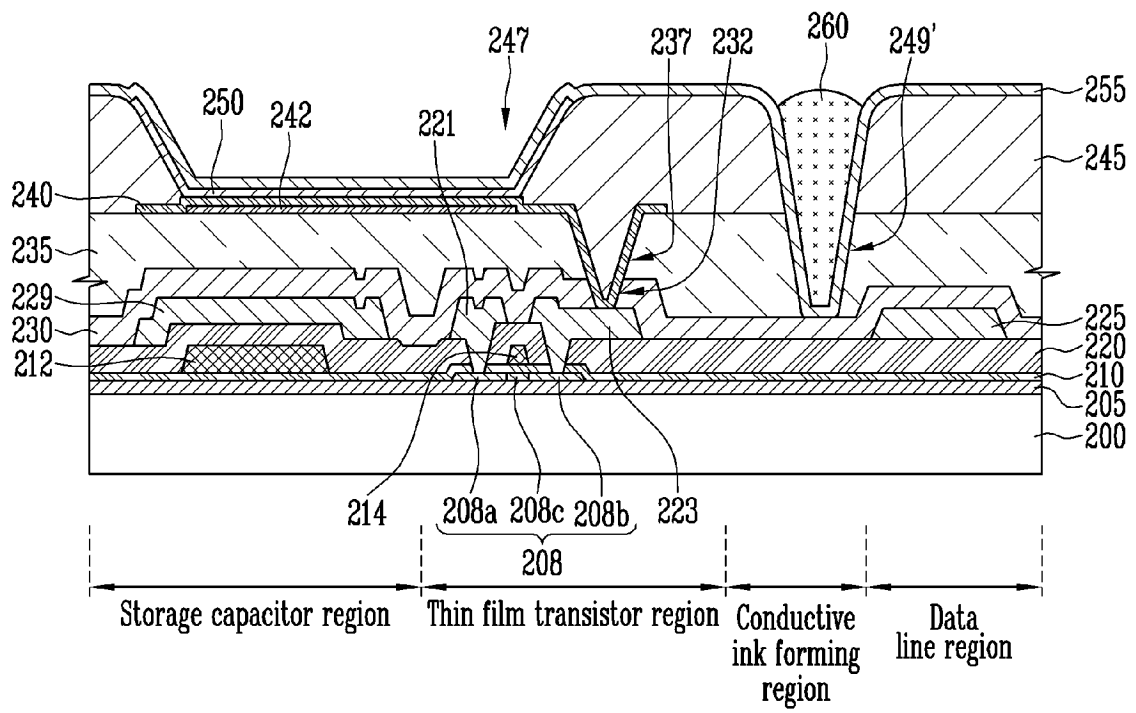


FIG. 2B



Description

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] This application claims priority to and the benefit of U.S. Provisional Patent Application No. 61/079,023, filed on July 8, 2008, the entire content of which is incorporated herein by reference.

BACKGROUND

1. Field of the Invention

[0002] The present invention relates to an organic light emitting display, and a method of fabricating the same.

2. Discussion of Related Art

[0003] Generally, electroluminescence displays, which are self-luminous displays, electrically excite fluorescent materials to emit light. These electroluminescence displays have generated significant interest as next-generation displays due to advantages such as low voltage operation, a very slim form factor, a wide viewing angle, a high speed response, etc., which are advantages that generally cannot be achieved in liquid crystal displays.

[0004] Electroluminescence displays include inorganic light emitting displays and organic light emitting displays, depending on whether the material that makes up the light emitting layer is an inorganic material or an organic material.

[0005] Organic light emitting displays have an organic layer in a pattern (e.g., a predetermined pattern) formed on a glass substrate or other transparent insulating substrate. An anode electrode layer and a cathode electrode layer are formed on an upper part and a lower part of the organic layer, respectively. The organic layer includes organic compounds that form a light emitting layer.

[0006] In the organic light emitting display configured as described above, a positive voltage and a negative voltage are applied to an anode electrode and a cathode electrode, respectively, such that holes move from the anode electrode with the positive voltage to the light emitting layer via a hole transportation layer, and electrons move from the cathode electrode with the negative voltage to the light emitting layer via an electron transportation layer. Accordingly, the electrons and the holes are recombined in the light emitting layer to generate excitons, and the excitons change from an excited state to a ground state, so that fluorescent molecules of the light emitting layer emit light to form an image.

[0007] FIG. 1 is a circuit diagram showing a conventional pixel of an organic light emitting display. Referring to FIG. 1, the pixel 4 of the organic light emitting display includes a pixel circuit 2 coupled to an organic light emitting diode OLED, a data line Dm, and a scan line Sn to control light emission of the organic light emitting diode

OLED.

[0008] An anode electrode of the organic light emitting diode OLED is coupled to the pixel circuit 2 and a cathode electrode of the organic light emitting diode OLED is coupled to a second power supply ELVSS. The organic light emitting diode OLED emits light corresponding to a current supplied from the pixel circuit 2.

[0009] The pixel circuit 2 controls the amount of current supplied to the organic light emitting diode OLED in accordance with to a data signal supplied to the data line Dm when a scan signal is supplied to the scan line Sn.

[0010] To this end, the pixel circuit 2 includes a second transistor M2 coupled between a first power supply ELVDD and the organic light emitting diode OLED; a first transistor M1 coupled between the second transistor M2 and the data line Dm, and coupled to the scan line Sn to receive the scan signal; and a storage capacitor Cst coupled between a first electrode and a gate electrode of the second transistor M2.

[0011] A gate electrode of the first transistor M1 is coupled to the scan line Sn and a first electrode of the first transistor M1 is coupled to the data line Dm. A second electrode of the first transistor M1 is coupled to one terminal of the storage capacitor Cst.

[0012] Herein, the first electrode is one of a source electrode and a drain electrode, and the second electrode is the other one of the source electrode and the drain electrode. For example, if the first electrode is the source electrode, the second electrode is the drain electrode. The first transistor M1 coupled to the scan line Sn and the data line Dm is turned on when the scan signal is supplied from the scan line Sn to supply the data signal supplied from the data line Dm to the storage capacitor Cst. At this time, the storage capacitor Cst charges a voltage corresponding to the data signal.

[0013] The gate electrode of the second transistor M2 is coupled to one terminal of the storage capacitor Cst and the first electrode of the second transistor M2 is coupled to the other terminal of the storage capacitor Cst and the first power supply ELVDD. The second electrode of the second transistor M2 is coupled to the anode electrode of the organic light emitting diode OLED.

[0014] The second transistor M2 controls the amount of current flowing from the first power supply ELVDD to the second power supply ELVSS via the organic light emitting diode OLED, where the amount of current corresponds to a voltage value stored in the storage capacitor Cst. According to this configuration, the organic light emitting diode OLED generates light corresponding to the amount of current supplied from the second transistor M2.

[0015] The organic light emitting display may be a top emission type, a bottom emission type, or both, according to a light emitting direction. Recently, as flat displays have become bigger in size, the adoption of the top emission type has been preferred.

[0016] In top emission type organic light emitting displays, the lower part of the organic layer is coupled to

the anode electrode and the upper part of the organic layer transmitting light is coupled to the cathode electrode.

[0017] In some organic light emitting displays, the cathode electrode includes a transfective layer (i.e., a layer that both transmits and reflects light), and in the case of the top emission type, a cathode electrode having a low work function and a transfective characteristic is frequently utilized.

[0018] Accordingly, the cathode electrode may be very thin to implement the transfective characteristic. In this case, the cathode electrode has a high resistance.

[0019] In particular, in the case where the organic light emitting display is driven with current, a voltage drop (i.e., an IR drop) occurs by resistance of a line and/or an electrode. Here, in the case of the top emission type organic light emitting display, the voltage drop is caused by the high resistance of the thin, transfective cathode electrode, making it difficult to properly implement the display.

[0020] In other words, a cathode electrode having a high resistance can result in a serious luminance non-uniformity in the inside of the panel and, particularly in large-area organic light emitting displays, a voltage drop (IR drop) occurs between the center portion and the outer portion of the panel, restricting current injection for high luminance emission in a high definition display.

SUMMARY OF THE INVENTION

[0021] An organic light emitting display and a method of fabricating the same, in which the resistance of a cathode electrode is reduced by applying a conductive ink material (preferably comprising silver or carbon nanotubes) on the cathode electrode, is provided in embodiments according to the present invention.

[0022] An organic light emitting display according to an exemplary embodiment of the present invention includes a pixel defining layer on a substrate, with a recess which is formed in (preferably completely within) a non-light emitting region of the pixel defining layer. An electrode on the substrate includes a portion within the recess of the pixel defining layer. A conductive ink is in the portion of the electrode in the recess.

[0023] Preferably, the non-light emitting region may be formed between a thin film transistor region of a pixel and a data line region. Furthermore, preferably, to provide for a deeper recess, and thus an increased amount of conductive ink, the recess may extend into a planarization layer under the pixel defining layer. Preferably, the electrode may be a transfective cathode electrode. Furthermore, preferably, the conductive ink may include or consist of a high-conductivity material. More preferably, the high-conductivity material comprises an electrical conductivity of more than $10 \times 10^6 \text{ S m}^{-1}$ (at 20°C), more preferably $30 \times 10^6 \text{ S m}^{-1}$ (at 20°C) and still more preferably $60 \times 10^6 \text{ S m}^{-1}$ (at 20°C). Preferably the conductive material (ink) fills at least 40% of the recess, more preferably least 70% of the recess, still more preferably least 85%

of the recess and more still preferably least 100% of the recess.

[0024] Preferably the plurality of pixels are arranged in a matrix. Preferably the cathode electrodes are formed in parallel lines extending over the whole pixel area of the display and the recesses are also formed in parallel lines extending over the whole pixel area of the display, the recess lines being adjacent to the cathode lines.

[0025] Another exemplary embodiment of the present invention is a method of fabricating an organic light emitting display. According to this embodiment, a pixel defining layer is formed on a substrate. A recess is formed in a non-light emitting region of the pixel defining layer. An electrode is formed on the pixel defining layer, the electrode having a portion in the recess. A conductive ink is placed on the portion of the electrode in the recess.

[0026] The non-light emitting region may preferably be between a thin film transistor region of a pixel and a data line region. Forming the recess may preferably include extending the recess into a planarization layer under the pixel defining layer. The electrode may preferably be a transfective cathode electrode. The conductive ink may preferably be placed in the recess by using an inkjet printing method, and may preferably include a solvent with a boiling point less than 100°C . Preferably the solvent comprises a material selected from the group consisting of alcohol, ether, benzene, ethyl acetate, chloroform, and combinations thereof.

[0027] Another exemplary embodiment of the present invention discloses a display device includes a plurality of light emitting regions and a plurality of non-light emitting regions. Each non-light emitting region is between adjacent ones of the light emitting regions. A cathode electrode is on the light emitting regions and the non-light emitting regions, the cathode electrode having a recess at at least one of the non-light emitting regions. A conductive ink is in the recess.

[0028] A pixel defining layer may preferably be under the cathode electrode, the pixel defining layer defining the plurality of pixels having the plurality of light emitting regions, and having a first depression corresponding to the recess, such that a portion of the cathode electrode is on an inner wall of the first depression. A planarization layer may preferably further be under the pixel defining layer, the planarization layer having a second depression corresponding to the recess, such that another portion of the cathode electrode is on an inner wall of the second depression.

[0029] It shall be understood, that the organic light emitting display according to the present invention may comprise a pixel such as described in Fig. 1 and paragraphs [0007]-[0014] in combination with the particular features disclosed in paragraphs [0021] ff., but the invention is not limited thereto. For example, also more sophisticated pixel circuits may be utilized having more transistors per pixel.

BRIEF DESCRIPTION OF THE DRAWINGS

[0030] The accompanying drawings, together with the specification, illustrate exemplary embodiments of the present invention, and, together with the description, serve to explain the principles of the present invention.

[0031] FIG. 1 is a circuit diagram illustrating a pixel in a conventional organic light emitting display;

[0032] FIGs. 2A and 2B are cross-sectional views of an organic light emitting display according to exemplary embodiments of the present invention;

[0033] FIGs. 3A to 3D are cross-sectional views illustrating a method of fabricating an organic light emitting display according to an exemplary embodiment of the present invention; and

[0034] FIGs. 4A to 4D are plan views of an organic light emitting display according to exemplary embodiments of the present invention.

DETAILED DESCRIPTION

[0035] In the following detailed description, only certain exemplary embodiments of the present invention are shown and described, simply by way of illustration. Here, when a first element is described as being coupled to a second element, the first element may be directly coupled to the second element, or may be indirectly coupled to the second element via a third element. Further, some of the elements that are not essential to obtain a complete understanding of the invention have been omitted for clarity. Like reference numerals refer to like elements throughout.

[0036] Hereinafter, exemplary embodiments of the present invention will be described in more detail with reference to the accompanying drawings.

[0037] FIGs. 2A and 2B are cross-sectional views of an organic light emitting display according to exemplary embodiments of the present invention.

[0038] FIG. 2A illustrates a pixel region including a storage capacitor region and a thin film transistor region, a conductive ink forming region, and a data line region in an organic light emitting display according to an exemplary embodiment of the present invention.

[0039] In order to reduce the resistance of a thin, transmissive cathode electrode 255, which has high resistance, a conductive ink forming region is provided between the thin film transistor region and the data line region. The conductive ink forming region is implemented in a concave shape so that a conductive ink 260 with a low resistance is placed on the cathode electrode 255 in the conductive ink forming region by an inkjet printing method.

[0040] In other words, the conductive ink forming region, which is a non-light emitting region, has a concave shape, formed by an etching of a pixel defining layer (PDL) 245 and/or a planarization layer 235 below the cathode electrode 255 (referring to FIG. 2B).

[0041] Thus, if a portion of the cathode electrode 255

is formed in the concave shaped portion (e.g., the depression or recess) of the pixel defining layer 245, the concave shape serves as a guide line for the conductive ink 260 to be applied later. The conductive ink 260 with low resistance is applied within the concave shaped portion, thereby reducing the resistance of the cathode electrode 255.

[0042] Referring to FIG. 2A, a buffer layer 205 is first formed on a substrate 200. The substrate 200 may be a substrate of glass, plastic, quartz, silicon, or metal, and the buffer layer 205 may be a silicon dioxide film (SiO_2), a silicon nitride film (SiN_x), a silicon oxynitride film (SiO_2N_x), or a multilayer thereof.

[0043] A semiconductor pattern 208 is formed on the buffer layer 205 and is formed in the thin film transistor region as shown in FIG. 2A. The semiconductor pattern 208 may be an amorphous silicon film (a-Si) or a polycrystalline silicon film (poly-Si) fabricated by crystallizing an amorphous silicon film. In one embodiment, the semiconductor pattern 208 is a polycrystalline silicon film having high charge mobility.

[0044] A gate insulating layer 210 is formed on the substrate 200 including the semiconductor layer 208. The gate insulating layer 210 may be a silicon dioxide film (SiO_2), a silicon nitride film (SiN_x), a silicon oxynitride film (SiO_2N_x), or a multilayer that includes two or more of these films and/or other suitable materials.

[0045] A first electrode 212 of a storage capacitor Cst is formed in the storage capacitor region on the gate insulating layer 210, and a gate electrode 214 overlapping the semiconductor layer 208 is formed in the thin film transistor region. At this time, scan lines (not shown) may concurrently be formed with the formation of the first electrode 212 and the gate electrode 214.

[0046] Then, source/drain regions 208a and 208b are formed in a region not overlapped with the gate electrode 214 on the semiconductor layer 208, by implanting conductive impurities (n- ion or p+ ion) in the semiconductor layer 208 of the thin film transistor region using the gate electrode 214 as a mask. At this time, a channel region 208c is defined between the source/drain regions 208a/208b.

[0047] Next, an interlayer insulating layer 220 is formed on the substrate on the first electrode 212 of the storage capacitor Cst and the gate electrode 214, and one or more contact holes are formed within the interlayer insulating layer 220 and the gate insulating layer 210 to expose the semiconductor layer 208.

[0048] A conductive layer is stacked and patterned on the substrate in the region of the contact holes so that source/drain electrodes 221 and 223 are formed in the thin film transistor region. A data line 225 is formed in a data line region and a second electrode 229 of the storage capacitor Cst is formed in the region overlapping the first electrode 212 of the storage capacitor Cst in the storage capacitor region.

[0049] Here, the semiconductor layer 208, the gate insulating layer 210, the gate electrode 214, the source

electrode 221, and the drain electrode 223, which are in the thin film transistor region, form the thin film transistor. The thin film transistor may be a driving transistor such as M2 in FIG. 1 for generating a driving current within the pixel, or in alternate embodiments, the thin film transistor may be a light emission control transistor (not illustrated in FIG. 1) for supplying the driving current to an EL element.

[0050] A stacked structure including the first electrode 212, the interlayer insulating layer 220, and the second electrode 229, which are in the storage capacitor region, forms the storage capacitor Cst illustrated in FIG. 1.

[0051] Thereafter, a passivation layer 230 is formed on the substrate 200 including the source electrode 221, the drain electrode 223, the data line 225, and the second electrode 229 patterned with the conductive layer. The passivation layer 230 may be formed of a silicon dioxide film (SiO_2), a silicon nitride film (SiN_x), or a multilayer including these two films and/or other suitable materials. In one embodiment, the passivation layer 230 is the silicon nitride film (SiN_x) that can protect the thin film transistor below it by effectively shielding gas and moisture, and can protect an incomplete binding that occurs at a grain boundary of the polycrystalline silicon film containing rich hydrogen.

[0052] A first via hole 232 is formed through the passivation layer 230 to expose the drain electrode 223 of the thin film transistor, and a planarization layer 235 for smoothing out any steps in lower layers is formed on the passivation layer 230. The planarization layer 235 may be formed of an organic layer such as a benzocyclobutene (BCB) film, a polyimide film, or a polyacrylic film. A second via hole 237 is formed within the thin film transistor region through the planarization layer 235 to expose the drain electrode 223 of the thin film transistor.

[0053] A first electrode 240 is formed in the storage capacitor region and the thin film transistor region, on the planarization layer 235. The first electrode 240 is coupled to the drain electrode 223 exposed by the first via hole 232 and the second via hole 237 in the thin film transistor region.

[0054] Because the pixel illustrated in FIG. 2A is a top emission type, the first electrode 240 serves as the anode electrode. The first electrode 240 may be formed of a reflective, conductive layer. The light reflective, conductive layer may include materials with a high work function such as Ag, Al, Ni, Pt, and/or Pd, or an alloy layer of two or more of these materials. In alternate embodiments, the light reflective, conductive layer may include materials with a low work function such as Mg, Ca, Al, Ag, and/or Ba, or an alloy layer thereof. In yet other embodiments, a reflective layer 242 is formed first, followed by forming the first electrode 240, such that the first electrode 240 is a light transmitting conductive layer formed on the reflective film 242. In this embodiment, the light transmitting conductive layer may be an indium tin oxide (ITO) film or an indium zinc oxide (IZO). In certain embodiments, the reflective film pattern 242 has a reflectivity above 60%.

The reflective film pattern 242 may be aluminum (Al), an aluminum alloy, silver (Ag), a silver alloy, or an alloy layer of one or more of these materials. Such a reflective pattern 242 may be formed to be spaced at an interval, e.g., a predetermined interval, from the via hole 237.

[0055] The first electrode 240 may be formed of an ITO film or an IZO film, and may be formed of Mg, Ca, Al, Ag, and Ba or an alloy layer of two or more of these materials that is thin enough to transmit light, i.e. the thickness of the first electrode 240 is adapted such that the first electrode 240 has a transmittance of more than 50% for light of a wavelength of 500nm, more preferably a transmittance of more than 70% and still more preferably a transmittance of more than 90%. Preferably, the thickness of the first electrode 240 is less than 3 μm , preferably less than 1 μm , preferably less than 500 nm, preferably less than 200 nm, preferably less than 100 nm, preferably less than 50 nm, preferably less than 30 nm, preferably less than 20 nm and still more preferably less than 10 nm.

[0056] Next, a pixel defining layer (PDL) 245 is formed on the first electrode 240. The pixel defining layer 245 may be formed, for example, of benzocyclobutene (BCB), acyclic based photoresist, phenol based photoresist, or imide based photoresist.

[0057] In one embodiment, the conductive ink forming region and an opening part 247 are defined in the pixel defining layer 245. According to this embodiment, a recess 249 is made by etching or otherwise forming a depression or an opening in the pixel defining layer 245 between the thin film transistor region and the data line region (part of the non-light emitting region). The opening part 247 is similarly formed by etching or otherwise forming an opening in the pixel defining layer 245 to expose at least a portion of the first electrode 240 in a light emitting region of the pixel.

[0058] The recess 249 is in the conductive ink forming region, where the conductive ink is to be placed, as described later. The recess 249 may extend only through the pixel defining layer 245, or, in another embodiment as illustrated in FIG. 2B, the recess 249' may extend at least partially through the planarization layer 235 in so that it has a deeper valley in a concave shape.

[0059] The recess 249 or 249' serves as a guide line for the conductive ink 260 to be applied later, and the conductive ink 260 with low resistance is applied to the region so that the resistance of the cathode electrode is reduced.

[0060] An organic light emitting layer 250 is formed on the first electrode 240 exposed within the opening part 247. The organic light emitting layer 250 may be formed, for example, using a vapor deposition method, an ink jet printing method, or a laser thermal transfer method. Furthermore, a hole injection layer, a hole transportation layer, a hole suppression layer, an electron transportation layer, or an electron injection layer may be formed on or under the organic light emitting layer 250.

[0061] The second electrode 255 is formed on the entire surface of the substrate including the organic light

emitting layer 250 and the pixel defining layer 245. The second electrode 255 serves as a cathode electrode and, in one embodiment, is formed of a transfective conductive layer. The transfective conductive layer may be an ITO film or an IZO film, and may include Mg, Ca, Al, Ag, Ba, or an alloy layer of two or more of these materials that is thin enough to transmit light, i.e. the thickness of the second electrode 255 is adapted such that the second electrode 255 has a transmittance of more than 50% for light of a wavelength of 500nm, more preferably a transmittance of more than 70% and still more preferably a transmittance of more than 90%. Preferably, the thickness of the second electrode 255 is less than 3 μ m, preferably less than 1 μ m, preferably less than 500 nm, preferably less than 200 nm, preferably less than 100 nm, preferably less than 50 nm, preferably less than 30 nm, preferably less than 20 nm and still more preferably less than 10 nm.

[0062] Because the second electrode 255 is formed on the entire surface of the substrate, it is formed in the recess 249 as well. That is, a portion of the second electrode 255 is on an inner wall of the depression or recess 249.

[0063] In the case of the top emission display, the second electrode 255 (the cathode electrode in this embodiment) is made of a material having a low work function and is thin enough to exhibit a transfective characteristic. Without any structure to reduce the resistance, this cathode electrode 255 exhibits a high resistance.

[0064] To reduce the resistance of the cathode electrode 255, an exemplary embodiment of the present invention further utilizes the recess 249 having a concave shape in the non-light emitting region as a conductive ink forming region. A conductive ink 260 with a low resistance is applied on the upper side of the cathode electrode 255 by an inkjet printing method.

[0065] In the embodiments shown in FIGs. 2A-2B, the conductive ink forming region is shown as formed between the thin film transistor region and the data line region for illustrative purposes only, and the present invention is not limited thereto. In alternative embodiments, the conductive ink forming region may be formed in the non-light emitting region overlapping a portion or all of the data line region.

[0066] FIGs. 3A to 3D are cross-sectional views showing a process of fabricating an organic light emitting display according to an exemplary embodiment of the present invention.

[0067] In this embodiment, the conductive ink forming region is formed in the non-light emitting region between the thin film transistor region and the data line region, as described above. Accordingly, for convenience of explanation, FIGs. 3A-3D only illustrate a fabrication process in the conductive ink forming region.

[0068] First, as shown in FIG. 3A, a gate insulating layer 310, an interlayer insulating layer 320, a passivation layer 330, a planarization layer 335, and a pixel defining layer 345 are sequentially formed on a first substrate 300

having a buffer layer 305 formed thereon, similar to the embodiment illustrated in FIG. 2A.

[0069] The planarization layer 335 may be formed of a benzocyclobutene (BCB) layer, a polyimide layer, or a polyacyclic layer, and the pixel defining layer 345 may be formed of a benzocyclobutene (BCB), acyclic based photoresist, phenol-based photoresist, or imide-based photoresist.

[0070] Next, referring to FIG. 3B, an etch is performed on the pixel defining layer 345 to form a recess 349 in a portion of the pixel defining layer 345.

[0071] The region where the recess 349 is formed is where the conductive ink will be placed, as described later. At this time, the recess 349 may extend more deeply into the planarization layer 335 in addition to the pixel defining layer 345, creating a deeper valley in a concave shape.

[0072] In other words, the recess 349 serves as a guide line for the conductive ink 360 to be applied later and the conductive ink 360 with a low resistance is applied to the region to reduce the resistance of the cathode electrode.

[0073] Then, as shown in FIG. 3C, the second electrode 355 as the cathode electrode is formed on the substrate 300 including the recess 349.

[0074] In the case of the top emission display, the second electrode 355 (the cathode electrode in this embodiment) is made of a material having a low work function and is thin enough to exhibit a transfective characteristic. Without any structure to reduce the resistance, this cathode electrode 355 exhibits a high resistance.

[0075] To reduce the resistance of the cathode electrode 355, as shown in FIG. 3D, an embodiment of the present invention places a conductive ink 360 with a low resistance on the cathode electrode 355 in the region formed with a valley in a concave shape by the recess 349 by utilizing an inkjet printing method.

[0076] According to an exemplary embodiment, the conductive ink contains high-conductivity materials, for example, Ag or carbon nanotubes. A solvent in the conductive ink may be a material with a boiling point less than 100°C, such as alcohol, ether, benzene, ethyl acetate, chloroform, etc. In this way, further wiring may be formed through low temperature firing after applying the conductive ink.

[0077] FIGs. 4A to 4D are plan views of an organic light emitting display according to exemplary embodiments of the present invention illustrating various configurations of the conductive ink forming regions. In each of the embodiments illustrated in FIGs. 4A to 4D, the conductive ink is placed in a recess to reduce the resistance of a surface electrode.

[0078] FIG. 4A illustrates one embodiment having conductive ink forming regions extending as continuous segments along a plurality of pixels. In this embodiment, the conductive ink forming regions extend vertically across the display region. As in the embodiments illustrated in FIGs. 2A and 2B, the conductive ink forming regions are placed between the pixels and the data line regions, with-

out overlapping the data line regions.

[0079] FIG. 4B illustrates an embodiment having conductive ink forming regions extending as continuous segments along a plurality of pixels. Unlike the embodiment illustrated in FIG. 4A, here the conductive ink forming regions are shown as partially overlapping the data line regions. One skilled in the art will comprehend that alternate embodiments are possible that are still within the scope of this invention. For example, the conductive ink forming regions may completely overlap with the data line regions, or the conductive ink forming regions may extend horizontally or in any other suitable direction.

[0080] FIG. 4C illustrates an embodiment having conductive ink forming regions as short segments that each extend vertically only partially across the length of a single pixel. Here, the conductive ink forming regions are located near the crossings of the data lines and the scan lines, but not overlapping with the data line regions.

[0081] FIG. 4D illustrates an embodiment having conductive ink forming regions as short segments that each extend vertically only partially across the length of a single pixel. Unlike the embodiment illustrated in FIG. 4C, here the conductive ink forming regions are shown partially overlapping the data line regions. One skilled in the art will comprehend that alternate embodiments are possible that are still within the scope of this invention. For example, the conductive ink forming regions may completely overlap with the data line regions, or the conductive ink forming regions may be placed in essentially any non-light emitting region on the surface electrode.

[0082] While the present invention has been described in connection with certain exemplary embodiments, it is to be understood that the invention is not limited to the disclosed embodiments, but, on the contrary, is intended to cover various modifications and equivalent arrangements included within the scope of the appended claims, and equivalent thereof.

Claims

1. An organic light emitting display comprising:

a plurality of pixels formed on a substrate (200), each pixel comprising a first electrode (240), an organic light emitting layer (250) and a second electrode (255);

wherein the organic light emitting display comprises a plurality of light emitting regions; a plurality of non-light emitting regions, each non-light emitting region being arranged between adjacent ones of the light emitting regions;

wherein each pixel comprises:

a pixel defining layer (245),
a recess (249) formed at a non-light emitting region of the pixel defining layer (245),

wherein the second electrode (255) includes a portion extending along the recess (249) of the pixel defining layer (249), and
a conductive material (260) is formed in the portion of the second electrode (255) in the recess (249).

2. The organic light emitting display according to claim 1, wherein the organic light emitting display comprises a plurality of data lines (Dm) and a plurality of scan lines (Sn), the data lines (Dm) and the scan lines (Sn) defining the pixel areas in portions where the data lines (Dm) and the scan lines (Sn) cross each other, the data lines (Dm) and the scan lines (Sn) being adapted to control light emission of the organic light emitting diodes (OLED), wherein each pixel (4) further comprises a pixel circuit (2) coupled to the respective organic light emitting diode (OLED).
3. The organic light emitting display according to claim 2, wherein the pixel circuit (2) includes a second transistor (M2) coupled between a first power supply (ELVDD) and the organic light emitting diode (OLED); a first transistor (M1) coupled between the second transistor (M2) and the data line (Dm), and coupled to the scan line (Sn) to receive a scan signal; and a storage capacitor (Cst) coupled between a first electrode (240) and a gate electrode of the second transistor (M2).
4. The organic light emitting display according to claim 3, wherein the non-light emitting regions of the pixels (4) are located between a region of the second transistor (M2) and a data line (Dm) region.
5. The organic light emitting display according to one of the preceding claims, wherein the recess (249) extends into a planarization layer (235) which is located under the pixel defining layer (255).
6. The organic light emitting display according to one of the preceding claims, wherein the second electrode (255) is a cathode electrode comprising a transfective layer and/or wherein the first electrode (240) is an anode electrode.
7. The organic light emitting display according to one of the preceding claims, wherein the conductive material (260) is a dried conductive ink comprising a high-conductivity material.
8. The organic light emitting display according to one of the preceding claims, wherein the plurality of pixels (4) are arranged in a matrix.
9. A method of fabricating an organic light emitting display comprising:

forming a plurality of pixels formed on a substrate (200), each pixel comprising a first electrode (240), an organic light emitting layer (250) and a second electrode (255),

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wherein the organic light emitting display comprises a plurality of light emitting regions where an organic light emitting layer (250) is formed such to directly contact a pair of first and second electrodes (240, 255); a plurality of non-light emitting regions where no organic light emitting layer (250) is formed such to directly contact a pair of first and second electrodes (240, 255), each non-light emitting region being arranged between adjacent ones of the light emitting regions;

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further comprising:

forming a pixel defining layer (245);

forming a recess (249) in a non-light emitting region of the pixel defining layer (245);

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forming the second electrode (255) such that it extends along a light emitting region and along a portion in the recess (249); and

placing a conductive ink (260) on the portion of the electrode (255) in the recess (260).

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10. The method of fabricating an organic light emitting display according to claim 9, wherein the second electrode (255) is formed such that it completely extends along a light emitting region and such that it completely extends along the recess (249) which is adjacent to the light emitting region. 30
11. The method of fabricating an organic light emitting display according to one of claims 9 and 10, wherein the non-light emitting region is formed between a thin film transistor region of a pixel and a data line region. 35
12. The method of fabricating an organic light emitting display according to one of claims 9 - 11, wherein forming the recess (249) further comprises extending the recess (249) into a planarization layer (235) under the pixel defining layer (245). 40
13. The method of fabricating an organic light emitting display according to one of claims 9 - 12, wherein the second electrode (255) is formed to be a cathode electrode comprising a transfective layer and/or wherein the step of placing the conductive ink (260) comprises inkjet printing. 45
14. The method of fabricating an organic light emitting display according to claim 13, wherein inkjet printing utilizes a solvent comprising a material with a boiling point less than 100°C. 50
15. The method of fabricating an organic light emitting 55

display according to claim 14, wherein the solvent comprises a material selected from the group consisting of alcohol, ether, benzene, ethyl acetate, chloroform, and combinations thereof.

FIG. 1
(PRIOR ART)

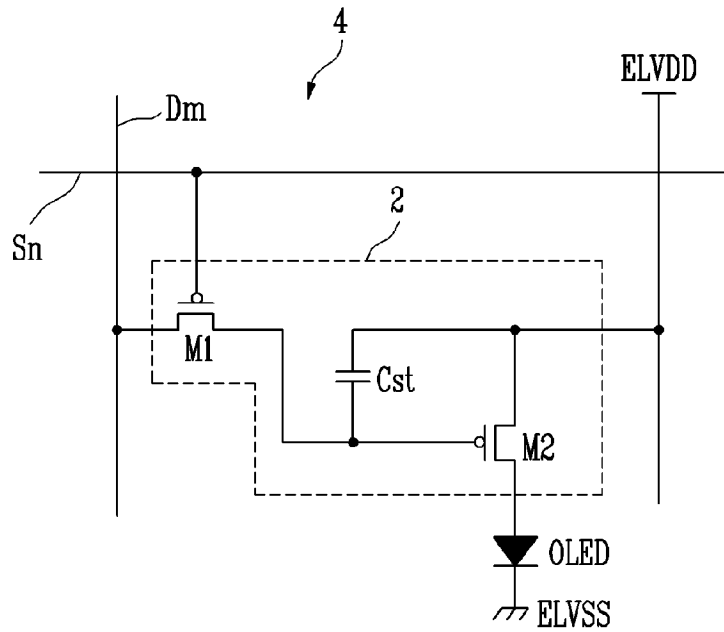


FIG. 2A

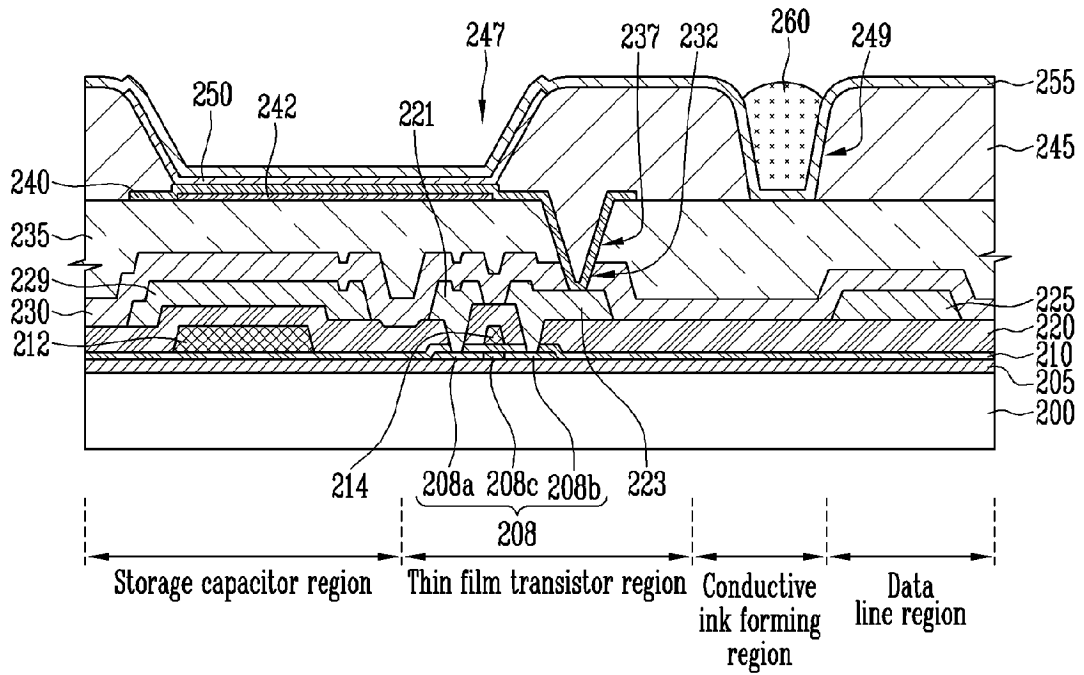


FIG. 2B

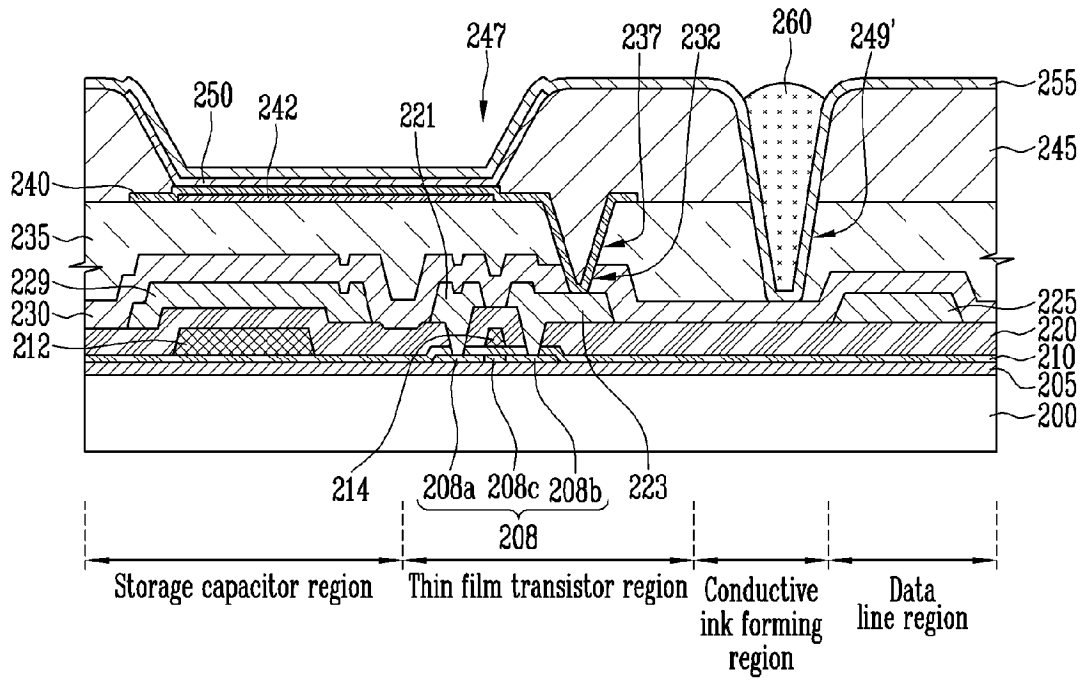


FIG. 3A

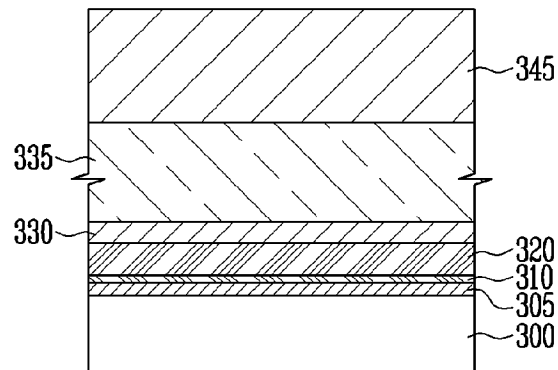


FIG. 3B

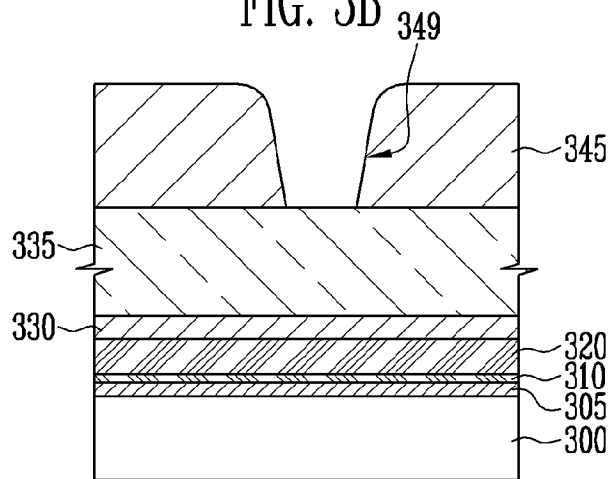


FIG. 3C

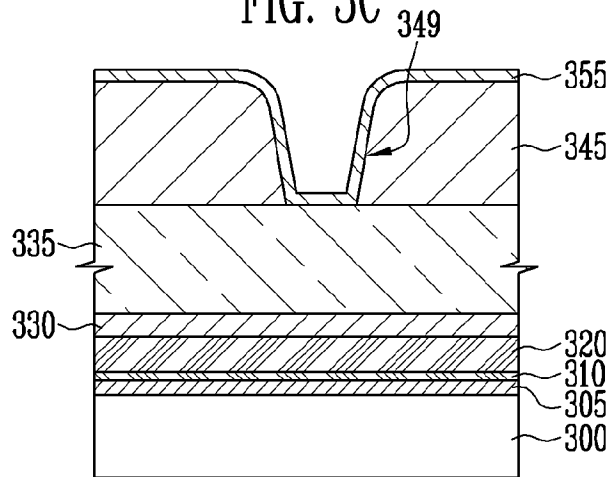


FIG. 3D

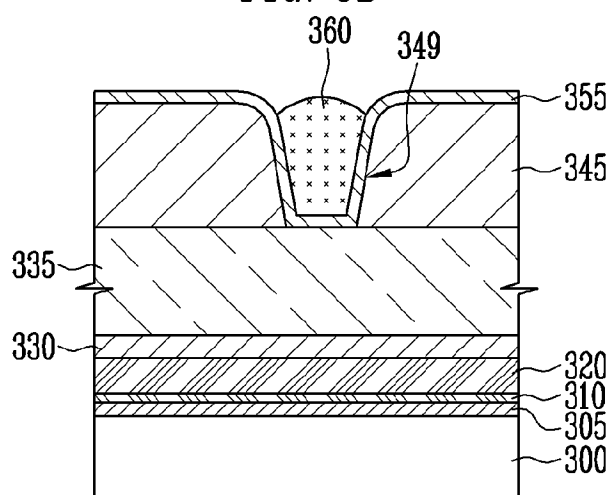


FIG. 4A

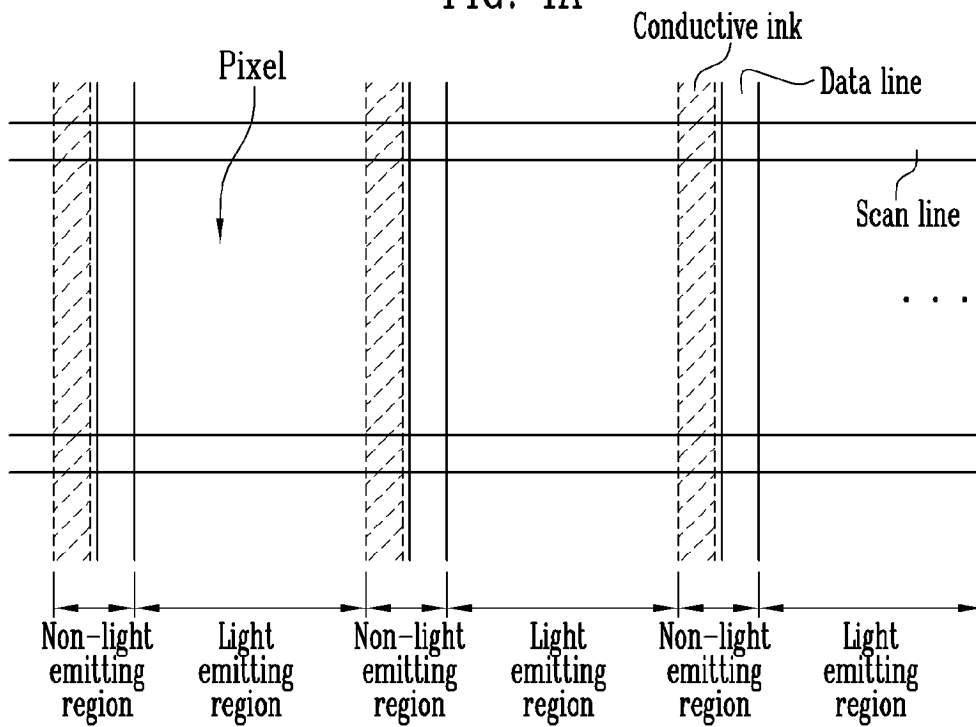


FIG. 4B

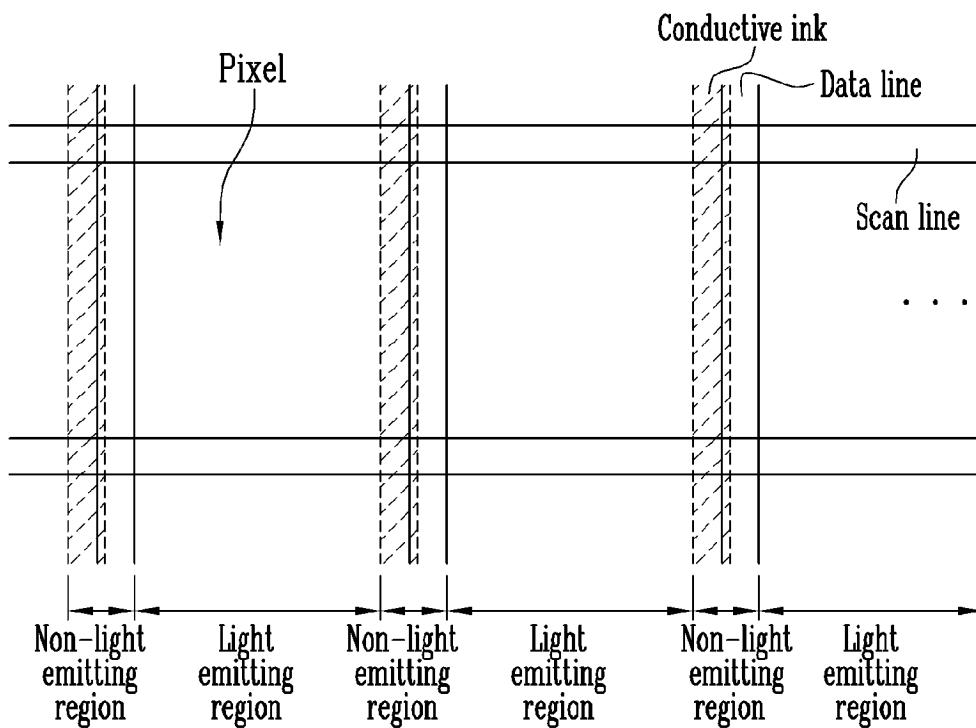


FIG. 4C

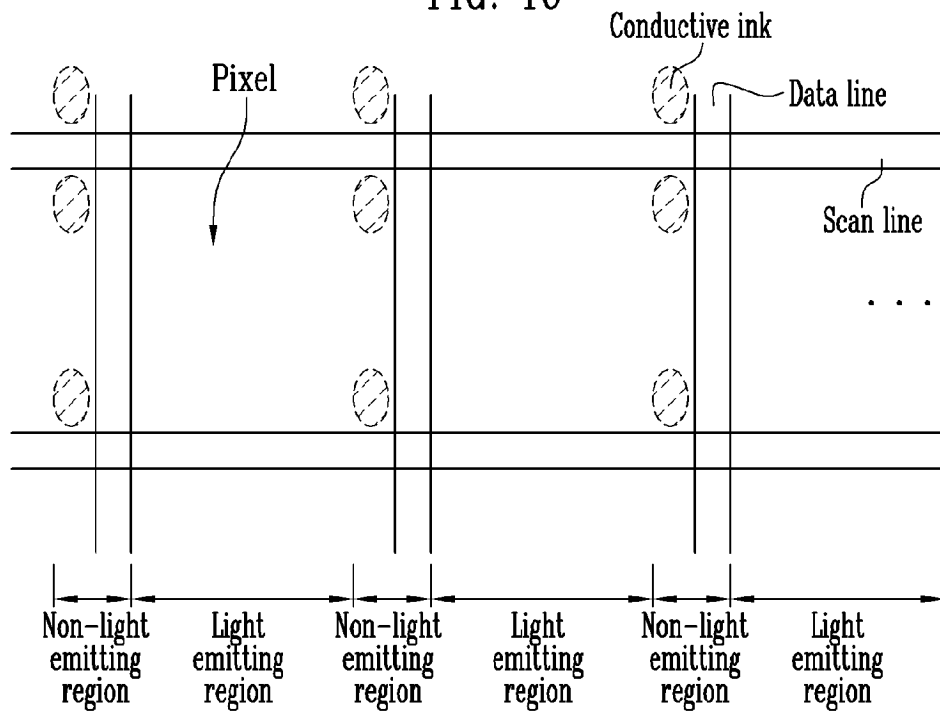
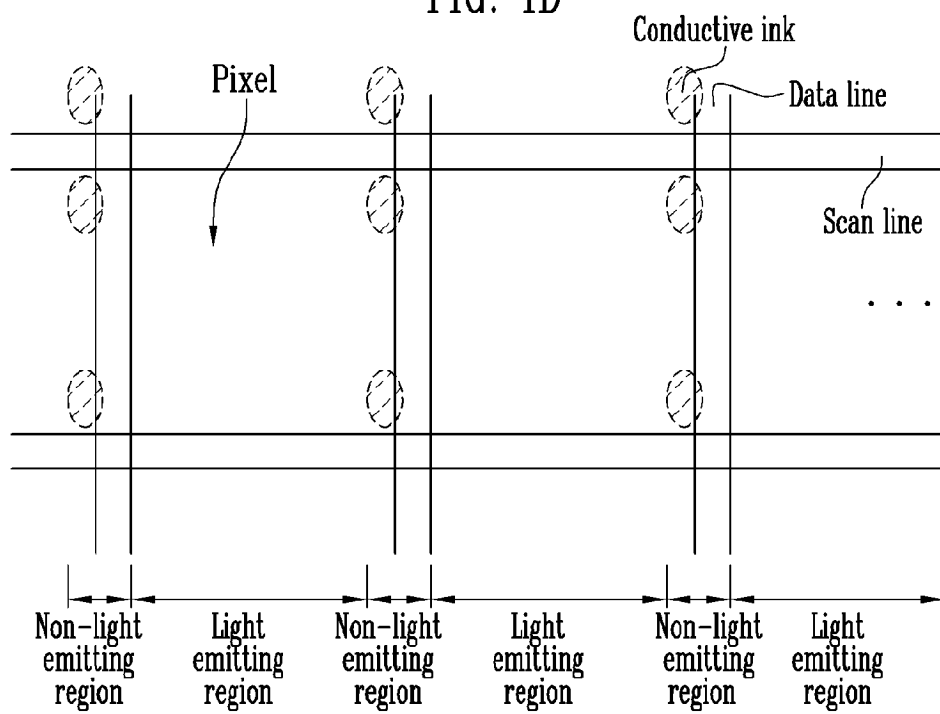


FIG. 4D





EUROPEAN SEARCH REPORT

Application Number
EP 09 16 4876

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
X	JP 2007 108469 A (SEIKO EPSON CORP) 26 April 2007 (2007-04-26)	1-6,8	INV. H01L27/32
Y	* the whole document * * figure 4 * * figures 1,6-8,10 *	7,9-15	
Y	US 2005/285987 A1 (AZUMADA KYOKO [JP] ET AL) 29 December 2005 (2005-12-29) * paragraphs [0003], [0004], [0007], [0014], [0025] - [0029] * * paragraphs [0066] - [0084] * * paragraphs [0103] - [0139] * * figures 1,3,8,9,11 *	1-15	TECHNICAL FIELDS SEARCHED (IPC) H01L
Y	US 2007/194699 A1 (LEE SANG-PIL [KR] ET AL) 23 August 2007 (2007-08-23) * paragraphs [0053] - [0091]; figures 1-5 *	1-15	
Y	US 2004/262613 A1 (MAEKAWA SHINJI [JP] ET AL) 30 December 2004 (2004-12-30) * paragraphs [0128] - [0131]; figure 4 *	7,9-15	H01L
X	EP 1 505 666 A (LG ELECTRONICS INC [KR]) 9 February 2005 (2005-02-09) * figures 2,3G,5F *	1,6,8	
X	WO 2007/148540 A (SONY CORP [JP]; FUJIOKA HIROFUMI [JP]; OZAWA NOBUO [JP]) 27 December 2007 (2007-12-27) * figure 2 *	1,5,6,8	
The present search report has been drawn up for all claims			
Place of search Munich		Date of completion of the search 7 October 2009	Examiner Bernabé Prieto, A
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

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EPO FORM 1503 03 82 (P04C01)

**ANNEX TO THE EUROPEAN SEARCH REPORT
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The members are as contained in the European Patent Office EDP file on
The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

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专利名称(译)	有机发光显示器及其制造方法		
公开(公告)号	EP2144291A1	公开(公告)日	2010-01-13
申请号	EP2009164876	申请日	2009-07-08
[标]申请(专利权)人(译)	三星显示有限公司		
申请(专利权)人(译)	三星移动显示器有限公司.		
当前申请(专利权)人(译)	三星移动显示器有限公司.		
[标]发明人	SONG MYUNG WON		
发明人	SONG, MYUNG-WON		
IPC分类号	H01L27/32 H01L51/00		
CPC分类号	H01L27/3276 H01L51/0005 H01L51/0022 H01L21/02288 H01L21/7684 H01L23/4828 H01L27/3246		
优先权	61/079023 2008-07-08 US 12/401465 2009-03-10 US		
其他公开文献	EP2144291B1		
外部链接	Espacenet		

摘要(译)

一种有机发光显示器及其制造方法，其中通过在阴极上施加导电油墨材料来降低阴极电阻。像素限定层形成在基板上，在像素限定层的非发光区域中具有凹陷或凹陷。基板上的电极包括像素限定层的凹陷内的部分。导电墨水位于凹槽中的电极部分中。

