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(54) **Organic light emitting display**

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**Description**

## BACKGROUND

5 Field of the Invention

**[0001]** The field relates to an organic light emitting display.

10 Description of the Related Technology

**[0002]** An organic light emitting display has beneficial aspects of being thin, having a wide viewing angle and high speed. The organic light emitting display can control the brightness of each pixel and display an image by controlling the amount of current which flows through an organic light emitting diode (OLED). In the display, once a current corresponding to a data is supplied to an organic light emitting diode, the organic light emitting diode emits light corresponding to the current supplied. The data applied to the organic light emitting diode has a quantized grey scale value within a predetermined range in order to express a grey scale.

**[0003]** When a thin film transistor which has amorphous silicon (a-Si) is used as a driving transistor, it has a weakness in that current driving ability can be relatively low. However, it also has advantages in that the uniformity of the display device is excellent, and it is more suitable for being manufactured in a large size display. The uniformity of the luminance of the display panel can be low because a driving transistor of the respective pixel circuits of the organic light emitting display can have different threshold voltages from one another. Furthermore, one portion of the panel may be brighter than another because IR-drop occurs in a power supply line (VDD) connecting the respective pixel circuits one another. Moreover, in case that the pixel circuit of the organic light emitting display includes many transistors, it is difficult to achieve high resolution of the panel because high integration becomes impossible. In the case of conventional circuits for compensating for the threshold voltage of a driving transistor in the pixel circuit, a path from a control electrode of the driving transistor to a negative power supply is formed, and then a leakage current can flow through the path. Consequently, it can cause an improper emission of the organic light emitting diode.

**[0004]** In addition, in case that RGB data signals are applied to the pixel circuits using a demux, if the emission control signals applied through the emission control line coupled to the pixel circuits are turned off, the RGB data signals can be stored in a storage capacitor of the pixel circuit improperly. When RGB data signals (voltages) are applied continuously by driving the RGB data signals (voltages) to the storage capacitors not yet initialized, accurate RGB data signals (voltages) cannot be stored in the storage capacitors properly.

**[0005]** In the case of a color organic light emitting display, a color display can be accomplished by including the display device with an organic light emitting diode which emits light of three colors of red, green and blue. However, the materials used as an organic light emission layer can be degraded by the heat generated during emission. Because of the degradation, the luminance of the organic light emitting diode can deteriorate. As a result, the life span of the organic light emitting diode can be decreased. Because the degree of the degradation of an organic light emission layer which forms a red, green and blue organic light emission layer differs from one another, the difference of the luminance of the red, green and blue organic light emission layer can become larger as time goes by. Accordingly, the desired color cannot be reproduced accurately because transition of the color data occurs as the white balance is changed compared with the initial value. Because each emission layer corresponding to red, green and blue color has a different life span from one another, it is difficult to maintain the white balance when the emission layer is driven for a long time.

**[0006]** US2004004443 discloses an organic electro-luminescence device that includes column lines, row lines crossing the column line and cells formed at pixel areas defined between the column lines and the row lines. A first switching device is provided for controlling a current applied to the cell in response to data voltages applied from the column lines. A second switching device connected in parallel with the first switching device to the cell controls a current applied to the cell in response to the data voltages. A third switching device applies the data voltages from the column lines to the first and second switching devices in response to scan voltages applied from the row lines.

**[0007]** EP1655719 discloses an organic light emitting display that includes: a scan driver for supplying a plurality of first scan signals at substantially a same time to a plurality of scan lines in a first period of one frame and for supplying a plurality of second scan signals in sequence to the scan lines in a second period of the one frame.

**[0008]** WO2004066249 discloses an active matrix display device that uses an amorphous silicon drive transistor for driving a current through an LED display element. First and second capacitors are connected in series between the gate and source of the drive transistor, with a data input to the pixel provided to the junction between the first and second capacitors.

**[0009]** EP1531450 discloses a pixel circuit of a display device for emitting a certain color during a display time period, the pixel circuit including at least two light emitting elements, each said light emitting element for emitting a corresponding one of colors during the display time period. An active element is commonly connected to the at least two light emitting

elements to drive the at least two light emitting elements in response to at least one emission control signal.

[0010] US2006151745 discloses an organic light emitting display in which a scan driver sequentially supplies a scan signal to a scan line during a second period of one horizontal period. A data driver includes a plurality of output lines, for supplying a plurality of data signals to the respective output lines during the second period. Demultiplexers are installed at the respective output lines, and include a plurality of data transistors for supplying the data signals to the output lines during the second period, to a plurality of data lines.

SUMMARY OF CERTAIN INVENTIVE ASPECTS

[0011] One aspect is an organic light emitting display, including a scan line, a data line, and a pixel coupled to the scan line and the data line. The pixel includes a first switching transistor including a control electrode coupled to the scan line, and a first electrode coupled to the data line. The pixel also includes a driving transistor coupled between a first power supply line and a second power supply line, the driving transistor including a control electrode coupled to the first switching transistors. The pixel also includes a first storage capacitor connected to the first switching transistor, the first power supply line and the driving transistor. The pixel also includes a second switching transistor coupled between the first power supply line and the driving transistor, the second switching transistor including a control electrode coupled to an emission control line. The pixel also includes a second storage capacitor connected to the first switching transistor, the first storage capacitor, the second switching transistor and the driving transistor, and an organic light emitting diode coupled between the driving transistor and the second power supply line.

[0012] Another aspect is an organic light emitting display, including a scan line, a data line, and a pixel coupled to the scan line and the data line, the pixel configured to at least partially compensate for transistor threshold variation and for IR-drop in a power supply line, where the pixel includes no more than three transistors.

[0013] The invention provides an organic light emitting display as set out in Claim 1. Preferred features of the invention are set out in Claims 2 to 19.

BRIEF DESCRIPTION OF THE DRAWINGS

[0014]

- FIG.1 is a block diagram of the basic structure of an organic light emitting diode display;
- FIG.2 is a circuit diagram depicting a pixel circuit according to an embodiment of the organic light emitting diode display;
- FIG.3 is a driving timing diagram of the pixel circuit shown in FIG.2;
- FIG.4 is a drawing depicting how a current flows through the pixel circuit shown in FIG.2 during the data writing period (T1);
- FIG.5 is a drawing depicting how a current flows through the pixel circuit shown in FIG.2 during the period for storing the threshold voltage of a driving transistor (T2);
- FIG.6 is a drawing depicting how a current flows through the pixel circuit shown in FIG.2 during the emission period (T3);
- FIG.7 is a drawing depicting how RGB pixel circuits and a demux are coupled according to an embodiment;
- FIG.8 is a driving timing diagram according to an embodiment of the RGB circuits shown in FIG.7;
- FIG.9 is a driving timing diagram according to an embodiment of the RGB circuits shown in FIG.7;
- FIG.10 is a drawing depicting how RGB pixel circuits and a demux are coupled according to an embodiment; and
- FIG.11 is a driving timing diagram of the RGB pixel circuits shown in FIG.10.

DETAILED DESCRIPTION OF CERTAIN INVENTIVE EMBODIMENTS

[0015] Referring to FIG.1, an organic light emitting display is depicted as a block diagram.

[0016] As shown in FIG.1, an organic light emitting display 100 includes a scan driver 110; a data driver 120; an emission control driver 130; an organic light emitting display panel 140 (hereinafter, referred to as panel 140); a first power supply 150; and a second power supply 160.

[0017] The scan driver 110 can supply the panel 140 with a scan signal through a plurality of scan lines (S[1],...,S[N]) in sequence.

[0018] The data driver 120 can supply the panel 140 with a data signal through a plurality of data lines (D[1],...,D[M]).

[0019] The emission control driver 130 can supply the panel 140 with an emission control signal through a plurality of emission control lines (EM[1],...,EM[N]) in sequence.

[0020] In addition, the panel 140 includes a plurality of scan lines (S[1],...,S[N]) arranged in a row direction, a plurality of emission control lines (EM[1],...,EM[M]) arranged in a row column direction, a plurality of data lines (D[1],...,D[M])

arranged in a column direction, and a pixel circuit (142, Pixel) which is defined by the scan lines (S[1],...,S[N]), the emission control lines (EM[1],...,EM[N]) and the data lines (D[1],...,D[M]).

**[0021]** Here, the pixel circuit (140, Pixel) can be formed at the pixel region which is defined by the scan lines and the data lines. As described above, the scan lines (S[1],...,S[N]) can be supplied with a scan signal from the scan driver 110, and the data lines (D[1],...,D[M]) can be supplied with a data signal from the data driver 120, and the emission control signal line (EM[1],...,EM[N]) can be supplied with an emission control signal from the emission control driver 130.

**[0022]** The first power supply 150 and the second power supply 160 supply each pixel circuit 142 placed at the panel 140 with a first power supply voltage and a second power supply voltage.

**[0023]** As shown in FIG.1, the scan driver 110, the data driver 120, the emission control driver 130, the panel 140, the first power supply 150 and the second power supply voltage driver 140 are formed on one substrate 102.

**[0024]** Particularly, the drivers and power supply voltage suppliers 110, 120, 130, 150 and 160 are formed on the same layer as the layer on which the scan lines (S[1],...,S[N]), the data lines (D[1],...,D[M]), the emission control lines (EM[1],...,EM[N]), and a transistor (not shown in drawings) of the pixel circuit 142 are formed. Of course, the drivers and the power supply voltage suppliers 110, 120, 130, 150 and 160 can be formed on another substrate (not shown in drawings), which can be coupled to the substrate 102. Furthermore, the drivers and the power supply voltage suppliers 110, 120, 130, 150 and 160 can be formed in a form such as TCP (Tape Carrier Package), FPC (Flexible Printed Circuit), TAB (Tape Automatic Bonding), COG (Chip On Glass), and the equivalent thereof, which couple the drivers and the suppliers to the substrate 102. However, the form and the location of the drivers and the suppliers 110, 120, 130, 150 and 160 are not limited.

**[0025]** Referring to FIG.2, a circuit diagram of a pixel circuit according to one embodiment of the organic light emitting display is depicted. A pixel circuit which will be described in the following means the pixel circuit formed on the panel 140 shown in FIG.1.

**[0026]** As shown in FIG.2, the pixel circuit of the organic light emitting display includes a scan line (S[N]); a data line (D[M]); an emission control line (EM[N]); a first power supply line (VDD); a second power supply line (VSS); a first switching transistor (SW\_TR1); a second switching transistor (SW\_TR2); a driving transistor (DR\_TR); a first storage capacitor (C1); a second storage capacitor (C2); and an organic light emitting diode (OLED).

**[0027]** The scan line (S[N]) supplies a control electrode of the first switching transistor (SW\_TR1) with a scan signal which selects an organic light emitting diode (OLED) which will emit light. The scan line (S[N]) is coupled to the scan driver 110 (referring to FIG.1) which generates a scan signal.

**[0028]** The data line (D[M]) supplies a second electrode of the first storage capacitor, a second electrode of the second storage capacitor, and a control electrode of the driving transistor (DR\_TR) with a data signal (voltage) which is in proportion to the luminance. The data line (D[M]) is coupled to the data driver 120 (referring to FIG.1) which generates a data signal.

**[0029]** The emission control line (EM[N]) supplies a control electrode of the second switching transistor (SW\_TR2) with an emission control signal as it is coupled to the control electrode of the second switching transistor (SW\_TR2). Once the second switching transistor (SW\_TR2) is turned on by the emission control signal, a first power supply voltage from the first power supply line (VDD) can be applied to a first electrode of the first storage capacitor (C1), a first electrode of the second storage capacitor (C2) and a first electrode of the first driving transistor (DR\_TR). The emission control line (EM[N]) is coupled to the emission control driver 130 (referring to FIG.1) which generates an emission control signal.

**[0030]** The first power supply line (VDD) supplies the organic light emitting diode (OLED) with a first power supply voltage. The first power supply line (VDD) is coupled to the first power supply 150 (referring to FIG.1) which supplies a first power supply voltage.

**[0031]** The second power supply line (VSS) supplies the organic light emitting diode (OLED) with a second power supply voltage. The second power supply line (VSS) is coupled to the second power supply 160 (referring to FIG.1) which supplies a second power supply voltage. Here, the first power supply voltage can have a higher voltage level than that of the second power supply voltage in general.

**[0032]** In addition, the second power supply voltage can use a ground voltage.

**[0033]** The first switching transistor (SW\_TR1) includes a first electrode (source or drain electrode) coupled to the data line (D[M]); a second electrode (source or drain electrode) coupled to a control electrode (gate electrode) of the driving transistor (DR\_TR), a second electrode of the first storage capacitor (C1) and a second electrode of the second storage capacitor (C2); and a control electrode (gate electrode) coupled to the scan line (S[N]). The first switching transistor (SW\_TR1) can be a P type channel transistor. Once the first switching transistor (SW\_TR1) is turned on by the scan signal of low level applied to the control electrode through the scan line (S[N]), the first switching transistor (SW\_TR1) applies a data voltage to a second electrode of the first storage capacitor (C1), a second electrode of the second storage capacitor (C2) and a control electrode of the driving transistor (DR\_TR) through the data line (D[M]).

**[0034]** The driving transistor (DR\_TR) includes a first electrode coupled to a first electrode of the second storage capacitor (C2) and a second electrode of the second switching transistor (SW\_TR2); a second electrode coupled to an anode of the organic light emitting diode (OLED); and a control electrode coupled to the second electrode of the first

switching transistor (SW\_TR1), a second electrode of the first storage capacitor (C1) and a second electrode of the second storage capacitor (C2). The driving transistor can be a P type channel transistor. A method for driving the driving transistor (DR\_TR), according to an embodiment, supplies an amount of current from the first power supply line (VDD) to the organic light emitting diode (OLED), once the driving transistor (DR\_TR) is turned on by the signal of low level applied to the control electrode. A data signal is supplied to the storage capacitors, and is stored in the storage capacitors. Consequently, even if the electric connection with the data line (D[M]) is discontinued as the first switching transistor (SW\_TR1) is turned off, a signal of low level can be applied to the control electrode of the driving transistor (DR\_TR) continuously by the voltage charged in the storage capacitors.

**[0035]** The driving transistor (DR\_TR) can be, for example, any one selected from an amorphous silicon thin film transistor, a poly silicon thin film transistor, an organic thin film transistor, a nano thin film transistor, and the equivalent thereof. However, the material or the kind of the driving transistor is not limited.

**[0036]** When the driving transistor (DR\_TR) is a poly silicon thin film transistor, there are various crystallization methods such as an laser crystallization method (excimer laser annealing: ELA) using an excimer laser, a metal induced crystallization (MIC) using catalytic metals, a solid phase crystallization, a high pressure annealing wherein a crystallization is executed at a high temperature and a high humidity environment, and a sequential lateral solidification (SLS) using a mask in addition to a conventional laser crystallization.

**[0037]** The organic light emitting diode (OLED) includes an anode coupled to the second electrode of the driving transistor (DR\_TR) and a cathode coupled to the second power supply line (VSS). The organic light emitting diode (OLED) emits light in a luminance determined by the current controlled through the driving transistor (DR\_TR) while the second switching transistor (SW\_TR2) is turned on.

**[0038]** The organic light emitting diode (OLED) includes an emission layer (not shown). The emission layer can be, for example, any one selected from a fluorescent material, a phosphorescent material, a mixture of them, and the equivalent thereof. However, the material or the kind of the emission layer is not limited.

**[0039]** In addition, the emission layer can be, for example, one selected from a red emitting material, a green emitting material, a blue emitting material, a mixture of them, and the equivalent of them. However, the material or the kind of the emission layer is not limited to this exemplary embodiment.

**[0040]** The second switching transistor (SW\_TR2) includes a first electrode coupled to the first power supply line (VDD) and a first electrode of the first storage capacitor (C1); a second electrode coupled to a first electrode of the second storage capacitor (C2) and the first electrode of the driving transistor (DR\_TR); and a control electrode coupled to the emission control line (EM[N]). The second switching transistor (SW\_TR2) in this embodiment is a P type channel transistor. Once the second switching transistor (SW\_TR2) is turned on by the signal of low level applied to the control electrode through the emission control line (EM[N]), a current flows from the first power supply line (VDD) to the organic light emitting diode (OLED).

**[0041]** The first storage capacitor (C1) includes a first electrode coupled to the first power supply line (VDD) and the first electrode of the second switching transistor (SW\_TR2), and a second electrode coupled to a second electrode of the second storage capacitor (C2), the second electrode of the first switching transistor (SW\_TR1) and the control electrode of the driving transistor (DR\_TR).

**[0042]** The second storage capacitor (C2) includes a first electrode coupled to the second electrode of the second switching transistor (SW\_TR2) and the first electrode of the driving transistor (DR\_TR), and a second electrode coupled to the second electrode of the first storage capacitor (C1), the second electrode of the first switching transistor (SW\_TR1) and the control electrode of the driving transistor (DR\_TR).

**[0043]** The second storage capacitor (C2) maintains a data signal voltage and the threshold voltage of the driving transistor for a period. In addition, once the second switching transistor (SW\_TR2) is turned on (as a signal of low level is applied to the control electrode of the second switching transistor (SW\_TR2) by the emission control line (EM[N])), the voltage on the second storage capacitor (C2) controls a current, which is in proportion to the strength of a data signal, from the first power supply line to the organic light emitting diode. Consequently, the organic light emitting diode emits light. Furthermore, the compensation for IR-drop or the threshold voltage of the driving transistor which will be described in the following can be accomplished by controlling the capacitance ratio (C1:C2) of the first storage capacitor to the second storage capacitor.

**[0044]** The first switching transistor (SW\_TR1), the driving transistor (DR\_TR) and the second switching transistor (SW\_TR2) can, for example, be any one selected from a P type channel transistor and its equivalent. However, the kind of the transistor is not limited.

**[0045]** Referring to FIG.3, a driving timing diagram of the pixel circuit shown in FIG.2 is depicted. As shown in FIG.3, in the pixel circuit of the organic light emitting display, one frame is classified into the first period, the second period and the third period. More particularly, one frame comprises a data writing period (T1), a period for storing the threshold voltage of the driving transistor (T2), and an emission period (T3). Various ratios of the data writing period (T1) to the period for storing the threshold voltage of the driving transistor (T2) to the emission period (T3) can be formed. In some embodiments, the data writing period (T1) and the period for storing the threshold voltage of the driving transistor (T2)

are shorter than the emission period (T3).

**[0046]** Referring to FIG.4, it is depicted how current flows through the pixel circuit shown in FIG.2 during the data writing period (T1). The operation of the pixel circuit mentioned above will be described with reference to the timing diagram of FIG.3.

**[0047]** The first switching transistor (SW\_TR1) is turned on as a scan signal of low level is applied to the control electrode of the first switching transistor (SW\_TR1). Then the second switching transistor (SW\_TR2) is turned on as a signal of low level of the emission control line (EM[N]) is applied to the control electrode of the second switching transistor (SW\_TR2).

**[0048]** As the first switching transistor (SW\_TR1) is turned on, a data voltage (Vdata) of the data line (D[M]) is applied in a direction from the first electrode of the first switching transistor (SW\_TR1) to the second electrode of the first switching transistor (SW\_TR2). Consequently, the data voltage (Vdata) is applied to the second electrode of the first switching transistor (SW\_TR1), the second electrode of the first storage capacitor (C1), the second electrode of the second storage capacitor (C2) and the control electrode of the driving transistor (DR\_TR).

**[0049]** As the second switching transistor (SW\_TR2) is turned on, a first power supply voltage from the first power supply line VDD is applied in a direction from the first electrode of the second switching transistor (SW\_TR2) to the second electrode of the second switching transistor (SW\_TR2). Consequently, the first power supply voltage is applied to the second electrode of the second switching transistor (SW\_TR2), the first electrode of the second storage capacitor (C2) and the first electrode of the driving transistor (DR\_TR).

**[0050]** In addition, the first power supply voltage from the first power supply line (VDD) can also be applied to the first electrode of the first storage capacitor (C1).

**[0051]** During the data writing period (T1) described above, the driving transistor (DR\_TR) is turned on, thus current flows through the organic light emitting diode (OLED). Consequently, the organic light emitting diode (OLED) emits light.

**[0052]** During the data writing period (T1), the voltage of Vdata is applied to the control electrode (gate electrode) of the driving transistor (DR\_TR), the second electrode of the second storage capacitor (C2) and the second electrode of the first storage capacitor (C1). In addition, the voltage of VDD is applied to the first electrode (source electrode) of the driving transistor (DR\_TR), the first electrode of the second storage capacitor (C2) and the first electrode of the first storage capacitor (C1). Accordingly, the voltage (VDD - Vdata), is stored in the storage capacitors.

**[0053]** Referring to FIG.5, it is depicted how a current flows through the pixel circuit shown in FIG.2 during storing the threshold voltage of the driving transistor (T2). Here, the operation of the pixel circuit will be described with reference to the timing diagram of FIG.3.

**[0054]** First of all, the first switching transistor (SW\_TR1) is turned on as a scan signal of low level from the scan line (S[N]) is applied to the control electrode of the first switching transistor (SW\_TR1), and the second switching transistor (SW\_TR2) is turned off as a signal of high level from the emission control line (BM[N]) is applied to the control electrode of the second switching transistor (SW\_TR2).

**[0055]** As the first switching transistor (SW\_TR1) is turned on, a data voltage (Vdata) of the data line (D[M]) is applied from the first electrode of the first switching transistor (SW\_TR1) to the second electrode of the first switching transistor (SW\_TR1). consequently, the data voltage (Vdata) can be applied to the second electrode of the first switching transistor (SW-TP1), the second electrode of the first storage capacitor (C1), the second electrode of the second storage capacitor (C2) and the control electrode of the driving transistor (DR\_TR).

**[0056]** Here, as the second switching transistor (SW\_TR2) is turned off, a first power supply voltage from the first power supply line (VDD) can be applied to the first electrode of the first storage capacitor (C1).

**[0057]** During the period for storing the threshold voltage of the driving transistor (T2) described above, the driving transistor (DR\_TR) is turned on, thus current is applied to the organic light emitting diode (OLED). Consequently, the organic light emitting diode (OLED) emits light.

**[0058]** During the period for storing the threshold voltage of the driving transistor (T2), the voltage of Vdata is applied to the control electrode (gate electrode) of the driving transistor (DR\_TR), the second electrode of the second storage capacitor (C2) and the second electrode of the first storage capacitor (C1). In addition, the voltage of VDD is applied to the first electrode of the first storage capacitor (C1). Accordingly, the voltage (VDD-Vdata) is stored in the first storage capacitor (C1).

**[0059]** Here, the voltage (Vs) of the first electrode (source electrode) of the driving transistor (DR\_TR) is a value (Vs = Vdata + Vth). Accordingly, the voltage (Vth) of the driving transistor (DR\_TR) is stored in the second storage capacitor (C2).

**[0060]** Referring to the timing diagram of FIG.3, at the beginning of the third period (T3), the first switching transistor (SW\_TR1) is turned off as a signal of high level is applied from the scan line (S[N]) to the control electrode of the first switching transistor (SW\_TR1), and the second switching transistor (SW\_TR2) is turned off as a signal of high level is applied from the emission control line (EM[N]) to the control electrode of the second switching transistor (SW\_TR2).

**[0061]** Accordingly, during the third period (T3), the voltage stored in the storage capacitors during the second period (T2) is maintained without any changes.

**[0062]** Referring to FIG.6, it is depicted how a current flows through the pixel circuit shown in FIG.2 during the emission period (T3). Here, the operation of the pixel circuit will be described with reference to the timing diagram of FIG.3.

**[0063]** The first switching transistor (SW\_TR1) is turned off as a signal of high level from the scan line (S[N]) is applied to the control electrode of the first switching transistor (SW\_TR1), and the second switching transistor (SW\_TR2) is turned on as a signal of low level of the emission control line (EM[N]) is applied to the control electrode of the second switching transistor (SW\_TR2).

**[0064]** As the first switching transistor (SW\_TR1) is turned off, a data voltage (Vdata) of the data line (D[M]) is not further applied to the pixel circuit.

**[0065]** Here, as the second switching transistor (SW\_TR2) is turned on, a first power supply voltage from the first power supply line (VDD) is applied from the first electrode of the second switching transistor (SW\_TR2) to the second electrode of the second switching transistor (SW\_TR2). Consequently, the first power supply voltage can be applied to the first electrode (source electrode) of the driving transistor (DR\_TR). A current from the first power supply line (VDD) can flow toward the second power supply line (VSS) through the organic light emitting diode (OLED) during the emission period (T3). Accordingly, the organic light emitting diode can emit light.

**[0066]** During the emission period (T3), the voltage (Vs) of the first electrode (source electrode) of the driving transistor (DR\_TR) becomes VDD. In addition, the voltage (Vg) of the control electrode (gate electrode) of the driving transistor (DR\_TR) and the voltage difference (Vsg) between the source electrode and the gate electrode of the driving transistor (DR\_TR) can be calculated from the Formula 1 in the following.

**【Formula 1】**

$$V_g = V_{data} + \left( \frac{C2}{C1+C2} \right) * (VDD - V_{data} - V_{th})$$

$$V_s = VDD$$

$$V_{sg} = V_s - V_g$$

$$V_{sg} = VDD - \left[ V_{data} + \left( \frac{C2}{C1+C2} \right) * (VDD - V_{data} - V_{th}) \right]$$

The current flowing through the organic light emitting diode (OLED) can be calculated from the Formula 2 in the following.

**【Formula 2】**

$$I_{OLED} = \frac{\beta}{2} * (V_{sg} - |V_{th}|)^2$$

That is to say, the threshold voltage (Vth) of the driving transistor (DR\_TR) is stored in the second storage capacitor (C2) during the second period (T2). Subsequently, data is expressed by the data voltage (Vdata) and the ratio of C1 to C2 during the emission period (T3).

**[0067]** Here, the optimal ratio of C1 to C2 can change according to the variation of the threshold voltage (Vth) of the driving transistor included in respective pixel circuit. For example, if the variation of the threshold voltage (Vth) at the panel of the organic light emitting display is 0.1V, it can be said that the image quality is not affected. However, if the variation of the threshold voltage (Vth) during the fabricating process is 0.5V, degradation of the image quality can occur. However, if the ratio of C1 to C2 is set to 1:5 (C1 : C2 = 1:5), even if the variation of the threshold voltage (Vth) during the fabricating process is 0.5V, the effective variation of the threshold voltage (Vth) at the panel can be smaller than 0.1V. Consequently, the image quality has no problem.

**[0068]** If, C2 is set to have a larger value than that of C1 (that is, C2 >> C1), C2 divided by C1 added to C2 (C2/(C1+C2)) can be approximately 1. Here, only Vth is left in Vsg in the Formula 1 described above. In addition, when Vsg is substituted with Vth in Formula 2, the threshold voltage (Vth) of the driving transistor can be compensated to a current flowing through the organic light emitting diode (OLED).

**[0069]** If C2 is much larger than C1, then C2 divided by C1 added to C2 (C2/(C1+C2)) becomes 1, Vsg becomes Vth. Here, no matter how much Vdata changes, Vsg of the driving transistor (DR\_TR) is Vth. Therefore, as shown in Formula 2, no data voltage (Vdata) appears in the Formula of the organic light emitting diode. Accordingly, the current wanted

according to a data voltage ( $V_{data}$ ) cannot be generated. Therefore, the data range expands infinitely. Nevertheless, if  $C1$  is set to have a much larger value than that of  $C2$ , then  $C2$  divided by  $C1$  added to  $C2$  ( $C2/(C1+C2)$ ) becomes 0 approximately. Consequently,  $V_{sg}$  in Formula 1 becomes  $VDD-V_{data}$ . As a result, the current wanted can be generated according to a data voltage ( $V_{data}$ ). However, the compensation for the threshold voltage ( $V_{th}$ ) of the driving transistor (DR\_TR) or the compensation for IR-drop of the first power supply line (VDD) cannot be accomplished properly.

**[0070]** That is to say, in the organic light emitting display, the threshold voltage ( $V_{th}$ ) of the driving transistor (DR\_TR) and IR-drop by the first power supply line (VDD) can be compensated by controlling the ratio of  $C1$  to  $C2$  properly.

**[0071]** For example, if  $C2$  divided by  $C1$  added to  $C2$  ( $C2/(C1+C2)$ ) is 0.5,  $V_{sg}$  becomes  $VDD - V_{data} - 0.5 VDD + 0.5V_{data} + 0.5V_{th}$ . Consequently, the data range is increased twofold, and the influence of the threshold voltage ( $V_{th}$ ) of the driving transistor (DR\_TR) and IR-drop of the first power supply line (VDD) can be reduced to half. That is, the influence of the threshold voltage ( $V_{th}$ ) of the driving transistor (DR\_TR) and IR-drop of the first power supply line (VDD) can be minimized by determining  $C2$  to have a larger value than that of  $C1$ .

**[0072]** Furthermore, conventional circuits for compensating the threshold voltage of a driving transistor and IR-drop of a first power supply line require more diodes than the pixel circuit of FIG. 2. Therefore, it can be difficult to accomplish high integration. However, the pixel circuit of FIG. 2 can accomplish high integration because it consists of only three transistors and two storage capacitors. Consequently, an organic light emitting display of high resolution can be realized.

**[0073]** In some circuits for compensating the threshold voltage of a driving transistor, because a path is formed from a control electrode of the driving transistor to a negative power supply voltage, a leakage current can flow through the path. In the circuit of FIG. 2, if the leakage current (off current of the driving transistor) is large, although a black image should be expressed, improper emission can be generated by the leakage current which flows into the organic light emitting diode (OLED). Because the leakage characteristics of driving transistors in a panel differ from one another, although a black image should be expressed, some pixels which have large leakage characteristics can emit some light. The improper emission described above can be reduced by having the driving transistor undergo a reverse aging because the reverse aging can reduce the leakage current of the driving transistor. However, the pixel circuit of FIG. 2 has essentially no leakage. Consequently, the reverse aging for the driving transistor described above is not required.

**[0074]** Preferably, the data writing period ( $T1$ ) and the period for storing the threshold voltage of the driving transistor ( $T2$ ) should be shorter than the emission period ( $T3$ ) so that the time during which the organic light emitting diode (OLED) emits light can become maximized.

**[0075]** Referring to FIG.7, RGB pixel circuits and a demux are coupled according to one embodiment.

**[0076]** The demux may have a layout structure which corresponds to each RGB data signal of the data driver of the organic light emitting display.

**[0077]** Because high resolution is required, the number of data lines of the organic light emitting display increases, and the data driver which drives the organic light emitting display includes more integrated circuits. To solve the problem of excessive data lines, a demux which includes fewer output lines of the data driver may be used. The demux includes a plurality of data supplying switching elements which are connected in common to the output line of the data driver, and the respective data supplying switching elements are coupled to separate data lines. Therefore, the demux supplies each data line with a data signal in sequence through the operation of the data supplying switching elements.

**[0078]** Herein, RGB means red (Red, R), green (Green, G) and blue (Blue, B). In FIG.7, three pixel circuits are coupled to the demux 1000, however, the number of pixel circuits is not limited. In addition, a data signal can be applied to pixel circuits by using a plurality of demuxes, the number of demuxes used is not limited.

**[0079]** In the demux 1000, each red data line, green data line and blue data line is coupled to the data line ( $D[M]$ ) of the respective pixel circuits. In addition, each RGB data line is coupled to a RGB switching transistor (SW\_TR3). The RGB switching transistor consists of a red data line switching transistor (SW\_TR3R), a green data line switching transistor (SW\_TR3G) and a blue data line switching transistor (SW\_TR3G). RGB control signals can be applied to a control electrode) of the RGB switching transistors through RGB control lines (CR, CG and CB) respectively.

**[0080]** Once the RGB switching transistor is turned on by the RGB control signals (CR, CG and CB), a proper data signal (voltage) can be applied to each RGB pixel circuit from the data driver through the demux.

**[0081]** The RGB switching transistors can be P type channel transistors, but the kind of the RGB switching transistor is not limited.

**[0082]** Referring to FIG.8 and FIG.9, a driving timing diagram of the RGB pixel circuits of FIG.7 is depicted.

**[0083]** First of all, the operation of the RGB pixel circuits shown in FIG.7 will be described with reference to the driving timing diagram of FIG.8.

**[0084]** Once a scan signal of low level is applied through the scan line ( $S[N]$ ), each first switching transistor (SW\_TR1) of the RGB pixel circuits is turned on. And, once a low level emission control signal is applied through the emission control line (EM[N]), each second switching transistor (SW\_TR2) of the RGB pixel circuits is turned on.

**[0085]** In a driving method for the organic light emitting display as shown in FIG.8, the RGB switching transistors (SW\_TR3) are turned on by applying a signal of low level through the RGB control lines (CR, CG and CB) during a period during which the scan signal and the emission control signal are low level. Consequently, the RGB data signal

can be applied.

**[0086]** When a P type channel transistor is used as shown in FIG.7, the RGB switching transistors (SW\_TR3) are turned on when a signal of low level is applied to them, as described above. However, if an N type channel transistor is used, the RGB switching transistors (SW\_TR3) are turned on as a signal of high level is applied to them. Consequently, the driving timing diagrams can be different. However, the kind of the transistor and the driving timing diagram are not limited to the specific examples described.

**[0087]** The operation of the RGB pixel circuits shown in FIG.7 will be described with reference to the driving timing diagram of FIG.9.

**[0088]** As a signal of high level is applied through the scan line (S[N]), each first switching transistor (SW\_TR1) of the RGB pixel circuits is turned off. And, as a signal of low level is applied through the scan line (S[N]), each second switching transistor (SW\_TR2) of the RGB pixel circuits is turned on.

**[0089]** In a driving method for the organic light emitting display shown in FIG.9, the RGB switching transistors (SW\_TR3) are turned on by applying a signal of low level through the RGB control lines (CR, CG and CB) during the period during which the scan signal is high level and the emission control signal is low level. Consequently, the RGB data signal can be applied.

**[0090]** When a scan signal of high level is applied to the control electrode of the first switching transistor (SW\_TR1) of the pixel circuit, the first switching transistor (SW\_TR1) is turned off. Consequently, during the period which a turn-off scan signal is applied, the RGB data signal is not applied to the storage capacitor of the pixel circuit. Once the first switching transistor (SW\_TR1) is turned on as a turn-on scan signal is applied to the control electrode of the first switching transistor (SW\_TR1) after the data signal (voltage) is charged by a parasitic capacitor (Cd) formed by the data lines (D[M]), the data signals charged in the parasitic capacitor (Cd) is applied through the first switching transistor (SW\_TR1). The capacitance of the parasitic capacitor (Cd) can be larger than that of the first storage capacitor (C1) and the second storage capacitor (C2) included in the pixel circuit.

**[0091]** In case that a P type channel transistor is used as shown in FIG.7, the RGB switching transistors (SW\_TR3) are turned on when a signal of low level is applied to them. However, if an N type channel transistor is used, the RGB switching transistors (SW\_TR3) are turned on as a signal of high level is applied to them. Consequently, the driving timing diagrams can be different. However, the kind of the transistor and the driving timing diagram is not limited to those disclosed in the specification.

**[0092]** As describe above, the RGB switching transistors (SW\_TR3) are turned on by applying a signal of low level through the RGB control lines (CR, CG and CB) during the period which a signal of low level is applied from the emission control line (EM[N]), regardless of whether a high level or low level is applied from the scan line (S[N]). As a result, the storage capacitors, which have stored a previous data voltage, can be initialized as they are coupled to the first power supply line (VDD). Furthermore, the storage capacitors can be coupled to the first power supply line (VDD) as the second switching transistors (SW\_TR2) of the pixel circuits are turned on by a signal of low level applied from the emission control line (EM[N]). Consequently, proper data can be written on the storage capacitors by applying new RGB data signals after the storage capacitors are initialized.

**[0093]** FIG.10 depicts how the RGB pixel circuits and the demux may be coupled.

**[0094]** The demux 1000 has a layout structure corresponding to each RGB data signal of the data driver of the organic light emitting display, and it is similar to the demux shown in FIG.7. However, the Demux 1000 also includes an initializing power supply voltage line (Vrst) and an initializing switching transistor (SW\_TR4) which couples the initialing power supply voltage line (Vrst) to a RGB data voltage line.

**[0095]** Three pixel circuits are coupled to the demux 1000 in FIG.10, however, the number of pixel circuits coupled to the demux is not limited. In addition, a data signal can be applied to the pixel circuits by using a plurality of demuxes, and the number of demuxes used is not limited.

**[0096]** In the demux 1000 shown in FIG.10, each red data line, green data line and blue data line is coupled to the data line (D[M]) of the respective pixel circuits. In addition, each RGB data line is coupled to RGB switching transistor (SW\_TR3). The RGB switching transistor comprises a red data line switching transistor (SW\_TR3R), a green data line switching transistor (SW\_TR3G) and a blue data line switching transistor (SW\_TR3B). RGB control signals can be applied to a control electrode of the RGB switching transistors through RGB control lines (CR, CG and CB) respectively.

**[0097]** Once the RGB switching transistor is turned on by the respective RGB control signals (CR, CG and CB), a proper data signal (voltage) from the data driver can be applied to the respective RGB pixel circuit through the demux.

**[0098]** In addition, the initializing power supply voltage line (Vrst) is coupled to the respective RGB data line through the initializing switching transistor (SW\_TR4). Once a turn-on initializing signal (Rst) is applied to the initializing switching transistor (SW\_TR4), the initializing switching transistors (SW\_TR4G, SW\_TR4R and SW\_TR4B) are turned on, then an initializing power supply voltage can be applied to each RGB data line from the initializing power supply voltage line (Vrst). As the initializing power supply voltage is applied, the previous data voltages applied to the RGB data lines are initialized. Consequently, new RGB data signals (voltages) can be applied.

**[0099]** The RGB switching transistor and the initializing power supply voltage can be a P type channel transistor,

however, the kind of the transistor is not limited.

**[0100]** A thin film transistor can be used as the RGB switching transistor (SW\_TR3) shown in FIG.7 and the initializing switching transistor (SW\_TR4) shown in FIG.10. Furthermore, as a crystallization method for the thin film transistor, a laser crystallization method (ELA) using an excimer laser, a metal induced crystallization (MIC) using a catalytic metal and a solid phase crystallization can be used. In addition, a high pressure annealing (HPA) wherein crystallization is executed at a high temperature and a high humidity environment and a sequential lateral solidification using a mask in addition to conventional laser crystallization can be used as well.

**[0101]** The laser crystallization method is a widely used crystallization method in which a thin film transistor is crystallized into poly silicon. Not only can the method directly use existing crystallization processes for poly silicon liquid crystal display devices, but also the process is simple, and the technology of the process has been completely established.

**[0102]** Referring to FIG.11, a driving timing diagram of the RGB pixel circuits shown in FIG.10 is depicted.

**[0103]** The operation of the RGB pixel circuits shown in FIG.10 will be described with reference to the driving timing diagram of FIG.11.

**[0104]** Once an initializing signal of low level is applied through an initializing signal line (Rst), the initializing switching transistors (SW\_TR4) in the demux are turned on. Consequently, data lines are initialized by the initializing power supply voltage from the initializing power supply voltage line (Vrst).

**[0105]** Once an emission control signal of low level is applied through the emission control line (EM[N]), and a scan signal of low level is applied from the scan line (S[N]), then the RGB switching transistors (SW\_TR3R, SW\_TR3G and SW\_TR3B) can be turned on as a signal of low level is applied through the RGB control signal line.

**[0106]** The RGB control signal is applied in order of a green, red and blue control signals. Consequently, the RGB data voltage is applied to the respective green, red and blue pixel circuits in sequence.

**[0107]** As shown in FIG.11, a green organic light emitting diode (OLED Green) emits light as a current flows through the green organic light emitting diode (OLED Green) from the period during which a green emission control signal is applied to the period during which an emission control signal of high level from the emission control line (EM[N]) is applied.

**[0108]** A red organic light emitting diode (OLED Red) emits light as a current flows through the red organic light emitting diode (OLED Red) from the period during which a red emission control signal is applied to the period during which an emission control signal of high level from the emission control line (EM[N]) is applied.

**[0109]** In addition, a blue organic light emitting diode (OLED Blue) emits light as a current flows through the blue organic light emitting diode (OLED Blue) from the period during which a blue emission control signal is applied to the period during which an emission control signal of high level from the emission control line (EM[N]) is applied.

**[0110]** As shown in FIG.1, during the period for compensating the white balance, a current flows through a green organic light emitting diode for the longest time, and a red green organic light emitting diode is next, and a blue organic light emitting diode is the shortest.

**[0111]** In this embodiment, the reason why the time for compensating the white balance is arranged in order of green, red and blue is that a green OLED has a higher luminous efficiency than red and green OLEDs. To adjust the white balance, a current flows through a green organic light emitting diode of the best luminous efficiency for the longest time during the non-emission period (the period for compensating the white balance). Next, in order of a red and blue, the period for compensating the white balance is performed. Therefore, a uniform luminance can be accomplished. In some embodiments, during the period for compensating the white balance, a larger current flows through the organic light emitting diode than a current flowing during the emission period.

**[0112]** In some embodiments, during a period for displaying a frame, the period for compensating the white balance can be shorter than the emission period.

**[0113]** As described above, the organic light emitting display can divide a period for displaying one frame into the first period (T1), the second period (T2) and the third period (T3). Each period consists of a data writing period (T1), a period for storing the threshold voltage of the driving transistor (T2) and an emission period (T3).

**[0114]** In the organic light emitting display, high integration can be accomplished by using three transistors, which is fewer than the number of the transistors of a conventional pixel circuit. Consequently, high resolution also becomes possible.

**[0115]** The uniformity of the luminance can be improved by compensating the threshold voltage ( $V_{th}$ ) and controlling the ratio (C1:C2) of a first storage capacitor to a second storage capacitor properly. Furthermore, IR-drop by a first power supply line (VDD) can be improved by controlling the capacitance ratio of the first storage capacitor to the second storage capacitor.

**[0116]** In the pixel circuit, an improper emission of the organic light emitting diode can be suppressed because an electric connection through which a leakage current can flow from the control electrode of the driving transistor to the negative power supply voltage does not exist.

**[0117]** In the case of a driving method an RGB data signal is applied by using the demux, the RGB data signal is applied during a period which an emission control signal is turned on regardless of the scan signal being turned on or off. Consequently, the RGB data can be stored in each storage capacitor properly. A new RGB data signal can be stored

in the storage capacitors properly because the respective storage capacitors are initialized by the first power supply voltage of the first power supply line, before the RGB data is applied to each storage capacitor of the respective pixel circuits.

[0118] Furthermore, in the case of one driving method, an RGB data signal is applied using the demux during the non-emission period (the period for compensating the white balance). During this period a current should flow through the light emitting diode of the longest lifetime for the longest time. Next, in order of a red and blue organic light emitting diode, the period for compensating the white balance is performed. Consequently, the lifetime of uniform luminance level can be extended. Accordingly, the color wanted can be reproduced because the white balance is maintained as time goes by, because the period for compensating the white balance is performed.

## Claims

1. An organic light emitting display, comprising:

a scan line (S[N]);  
 a data line (D[M]); and  
 a pixel coupled to the scan line (S[N]) and the data line (D[M]);  
 wherein the pixel comprises:

a first switching transistor (SW\_TR1) including a control electrode connected to the scan line (S[N]), a first electrode connected to the data line (D[M]), and a second electrode;  
 a driving transistor (DR\_TR) including a first electrode connected to a first power supply line (VDD), a second electrode connected to a second power supply line (VSS), and a control electrode connected to the second electrode of the first switching transistor (SW\_TR1);  
 a first storage capacitor (C1) including a first electrode connected to the first power supply line (VDD), and a second electrode connected to the second electrode of the first switching transistor (SW\_TR1);  
 a second switching transistor (SW\_TR2) including a first electrode connected to the first power supply line (VDD), a second electrode connected to the first electrode of the driving transistor (DR\_TR), and a control electrode connected to an emission control line (EM[N]);  
 a second storage capacitor (C2) including a first electrode connected to the first electrode of the driving transistor (DR\_TR), and a second electrode connected to the second electrode of the first storage capacitor (C1); and  
 an organic light emitting diode (OLED) connected between the second electrode of the driving transistor (DR\_TR) and the second power supply line (VSS).

2. An organic light emitting display as claimed in claim 1, wherein the first switching transistor (SW\_TR1) includes a second electrode coupled to a control electrode of the driving transistor (DR\_TR).

3. An organic light emitting display as claimed in claim 1 or 2, wherein the first switching transistor (SW\_TR1) is configured to transfer data from the first electrode to the second electrode when the control electrode of the first switching transistor (SW\_TR1) receives a scan signal from the scan line.

4. An organic light emitting display as claimed in claim 1, 2 or 3, wherein the control electrode of the driving transistor (DR\_TR) is coupled to a second electrode of the first switching transistor (SW\_TR1), the driving transistor (DR\_TR) including a first electrode coupled to a second electrode of the second switching transistor (SW\_TR2), and a second electrode coupled to an anode of the organic light emitting diode (OLED).

5. An organic light emitting display as claimed in any preceding claim, wherein the driving transistor (DR\_TR) is configured to control a driving current from the first power supply line (VDD) according to a data signal at the control electrode of the driving transistor (DR\_TR).

6. An organic light emitting display as claimed in any preceding claim, wherein the first storage capacitor (C1) includes a first electrode coupled to the first power supply line (VDD), and a second electrode coupled to the second electrode of the first switching transistor (SW\_TR1) and the control electrode of the driving transistor (DR\_TR).

7. An organic light emitting display as claimed in any preceding claim, wherein the first storage capacitor (C1) includes a first electrode coupled to the first power supply line (VDD) and a second electrode coupled to a second electrode

of the second storage capacitor (C2).

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8. An organic light emitting display as claimed in any preceding claim, wherein the control electrode of the second switching transistor (SW\_TR2) is coupled to the emission control line (EM[N]), and the second switching transistor (SW\_TR2) includes a first electrode coupled to the first power supply line (VDD), and a second electrode coupled to the first electrode of the driving transistor (DR\_TR).
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9. An organic light emitting display as claimed in any preceding claim, wherein the control electrode of the second switching transistor (SW\_TR2) is coupled to the emission control line (EM[N]), and the second switching transistor (SW\_TR2) includes a first electrode coupled to the first power supply line (VDD), and a second electrode coupled to a first electrode of the second storage capacitor (C2).
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10. An organic light emitting display as claimed in any preceding claim, wherein the second storage capacitor (C2) includes a first electrode coupled to a second electrode of the second switching transistor (SW\_TR2) and a first electrode of the driving transistor (DR\_TR), and a second electrode coupled to a second electrode of the first storage capacitor (C1), the second electrode of the first switching transistor (SW\_TR1) and a first electrode of the driving transistor (DR\_TR).
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11. An organic light emitting display as claimed in any preceding claim, wherein the second storage capacitor (C1) is coupled between the control electrode of the driving transistor (DR\_TR) and a first electrode of the driving transistor (DR\_TR).
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12. An organic light emitting display as claimed in any preceding claim, wherein the organic light emitting diode includes an anode coupled to a second electrode of the driving transistor (DR\_TR) and a cathode coupled to the second power supply line (VSS).
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13. An organic light emitting display as claimed in any preceding claim, wherein a second power supply voltage of the second power supply line (VSS) is lower than a first power supply voltage of the first power supply line (VDD).
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14. An organic light emitting display as claimed in any preceding claim, wherein the second power supply voltage of the second power supply line (VSS) is a ground voltage.
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15. An organic light emitting display as claimed in any preceding claim, wherein during a period for displaying one frame, when the first switching transistor (SW\_TR1) and the second switching transistor (SW\_TR2) are turned on, a data voltage from the data line is applied to a second electrode of the first storage capacitor, a second electrode of the second storage capacitor (C2) and the control electrode of the driving transistor (DR\_TR), after which the first power supply voltage from the first power supply line (VDD) is applied to a first electrode of the first storage capacitor (C1) and to a first electrode of the second storage capacitor (C2).
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16. An organic light emitting display as claimed in any preceding claim, wherein during a period for displaying one frame, when the first switching transistor (SW\_TR1) is turned on, and the second switching transistor (SW\_TR2) is turned off, a data voltage from the data line is applied to a second electrode of the first storage capacitor (C1), a second electrode of the second storage capacitor (C2) and the control electrode of the driving transistor (DR\_TR), after which the first power supply voltage from the first power supply line (VDD) is applied to the first electrode of the first storage capacitor (C1).
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17. An organic light emitting display as claimed in any preceding claim, wherein during a period for displaying one frame, when the first switching transistor (SW\_TR1) is turned off, and the second switching transistor (SW\_TR2) is turned on, the first power supply line (VDD), the driving transistor (DR\_TR) and the organic light emitting diode (OLED) are coupled one another, and a current is flows from the anode of the organic light emitting diode (OLED) to the cathode of the organic light emitting diode (OLED).

## Patentansprüche

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1. Organische lichtemittierende Anzeige, umfassend:

eine Scan-Leitung (S[N]);

eine Datenleitung (D[M]); und  
ein Pixel, das mit der Scan-Leitung (S[N]) und der Datenleitung (D[M]) gekoppelt ist;

wobei das Pixel umfasst:

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einen ersten Schalttransistor (SW\_TR1), umfassend eine Steuerelektrode, die mit der Scan-Leitung (S[N]) verbunden ist, eine erste Elektrode, die mit der Datenleitung (D[M]) verbunden ist, und eine zweite Elektrode;  
einen Treibertransistor (DR\_TR), umfassend eine erste Elektrode, die mit einer ersten Stromversorgungsleitung (VDD) verbunden ist, eine zweite Elektrode, die mit einer zweiten Stromversorgungsleitung (VSS) verbunden ist, und eine Steuerelektrode, die mit der zweiten Elektrode des ersten Schalttransistors (SW\_TR1) verbunden ist;

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einen ersten Speicherkondensator (C1), umfassend eine erste Elektrode, die mit der ersten Stromversorgungsleitung (VDD) verbunden ist und eine zweite Elektrode, die mit der zweiten Elektrode des ersten Schalttransistors (SW\_TR1) verbunden ist;

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einen zweiten Schalttransistor (SW\_TR2), umfassend eine erste Elektrode, die mit der ersten Stromversorgungsleitung (VDD) verbunden ist, eine zweite Elektrode, die mit der ersten Elektrode des Treibertransistors (DR\_TR) verbunden ist, und eine Steuerelektrode, die mit einer Emissionssteuerleitung (EM[N]) verbunden ist;  
einen zweiten Speicherkondensator (C2), umfassend eine erste Elektrode, die mit der ersten Elektrode des Treibertransistors (DR\_TR) verbunden ist, und eine zweite Elektrode, die mit der zweiten Elektrode des ersten Speicherkondensators (C1) verbunden ist; und

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eine organische lichtemittierende Diode (OLED), die zwischen der zweiten Elektrode des Treibertransistors (DR\_TR) und der zweiten Stromversorgungsleitung (VSS) verbunden ist.

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2. Organische lichtemittierende Anzeige nach Anspruch 1, wobei der erste Schalttransistor (SW\_TR1) eine zweite Elektrode umfasst, die mit einer Steuerelektrode des Treibertransistors (DR\_TR) gekoppelt ist.

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3. Organische lichtemittierende Anzeige nach Anspruch 1 oder 2, wobei der erste Schalttransistor (SW\_TR1) konfiguriert ist, Daten von der ersten Elektrode an die zweite Elektrode zu übertragen, wenn die Steuerelektrode des ersten Schalttransistors (SW\_TR1) ein Scan-Signal von der Scan-Leitung empfängt.

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4. Organische lichtemittierende Anzeige nach Anspruch 1, 2 oder 3, wobei die Steuerelektrode des Treibertransistors (DR\_TR) mit einer zweiten Elektrode des ersten Schalttransistors (SW\_TR1) gekoppelt ist und der Treibertransistor (DR\_TR) eine erste Elektrode umfasst, die mit einer zweiten Elektrode des zweiten Schalttransistors (SW\_TR2) gekoppelt ist, und eine zweite Elektrode, die mit einer Anode der organischen lichtemittierenden Diode (OLED) gekoppelt ist.

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5. Organische lichtemittierende Anzeige nach einem der vorstehenden Ansprüche, wobei der Treibertransistor (DR\_TR) konfiguriert ist, einen Treiberstrom von der ersten Stromversorgungsleitung (VDD) in Übereinstimmung mit einem Datensignal der Steuerelektrode des Treibertransistors (DR\_TR) zu steuern.

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6. Organische lichtemittierende Anzeige nach einem der vorstehenden Ansprüche, wobei der erste Speicherkondensator (C1) eine erste Elektrode umfasst, die mit der ersten Stromversorgungsleitung (VDD) gekoppelt ist, und eine zweite Elektrode, die mit der zweiten Elektrode des ersten Schalttransistors (SW\_TR1) und der Steuerelektrode des Treibertransistors (DR\_TR) gekoppelt ist.

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7. Organische lichtemittierende Anzeige nach einem der vorstehenden Ansprüche, wobei der erste Speicherkondensator (C1) eine erste Elektrode umfasst, die mit der ersten Stromversorgungsleitung (VDD) gekoppelt ist, und eine zweiten Elektrode, die mit einer zweiten Elektrode des zweiten Speicherkondensators (C2) gekoppelt ist.

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8. Organische lichtemittierende Anzeige nach einem der vorstehenden Ansprüche, wobei die Steuerelektrode des zweiten Schalttransistors (SW\_TR2) mit der Emissionssteuerleitung (EM[N]) gekoppelt ist und der zweite Schalttransistor (SW\_TR2) eine erste Elektrode umfasst, die mit der ersten Stromversorgungsleitung (VDD) gekoppelt ist, und eine zweite Elektrode, die mit der ersten Elektrode des Treibertransistors (DR\_TR) gekoppelt ist.

9. Organische lichtemittierende Anzeige nach einem der vorstehenden Ansprüche, wobei die Steuerelektrode des zweiten Schalttransistors (SW\_TR2) mit der Emissionssteuerleitung (EM[N]) gekop-

pelt ist und der zweite Schalttransistor (SW\_TR2) eine erste Elektrode umfasst, die mit der ersten Stromversorgungsleitung (VDD) verbunden ist, und eine zweite Elektrode, die mit einer ersten Elektrode des zweiten Speicherkondensators (C2) verbunden ist.

- 5 10. Organische lichtemittierende Anzeige nach einem der vorstehenden Ansprüche,  
wobei der zweite Speicherkondensator (C2) eine erste Elektrode umfasst, die mit einer zweiten Elektrode des  
zweiten Schalttransistors (SW\_TR2) und einer ersten Elektrode des Treibertransistors (DR\_TR) gekoppelt ist, und  
eine zweite Elektrode, die mit einer zweiten Elektrode des ersten Speicherkondensators (C1), der zweiten Elektrode  
10 des ersten Schalttransistors (SW\_TR1) und einer ersten Elektrode des Treibertransistors (DR\_TR) gekoppelt ist.
11. Organische lichtemittierende Anzeige nach einem der vorstehenden Ansprüche,  
wobei der zweite Speicherkondensator (C1) mit der Steuerelektrode des Treibertransistors (DR\_TR) und einer  
ersten Elektrode des Treibertransistors (DR\_TR) gekoppelt ist.
- 15 12. Organische lichtemittierende Anzeige nach einem der vorstehenden Ansprüche,  
wobei die organische lichtemittierende Diode eine Anode umfasst, die mit einer zweiten Elektrode des Treibertran-  
sistors (DR\_TR) verbunden ist, und eine Kathode, die mit der zweiten Stromversorgungsleitung (VSS) verbunden ist.
- 20 13. Organische lichtemittierende Anzeige nach einem der vorstehenden Ansprüche,  
wobei eine zweite Stromversorgungsspannung der zweiten Stromversorgungsleitung (VSS) geringer ist als eine  
erste Stromversorgungsspannung der ersten Stromversorgungsleitung (VDD).
- 25 14. Organische lichtemittierende Anzeige nach einem der vorstehenden Ansprüche,  
wobei die zweite Stromversorgungsspannung der zweiten Stromversorgungsleitung (VSS) eine Massespannung ist.
- 30 15. Organische lichtemittierende Anzeige nach einem der vorstehenden Ansprüche,  
wobei während eines Zeitraums zum Anzeigen eines Rahmens, wenn der erste Schalttransistor (SW\_TR1) und der  
zweite Schalttransistor (SW\_TR2) eingeschaltet sind, eine Datenspannung von der Datenleitung an eine zweite  
Elektrode des ersten Speicherkondensators, eine zweite Elektrode des zweiten Speicherkondensators (C2) und  
die Steuerelektrode des Treibertransistors (DR\_TR) angelegt wird, wonach die erste Stromversorgungsspannung  
der ersten Stromversorgungsleitung (VDD) an eine erste Elektrode des ersten Speicherkondensators (C1) und eine  
erste Elektrode des zweiten Speicherkondensators (C2) angelegt wird.
- 35 16. Organische lichtemittierende Anzeige nach einem der vorstehenden Ansprüche,  
wobei während eines Zeitraums zum Anzeigen eines Rahmens, wenn der erste Schalttransistor (SW\_TR1) einge-  
schaltet ist und der zweite Schalttransistor (SW\_TR2) ausgeschaltet ist, eine Datenspannung von der Datenleitung  
an eine zweite Elektrode des ersten Speicherkondensators (C1), eine zweite Elektrode des zweiten Speicherkon-  
densators (C2) und die Steuerelektrode des Treibertransistors (DR\_TR) angelegt wird, wonach die erste Stromver-  
sorgungsspannung der ersten Stromversorgungsleitung (VDD) an die erste Elektrode des ersten Speicherkonden-  
40 sators (C1) angelegt wird.
- 45 17. Organische lichtemittierende Anzeige nach einem der vorstehenden Ansprüche,  
wobei während eines Zeitraums zum Anzeigen eines Rahmens, wenn der erste Schalttransistor (SW\_TR1) ausge-  
schaltet ist und der zweite Schalttransistor (SW\_TR2) eingeschaltet ist, die erste Stromversorgungsleitung (VDD),  
der Treibertransistor (DR\_TR) und die organische lichtemittierende Diode (OLED) miteinander gekoppelt sind und  
Strom von der Anode der organischen lichtemittierenden Diode (OLED) zur Kathode der organischen lichtemittie-  
renden Diode (OLED) fließt.

50 **Revendications**

1. Écran à diodes électroluminescentes organiques, comprenant :

55 une ligne de balayage (S[N]) ;  
une ligne de données (D[M]) ; et  
un pixel couplé à la ligne de balayage (S[N]) et à la ligne de données (D[M]) ;

où le pixel comprend :

un premier transistor de commutation (SW\_TR1) comprenant une électrode de contrôle connectée à la ligne de balayage (S[N]), une première électrode connectée à la ligne de données (D[M]), et une seconde électrode ; un transistor de commande (DR\_TR) comprenant une première électrode connectée à une première ligne d'alimentation électrique (VDD), une seconde électrode connectée à une seconde ligne d'alimentation électrique (VSS), et une électrode de contrôle connectée à la seconde électrode du premier transistor de commutation (SW\_TR1) ;

un premier condensateur magasin (C1) comprenant une première électrode connectée à une première ligne d'alimentation électrique (VDD), et une seconde électrode connectée à la seconde électrode du premier transistor de commutation (SW\_TR1) ;

un second transistor de commutation (SW\_TR2) comprenant une première électrode connectée à la première ligne d'alimentation électrique (VDD), une seconde électrode connectée à la première électrode du transistor de commande (DR\_TR), et une électrode de contrôle connectée à une ligne de contrôle d'émission (EM[N]) ; un second condensateur magasin (C2) comprenant une première électrode connectée à la première électrode du transistor de commande (DR\_TR), et une seconde électrode connectée à la seconde électrode du premier condensateur magasin (C1) ; et

une diode électroluminescente organique (OLED) connectée entre la seconde électrode du transistor de commande (DR\_TR) et la seconde ligne d'alimentation électrique (VSS).

2. Écran à diodes électroluminescentes organiques tel que revendiqué dans la revendication 1, dans lequel le premier transistor de commutation (SW\_TR1) comprend une seconde électrode couplée à une électrode de contrôle du transistor de commande (DR\_TR).

3. Écran à diodes électroluminescentes organiques tel que revendiqué dans la revendication 1 ou la revendication 2, dans lequel le premier transistor de commutation (SW\_TR1) est configuré pour transférer des données de la première électrode vers la seconde électrode lorsque l'électrode de contrôle du premier transistor de commutation (SW\_TR1) reçoit un signal de balayage de la ligne de balayage.

4. Écran à diodes électroluminescentes organiques tel que revendiqué dans la revendication 1, 2 ou 3, dans lequel l'électrode de contrôle du transistor de commande (DR\_TR) est couplée à une seconde électrode du premier transistor de commutation (SW\_TR1), le transistor de commande (DR\_TR) comprenant une première électrode couplée à une seconde électrode du second transistor de commutation (SW\_TR2), et une seconde électrode couplée à une anode de la diode électroluminescente organique (OLED).

5. Écran à diodes électroluminescentes organiques tel que revendiqué dans l'une quelconque des revendications précédentes, dans lequel le transistor de commande (DR\_TR) est configuré pour contrôler un courant de commande depuis la première ligne d'alimentation électrique (VDD) conformément à un signal de données au niveau de l'électrode de contrôle du transistor de commande (DR\_TR).

6. Écran à diodes électroluminescentes organiques tel que revendiqué dans l'une quelconque des revendications précédentes, dans lequel le premier condensateur magasin (C1) comprend une première électrode couplée à la première ligne d'alimentation électrique (VDD), et une seconde électrode couplée à la seconde électrode du premier transistor de commutation (SW\_TR1) et à l'électrode de contrôle du transistor de commande (DR\_TR).

7. Écran à diodes électroluminescentes organiques tel que revendiqué dans l'une quelconque des revendications précédentes, dans lequel le premier condensateur magasin (C1) comprend une première électrode couplée à la première ligne d'alimentation électrique (VDD), et une seconde électrode couplée à une seconde électrode du second condensateur magasin (C2).

8. Écran à diodes électroluminescentes organiques tel que revendiqué dans l'une quelconque des revendications précédentes, dans lequel l'électrode de contrôle du second transistor de commutation (SW\_TR2) est couplée à une ligne de contrôle d'émission (EM[N]), et le second transistor de commutation (SW\_TR2) comprend une première électrode couplée à la première ligne d'alimentation électrique (VDD), et une seconde électrode couplée à la première électrode du transistor de commande (DR\_TR).

9. Écran à diodes électroluminescentes organiques tel que revendiqué dans l'une quelconque des revendications précédentes, dans lequel l'électrode de contrôle du second transistor de commutation (SW\_TR2) est couplée à la ligne de contrôle d'émission (EM[N]), et le second transistor de commutation (SW\_TR2) comprend une première électrode couplée à la première ligne d'alimentation électrique (VDD), et une seconde électrode couplée à une

première électrode du second condensateur magasin (C2).

- 5 10. Écran à diodes électroluminescentes organiques tel que revendiqué dans l'une quelconque des revendications précédentes, dans lequel le second condensateur magasin (C2) comprend une première électrode couplée à une seconde électrode du second transistor de commutation (SW\_TR2) et à une première électrode du transistor de commande (DR\_TR), et une seconde électrode couplée à une seconde électrode du premier condensateur magasin (C1), à la seconde électrode du premier transistor de commutation (SW\_TR1) et à une première électrode du transistor de commande (DR\_TR).
- 10 11. Écran à diodes électroluminescentes organiques tel que revendiqué dans l'une quelconque des revendications précédentes, dans lequel le second condensateur magasin (C2) est couplé entre l'électrode de contrôle du transistor de commande (DR\_TR) et une première électrode du transistor de commande (DR\_TR).
- 15 12. Écran à diodes électroluminescentes organiques tel que revendiqué dans l'une quelconque des revendications précédentes, dans lequel la diode électroluminescente organique comprend une anode couplée à une seconde électrode du transistor de commande (DR\_TR) et une cathode couplée à la première ligne d'alimentation électrique (VSS).
- 20 13. Écran à diodes électroluminescentes organiques tel que revendiqué dans l'une quelconque des revendications précédentes, dans lequel une seconde tension d'alimentation électrique de la seconde ligne d'alimentation électrique (VSS) est plus basse qu'une première tension d'alimentation électrique de la première ligne d'alimentation électrique (VDD).
- 25 14. Écran à diodes électroluminescentes organiques tel que revendiqué dans l'une quelconque des revendications précédentes, dans lequel la seconde tension d'alimentation électrique de la seconde ligne d'alimentation électrique (VSS) est une tension de masse.
- 30 15. Écran à diodes électroluminescentes organiques tel que revendiqué dans l'une quelconque des revendications précédentes, dans lequel au cours d'une période d'affichage d'une trame, lorsque le premier transistor de commutation (SW\_TR1) et le second transistor de commutation (SW\_TR2) sont allumés, une tension de données de la ligne de données est appliquée à une seconde électrode du premier condensateur magasin, à une seconde électrode du second condensateur magasin (C2) et à l'électrode de contrôle du transistor de commande (DR\_TR), après quoi la première tension d'alimentation électrique de la première ligne d'alimentation électrique (VDD) est appliquée à une première électrode du premier condensateur magasin (C1) et à une première électrode du second condensateur magasin (C2).
- 35 16. Écran à diodes électroluminescentes organiques tel que revendiqué dans l'une quelconque des revendications précédentes, dans lequel au cours d'une période d'affichage d'une trame, lorsque le premier transistor de commutation (SW\_TR1) est allumé, et le second transistor de commutation (SW\_TR2) est éteint, une tension de données de la ligne de données est appliquée à une seconde électrode du premier condensateur magasin (C1), à une seconde électrode du second condensateur magasin (C2) et à l'électrode de contrôle du transistor de commande (DR\_TR), après quoi la première tension d'alimentation électrique de la première ligne d'alimentation électrique (VDD) est appliquée à la première électrode du premier condensateur magasin (C1).
- 40 17. Écran à diodes électroluminescentes organiques tel que revendiqué dans l'une quelconque des revendications précédentes, dans lequel au cours d'une période d'affichage d'une trame, lorsque le premier transistor de commutation (SW\_TR1) est éteint, et le second transistor de commutation (SW\_TR2) est allumé, la première ligne d'alimentation électrique (VDD), le transistor de commande (DR\_TR) et la diode électroluminescente organique (OLED) sont couplés les uns aux autres, et un courant circule depuis l'anode de la diode électroluminescente organique (OLED) vers la cathode de la diode électroluminescente organique (OLED).
- 45 50 55

FIG. 1

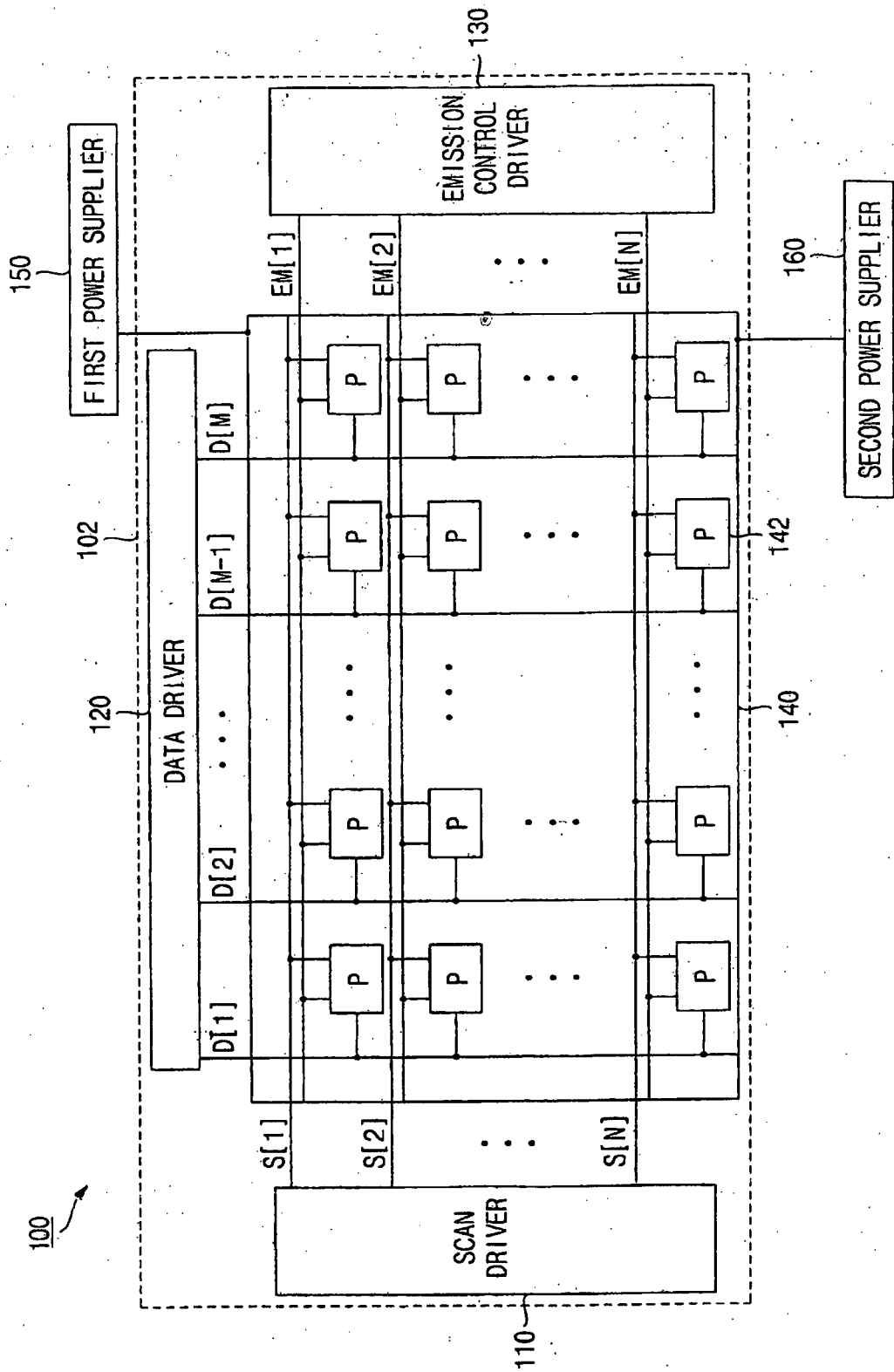


FIG.2

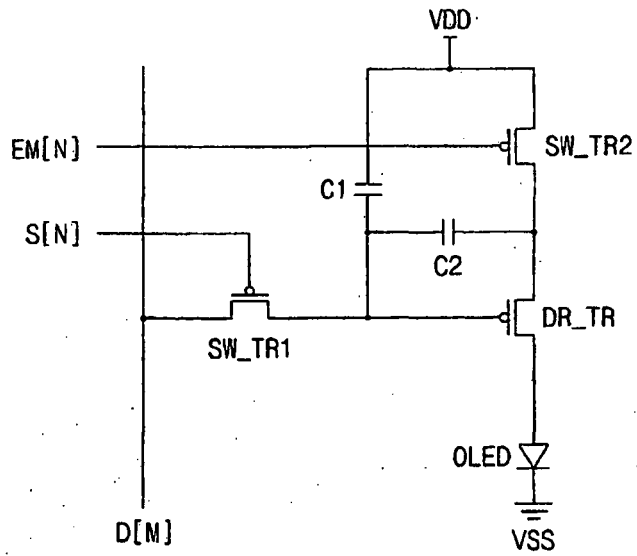


FIG.3

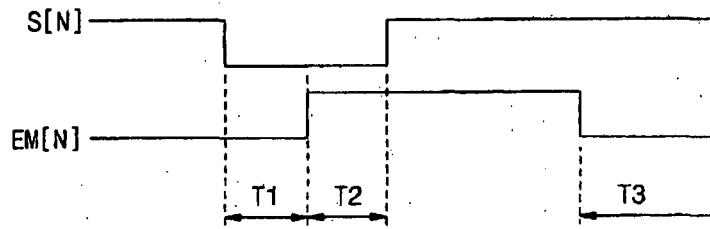


FIG.4

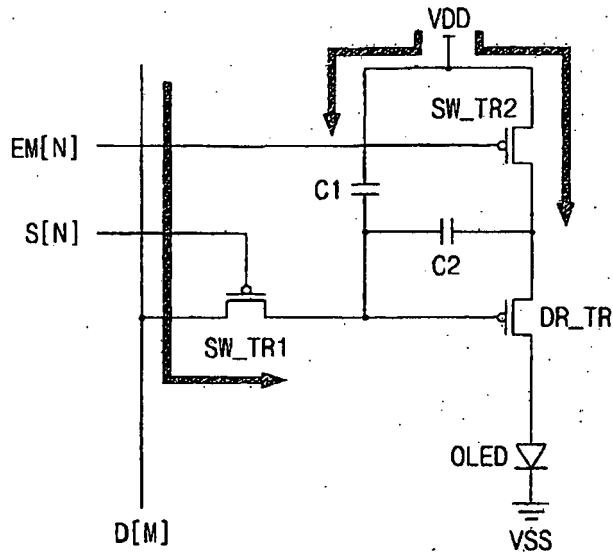


FIG.5

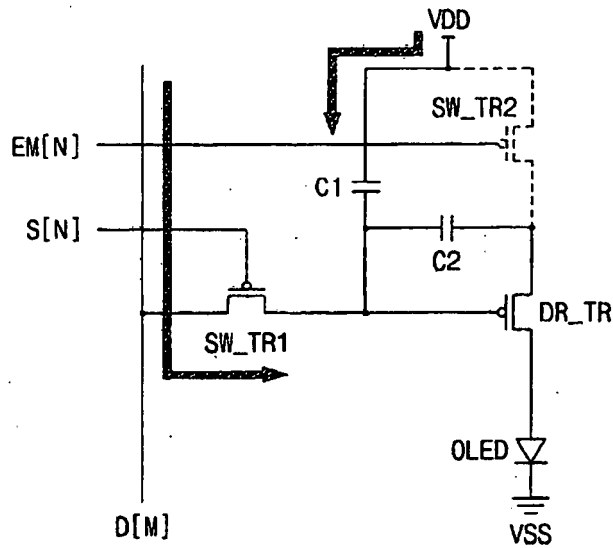


FIG.6

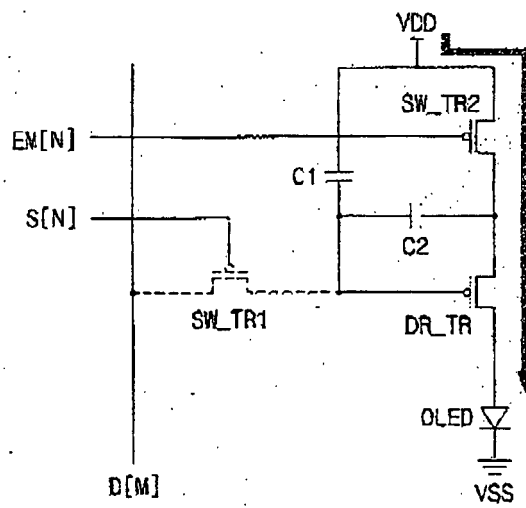


FIG.7

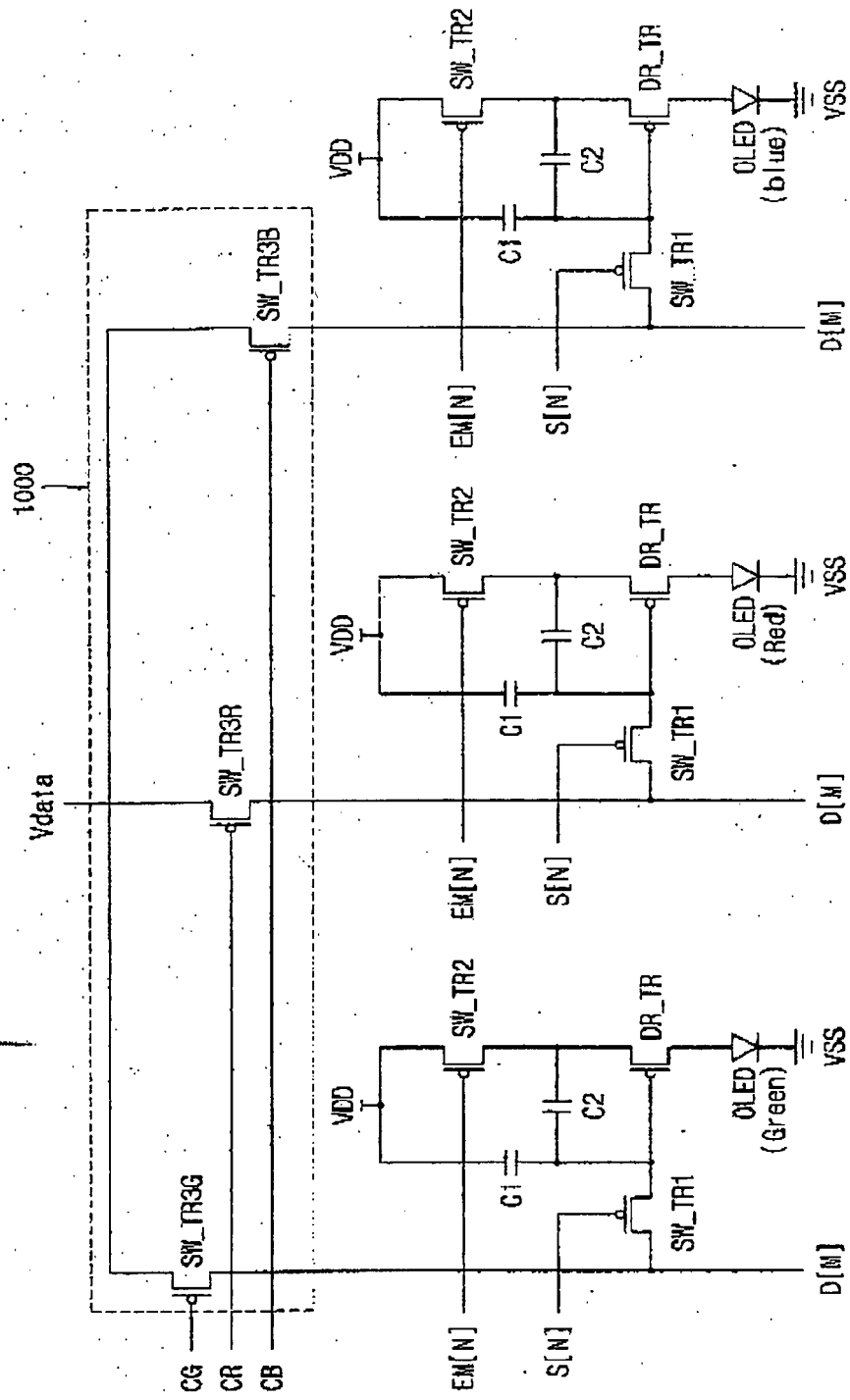


FIG.8

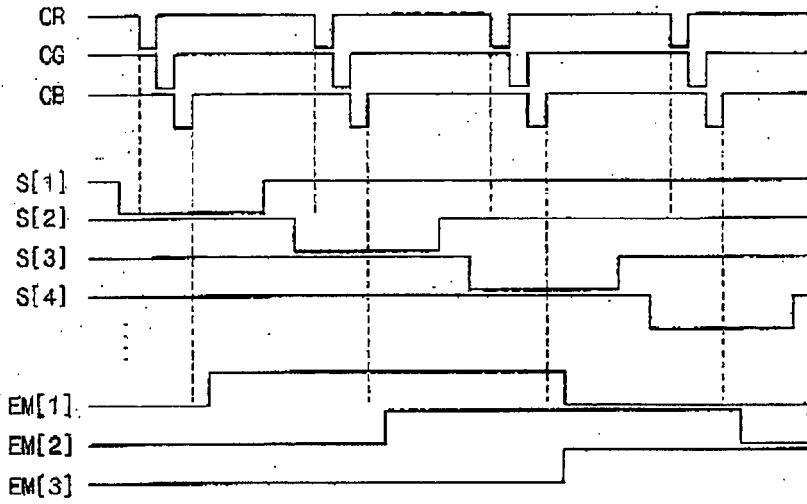


FIG.9

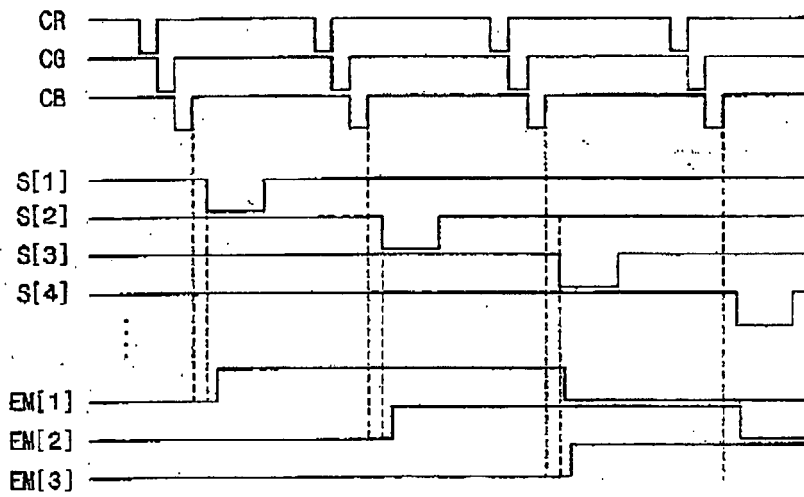
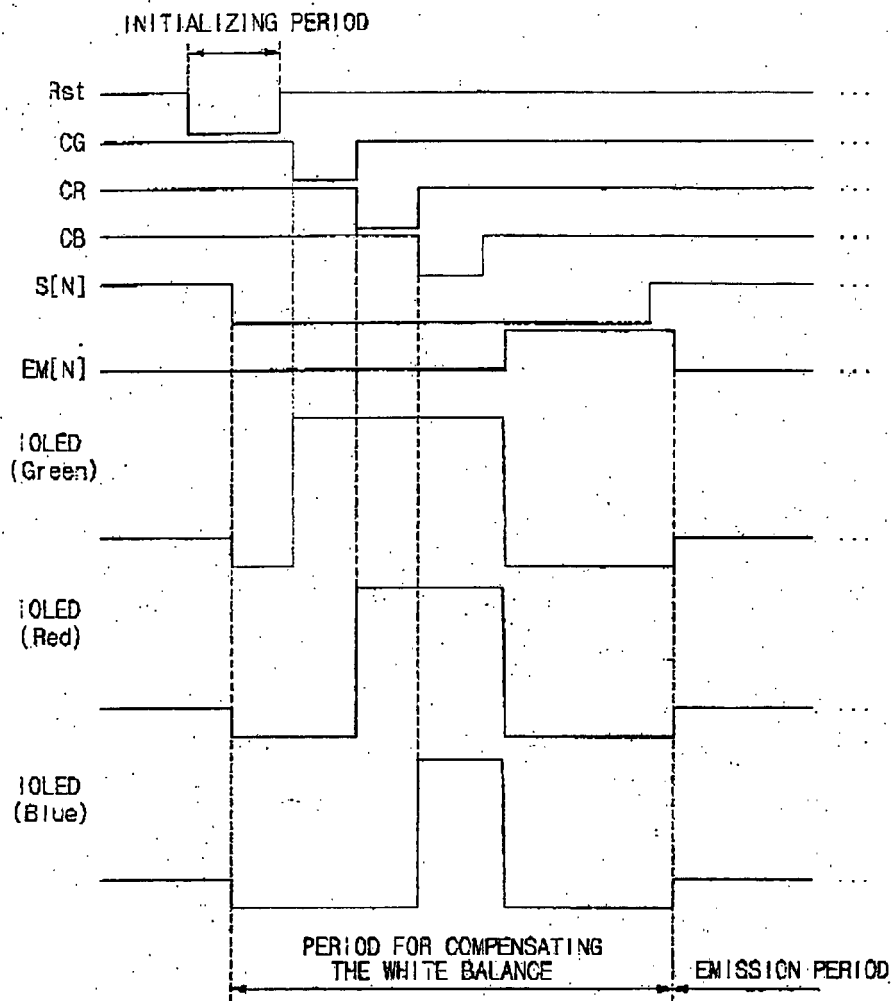




FIG. 11



**REFERENCES CITED IN THE DESCRIPTION**

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专利名称(译)	有机发光显示器		
公开(公告)号	<a href="#">EP1947633B1</a>	公开(公告)日	2015-12-02
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[标]申请(专利权)人(译)	三星斯笛爱股份有限公司		
申请(专利权)人(译)	三星SDI CO. , LTD.		
当前申请(专利权)人(译)	三星DISPLAY CO. , LTD.		
发明人	KIM, YANG WAN, C/O SAMSUNG SDI CO., LTD.		
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优先权	1020070004860 2007-01-16 KR		
其他公开文献	EP1947633A3 EP1947633A2		
外部链接	<a href="#">Espacenet</a>		

摘要(译)

有机发光显示器包括扫描线，数据线和耦合到扫描线和数据线的像素。像素被配置为至少部分地补偿晶体管阈值变化和电源线中的IR降，其中像素包括不多于三个晶体管且不多于两个电容器。

**【Formula 1】**

$$V_g = V_{data} + \left( \frac{C2}{C1+C2} \right) * (VDD - V_{data} - V_{th})$$

$$V_s = VDD$$

$$V_{Sg} = V_s - V_g$$

$$V_{Sg} = VDD - \left[ V_{data} + \left( \frac{C2}{C1+C2} \right) * (VDD - V_{data} - V_{th}) \right]$$