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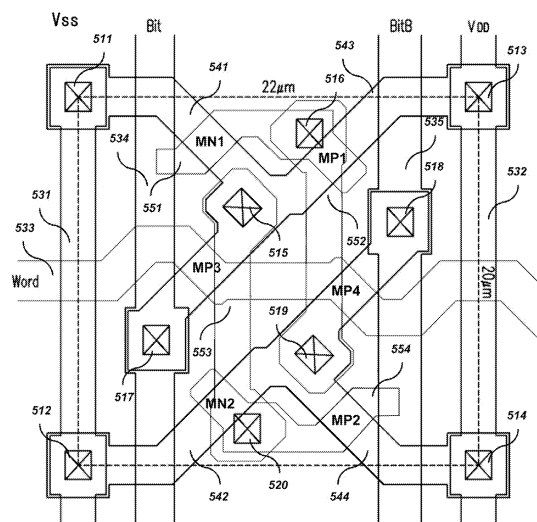
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(54) **SRAM core cell for light-emitting display**

(57) A SRAM core cell for a light-emitting display applicable to a data driver of an organic electro-luminescence light-emitting display device includes thin film transistors as a data memory for a data driver of the light-emitting display. The SRAM core cell also includes switching transistors and data memory transistors. The switching transistors are coupled to a bit line and a word line to select data writing or reading. The data memory transistors are coupled to a power voltage or a ground voltage to enable data writing and reading. The bit line and the word line are formed in first and second directions. The channels of the switching transistors and the data memory transistors are formed in an oblique direction with respect to the first and second directions.

FIG.5



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Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
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			H01L G11C
The present search report has been drawn up for all claims			
Place of search Munich		Date of completion of the search 2 April 2008	Examiner Lindquist, Jim
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document			

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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专利名称(译)	用于发光显示器的SRAM核心单元		
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摘要(译)

用于有机电致发光显示装置的数据驱动器的发光显示器的SRAM核心单元包括薄膜晶体管作为用于发光显示器的数据驱动器的数据存储单元。SRAM核心单元还包括开关晶体管和数据存储晶体管。开关晶体管耦合到位线和字线以选择数据写入或读取。数据存储晶体管耦合到电源电压或地电压以实现数据写入和读取。位线和字线沿第一和第二方向形成。开关晶体管和数据存储晶体管的沟道相对于第一和第二方向在倾斜方向上形成。

