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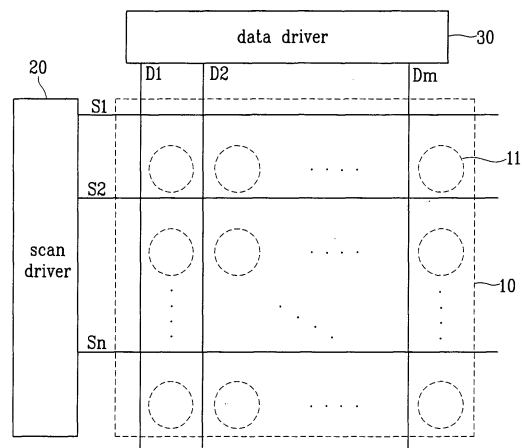
(54)

Pixel circuit, display panel, image display device and driving method thereof

(57)

In a pixel circuit of an organic EL display device, a gate of a driving transistor is coupled to a gate of a compensating transistor, which is configured to operate as a diode. A precharge voltage is applied to the gate of the driving transistor while a selection signal is applied to a previous scan line, so that the compensating transistor is biased in a forward direction to apply a data voltage on the gate of the drive transistor The driving transistor may be electrically isolated from the organic EL element (OLED) while precharging, so as to prevent the OLED from emitting a light using the precharge voltage. In addition, the driving transistor may be electrically isolated from the OLED while the data voltage is being charged, so as to prevent the OLED from emitting a light.

FIG.2





European Patent
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EUROPEAN SEARCH REPORT

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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
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			TECHNICAL FIELDS SEARCHED (Int.Cl.7)
			G09G
The present search report has been drawn up for all claims			
Place of search Munich		Date of completion of the search 2 September 2005	Examiner Fulcheri, A
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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EP 03 09 0266

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
The members are as contained in the European Patent Office EDP file on
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其他公开文献	EP1473689A2 EP1473689B1		
外部链接	Espacenet		

摘要(译)

在有机EL显示装置的像素电路中，驱动晶体管的栅极耦合到补偿晶体管的栅极，补偿晶体管被配置为用作二极管。将预充电电压施加到驱动晶体管的栅极，同时将选择信号施加到前一扫描线，使得补偿晶体管沿正向偏置，以在驱动晶体管的栅极上施加数据电压。在预充电的同时，可以与有机EL元件（OLED）电隔离，以防止OLED使用预充电电压发光。另外，在对数据电压进行充电时，驱动晶体管可以与OLED电隔离，以防止OLED发光。

FIG.2

