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(72) 가 7-806

(74)

(54)

Diagram illustrating the components and relationships in a system architecture:

- Top components: (STH), (TFT - LCD), IC, (LOAD), (TFT - LCD), IC, LCD, IC, (TFT - LCD), (LOAD).
- Bottom components: IC, (STH), (STH), (LOAD).
- Arrows indicate connections between these components.

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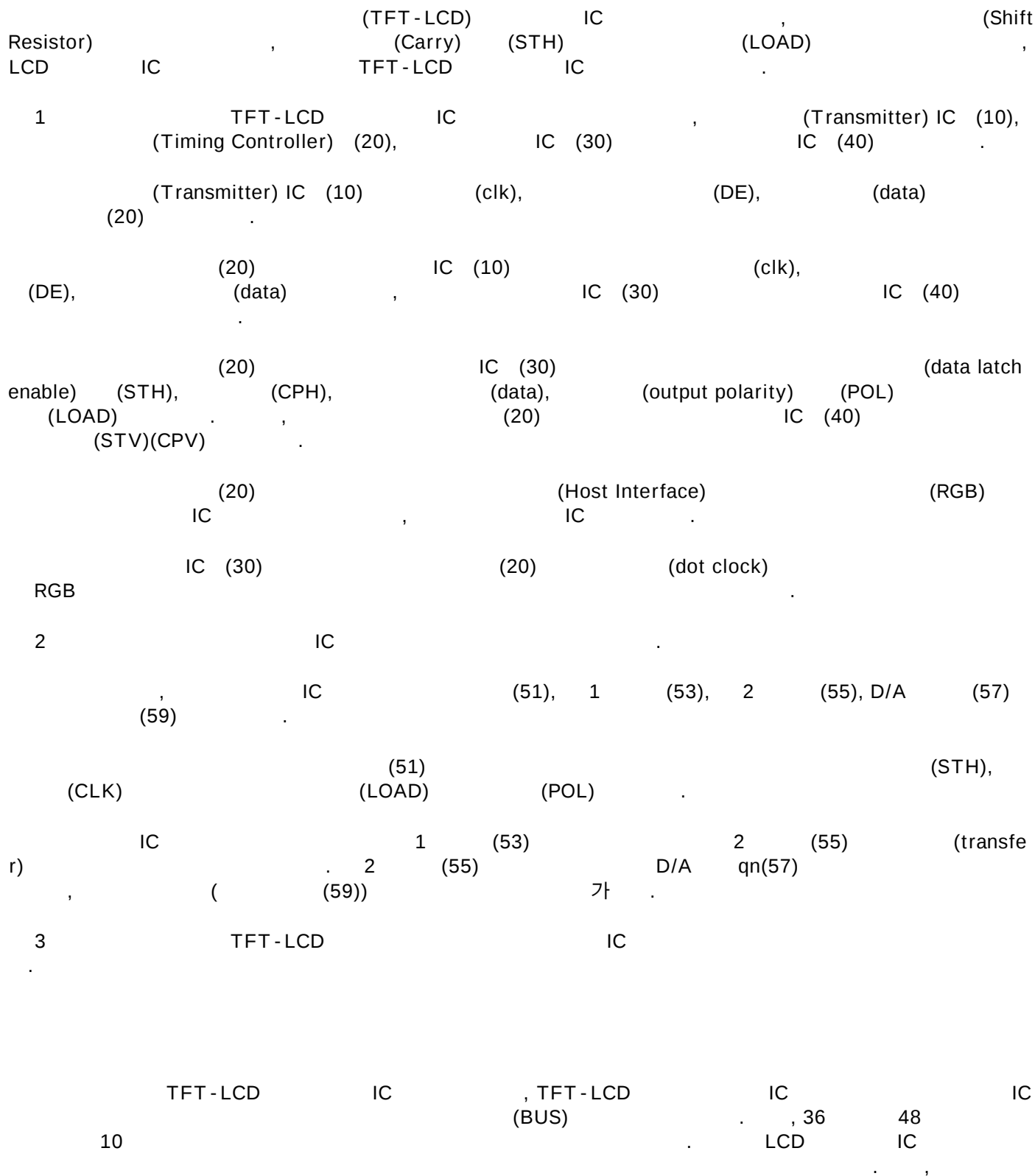
1	TFT - LCD		
2		IC	
3	TFT - LCD		IC
4		TFT - LCD	IC

()

DE : MCLK :

STH : POL :

LOAD :



가 PCB .

or) , (Shift Resist , LCD

IC , (Carry) TFT-LCD (STH) IC (LOAD) .

, TFT-LCD IC ,

TFT-LCD D) IC (STH) (STH) (LOA

4 TFT-LCD IC .

, IC (LOAD) , (STH) (transfer)

(LOAD) (rising edge)

(falling edge)

, (STH) (63 clk)

(trigger)

, 가 (LOAD)

IC 가 IC .

TFT-LCD (STH) (LOAD) 8 IC ,

STH2 IC IC (STH2) (LOAD)

(STH)

, TFT-LCD IC (STH) (STH)

(LOAD)

(Shift Resistor) , (Carry) (TFT-LCD) IC ,

, LCD IC (STH) (LOAD)

, , 가 가 ,

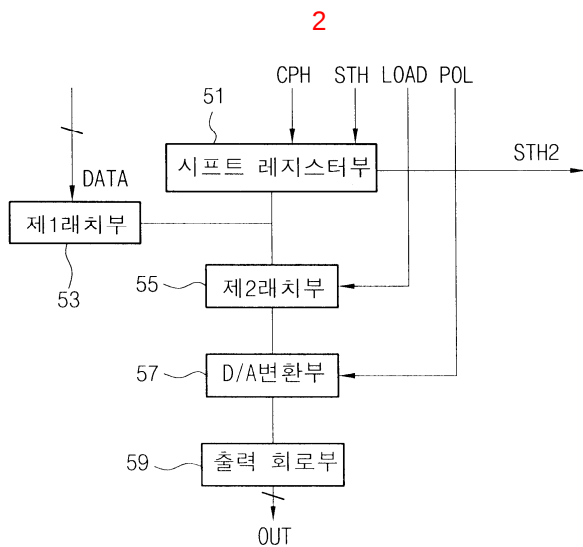
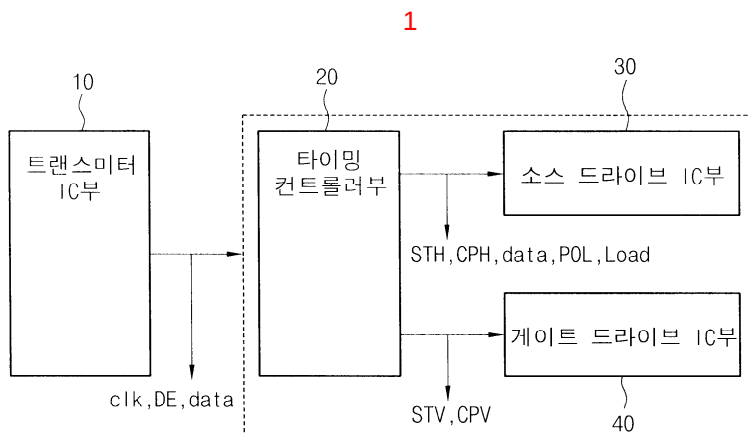
(57)

1.
TFT-LCD
D)

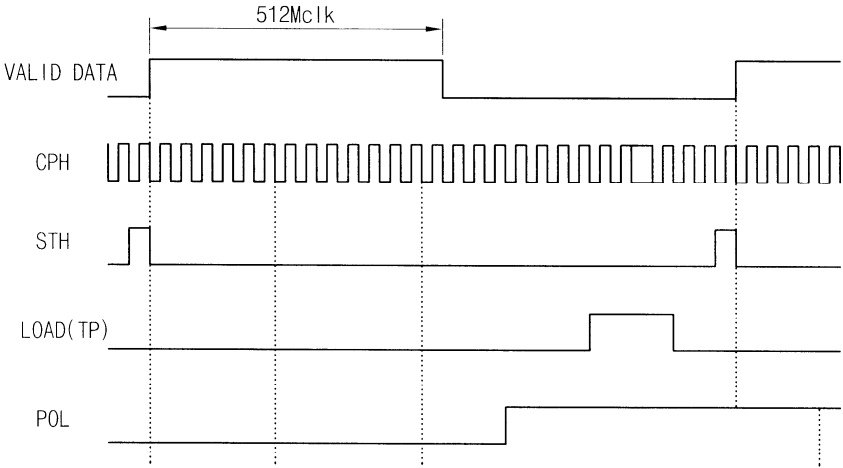
IC

(STH)
(TFT-LCD)
IC

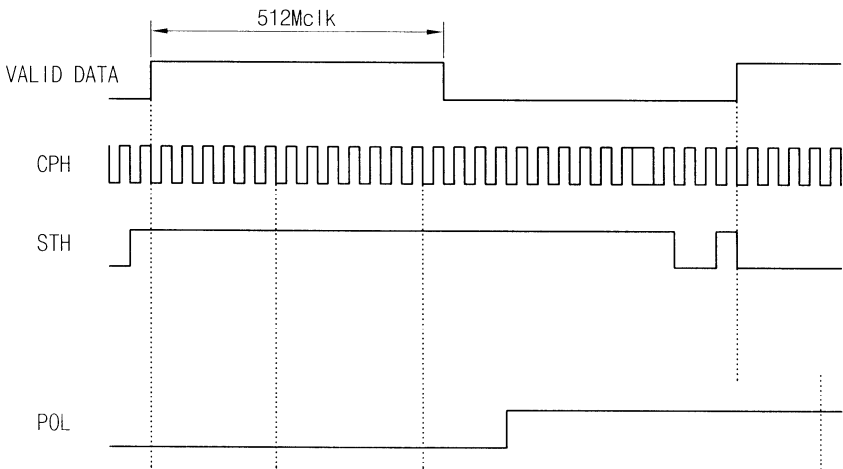
(LOA



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专利名称(译)	薄膜晶体管液晶显示器的驱动IC		
公开(公告)号	KR1020030058618A	公开(公告)日	2003-07-07
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IPC分类号	G09G3/36		
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其他公开文献	KR100867482B1		
外部链接	Espacenet		

摘要(译)

目的：提供一种用于TFT-LCD的驱动IC，通过同时使能移位寄存器的信号，即数据进位信号和数据锁存信号，并同时使用上行沿和下降沿，来减少信号线的数量。数据携带信号以执行信号处理过程。组成：TFT-LCD的驱动IC同时使用数据进位信号和控制信号的数据锁存信号，控制信号用于时钟控制器和TFT-LCD的驱动IC之间。用于TFT-LCD的驱动IC的特征在于，通过同时使用数据进位信号的上升沿和下降沿来减少信号线的数量，以便执行信号处理过程。

