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10-2004-0044539
2004 05 28

(21)	10-2004-7003258		
(22)	2004 03 04		
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(86)	PCT/IB2002/003383	(87)	WO 2003/021558
(86)	2002 08 21	(87)	2003 03 13

(30)	09/947,814	2001 09 06	(US)
(71)			
	,	,	1
(72)	,		
	,	-5656,	6
(74)			
	:		

(54)

$V_{AS7}, V_{AS8}, V_{AS9}$)
(13) 가
($V_{DS1}, V_{DS2}, V_{DS3}$)
9)

(V_{REF})

($V_{AS7}, V_{AS8}, V_{AS9}$)

(V_{REF})

가
.
(14) 가 (

(13) ($V_{AS7}, V_{AS8}, V_{AS9}$)
($V_{DS1}, V_{DS2}, V_{DS3}$)
($V_{AS7}, V_{AS8}, V_{AS9}$)
.

1 LCD (13) (V_{AS4-AS6}) (luminous output:14)
 (column:) 가 LCD (13) LCD ()
 (counter electrode) (top plate:)
 (V_{REF})

LCD (V_{REF}) (V_{AS4-AS6}) 2 (V_{REF}) 가 6 (V_{AS4-AS6})
 AS4-AS6) 0V 12V (V_{AS4-AS6})
 6V 12V (inversion polarity) 0V 6V (V_{AS4-AS6})
 (V_{AS4-AS6})

(10) LCD (13) (V_{AS4-AS6}) (V_{DS1-DS3})
 DS3) , LCD (13) (14) (V_{DS1-DS3})
 1111111(3 1) 256 00000000(3 0) 1
 (V_{DS1-DS3}) (V_{AS4-AS6}) (V_{DS1-DS3})
 (DAC: 11) (14) (luminance)
 V_{AS4-AS6}) (V_{DS1-DS3}) (V_{AS4-AS6})
 V_{AS4-AS6}) (V_{AS4-AS6}) (V_{AS1}) (V_{AS})
 4-AS6) (, (2*V_{REF}) - V_{AS1})

(14) (V_{AS4-AS6}) LCD (13) 가
 (V_{AS4-AS6}) LCD (13) , 4
 (14) (V_{DS1-DS3}) (time-based amplitude measurement)
 (14)

, LCD (13) (10), DAC(11), (12) (14)
 , LCD (13)

LCD

LCD 1 LCD 가 (listing)

2 가 1 1

1 (LCD)

2 2

3 1 LCD

4 1 LCD

5 1 LCD

6a 1 LCD

6b 1 LCD

6c 1 LCD

7a 6a-6c

7b 6a-6c

8a

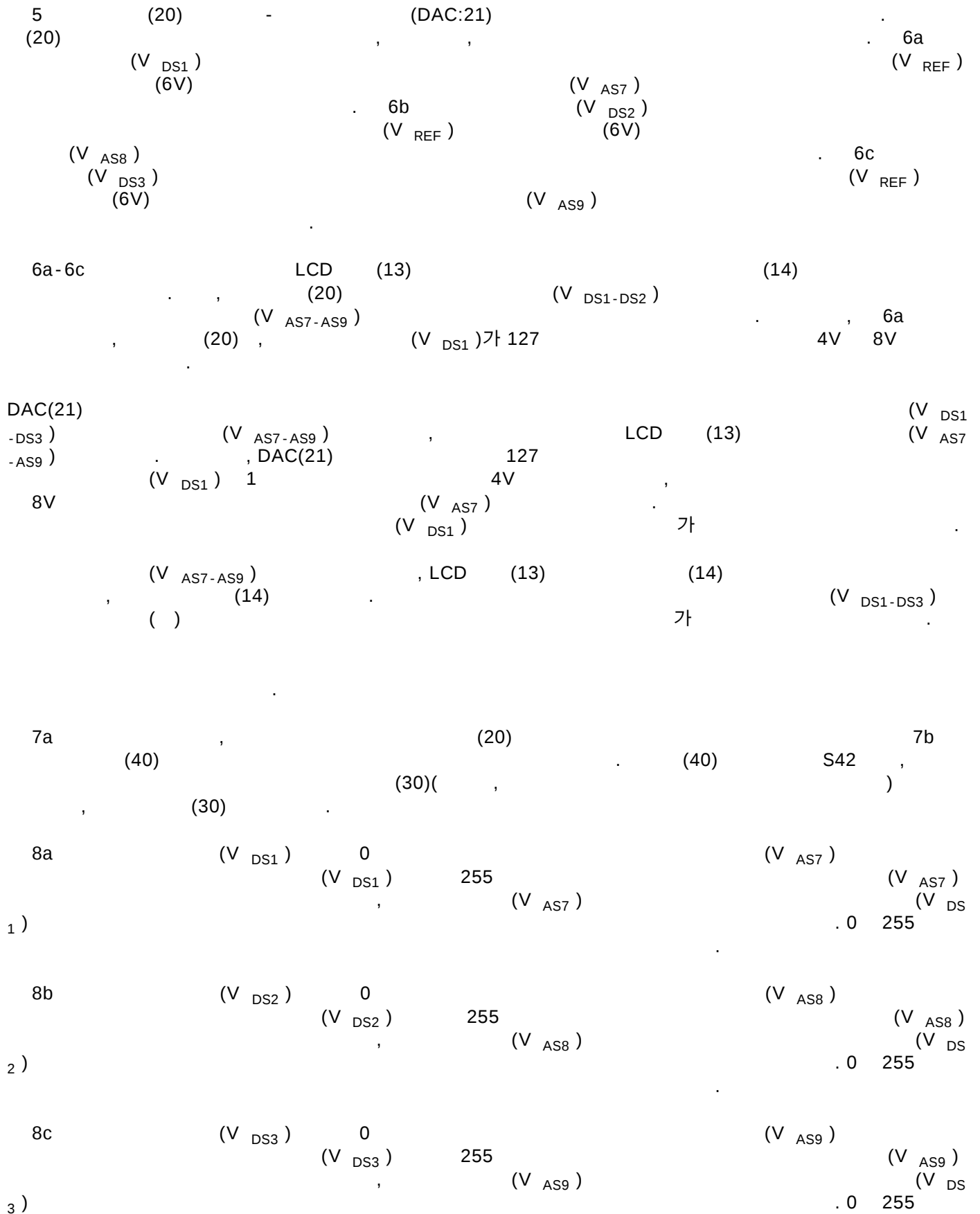
8b

8c

9

7

(projector)



, 7a 7b , (40) S44 (30) , (V_{DS1-DS3})
 O , S44 (30) (V_{DS1-DS3})
 33) (40) S46 (31) (blank out) (31) 가 (
 , S46 (31)
 , (33)
 (40) S48 (32) (33)
 , (32) 1 (33)
 (photometric filter)
 (data acquisition card) (32)
 가 (V_{MS})
 9 (33)
 (40) S50 (30) (V_{MS})
 6V 가 , 3 9
 (equalize)
 S44-S50 , 8a 6a
 , 8b 6b
 , 8c 6c , 6a-6c
 (10)(5) , LCD (13)(5) (1
 4)(5)
 가 , 가

(57)

1.

(V_{AS7}, V_{AS8}, V_{AS9}) (V_{REF}) (14)
 가 (13) ,

(gray level)

(13)

(V_{AS7}, V_{AS8}, V_{AS9}) (V_{DS1}, V_{DS2}, V_{DS3})
 가 (V_{AS7}, V_{AS8}, V_{AS9}) ,

(V_{DS1}, V_{DS2}, V_{DS3})
 (V_{AS7}, V_{AS8}, V_{AS9})

(V_{AS7}, V_{AS8}, V_{AS9})(V_{REF})

2.

1

$$\begin{matrix} (V_{DS1}, V_{DS2}, V_{DS3}) \\ \text{가} \end{matrix} \quad (20) ,$$

$$\begin{matrix} (V_{AS7}, V_{AS8}, V_{AS9}) \\ (31) \end{matrix} , \quad \begin{matrix} (20) \\ (V_{DS1}, V_{DS2}, V_{DS3}) \end{matrix} \quad \text{가} \quad -$$

3.

$$\begin{matrix} (V_{AS7}, V_{AS8}, V_{AS9}) \\ \text{가} \end{matrix} \quad (13) , \quad (V_{REF}) \quad (14)$$

$$\begin{matrix} (V_{DS1}, V_{DS2}, V_{DS3}) \\ \text{가} \end{matrix} \quad (14) , \quad (V_{AS7}, V_{AS8}, V_{AS9}) \quad (20,21) , \quad .$$

4.

$$\begin{matrix} (V_{AS7}, V_{AS8}, V_{AS9}) \\ (14) \end{matrix} \quad \text{가} \quad (V_{REF}) \quad (13)$$

$$\begin{matrix} 1 \\ S2, V_{DS3} \end{matrix} \quad 1 \quad 1 \quad 1 \quad (V_{DS1}, V_{DS2}, V_{DS3})$$

$$\begin{matrix} 1 \\ (V_{AS7}, V_{AS8}, V_{AS9}) \end{matrix} \quad 1 \quad (V_{DS1}, V_{DS2}, V_{DS3}) \quad 1 \quad (V_{REF})$$

$$\begin{matrix} 1 \\ \text{(frame inversion manner)} \end{matrix} \quad 1 \quad (13) \quad \text{가} \quad 1 \quad 1$$

5.

$$\begin{matrix} 4 \\ S2, V_{DS3} \end{matrix} \quad 2 \quad 2 \quad 2 \quad (V_{DS1}, V_{DS2}, V_{DS3})$$

$$\begin{matrix} 2 \\ (V_{AS7}, V_{AS8}, V_{AS9}) \end{matrix} \quad 2 \quad (V_{DS1}, V_{DS2}, V_{DS3}) \quad 2 \quad (V_{REF})$$

$$\begin{matrix} 2 \\ (13) \end{matrix} \quad 2 \quad (V_{DS1}, V_{DS2}, V_{DS3}) \quad 2$$

6.

$$\begin{matrix} 4 \\ DS1, V_{DS2}, V_{DS3} \end{matrix} \quad 1 \quad 2 \quad 2 \quad 1 \quad (V_{DS1}, V_{DS2}, V_{DS3})$$

$$\begin{matrix} 2 \\ (V_{AS7}, V_{AS8}, V_{AS9}) \end{matrix} \quad 1 \quad (V_{DS1}, V_{DS2}, V_{DS3}) \quad 2 \quad (V_{REF})$$

2

1

(13)

1

(V_{AS7}, V_{AS8}, V_{AS9})
가

2

7.

1

1

1

1

(V_{DS1}, V_D
S2, V_{DS3})

1

1

(V_{AS7}, V_{AS8}, V_{AS9})
(13)가

1

(V_{DS1}, V_{DS2}, V_{DS3})
1

(V_{REF})

1

1

(V_{AS7}, V_{AS8}
1

(13)

1

(V_{AS7}, V_{AS8}, V_{AS9})
(V_{AS7}, V_{AS8}, V_{AS9})

8.

7

2

2

2

2

(V_{DS1}, V_D
S2, V_{DS3})

2

2

(V_{DS1}, V_{DS2}, V_{DS3})
(13)가

2

(V_{REF})

2

2

(V_{DS1}, V_{DS2}
2

(13)

2

(V_{DS1}, V_{DS2}, V_{DS3})
(V_{DS1}, V_{DS2}, V_{DS3})

9.

8

1

2

2

1

(V_{DS1}, V_{DS2}, V_{DS3})

2

1

(V_{AS7}, V_{AS8}, V_{AS9})
(13)가

2

(V_{DS1}, V_{DS2}, V_{DS3})
2

(V_{REF})

2

1

(V_{AS7}, V_{AS8}
1

(13)

2

(V_{AS7}, V_{AS8}, V_{AS9})
(V_{AS7}, V_{AS8}, V_{AS9})

10.

(V_{DS1}, V_{DS2}, V_{DS3})
(14)

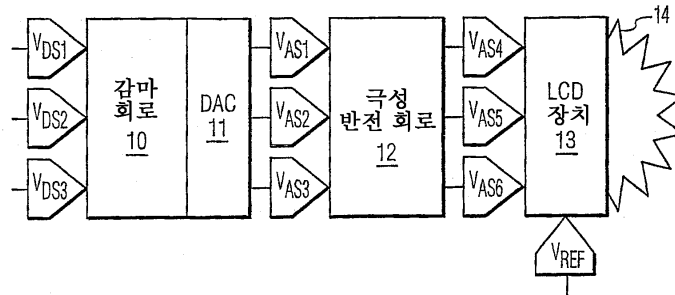
(13)

(14)

가

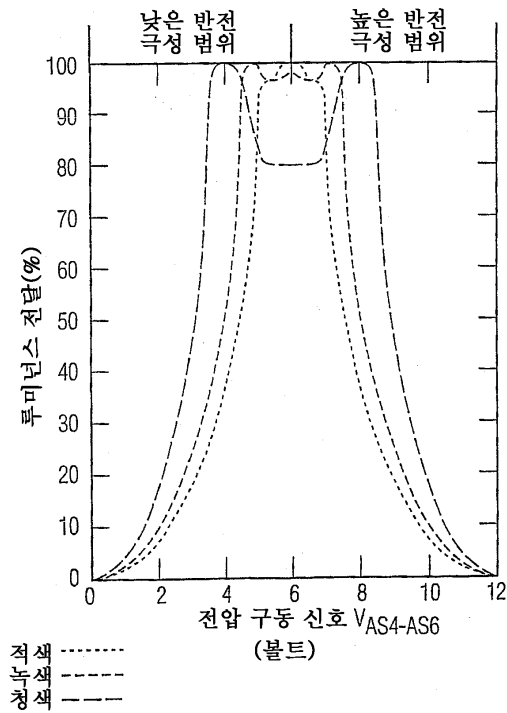
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종래기술



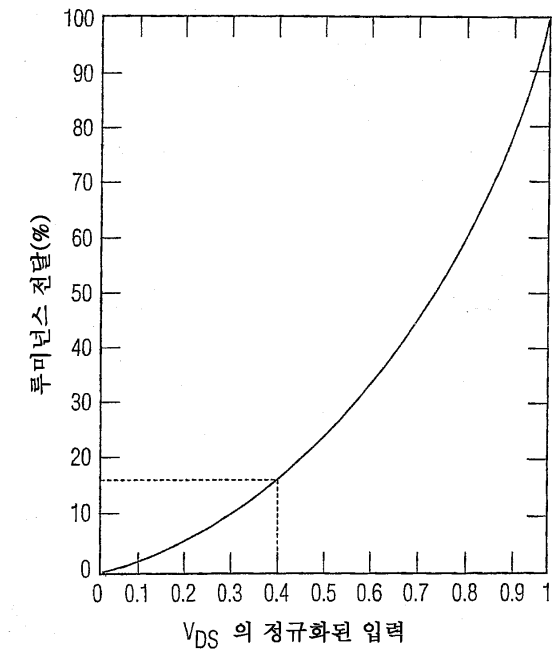
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종래기술



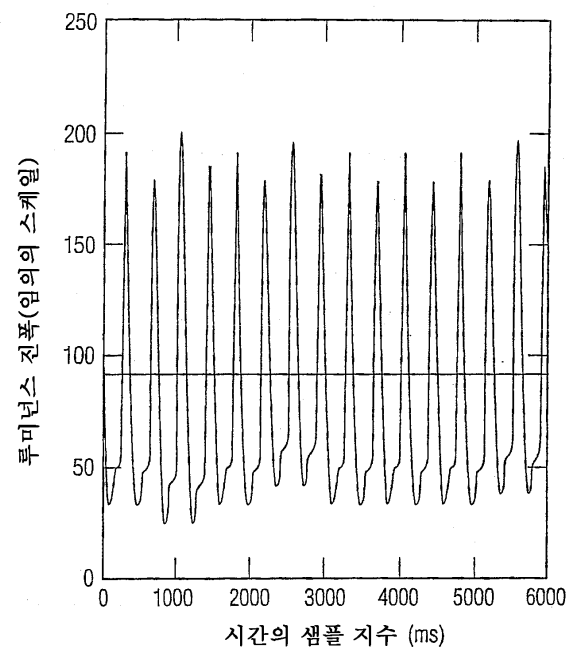
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종래기술

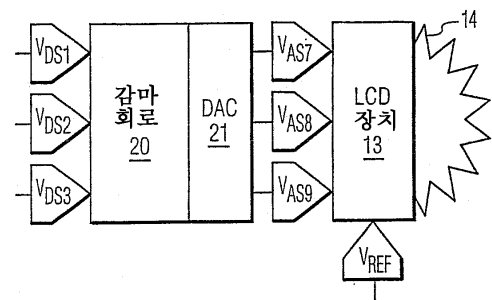


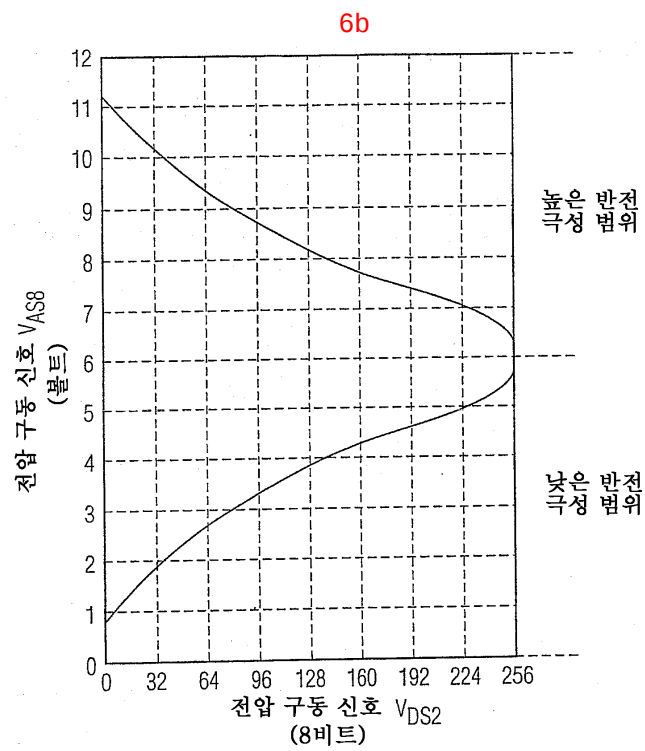
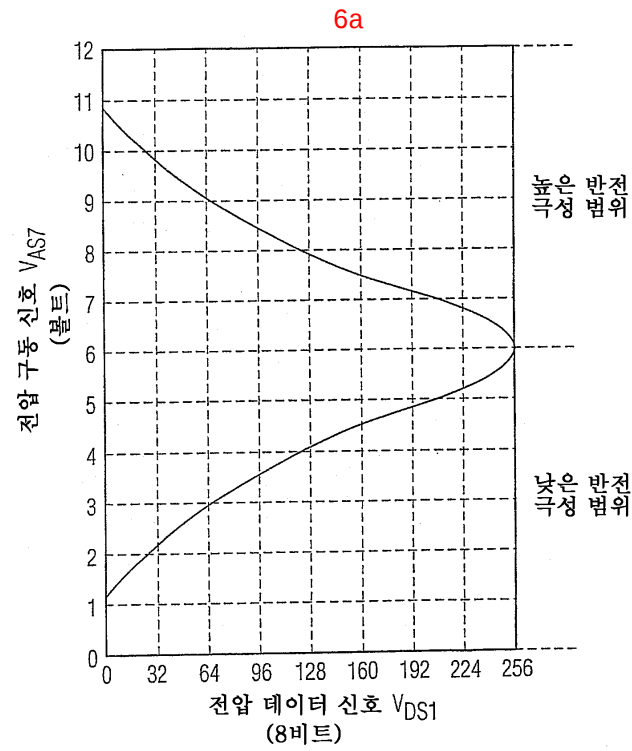
4

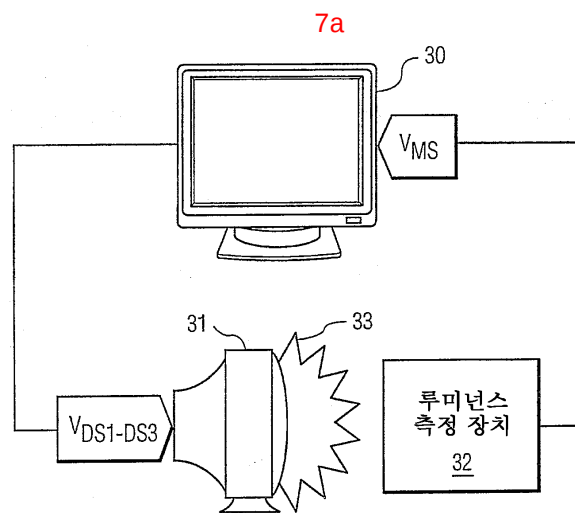
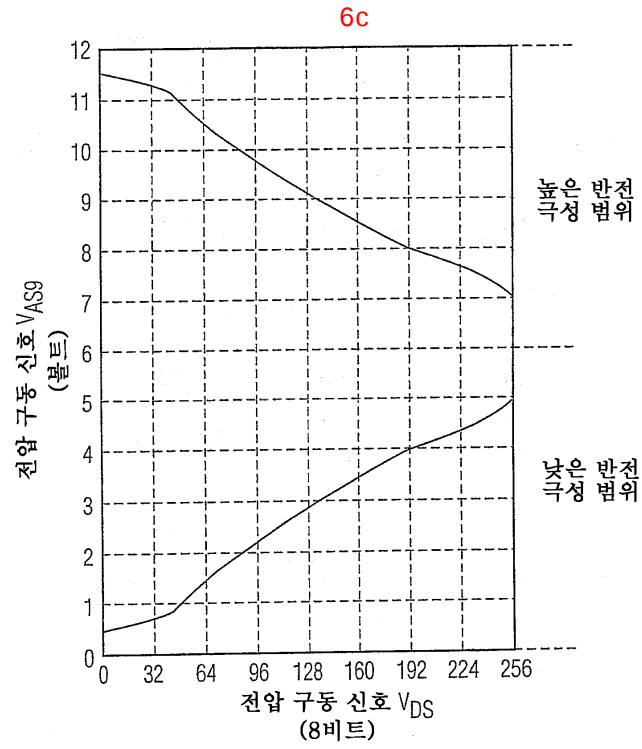
종래기술

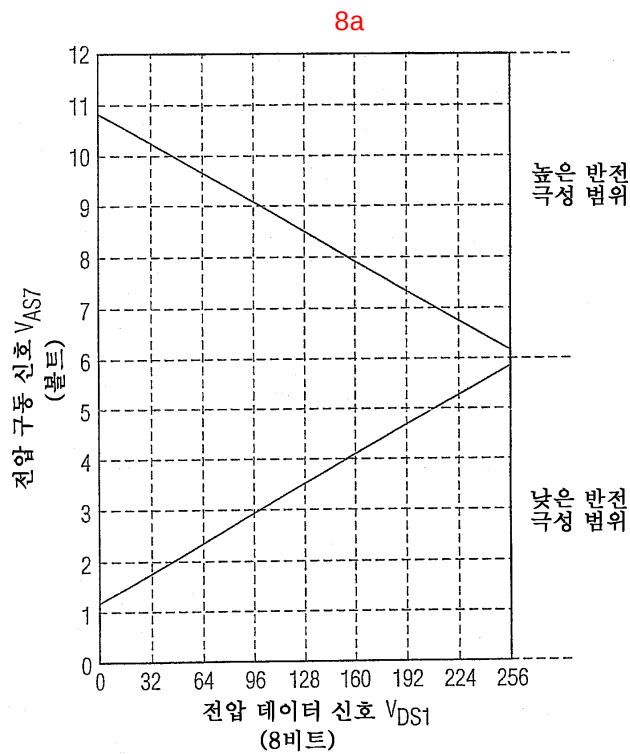
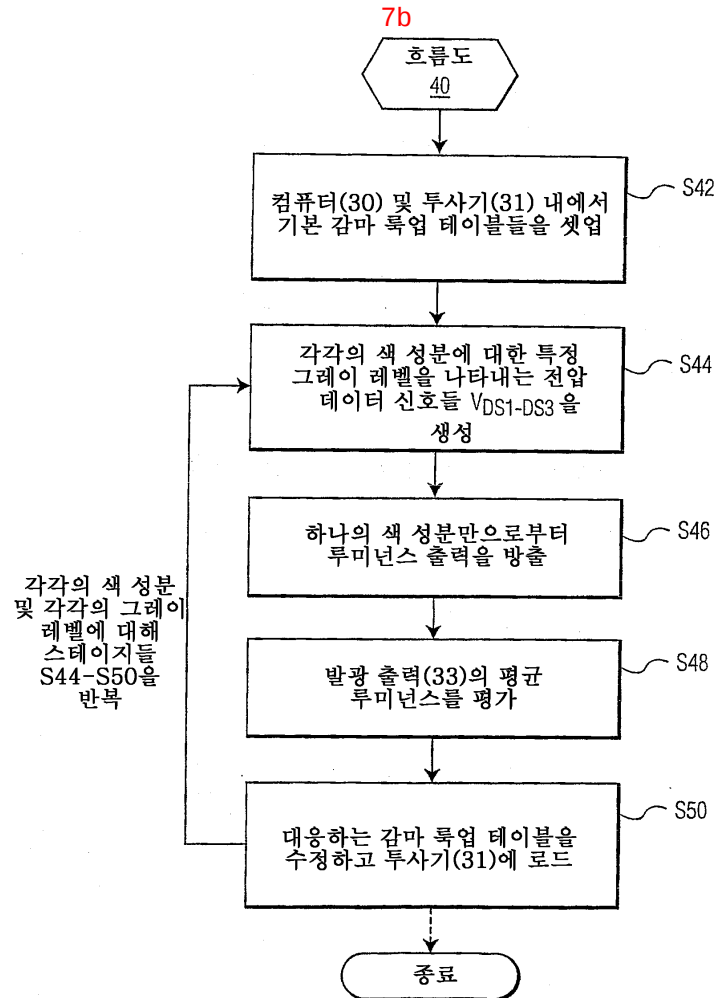


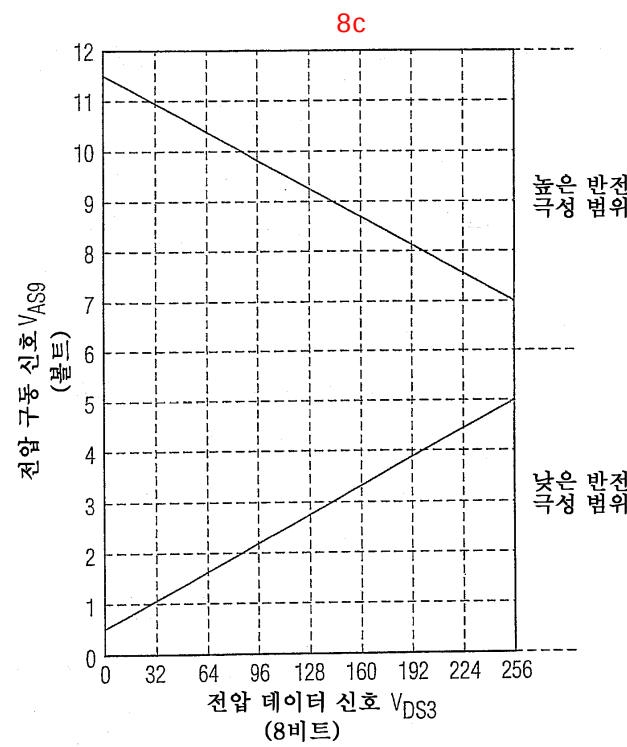
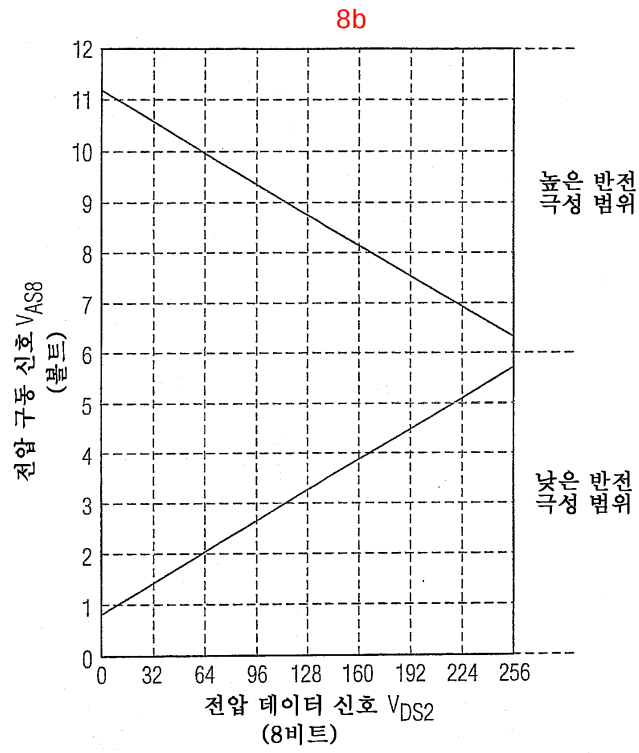
5

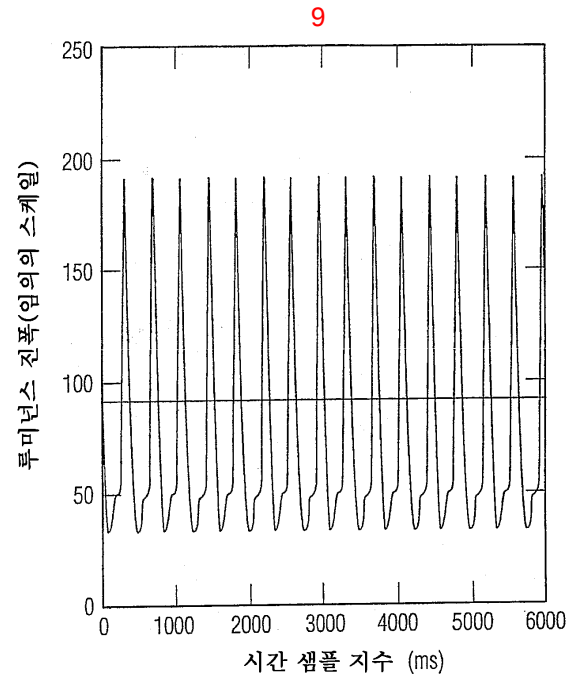












专利名称(译)	具有版本内闪烁补偿的液晶显示器		
公开(公告)号	KR1020040044539A	公开(公告)日	2004-05-28
申请号	KR1020047003258	申请日	2002-08-21
[标]申请(专利权)人(译)	皇家飞利浦电子股份有限公司		
申请(专利权)人(译)	科宁欣克利凯恩菲利普斯日元.V.		
当前申请(专利权)人(译)	科宁欣克利凯恩菲利普斯日元.V.		
[标]发明人	DALAL SANDEEP M		
发明人	DALAL,SANDEEP,M.		
IPC分类号	G09G3/36 G09G3/20 G02F1/133		
CPC分类号	G09G2320/0247 G09G2310/027 G09G3/2011 G09G3/3614 G09G3/3688 G09G2320/0276		
代理人(译)	李昌勋 李，何炳 申铉MOON 李贝尔		
优先权	09/947814 2001-09-06 US		
外部链接	Espacenet		

摘要(译)

公开了一种用于实现版本内闪烁补偿方法的显示驱动器。用于电压驱动信号 (V_{AS7}, V_{AS8}, V_{AS9}) 和电压参考信号 (V_{REF}) 的光输出 14, 液晶显示装置 (13) 可操作以响应于发射于接收到所述反转闪烁补偿方法它是适用的。显示驱动器具有具有表示灰度级的数据电压电平的电压数据信号的颜色分量 (V_{DS1}, V_{DS2}, V_{DS3}) 响应于接收的电压驱动信号, 液晶显示装置 (13) (的, V_{AS8} V_{AS7}, V_{AS9}。 < S > 由数据电压电平所指示的对应于灰度级的电压数据信号电压的驱动信号 (V_{AS7}, V_{AS8}, V_{AS9}) 的驱动电压电平的显示致动器 (V_{DS1}, V_{DS2}, V_{DS3}) 以及列出这些对的电压驱动信号 (V_{AS7}, V_{AS8}, V_{AS9}) 的伽玛查找表。驱动电压电平具有相对于电压参考信号 (V_{REF}) 的参考电压电平的相反极性。 1 指数方面 伽玛查找表, 灰度级, 反转闪烁补偿

종래기술

