

(19) (12) (KR) (A)

(51) 。 Int. Cl.⁷ (11) 10-2004-0017717
G02F 1/133 (43) 2004 02 27

(21) 10-2002-0050149
(22) 2002 08 23

(71) . 20
(72) 73 2 112 806
605-212

(74)
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(54)

RGB RGB , RGB
, , RGB
; , RGB RGB
; RGB RGB
;
7
1
2
3
4 , RGB

5 4 A

6

7

<
>

110 : 136 :

138 : 170 :

180 : 182 :

184 :

186a, 186b : 1 2

188 : 190 :

200 : T :

$$C_{LC} : \quad C_{ST} :$$

P :

(liquid crystal display) , (field sequential)

가 (display)

play : LCD)가 , (Cathode-Ray Tube : CRT) (Liquid Crystal Dis

가 가

가

1 (Active Matrix LCD : AM-LCD) , 가

$$(10) \quad (20) \quad (30) \quad , \quad (50)$$

(20) (24) (1) (22) ,
 (22) ,
 (30) , (26) .
 (30) (T) , (T) (10)
 (50) 가 (34) (25, 35) .
 (20, 30) 1 2 (25, 35) .
 (10) (62) (60)가
 (10) , (10)
 , (10) (T) (24) (34)
 (50) (60) (50)
 (22) 1 2 (25, 35)
 , (22) , ,
 (20, 30) (50) (50) 가
 (封函) , , (20, 30) (50)
 , .
 (10) 가 가 (60) , 가 ,
 33% (on) . 가 .
 (22) 가 , , 가
 .
 가 .
 B , 가 R, G, B R, G,
 .
 2 , 3
 , 1 .
 , (70)
 RGB (80) , (60)
 (10) , (10)
 .
 (10) , (20, 30) (36) (38)
 (P) , (P) (34) (T) ,
 (34) , (50) (24) .
 (34) (24) (C_{LC}) ,
 (C_{LC})
 C_{ST}
 (storage capacitor) .
 (10) 가 , (60) R, G, B
 R (64) , G (66) , B (68) ,
 1/60 (frame) 1 3
 (sub frame) .

B (64, 66, 68) 1/180 가 , R, G, (64, 66, 68) 가 R, G, B (64, 66, 68)

(80) (70) RGB (82) , (84) , (10) (86) , (88) , (90) , (84)

(82) (70) RGB (84)

가 , (84) (88) (82) RGB (90) (88)

(86) RGB (10) 가 RGB (88)

RGB (88) , (86) RGB (90) (scan) (36) (38) (T) / (on/off)

(P) (T) (C_{LC}) RGB

R, G, B (64, 66, 68) , R, G, B , 1 (84) (70) (82) R 가

(86) R (36) R (90)가 (88) R G₁ (全) Gm (P)

G₁ , (全) (38) Gm R (64) 가 R

(82) G 가 가 B G 2 1 3 3

1 3 1/180 가 1/60 R, G, B

R, G, B (black)

4 가 가 (OCB : O ptical Controlled Birefringence) (white) (black) RGB

5 4 A RGB

[illegible]

5)

, (110) , (120, 130) (136) (138)
) (P) (P) (T) ,
 (134) , (150) (134) (124)
 .
 (134) (124) (C_{LC}) ,
 C_{ST}
 (storage capacitor) .
 (110) (160) R, G, B
 R (164) , G (166) , B (168) .
 , (120, 130) (150) 가
 (封函) , , (120, 130) (150)
 , .
 (180) (170) RGB
 (110) , (182) , (184)
 , (188) , (190) .
 (180) RGB
 (200) , 1 2 (188)
 (186a, 186b)
 , (182) (170) RGB
 (184) .
 가 , (184)
 (188) (190)
 (182) RGB (188) .
 1 2 (186a, 186b) RGB RGB
 , (200) RGB (188) .
 RGB
 RGB (200)가 (188) RGB ,
 (188) , tm (200) RGB ((136) (138) RGB (T) / (on/off) (190) (scan)
 .
 (P) (T) (C_{LC}) RGB .
 (110) 가 TN STN
 가 .
 , , OCB B RGB R, G
 1 (186a)가 RGB (110) RGB B
 1 (186a) RG , 2 (186b)가 RGB B

1 (186a) 2 (186b) RGB (200)
 (186a) R, G (188) 1 2 1
 , 3 2 (186b) B

R, G, B (164, 166, 168) , R, G, B 1
 (170) (182) R 가 , (184)

1 2 (186a, 188b) R (200)
 (188) (188)가 R (全) (136)

(190)가 G₁ Gm G₁
 (P) 가 R 가 (164) R
 (全) (136)

2 (170) (182) G 가
 (184) (184)

0) 1 2 (186a, 188b) 가 G (20
 (136) (188) G (全)
 Gm (190)가 G₁
 (全) (136) 가 (P) G (166) G

3 1 2 (186a, 188b) B
 (200) (188) B
 (全) (136) (190)가 B (168) G₁
 Gm B

B 1 (186a) 2 (186b)가 RGB , 3
 1/180 가 1/60
 , R, G, B

3 가 , RGB
 가 , RGB
 가 ,

RGB

가 R, G, B 가 ,
가 RGB ,
가 .

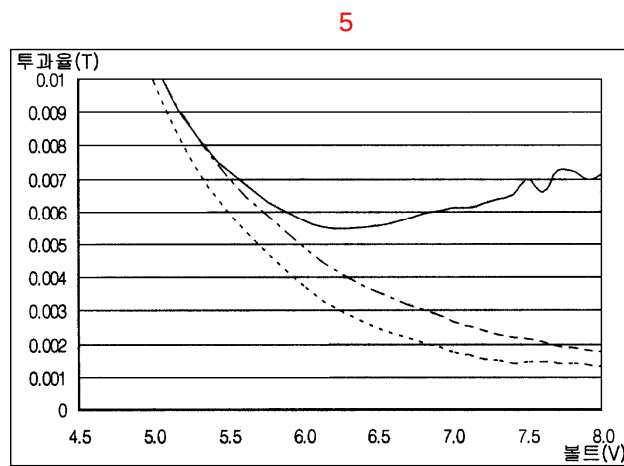
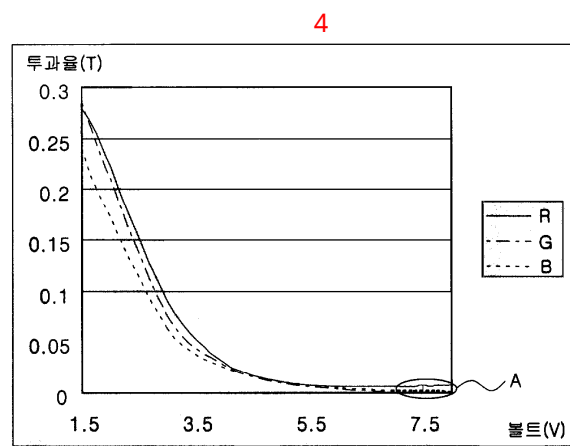
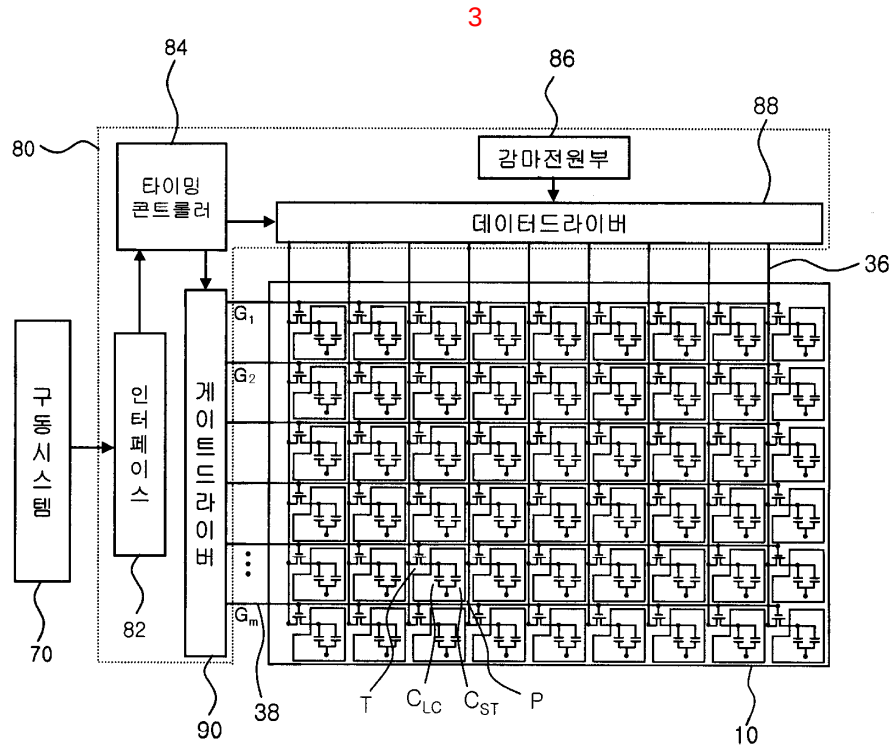
(57)

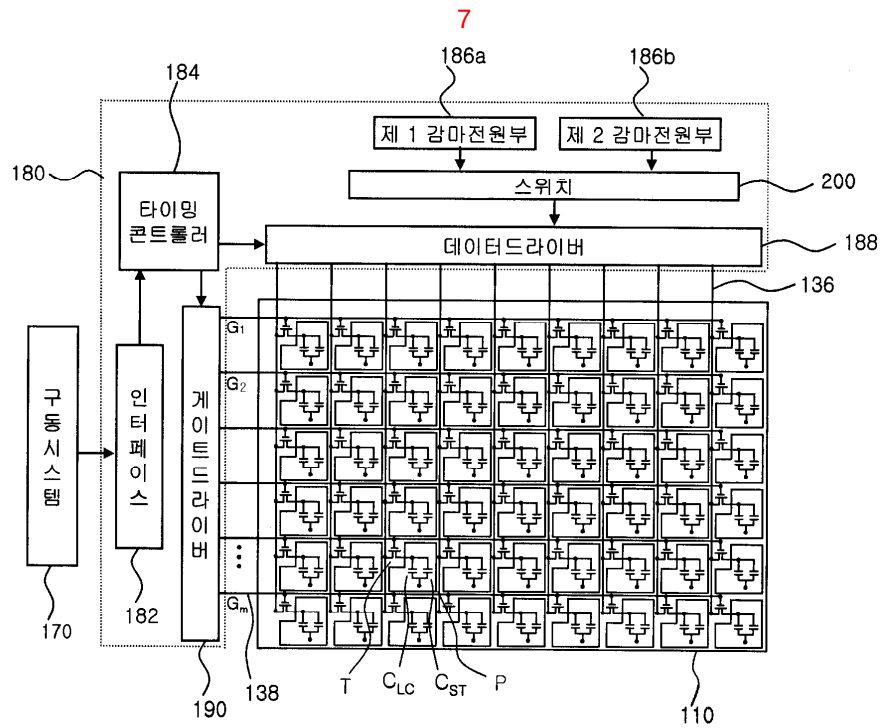
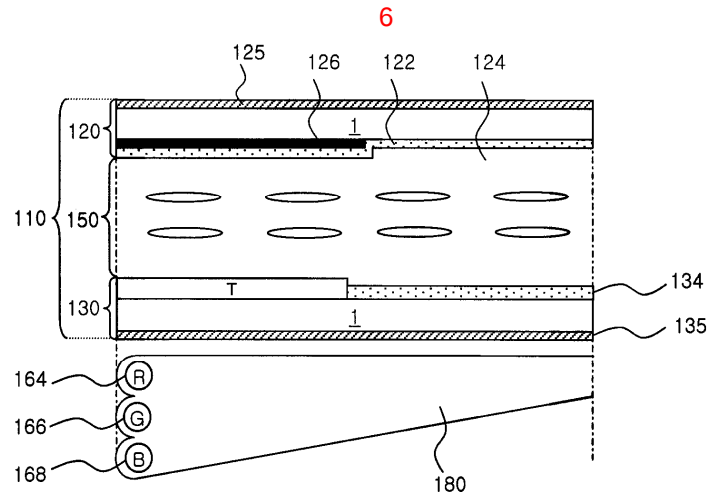
1. RGB RGB , RGB
 ,
 ,
RGB ;
RGB RGB ;
 ;
RGB RGB
 ;

2. 1 ,
 , B , R , G
 , B , G , B
G , B B R , G
G , B R , G
B

3. 2 ,

4. 2 ,
1 2 ,





The block diagram illustrates the architecture of a memory system. On the left, a "구동 시스템" (Driving System) labeled 170 is connected to an "인터페이스" (Interface) labeled 182. The interface 182 connects to a "타이밍 컨트롤러" (Timing Controller) labeled 184. The timing controller 184 sends signals to two components at the top: "제 1 감마전원부" (First Gamma Voltage Source) labeled 186a and "제 2 감마전원부" (Second Gamma Voltage Source) labeled 186b. These sources feed into a "스위치" (Switch) labeled 200. The switch 200 is controlled by the timing controller 184 and provides input to a "데이터드라이버" (Data Driver) labeled 188. The data driver 188 is also controlled by the timing controller 184 and drives a grid of pixels labeled 190. The pixel array 190 has rows labeled G₁, G₂, ..., G_n and columns labeled T, C_{LC}, C_{ST}, P, and 110. A label 136 points to the right side of the pixel array, and a label 138 points to the bottom row of the array.