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JP-P-2002-00063394 2002 03 08

(JP)

(71) 가 가 2 2 3

$$(72) \quad \begin{array}{ccccc} & 2 & 2 & 3 & \text{가} & \text{가} \end{array}$$

2 2 3 가 가

(74)

1

(54)

2

1	1
2	1
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4 1 ,
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10 1 ,
11 . 가 ,
12 2 ,
13 2 1 ,
14 13 ,
15 13 ,
16 2 2 ,
17 16 ,
18 16 ,
19 2 3 ,
20 19 ,
21 19 ,
22 3 ,
23 3 1 ,
24 23 ,
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26 3 2 ,
27 26 ,
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29 3 3 ,
30 29 ,
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32 4 ,

33	4		,
34	5		,
35	5		,
36	5	1	,
37	36		,
38	36		,
39	36		,
40	5	2	,
41	40		,
42	40		,
43	40		,
44	40		,
45	5	3	,
46	45		,
47	45		,
48	45		,
49	6		,
50	7		,
51	7		,
52	7	1	,
53	52		,
54	52		,
55	52		,
56	7	2	,
57	7	3	,
58	7	4	,
59	8		,
60	8		,
61	9		,

62 9 ,
63 10 ,
64 10 ,
65 10 ,
66 11 1 ,
67 11 ,
68 11 2 ,
69 11 3 ,
70 11 4 ,
71 11 5 ,
72 ,
73 ,
74 .

1 : 2 :
3 : 4 :
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7 : 8 :
9 : 10 :
11 : St1 :
St2 : St3 :
St4 : St5 :
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12	13	St6	,	(8) (12)	,	Db1	가	.
		St4	,		(13)	.		.
		Db0		Dc1	.		De1	.
		가	.				1	.
14	13	De1 3 (8)	(13)	0 7	.	14	,	.
2		256×8		,	3	De1 8	,	(13)
Db0		De1, Db0				Dc1=dt(De1, Db0)	.	.
						,		.
								(12)
15		De1	,				(13)	.
		,	가	(12)		Db1	De1	.
			가					15
	,			Dc1	,			.
16								.
	Db0	,		(8) 2		.	(14)	.
	Db0	,		8 3		.	,	.
	De0		Db1		De0	Dc1		(15)
17	16	De0 3 (8)	(15)	, 0 7	.	17	,	.
2		8×256		, 3		De0 8	,	(15)
Db1		Db1, De0				Dc1=dt(Db1, De0)	.	.
						,		.
								(14)
18		De0					(15)	.
19								.
	Db1, Db0			(8) 3			(12, 14)	.
,		Db1, Db0		, 8		.	,	.
			De0, De1			De1, De0		(16)
						Dc1	.	.
20	19	De1, De0 3 (8)	(16)	, 0 7	.	20	,	.
e1, De0	(16) 2	8×8		, 3		De1, De0	,	D
			Dc1=dt(De1, De0)	.				.
								.
								(12, 14)
21		De1, De0					(16)	.
		,		Db1 /		Db0		.
,		(13, 15, 16)		,		(8)		.

$$D_c 1 = (1 - k_1) \times D_f 1 + k_1 \times D_f 2$$

$$\begin{array}{ccccccc}
 25 & & 1 & & Dc1 & & . & 25 \\
 , s1, s2 & & (17) & & & & , s2 & \\
 . s1 & & & De1 & & & . & D \\
 e1 & 1 & & De1+1 & & & . & \\
 \\
 k1 & & 2 & & . & & . &
 \end{array}$$

$$k_1 = (D_{b1} - s_1) \bigg/ (s_2 - s_1)$$

) (9) Dj1 (10) . Dc1 , 2 (9) . , Di1 Dc Dc (8),
 , Db1 (De1, Db0) (De1+1, Db0) , Db1 k1 Df1, Df2 ,

Dc1	,	De1	가	Dc	
26	3	(8)	2	,	(20)
	Db0		,	, 8	
(21)	De0	,	(20)		
	3	De0	8	3	,
	Df3, Df4		(22)	k0	
k0	,	Dc1	,	Db1	,
				Df3, Df4	
27	(21)		.	,	De0
3	(8)	0	7	,	
256×9)	,	8	,	
+1)	dt(Db1, De0)	Df3	27	,	(21) 2
			Db1	3	De0
			,	Df3	dt(Db1, De0)
Df4					
		(22)	Df3, Df4	k0	3
Dc1					

$$D \in \mathbb{1} \equiv (1 - k \cdot 0) \times D_f \cdot 3 + k \cdot 0 \times D_f \cdot 4$$

28	3	(20)	Dc1	Db0	28
, s3, s4					
. s3					
De0	1		De0	, s4	
			De0+1		
k0	4				

$$k_0 = (D_{b0} - s_3) \big/ (s_4 - s_3)$$

단, $s_3 < D_{b0} \leq s_4$

3 (9) . (9) Dc1 Di1 Dc (8),
 Dj1 (10) .
 , Db0 k0,
 (Db1, De0) (Db1, De0+1) Df3, Df4 ,
 Dc1 De0 ㅏ Dc ,

$$\begin{array}{ccccccccc}
30 & & (23) & & & & & & \text{De1,} \\
\text{De0} & 3 & (8) & & 0 & 7 & . & , & (23) & 2 \\
& 9 \times 9 & & , 3 & & & 30 & , & \text{dt(De1,} \\
\text{De0}) & \text{Df1} & & , & \text{Df1} & & \text{De1, De0} & & \text{dt(De1+1, De0), dt(De1, De0+1),}
\end{array}$$

dt(De1+1, De0+1) Df2, Df3, Df4

(24) Df1 Df4 k1, k0 , 5
Dc1

$$Dc1 = (1 - k0) \times \{(1 - k1) \times Df1 + k1 \times Df2\}^5 \\ + k0 \times \{(1 - k1) \times Df3 + k1 \times Df4\}$$

31 5 Dc1 31
s1, s2 (17) Db1
, s3, s4 (20) Db0
. s1
De1 1 De1
De0 , s4 , s2
De0+1 . , s3
k1, k0 6, 7 De0 1

$$k1 = (Db1 - s1) / (s2 - s1) \\ 단, s1 < Db1 \leq s2$$

$$k0 = (Db0 - s3) / (s4 - s3) \\ 단, s3 < Db0 \leq s4$$

5 2 Dc1 , 2 , Dc
Dc (8) (9) (9) , Di1
, , Dj1 (10) ,
Db0, Db1
(De1, De0), (De1+1, De0), (De1, De0+1) (De1+1, De0+1) k0, k1
Df1, Df2, Df3, Df4 , , , , De0, De1
가 Dc
, , (19, 22, 24) , ,
Dc1 , ,
(4)

33 4 (25) (26)
25) , (5), (8) (9) (2)
6) Di1 , , , 8 (8) (8) (5)
Da1 1 Da1 (5) , , , 1
Da0 .

. (10) (9) (8) . Da1 1 . Da0 . Dc
 . , . Da1 . 3 (5) , Da1 1 . Dc
 . , (8) . Da1, Da0 . . .
 32 . (26) . Di1 . Da1 . . St8 , , (5)
) Di1 . Da1 1 . Da1, Da0 . Dc . St2 St4 . St5
 , (8) (9) , , Dc .
 , , 4 . Di1 (8) , ,
 (5)
 35 , 5 . (28) Dc . Di1 . 1 Db1 . 가 . (27)
 , , , , , , ,
 36 . Db0, Db1 . (28) 1 . (29) . (11) .
 Db1 . Db1 . (4) . (6) . Di1 ,
 Db1 . 가 . , , Dc1 . (29) . Di1 , j1
 (30) . , , , , , ,
 , (30) . Dc2 (29) . (9) (30) . j1 . Dc1 . Dc2 , Dc1
 , , , , , , , 35 Di1
 34 , 35 . 1 . 가 . Di1 . Dc1 . Db1 . 가 . St1 St4
 , , (29) . St10 . (29) . (29) . , , St9
 , , , , , , ,
 30) , (9) . Dc1 . , , Dc2 . , , St5
 , , , , , , ,
 , , , , , , ,
 37 36 . Db1 . (28) . (12) . , , 37
 De1 . Dc1 . , , , ,
 , (14) (28) , 38 . , , De0 . Db0 . Dc1
 , , , , , , ,
 , (28) , 39 . , , , , Db1, Db0

(12, 14)	,	De1, De0	Dc1		
,	가	(12, 14)	(13, 15, 16)	,	2
37	39			(13, 15, 16)	
,					
40		(28)	2		(31)
Di1		Db1		j2	j2
(32)		(31)		Dg1, Dg0	(11)
,					
8	10	Db0, Db1	,	j2	Dg0, Dg1

$$Dg1 = Db1 + j2$$

$$Dg0 = Db0 + j2$$

$$j2 = Di1 - Db1$$

8, 9	,	Db1, Db0	,	j2 (=Di1 - Db1)	가
,	.	Db1, Db0		j2	.
(28)	(11)	Dg1, Dg0	(11)	Dc1	Dc
,	35			Dc1	
(9)					
가	,	Di1	Db1	j2	Db1, Db0
,	.		Db1, Db0		.
,					
		Dg1	,	11	Di1

$$Dg1 = Db1 + Di1 - Db1 = Di1$$

,	41	,	Dg1	Di1	(11)
42	40	(28)			
	(32)		Dg1		
,					
42				42	
				(12)	,

Dg0	(28)	43	(14)	.	(32)	De0
	Dc1		.	.	.	
,	(28)	44	.	,	(32)	
Dg1, Dg0			(12, 14)	,	,	
De1, De0	Dc1		.	.	.	
,	42	44	.	,	(13, 15, 16)	,
.
45	Di1	Db1	(28) 3	,	.	(29)
	j1	(30)	가	,	,	
Db1		,	.	,	j2	(31)
.	.	,	.	,	.	Di1
1	(32)	(31)	.	.	j2	.
1)	,	,	.	.	,	Db0, Db
,	Dg1, Dg0	.	Dg1, Dg0	Dc1	(11)	(1)
(30)	j1	,	,	Dc1	,	(30)
.	Dc2
,	Dc1	Di1	Db1	,	Dg1, Dg0	.
,	,	,	,	,	Db1, Db0	가
,	,	,	,	,	,	.
46	45	(28)	.	.	46	.
,	(32)	(12)
Dg0	(28)	47
	Dc1	.	(14)	.	.	.
,	(28)	,	,	.	(32)	.
Dc1	De0
,	(28)	48
Dg1, Dg0	,
De1, De0	Dc1
,	46	48	.	.	(13, 15, 16)	.
,	,	,
(6)
49	6	(34)
(4)	(4),	(5),	(7),	.	.	
1	Da0	Di1	,	.	.	
a0	,	,	,	.	.	Da1
Da0	Da1	1	,	.	.	.
,	Db0	Db0	.	.	.	
Db0	,	,	.	.	.	
Dc	
49	Dc	,	(35)	,	Di1	Db0
,	(6)	,	.	,	.	Da1
(7)

(10)

65 65 , (2) ,
 가
 48) , (49) Y-C Di1 , R, G, B
 Dn2 Y-C (46, 47) Db1, Db0 R, G, B

 Dn1, Dn0

(11)

66	11	(50)	1	(4)	(9)	66	,
	Da1	.	,	(5)	Da1	1	Dj1
1, Db0	Da0	.	(6, 7)	Db1	Da1, Da0	.	Db
8)	Db0	,	Dj1	1	(9)	.	Dj1
	Db0, Db1	Dc	Di1	Dc	.	(9)	1
	Dj1	,	(10)	(4)	.	,	가
67	(10)	Dj1	,	(c)	.	67	, (a)
	Di1, (b)	.	67(b)	,	,	Dj1	가
가	.	Dc	67(c)	Dj1	.	Di1	.
	가	,	67(c)	Dj1	.	(10)	1
				.	1	,	Dj1

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2.

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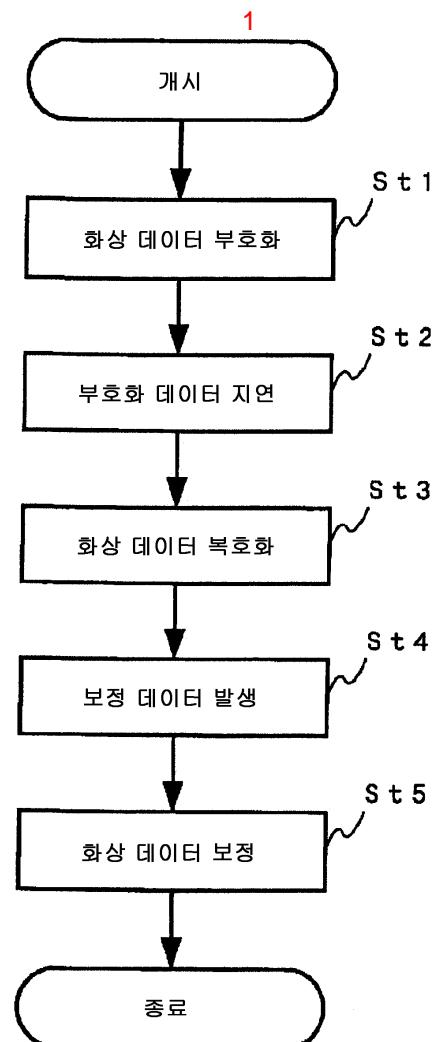
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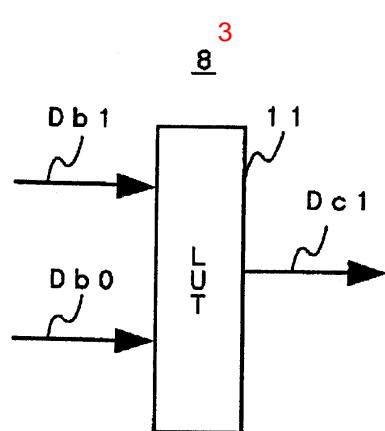
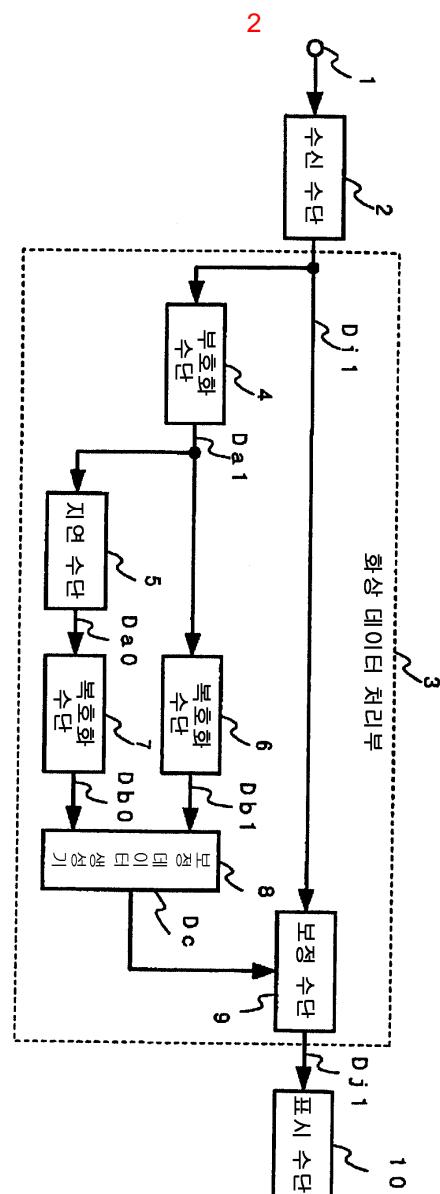
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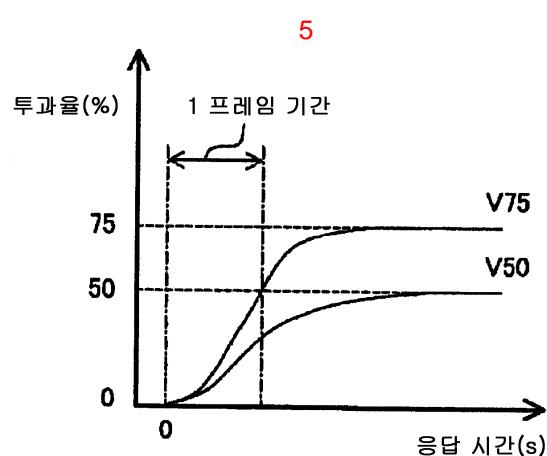
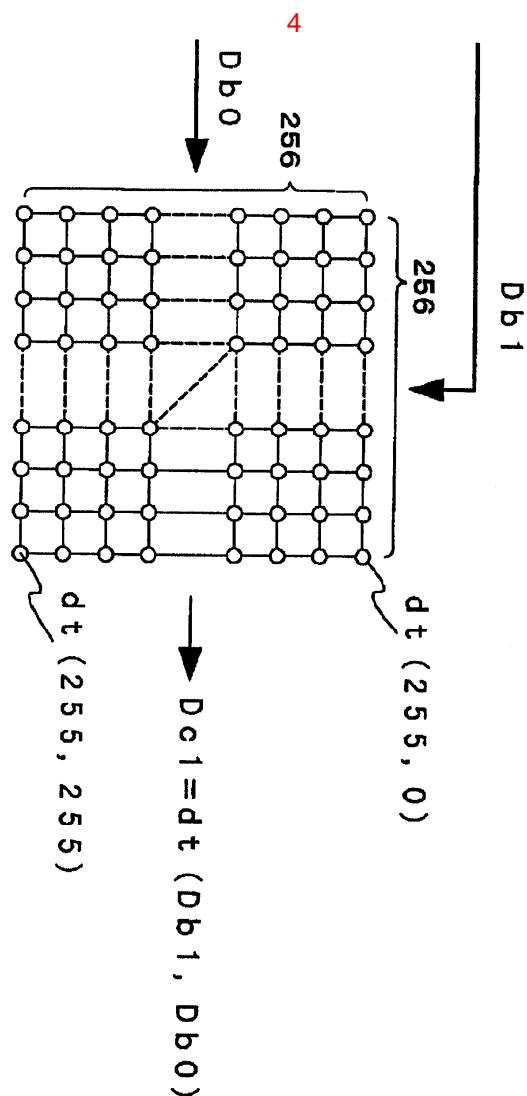
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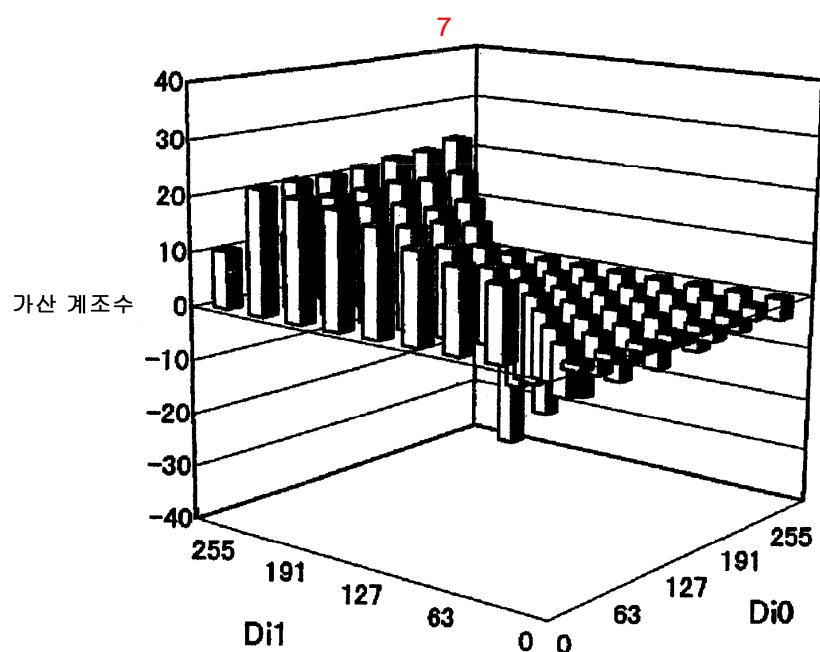
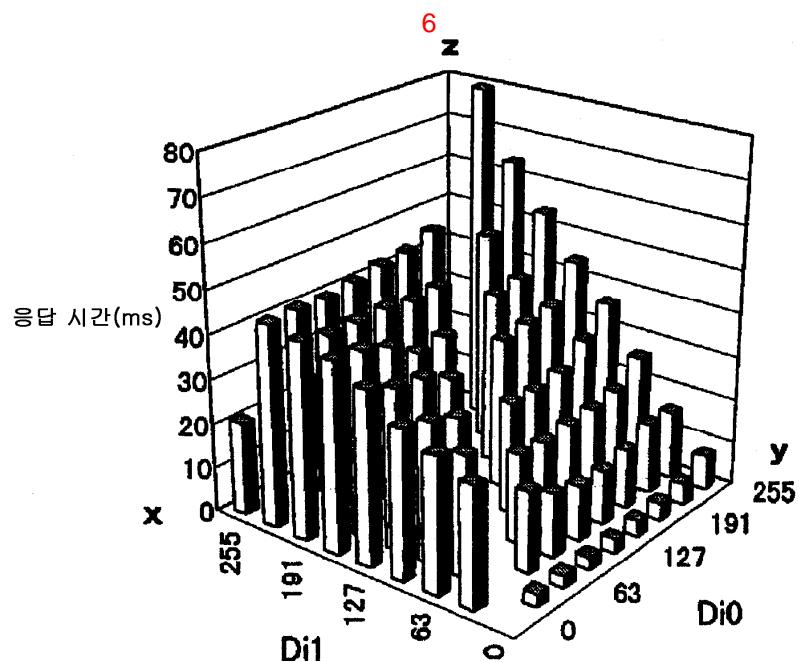
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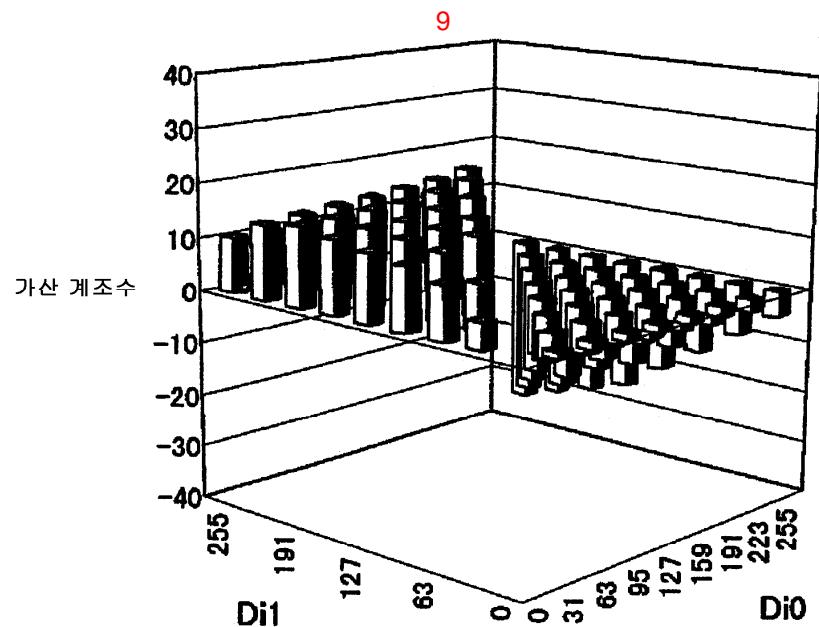
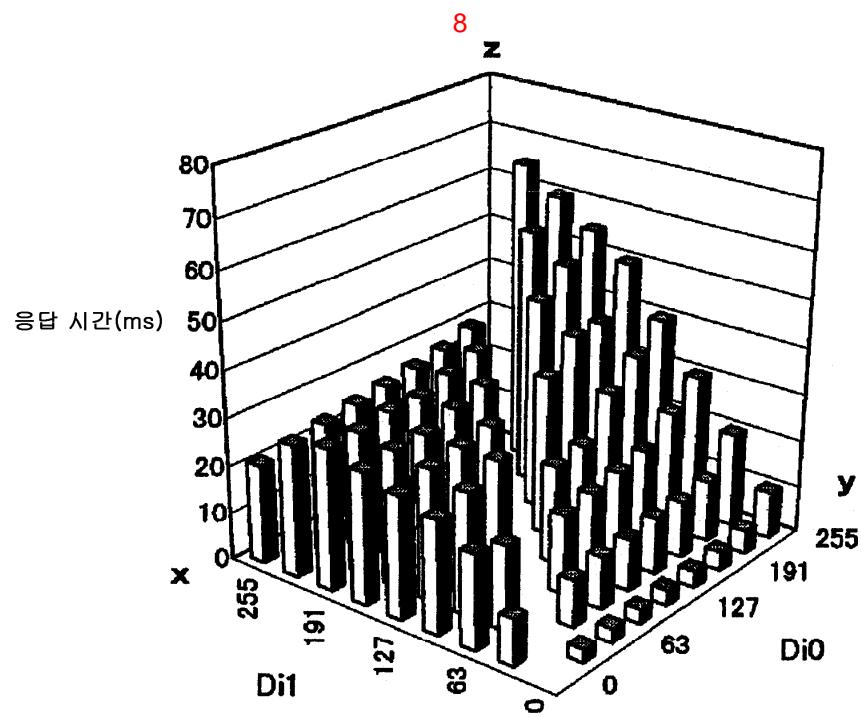
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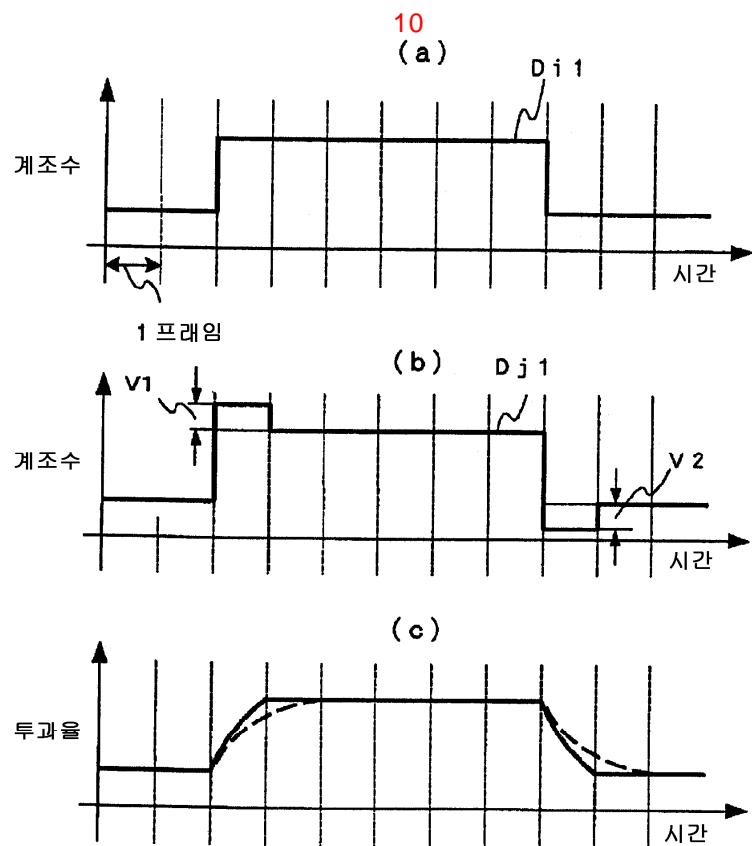












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(a)	52	152	52	52
	52	152	52	52
	48	148	48	48
	48	148	48	48
	48	148	48	48

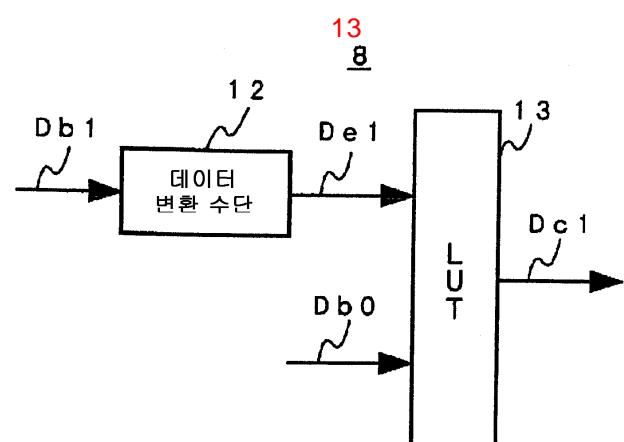
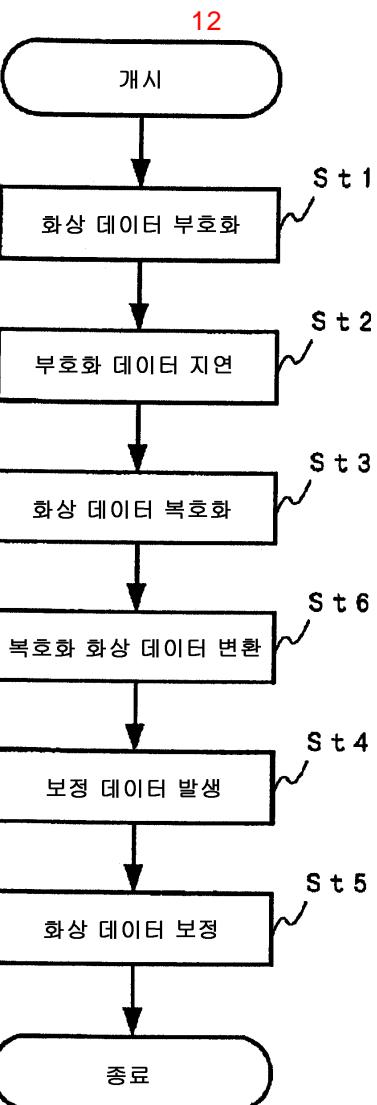
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0	0	0
0	1	1
1	0	0
0	0	0
0	0	0

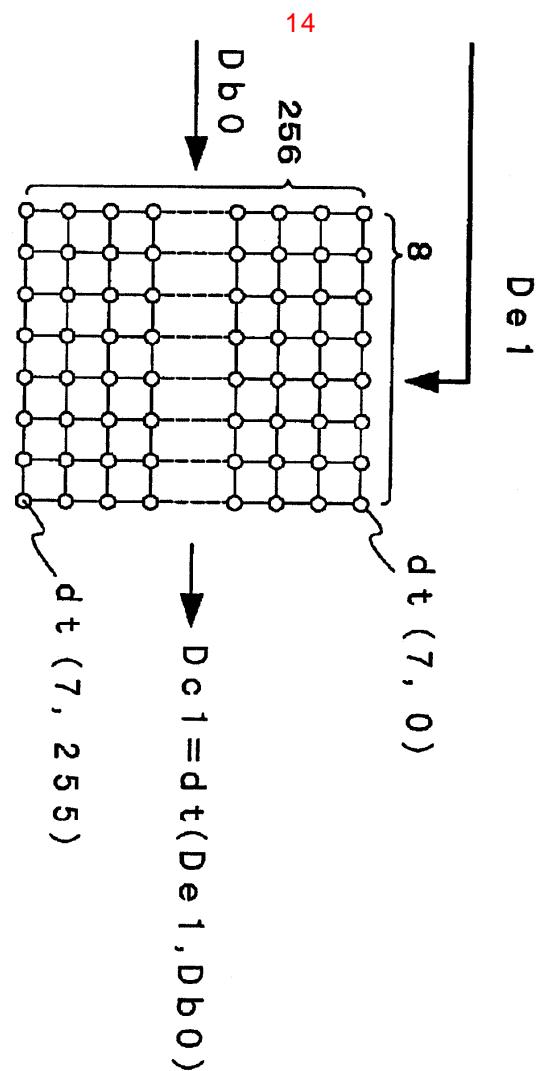
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	50	150	50	50
	50	150	50	50
	50	150	50	50
	50	150	50	50

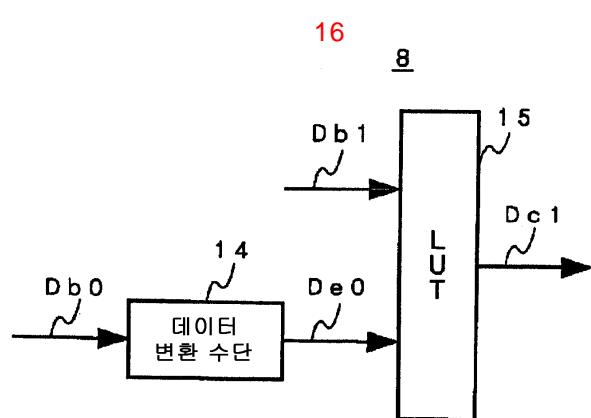
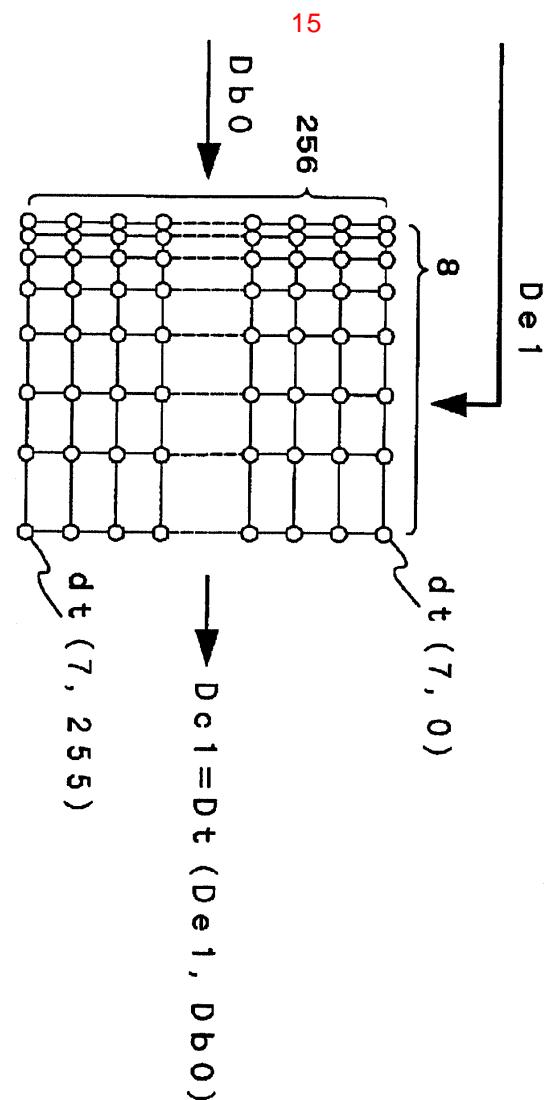
50	150	50	50
50	150	50	50
50	150	50	50
50	150	50	50
50	150	50	50

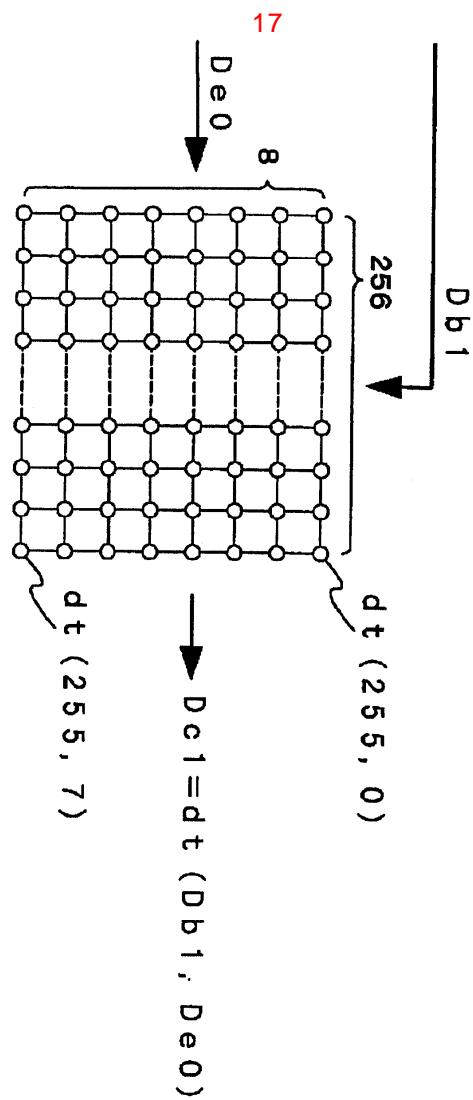
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0	0	0	0
0	0	0	0
0	0	0	0

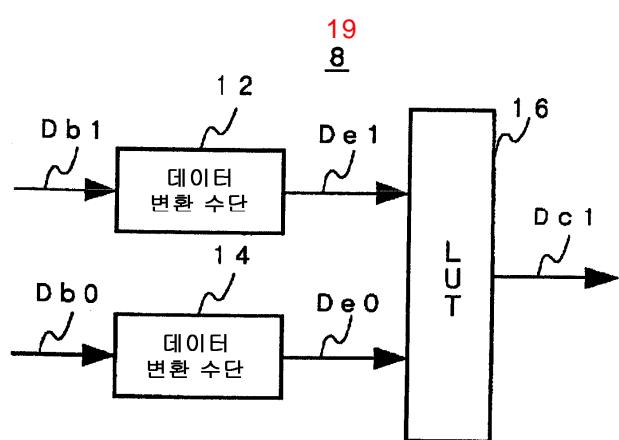
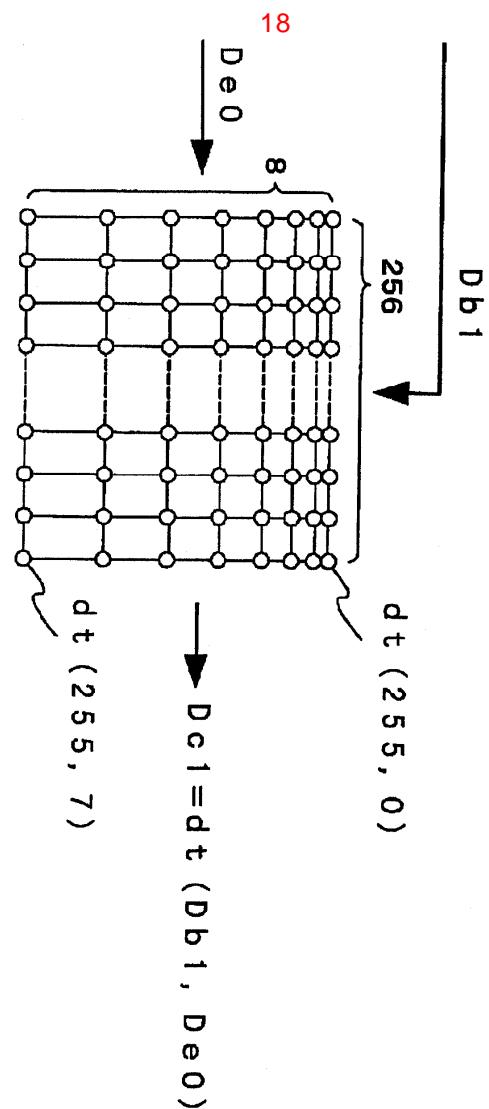
52	152	52	52
52	152	52	52
48	148	48	48
48	148	48	48

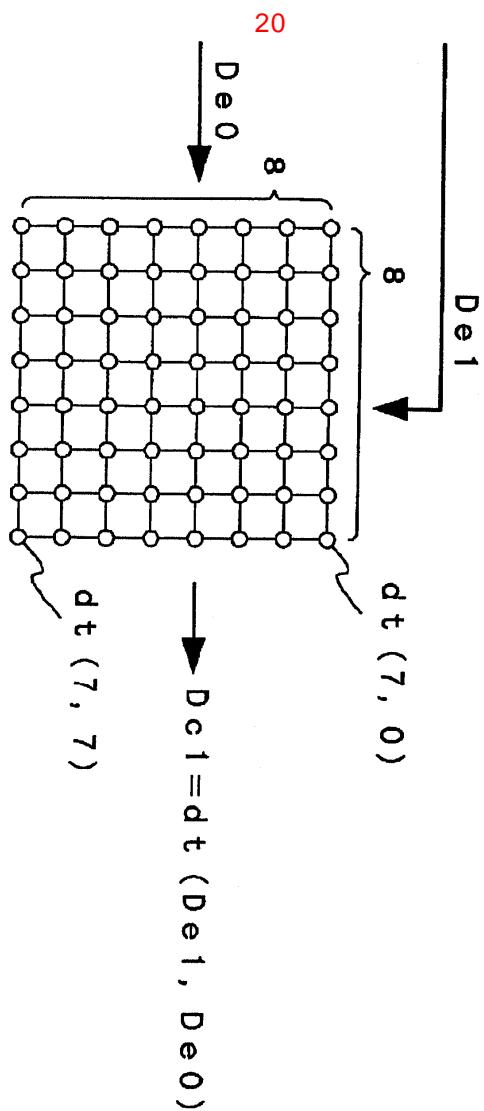


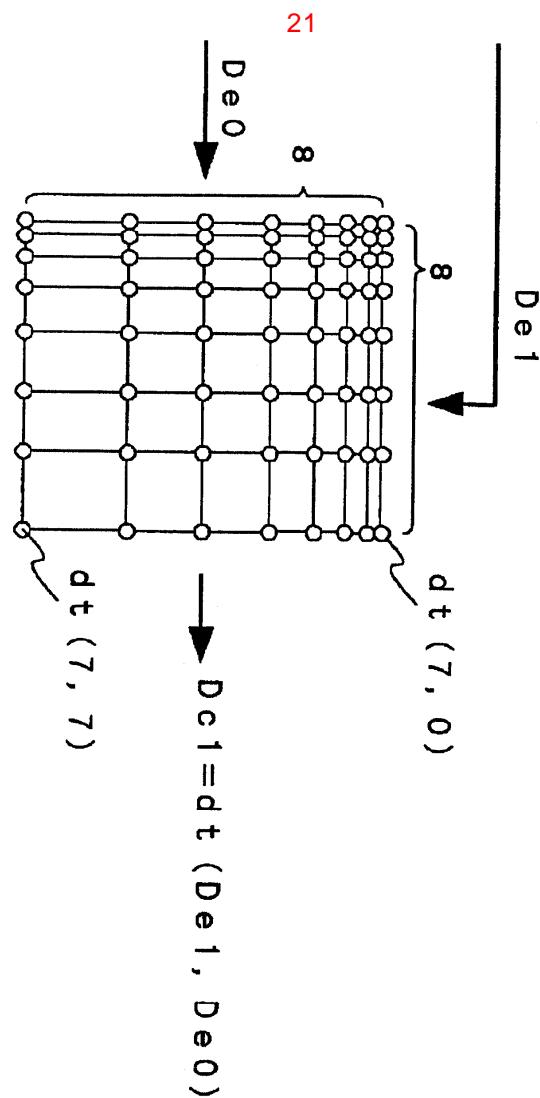


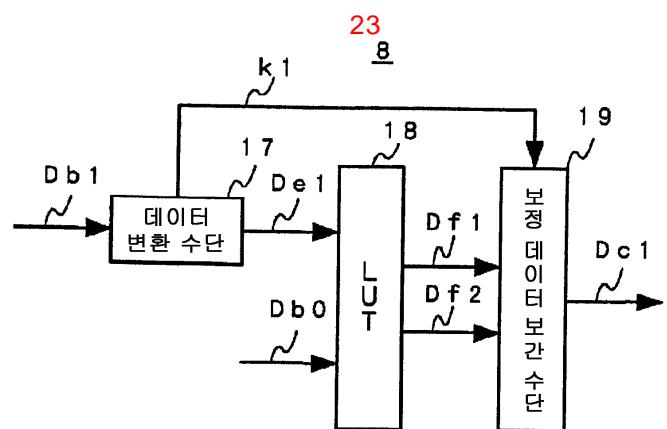
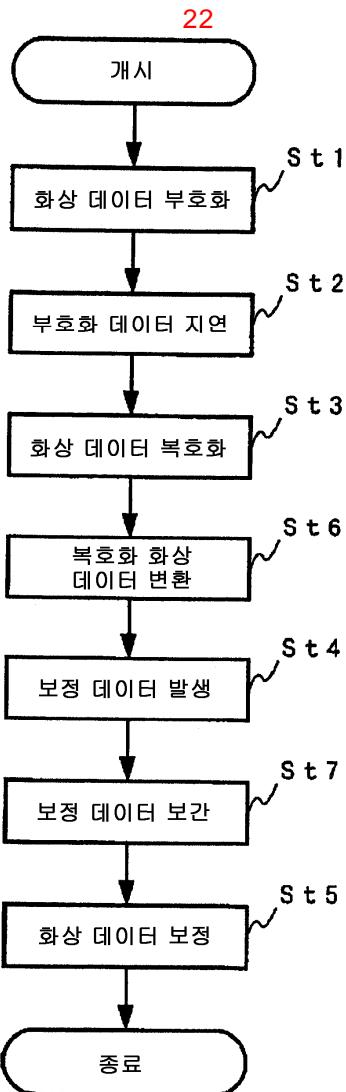


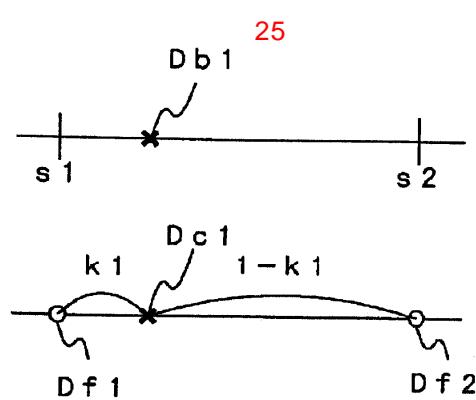
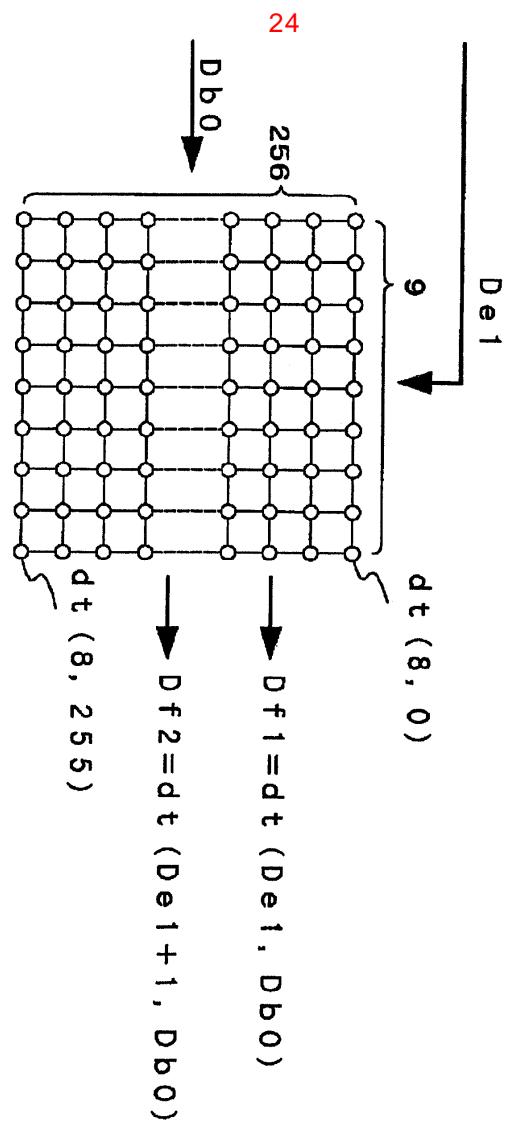


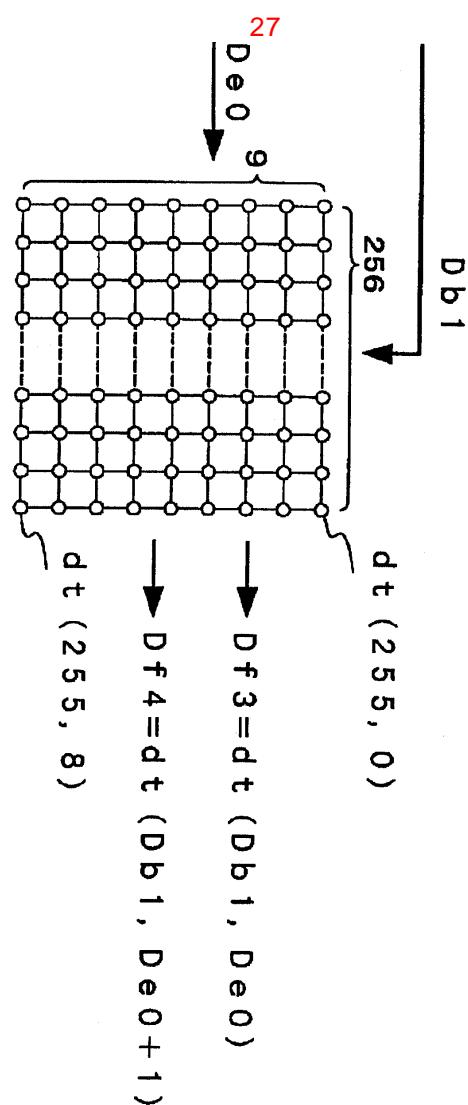
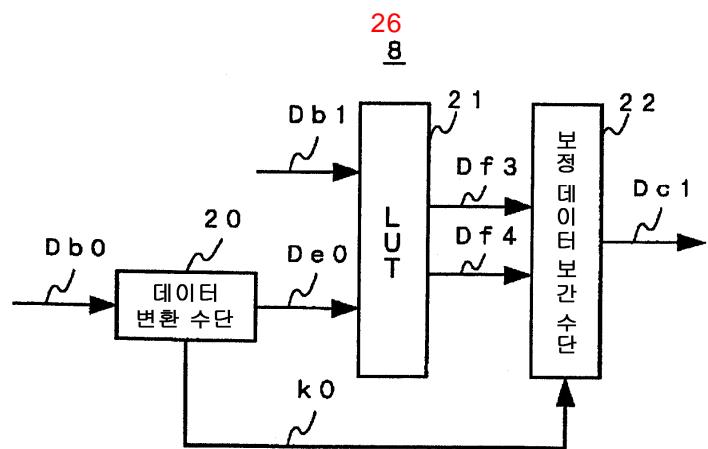


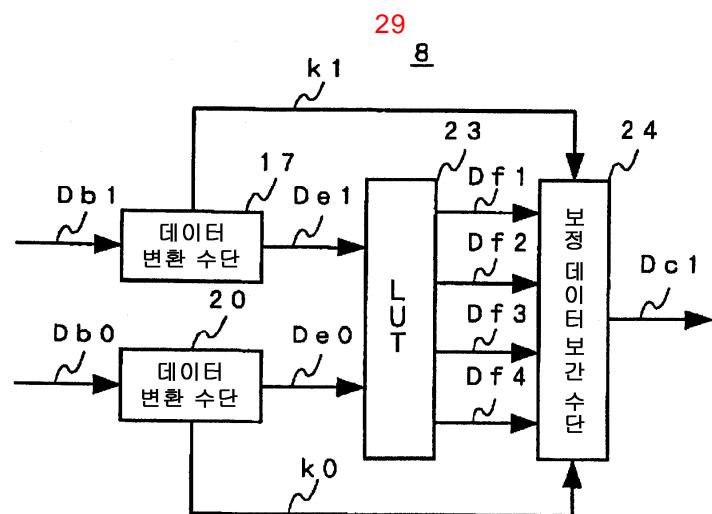
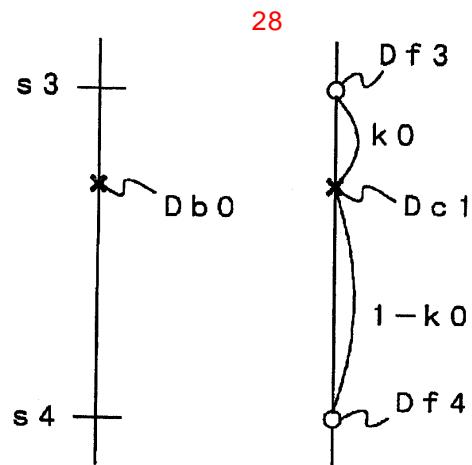


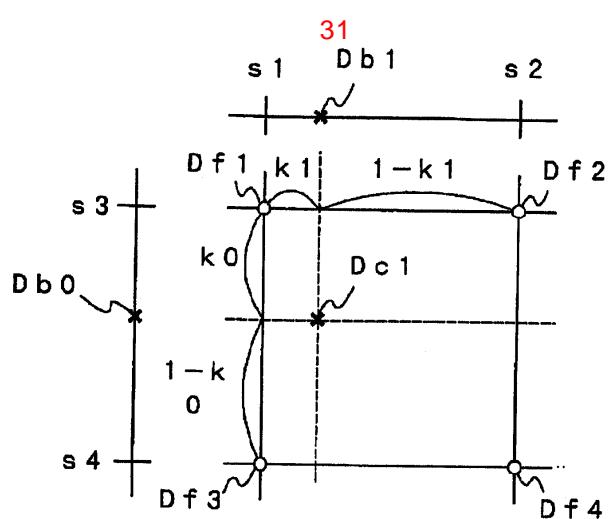
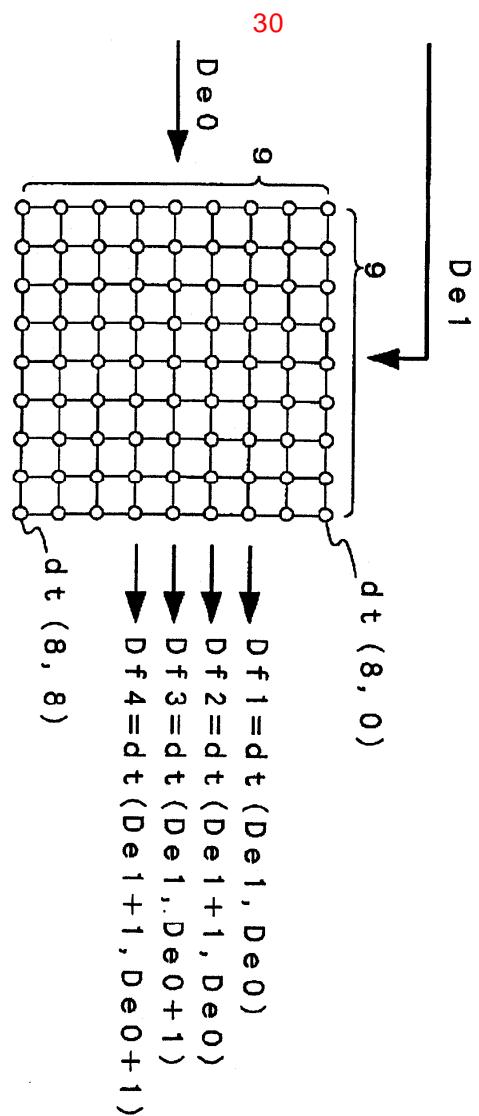


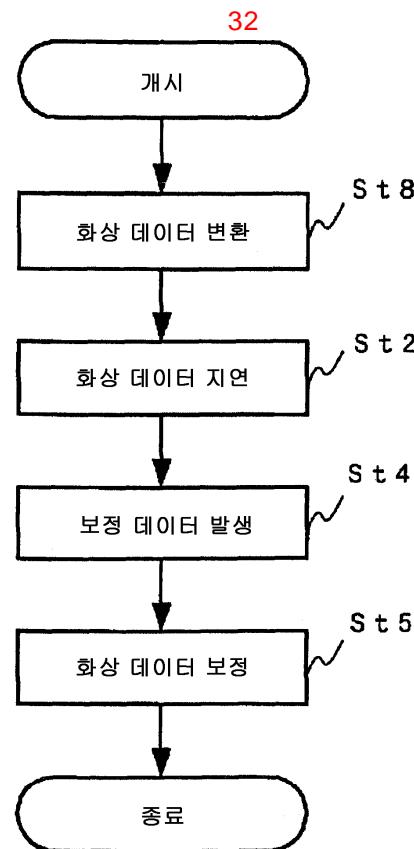


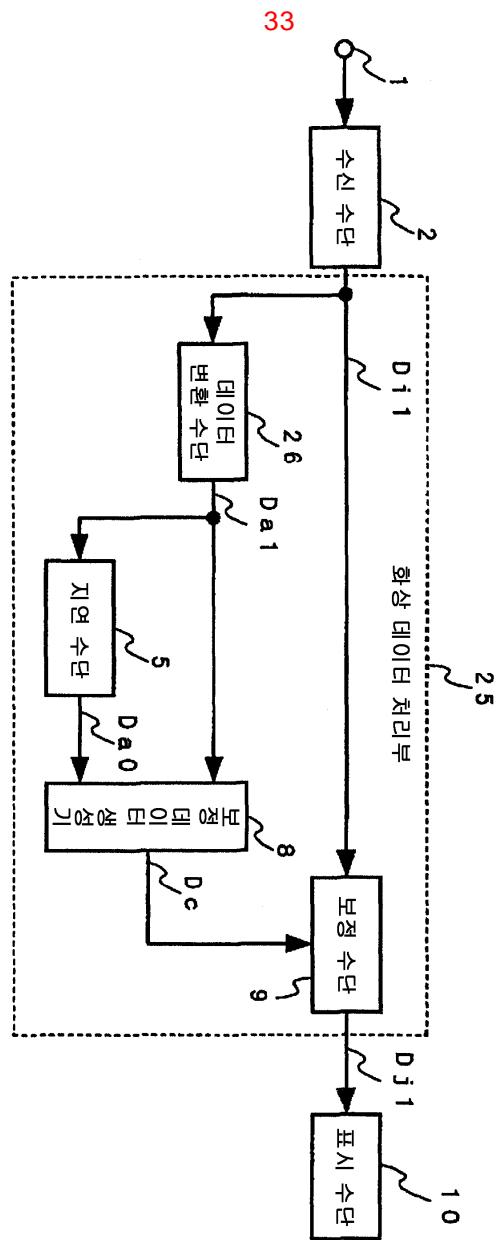


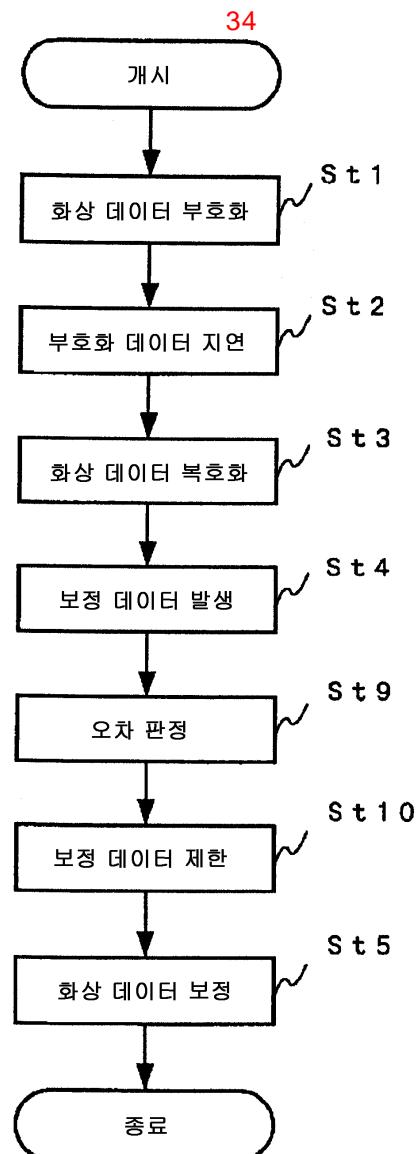


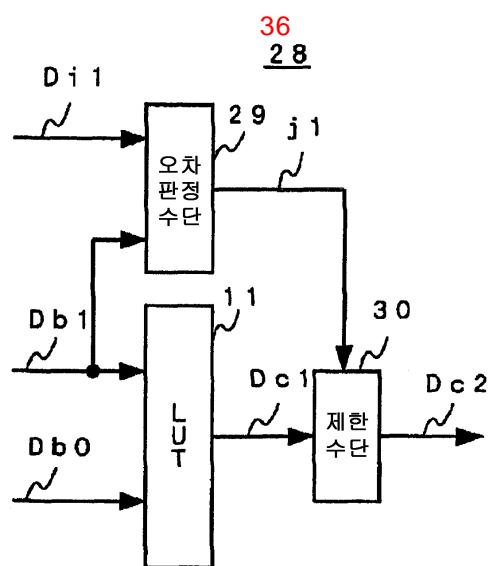
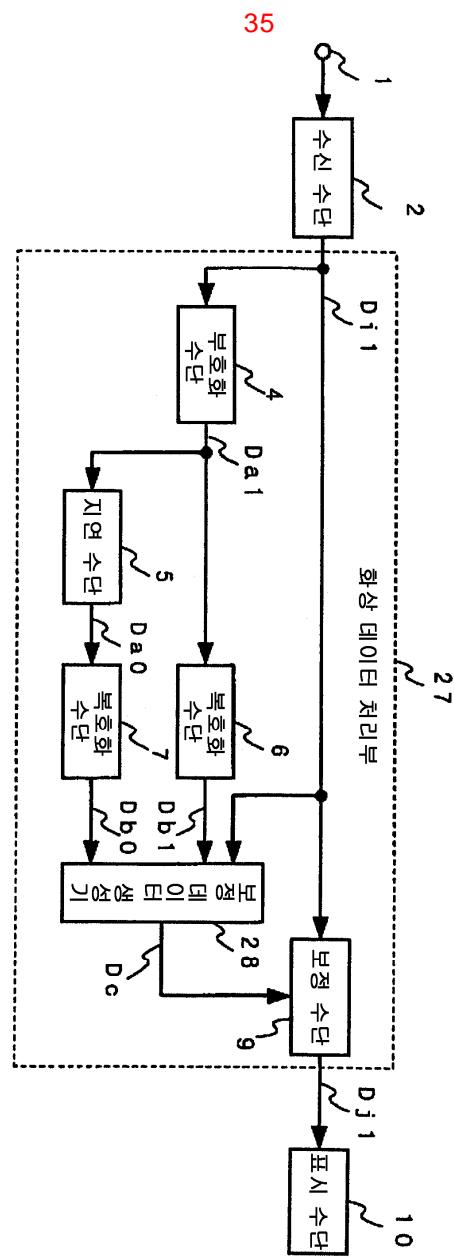


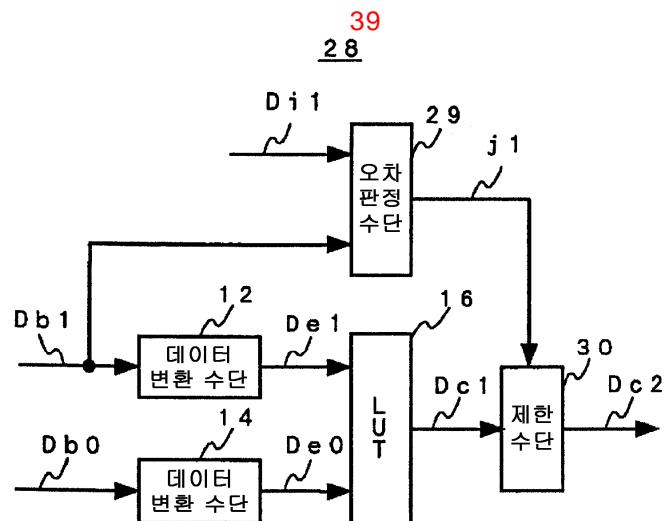
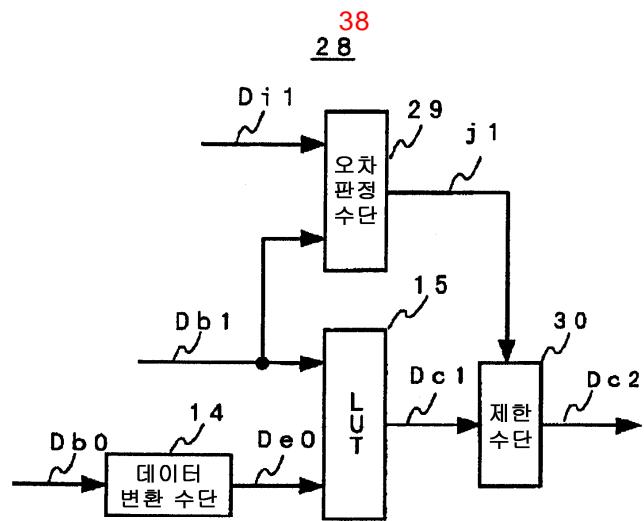
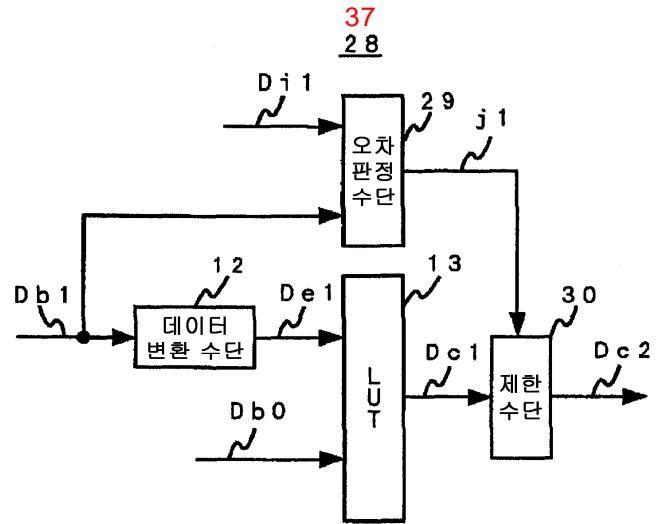


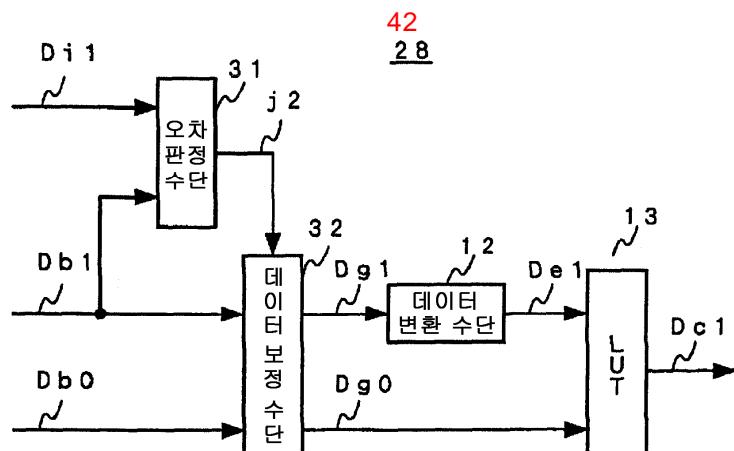
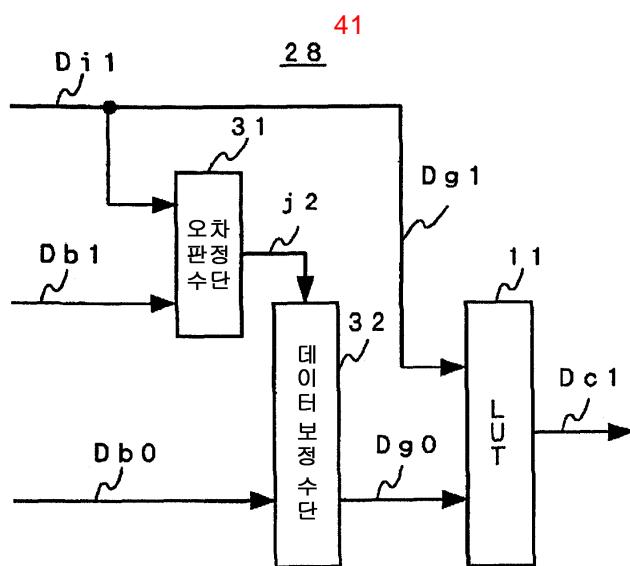
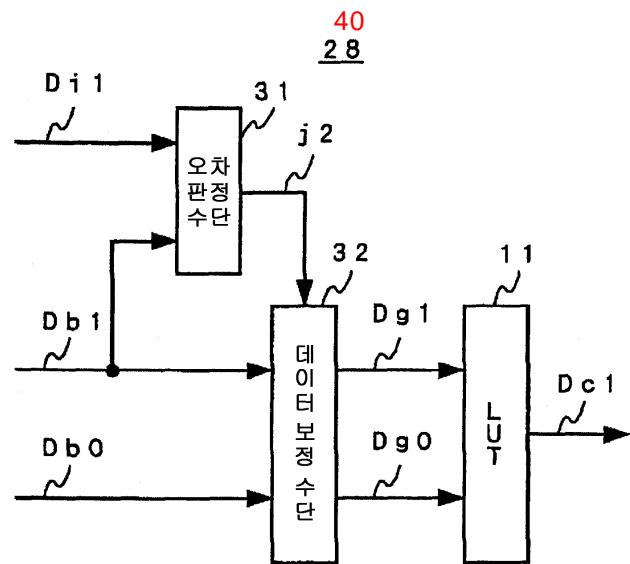


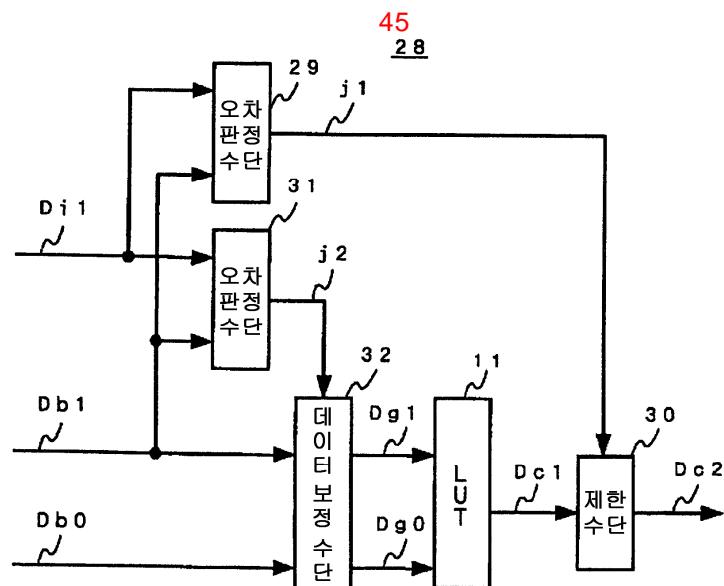
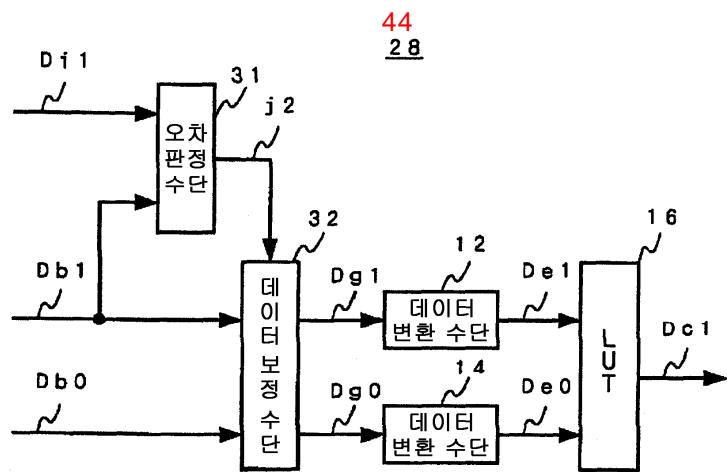
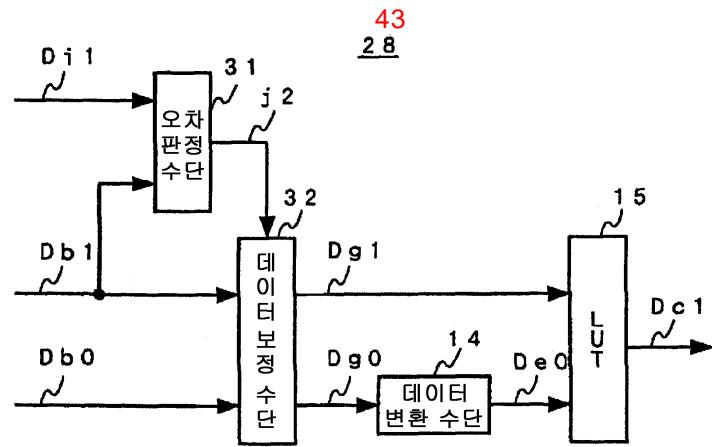


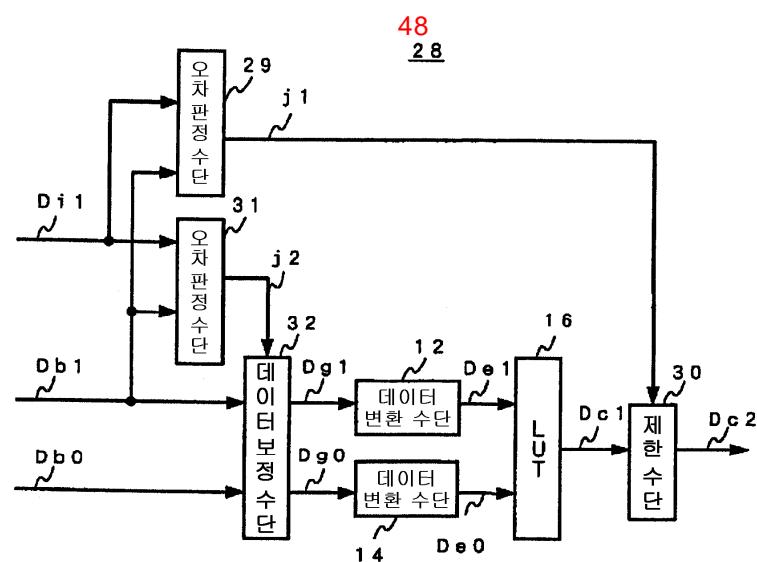
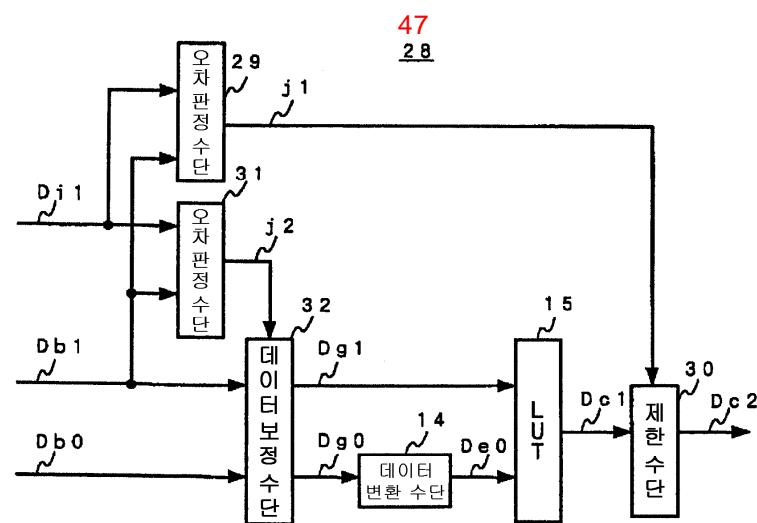
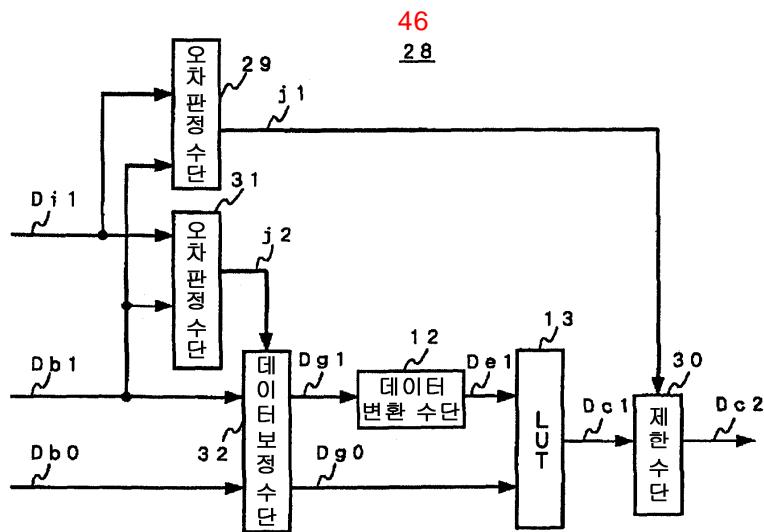


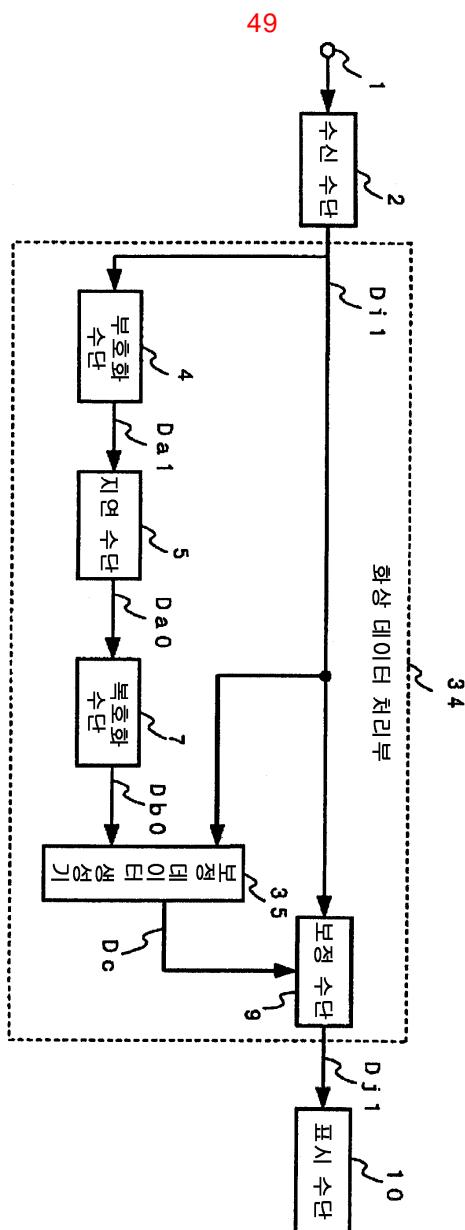


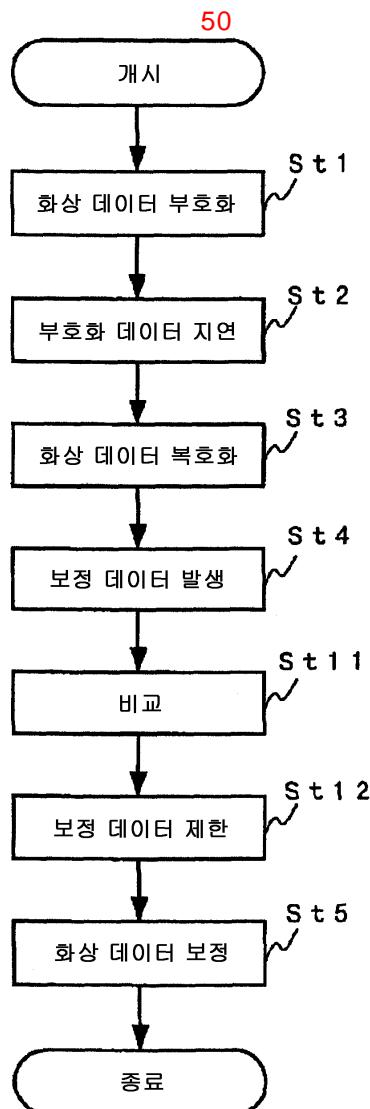


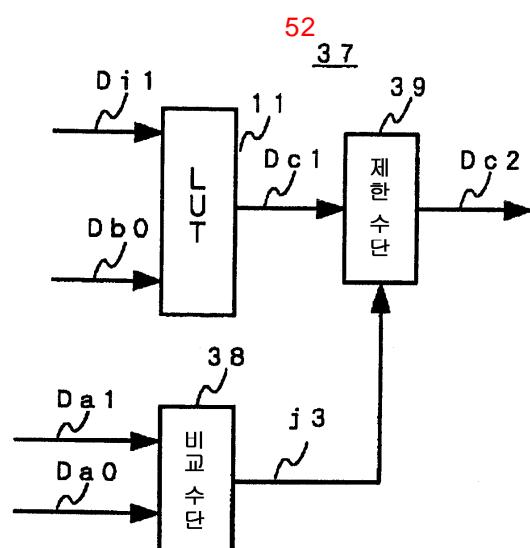
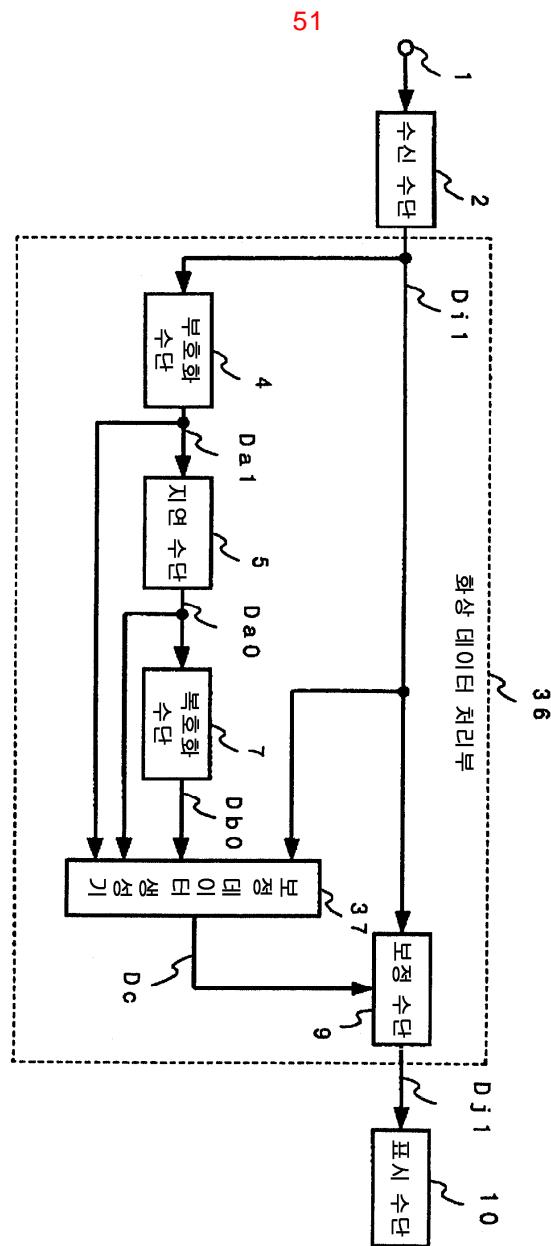


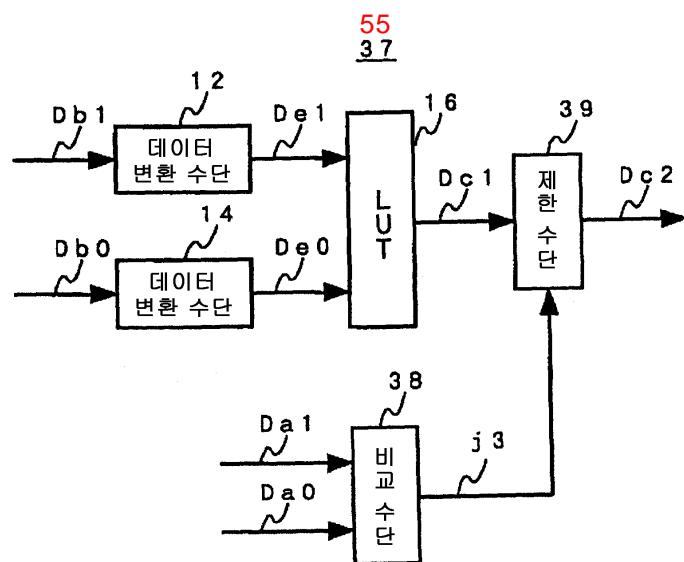
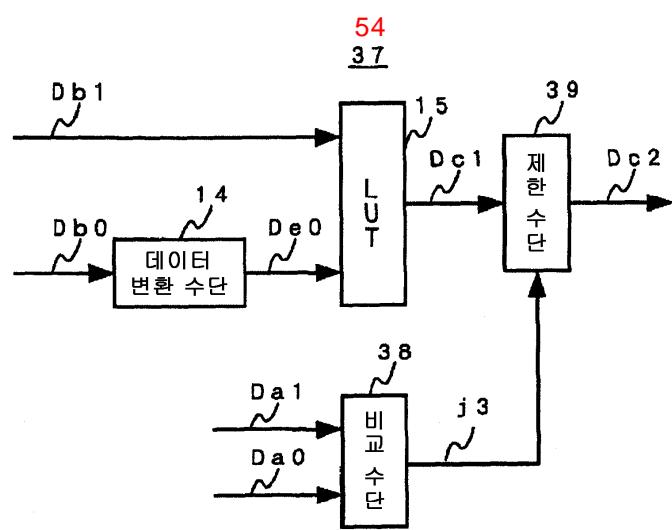
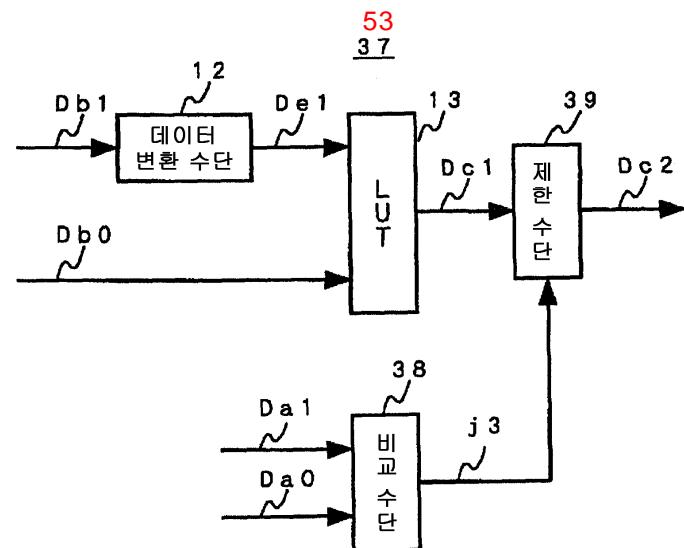


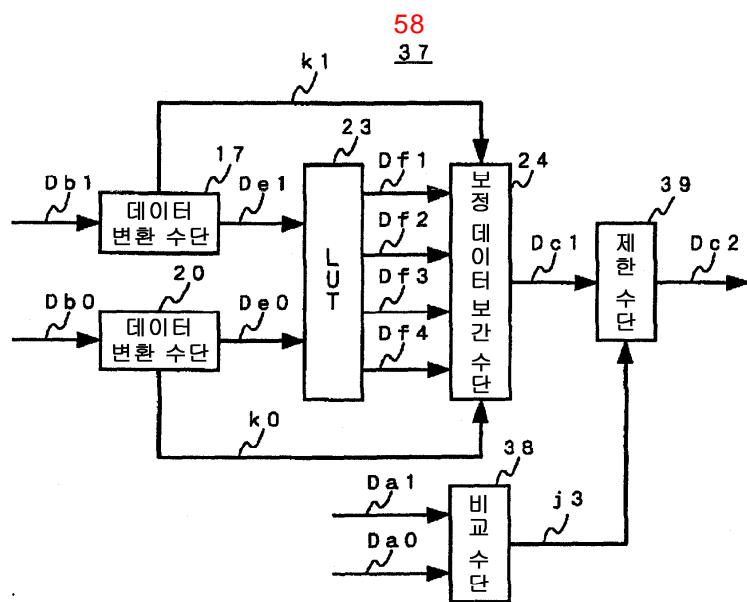
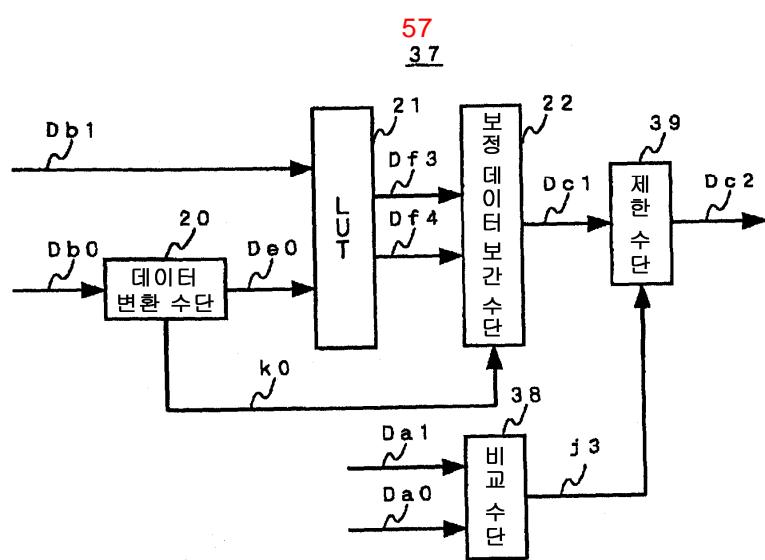
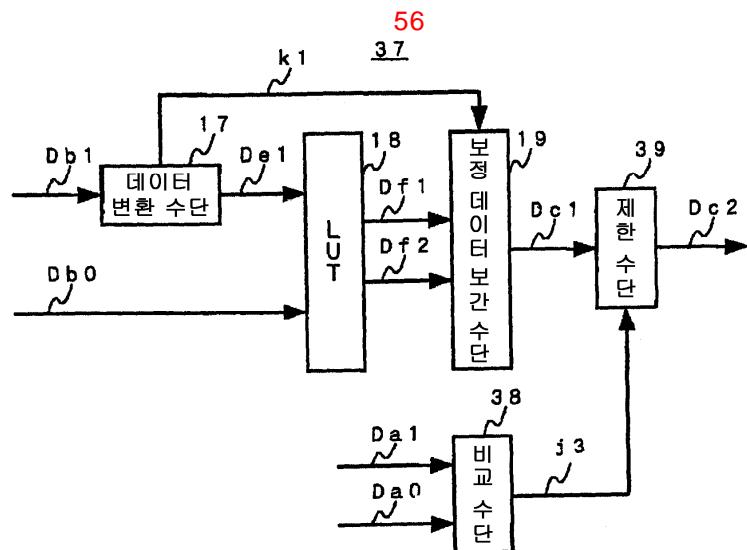


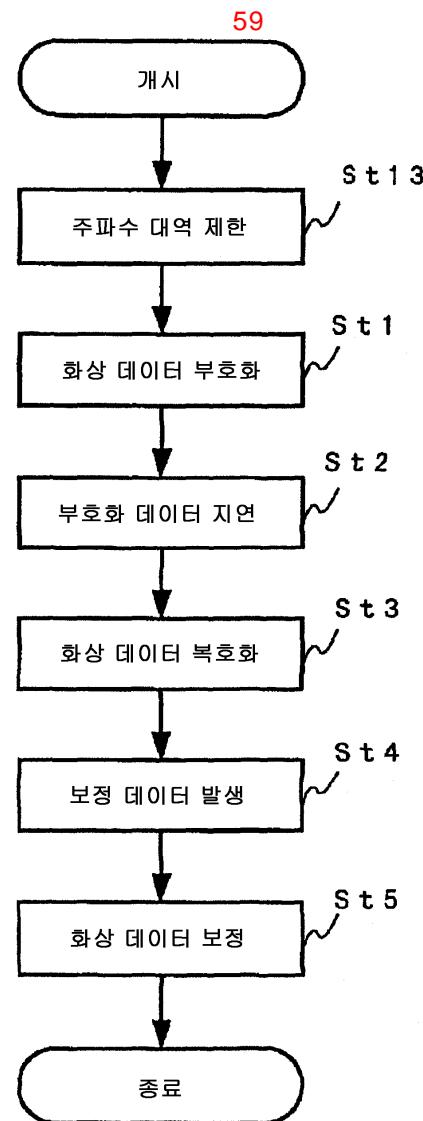


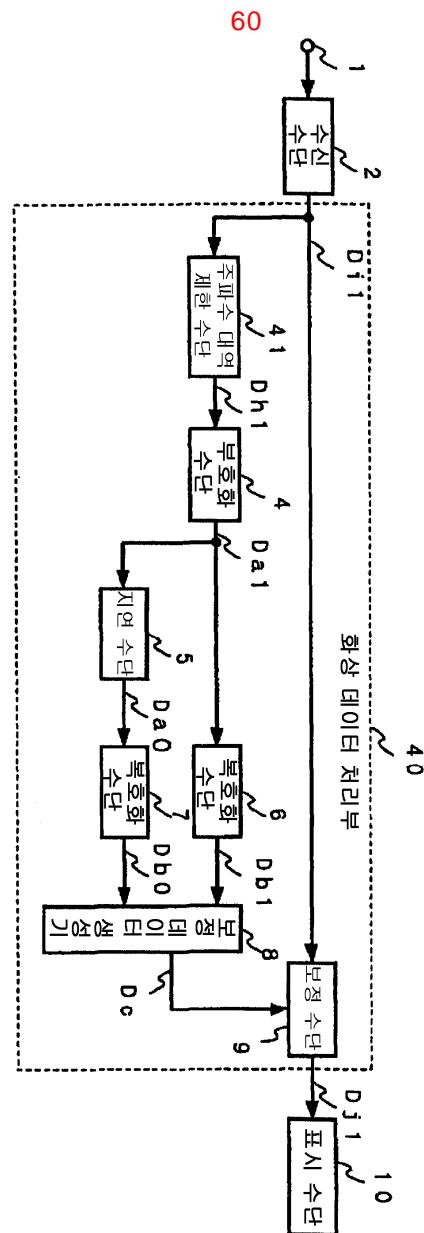


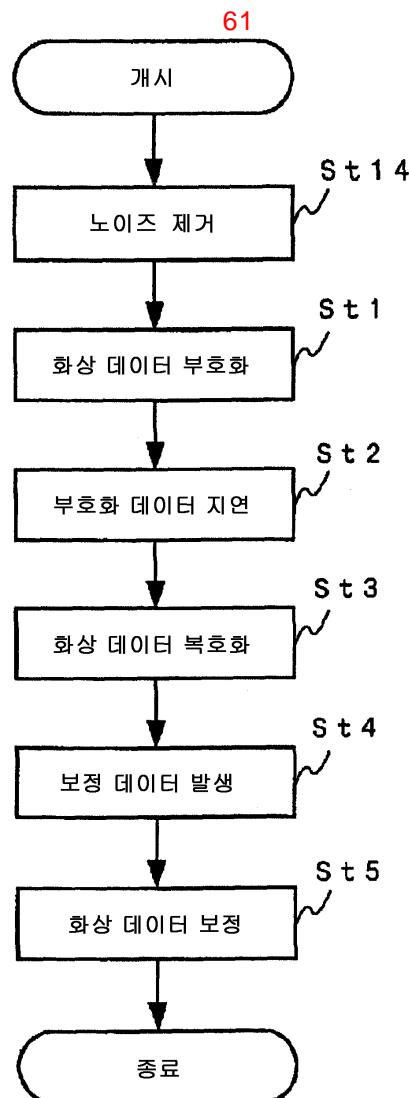


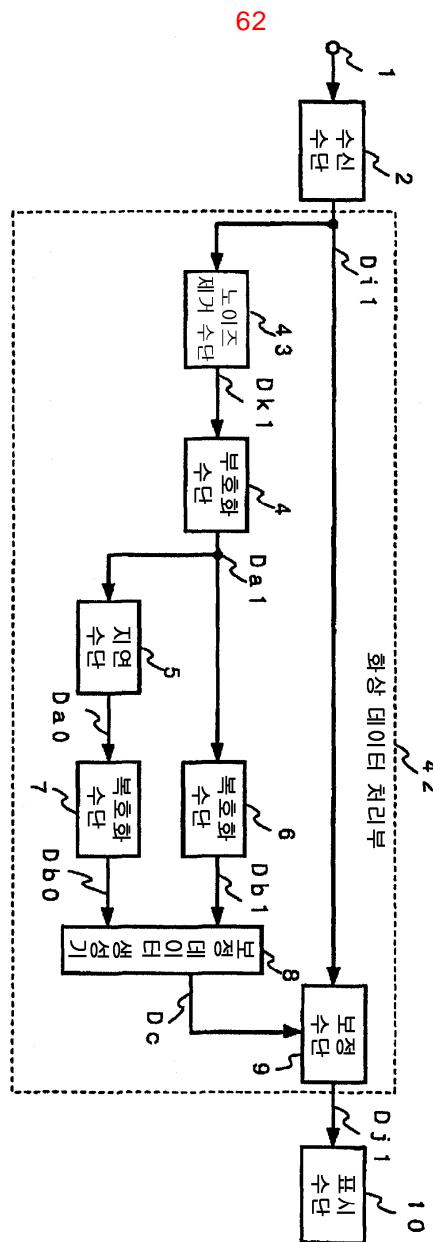


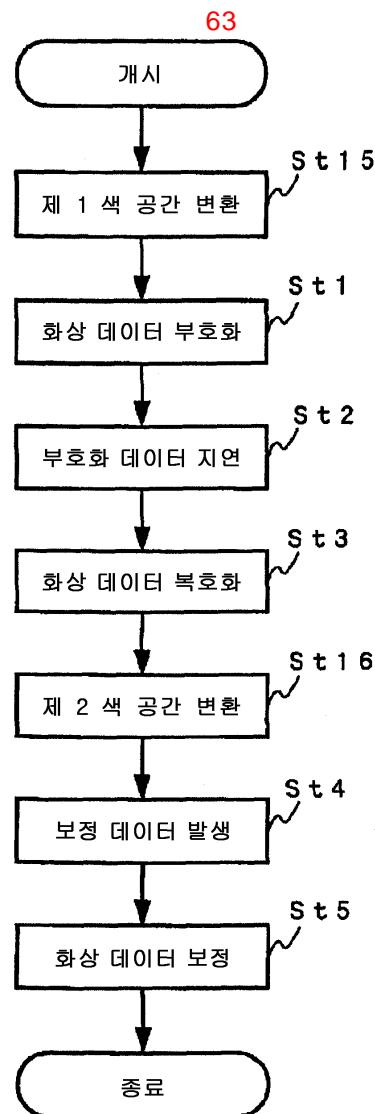


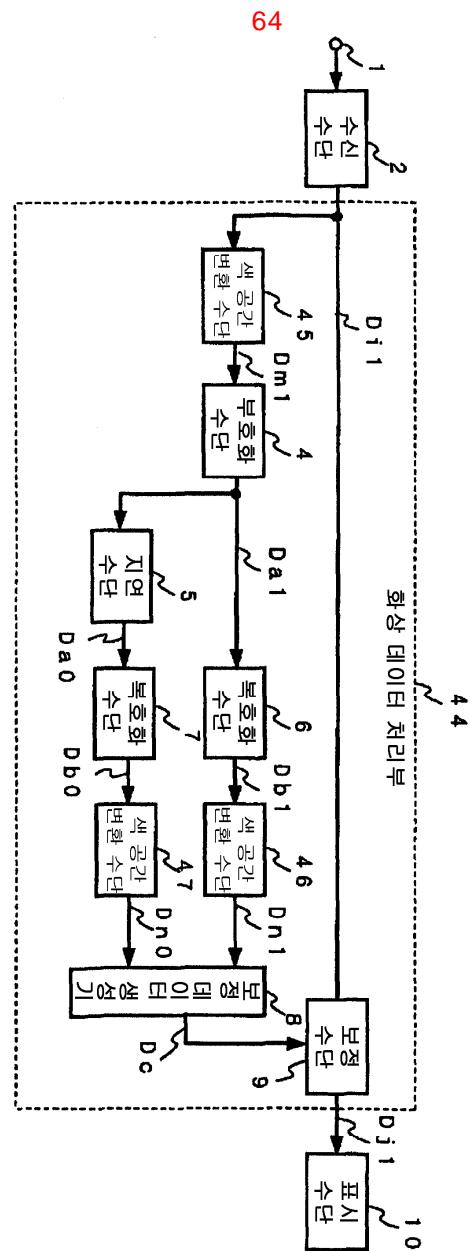


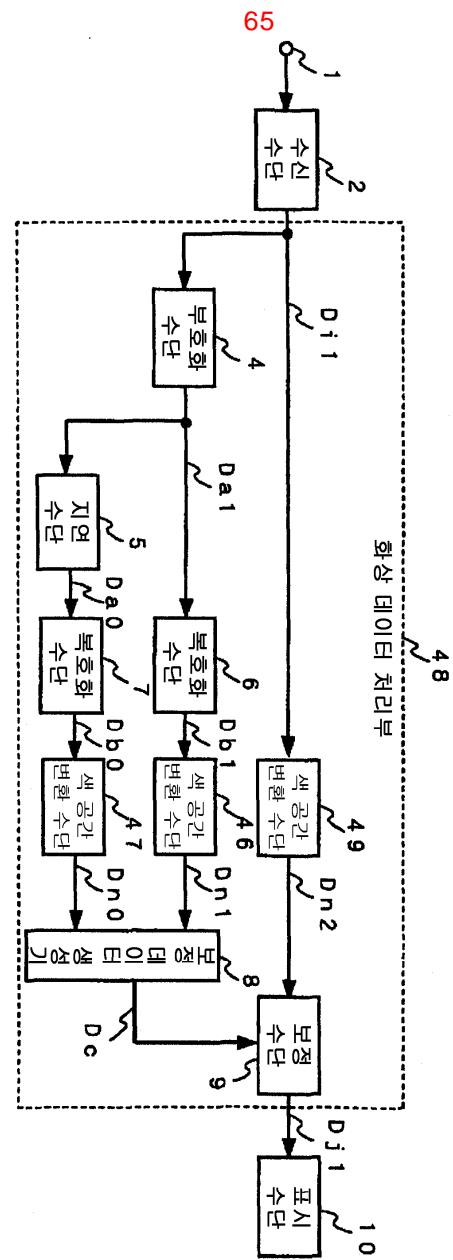


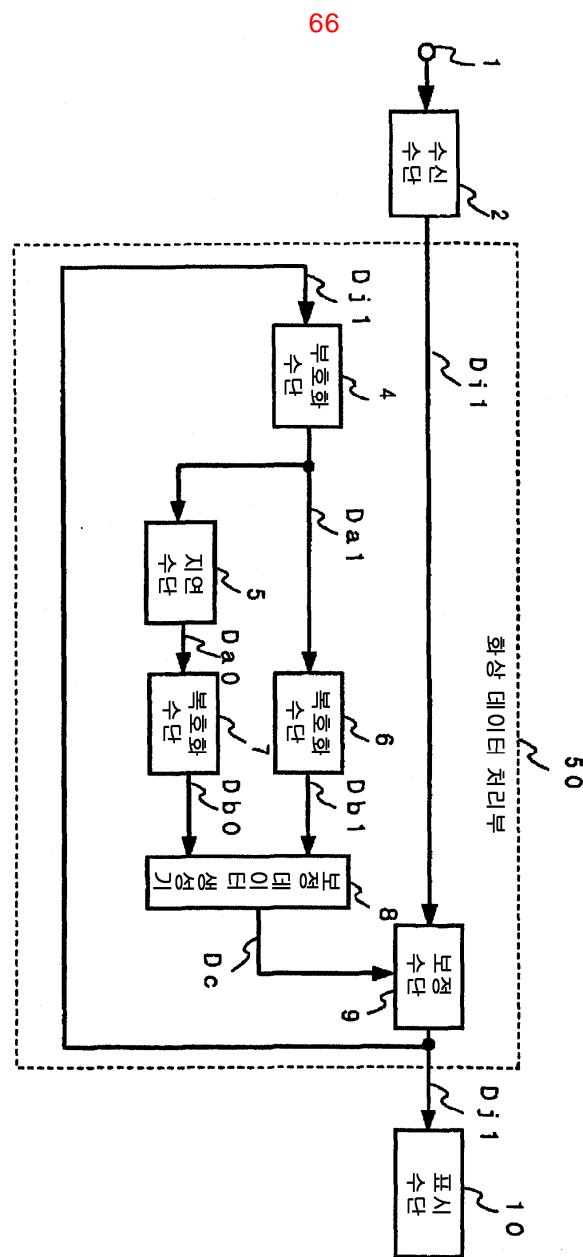


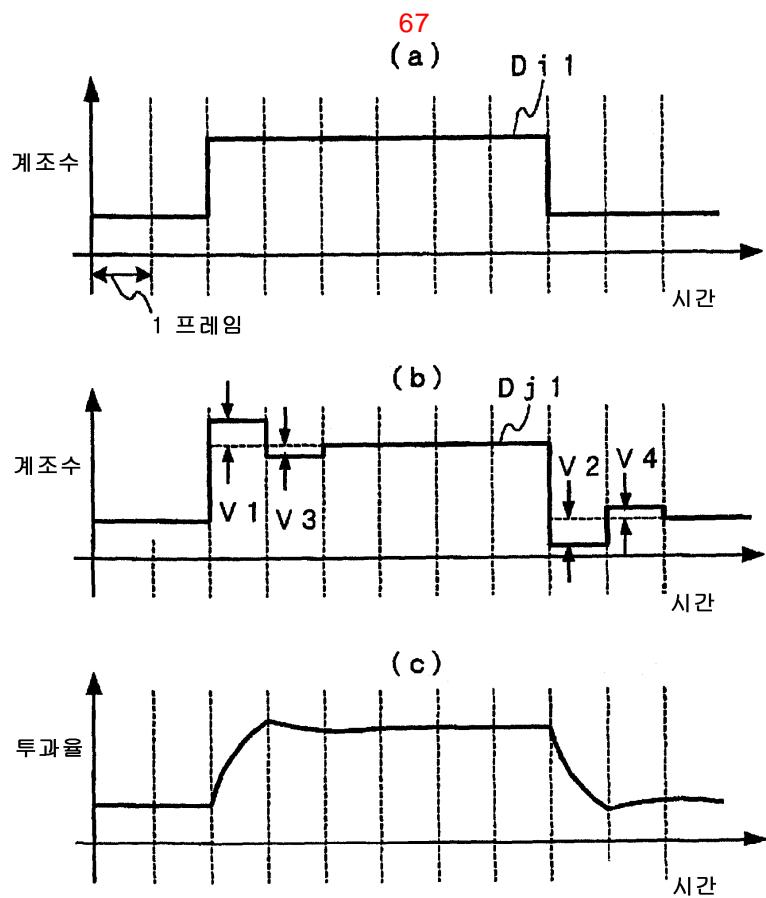




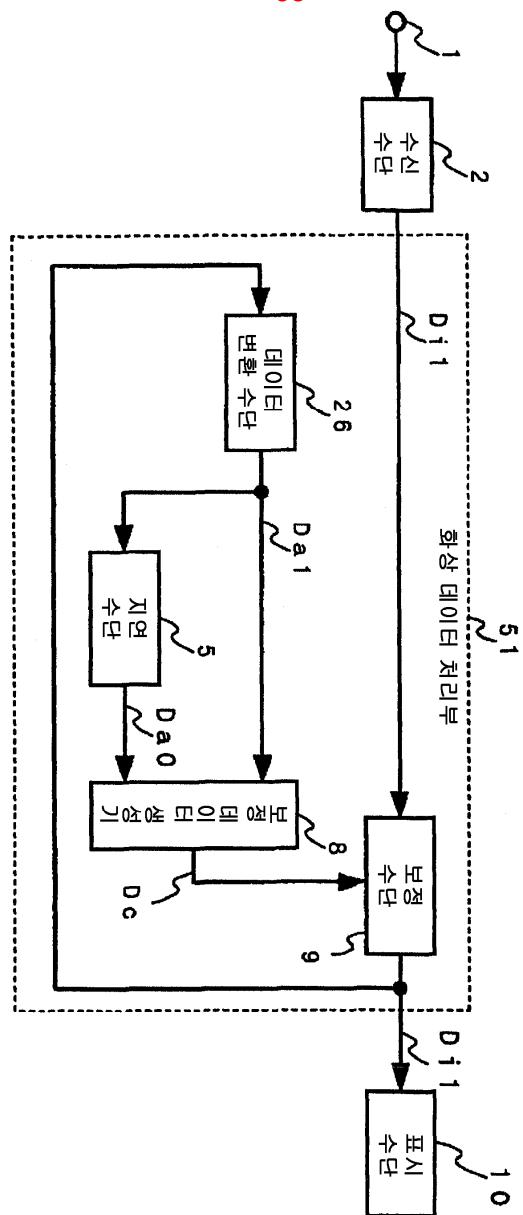


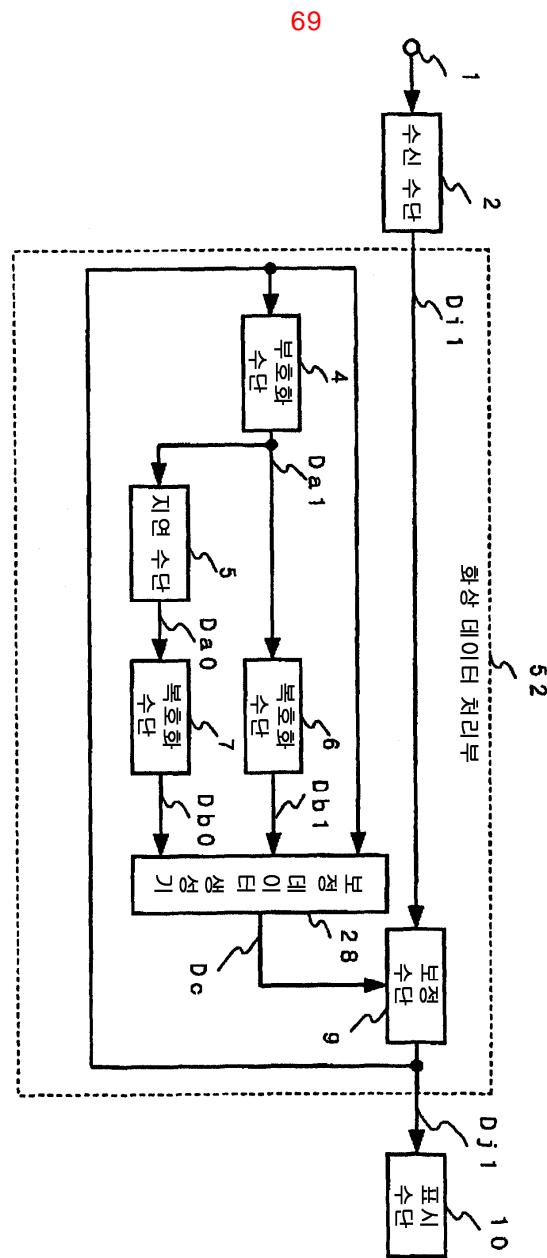


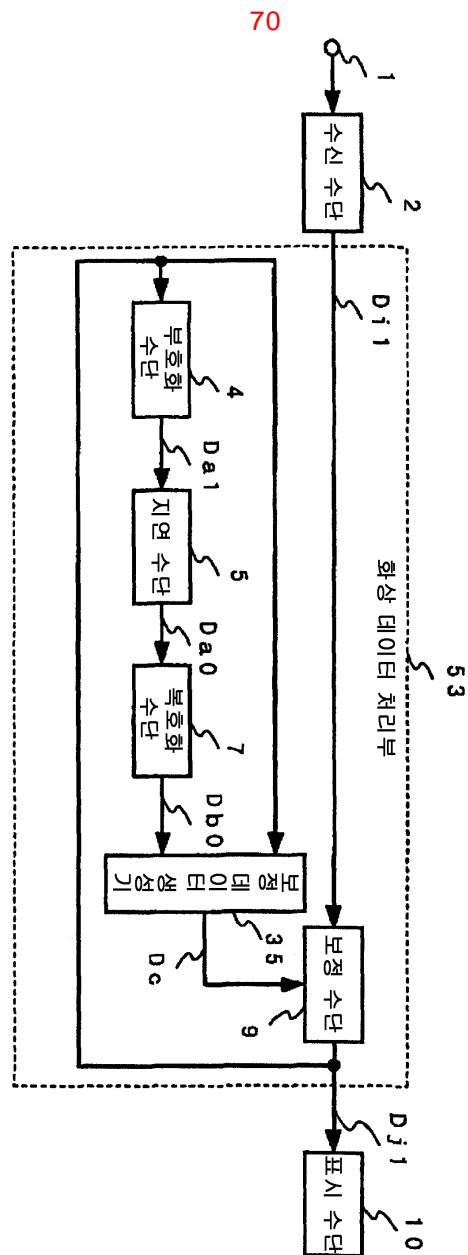




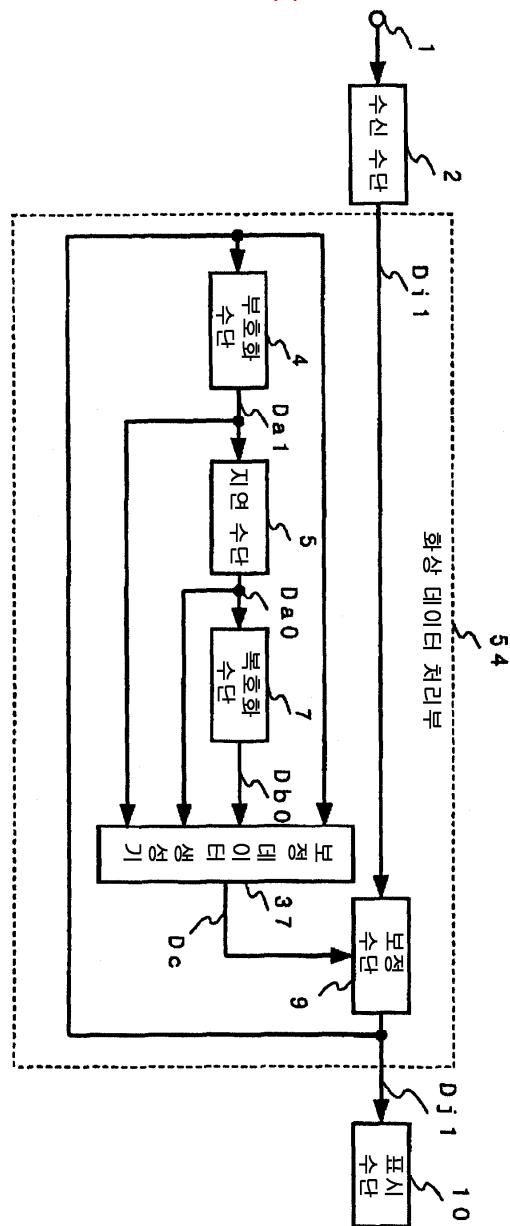
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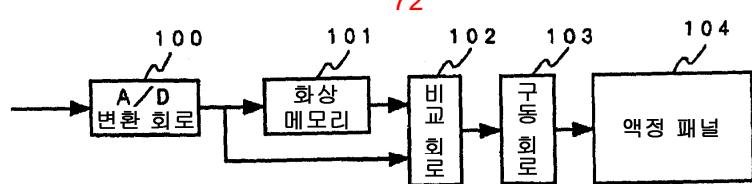




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	A	B	C	D	E	F
a	어드레스 0		어드레스 1		어드레스 2	
b						
c	어드레스 N		어드레스 N+1		어드레스 N+2	
d						
e	어드레스 2N+1		어드레스 2N+2		어드레스 2N+3	

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	n+1 프레임				n 프레임			
	A	B	C	D	A	B	C	D
a	50	150	50	50	a	50	1	50
b	50	150	50	50	b	-	-	-
c	50	150	50	50	c	50	-	-
d	50	150	50	50	d	-	-	-
(a)					a	50	50	50
(b)					b	50	50	50
(c)					c	50	50	50
(d)					d	50	50	50

专利名称(译)	液晶驱动电路		
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[标]申请(专利权)人(译)	三菱电机株式会社		
申请(专利权)人(译)	三菱电机有限公司		
当前申请(专利权)人(译)	三菱电机有限公司		
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摘要(译)

本发明的目的，通过适当地控制施加到液晶上的电压是，例如，通过编码当前(现)图像以提供液晶驱动电路，能精确控制液晶驱动本发明的电路的液晶的响应速度解码所述编码的图像，并以延迟的编码图像在一个帧周期的延迟的经编码图像进行解码，使用两个解码图像，并具有用于产生校正数据用于校正当前图像的灰度级的图像数据处理装置。在编码处理步骤中，通过减少图像数据量，减少了延迟图像所需的帧存储器的容量。校正数据期望使液晶在大约一个帧周期内达到与当前图像的色调值对应的透射率值。本发明的液晶驱动电路可以精确地控制液晶的响应速度。 2

