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(72) 519-2157 1141-9-1450

632-0072 126-4-303

639-1124 939-10- -202

575-0013 7-2-2-103

(74)

(54)

TFT

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12 3 ,
13 12 가 2 ,
14 1 3 ,
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23 21 ,
24 21 ,

, (4b) (G(1) . . . G(J)) 1 (4), (4a) 가, 1 (Hsync)
 (Q) (CO) (4b) (CLK) (CLK) (CO),
 Vnp) “L” 가 (Vs) (Hsync) 가 (4c),
 가 1 (5), (Vs) (4), 1 (180°) (Vs), (Vs)
 3 (5a,5b), (5c) (4d) (5d) (Vcom), (5), (Vs),
 (Vref1) (Vref2) (4) (5a) 가 (5d) 가 (Vcom), (4), (Vs),
 (5), (5b) 가 (CLK) (5d) 가 (Vcom) (5c),
 (Vcom) (G(1) . . . G(J)) 1 (4b,5b) (Tb2)) 가 (4c,5c) 가 (Vs) (Vcom),
 (5) (5) (system controller) (CLK), (2), (4),
 (CPU) (5) (4) 2 (Tb2) (6) 3 (Ta2) 1 (Tc2) 1 (Ta2), (2), (Vs),
 (4) (5) (6) (GP) (6) (G(1) . . . G(J)) 1 (),
 가 (Vs), (Vs), (Vs), (Vs), (Vs),
 4 J (1) (P(i,j)) i 1 (P(i,1)), i j (P(i,j)), i
 (Vcom) 가 4V (Vs) ± 2V (Vs) 2V
 1 1 (Ta2), 2 (Tb2) 1 (Tpo1) 1 (Ta2) 2 (Tb2) 3 (Tc2)
 3 (Tc2) (Vcom) 가 (Vcom) 가 (Vcom) 가 (Vcom),
 m) (P(i,1)) (Vd) (Vd) (Vd) (Vd) (Vd) (Vd)
 1 1 (Ta2), A (G(1)) (Vd) (GP) (Vy2) TFT(11) 가 ON
 (Vs) (Vd) (Vd) (Vd) (Vd) (Vd) (Vd)
 1) 가 OFF (Vd) (Vd) (Vd) (Vd) (Vd) (Vd)
 V LC (i,1) (Vd) (Vd) (Vd) (Vd) (Vd) (Vd)
 com) (P(i,1)) (Vcom) (Tfalse(1)) (C) (Vs) (B) (Tfalse(j))
 (Vcom) (Vcom) (Vcom) (Vcom) (Vcom) (Vcom) (Vcom)
 (P(i,1)) (Vcom) (Tfalse(1)) (C) (Vs) (B) (Tfalse(j))
 (Vcom) (Vcom) (Vcom) (Vcom) (Vcom) (Vcom) (Vcom)
 (Ta2)) (Vcom) (2) (Tb2) 3 (Tc2) 5 (Tpo1) 6 (P) (Tb2)
 (Vcom) (Vcom) (Vcom) (Vcom) (Vcom) (Vcom) (Vcom)

n	0	0.5	1	1.5	5.0	10
	x					

, $V_{x2}=2V$, $V_{ac2}(Vs) = 4V$, $C_{LC} = 4.7\text{pF}$, $C_{sd1} + C_{sd2} + C_{gd1} + C_{gd2} = 0.3\text{pF}$, $C_{sd1} + C_{sd2} = 0.15\text{pF}$, $CD = 5\text{pF}$, $Ta2 = 15\text{mS}$, $Tb2 = 80\text{mS}$, $Tc2 = 0.5\text{mS}$, $V_{LC rms(i,1)} = 1.999V_{rms}$, $V_{LC rms(i,J)} = 1.981V_{rms}$, $0.02V_{rms}$, (P) , $0.02V_{rms}$, 가

, (Ta2) (Vs) 11 . 2 (Tb2) , Vx2=2V, Vac=4V, C LC =4.7pF, Csd1+Csd2+Cgd1+Cgd2=0.3pF, Csd1+Csd2=0.15pF, CD=5pF, Ta2=15mS, Tc2=0.5mS

1 (Ta2)) 2 (Tb2) 3 (Tc2)) 가

가

(Vs)

가

가

3]

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(1),

(2),

3)

a)

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$$\begin{array}{ccccccccc} \cdot & 1 & (13a) & , & 2 & (S(i), S(i+1)) & & (12 \dots) \\ & (12 \dots) & & . & 2 & (13b) & 2 & (S(i), S(i+1)) \\ (12 \dots) & , & & & (12 \dots) & . & , & 1 & (13a) & 2 & (\\ , & 13 & & , & & & & & (Vcom1, Vcom2)가 & & \\ & 1 & (13a) & , & 2 & (13b) & & (Vcom1, Vcom2)가 & & . \end{array}$$

13b), 13, 1 (13a), 2 (13b) (Vcom1,Vcom2)가 (Vcom1,Vcom2)가 .

1 2 기

M(Metal Insulator Metal)

[, 14 15] , . 14 15 D-D (1)
 14 (1) (7) TFT (11 . . .)† (7) (8) (14)
 , TFT , MIM(Metal Insulator Metal) TFT
 . (8) (8) , RGB (44) (41), (42) (43)
 . (44) (44) 가 가 (44) (13)
 TFT(11) (7) (46) (45) , i
 (46) (47) (46) (45) (45) n + , i
 (47) (47) (48) (48) (49) (48) 2
 . n + (48) (48) (46)
 , n + (50) (50) (46) 15

(51)	,	(51)		(12a)	.	TFT(I1...)	
,	,	(51)		(12b . . .)	.		(12b . . .)
,	(12b)	가	(51)	.	(12b . . .)		.
(49)	가	,	TFT(11)	(52)	,	(50)	,
(12)	가	,	(12b)	(13)	(12b)	(14)	(52)
(12a)		(12b)		,	(12b)	(13)	,
.	,		(12a)	(12b)	(12)	(14)	.
,	,			(12b)	(12)	.	.
,	(1)	,			(12)	.	.
T(11)	(45)		14	(14)	.	15	, TF
	(S(i) ...)			(G(j) ...),	TFT(11)	(49)	
.			(7)	.	.	,	(12a . . .)
.	(53 . . .)		(G(j)...)	,	가	(53 . . .)	.
(12a . . .)				(G(j)...)		(46)	.
,	(53 . . .)		(7)	,	(G(j)...)	.	.
(12a . . .)			(G(j)...)	(46)	.	15	.
,				.	,	(12b . . .)	.
.				.	.		.
.			14	(51)	15		.

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가

가

가

가

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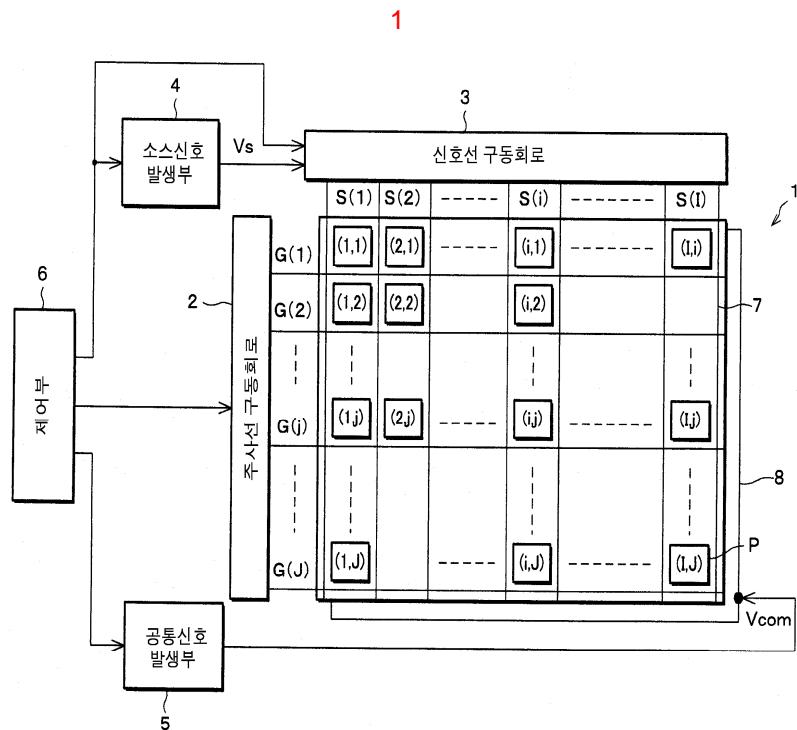
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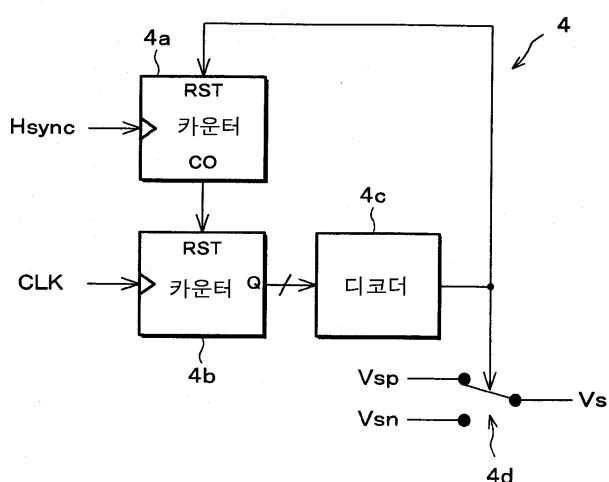
18

16 가
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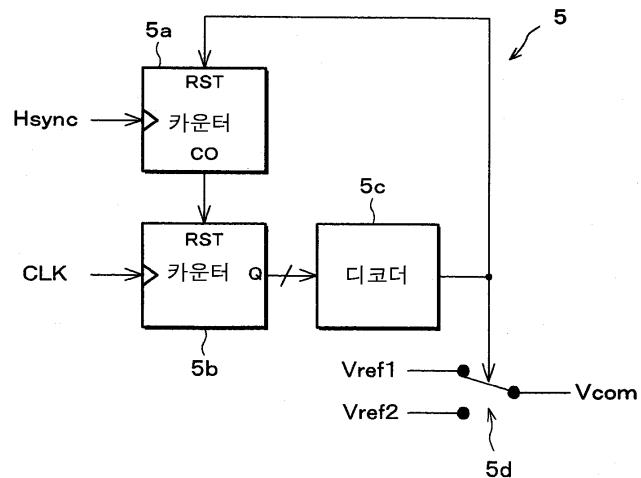
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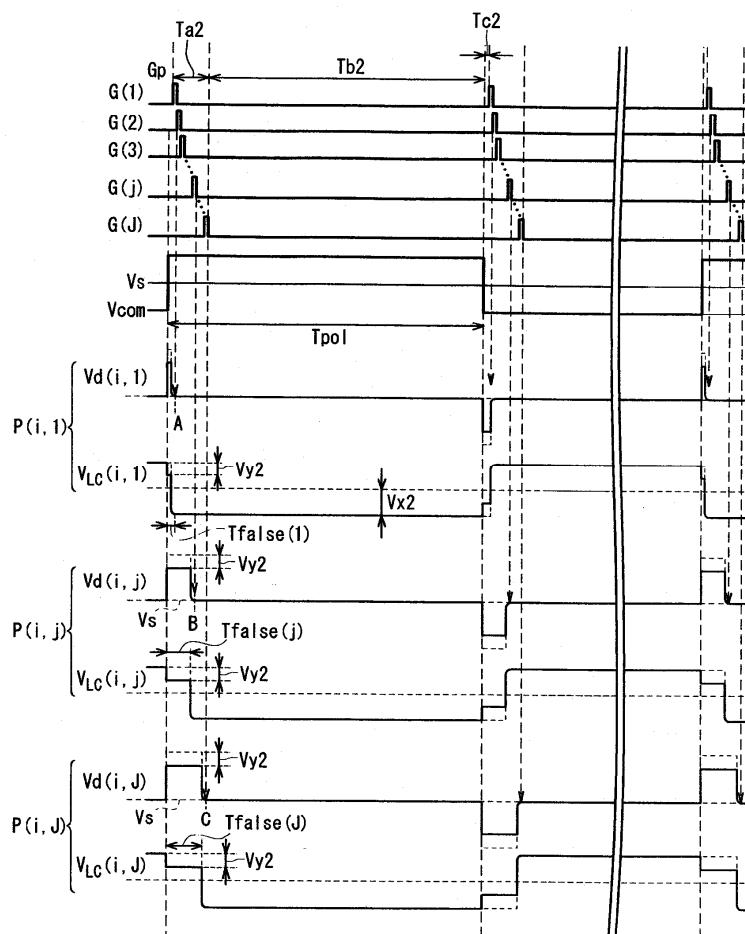
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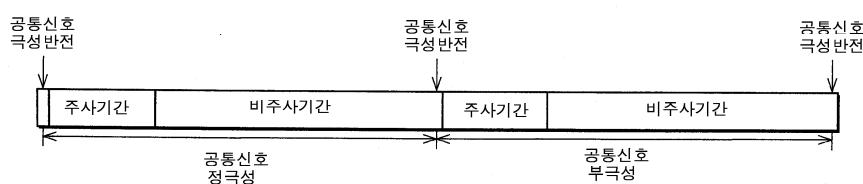
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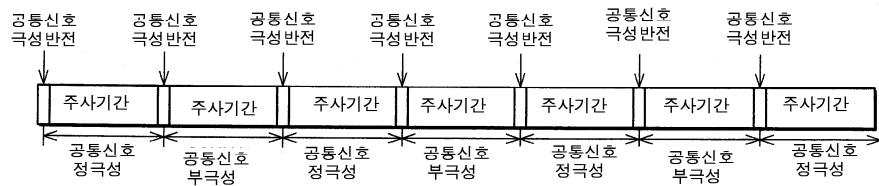


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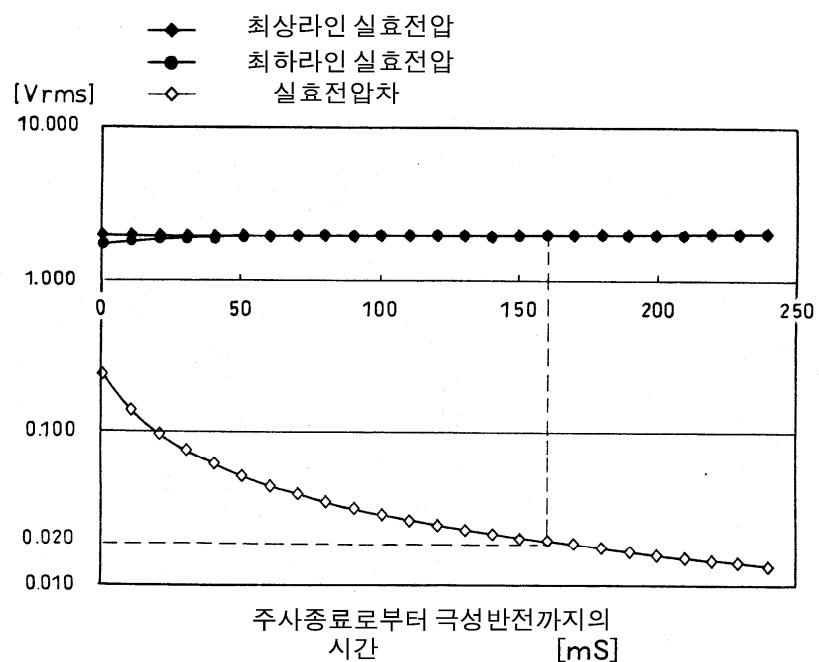


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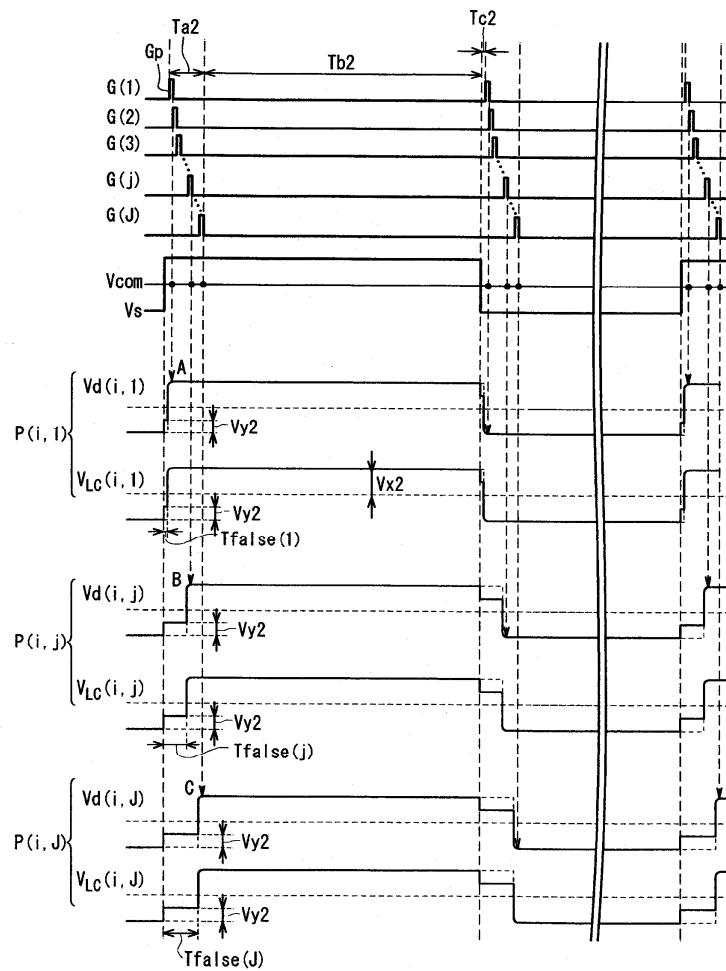
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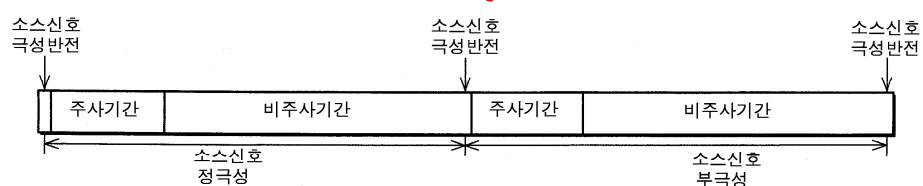
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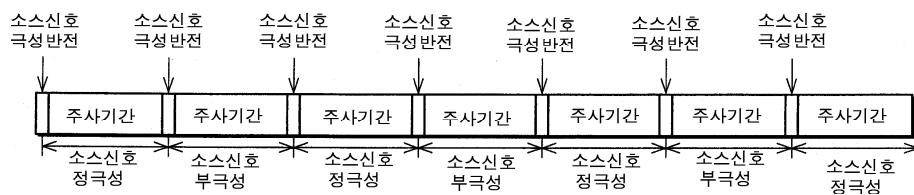


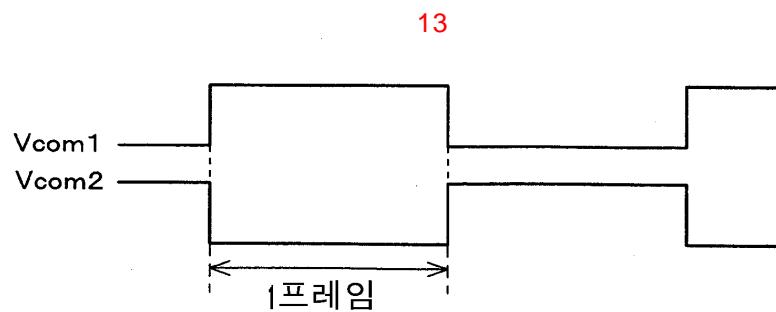
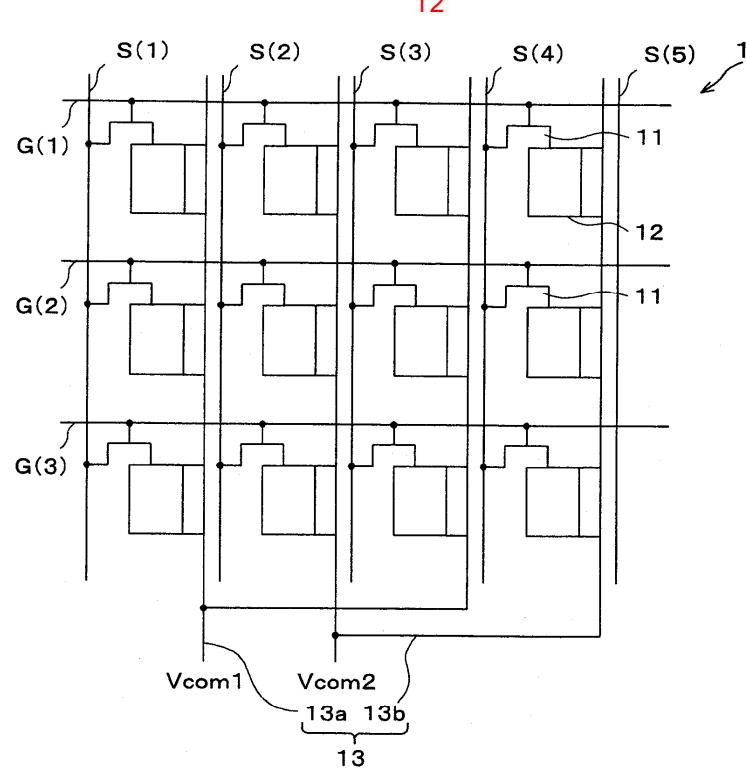
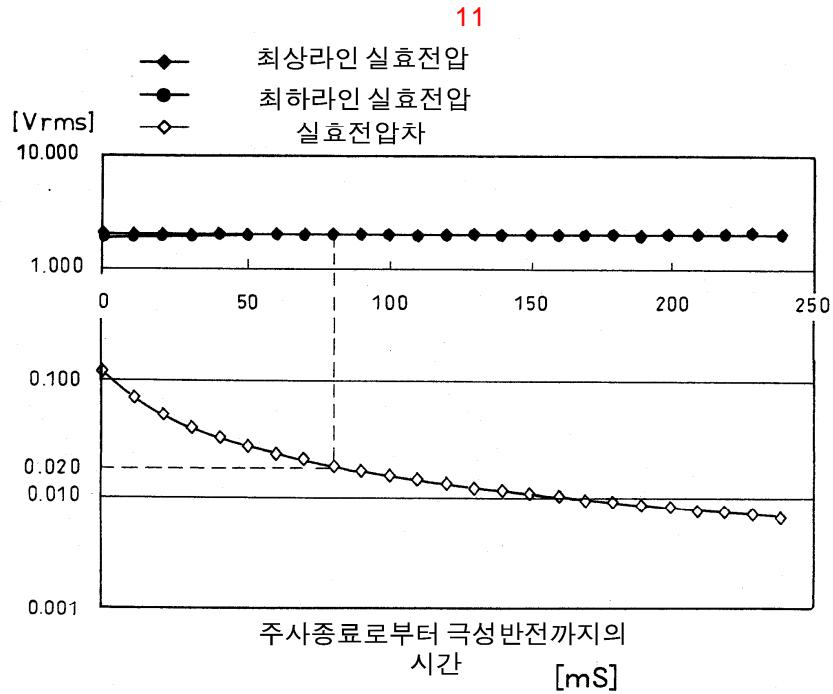
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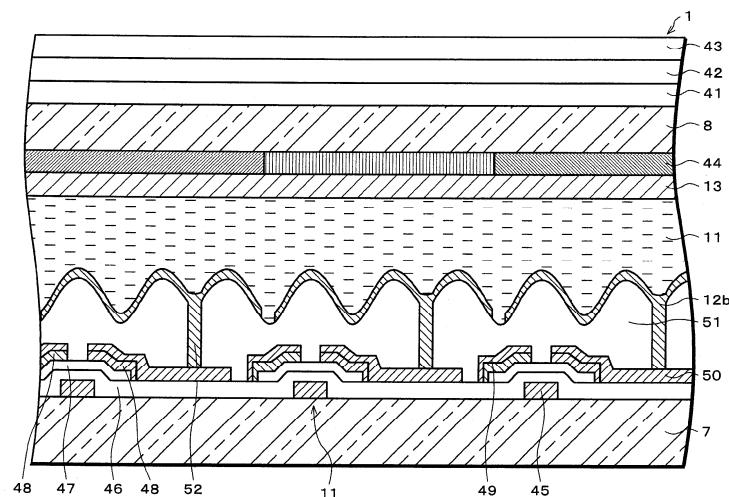
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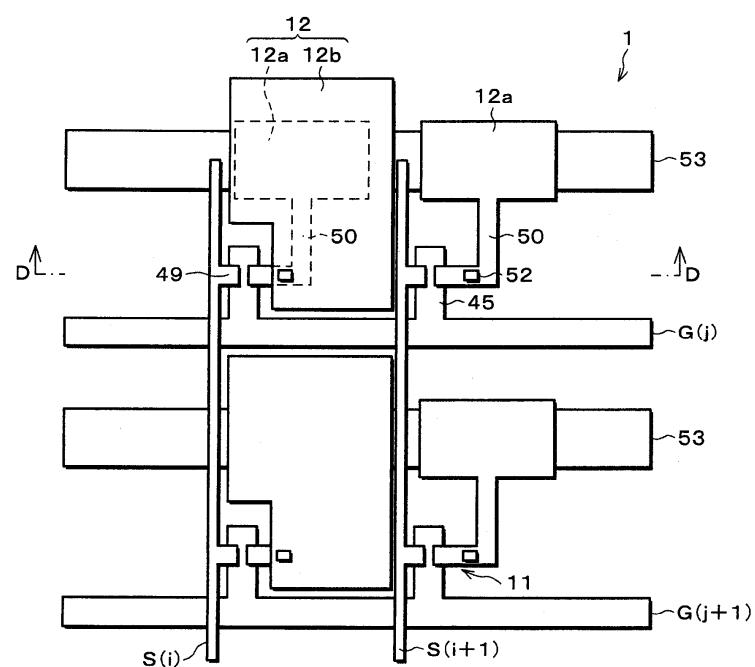




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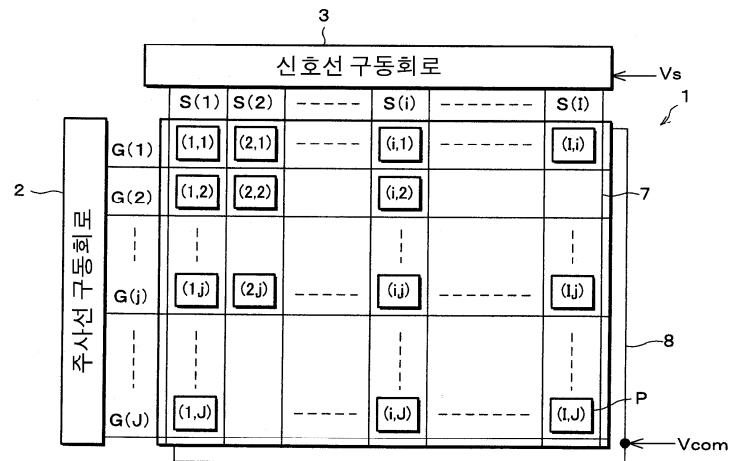


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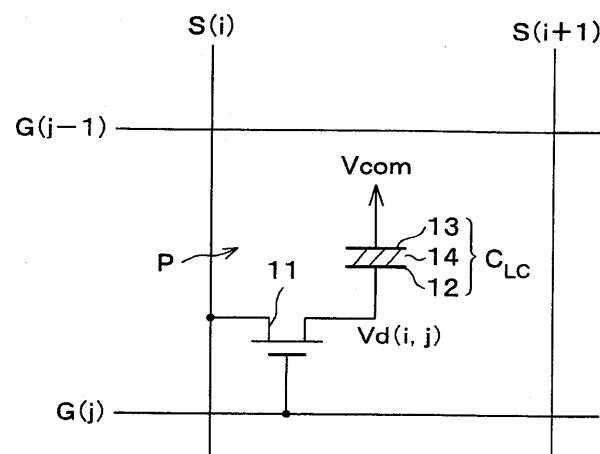


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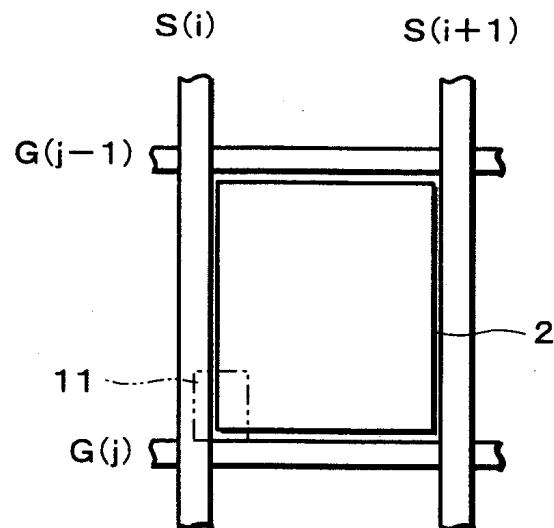
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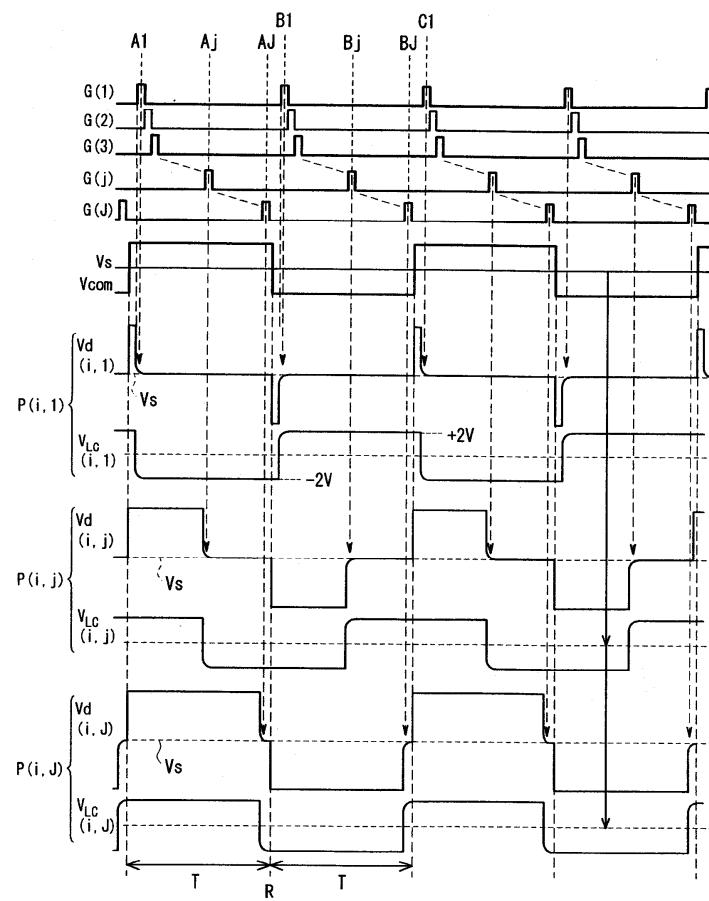
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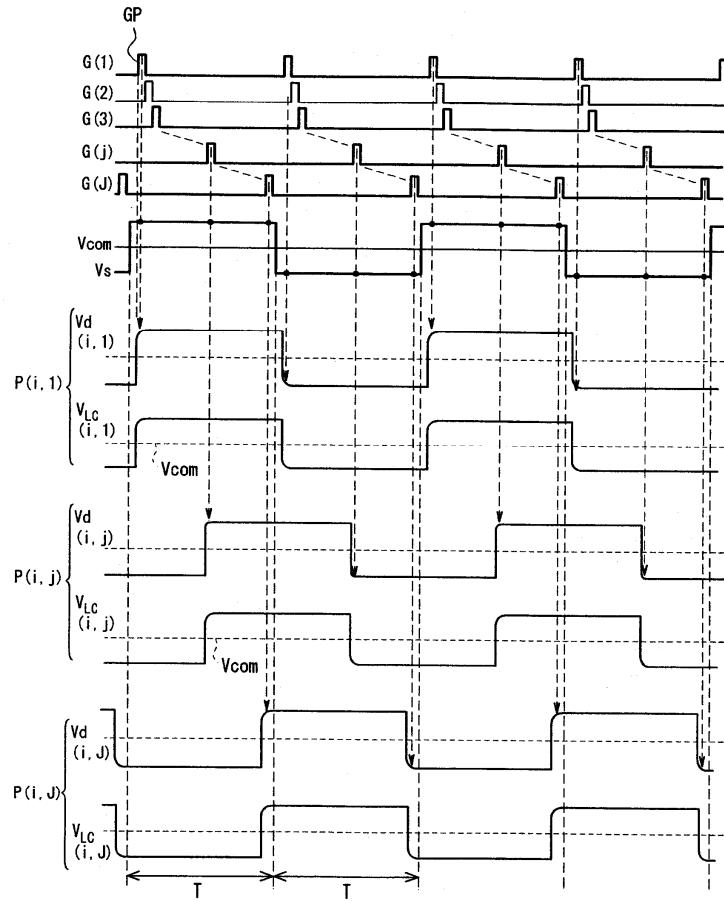
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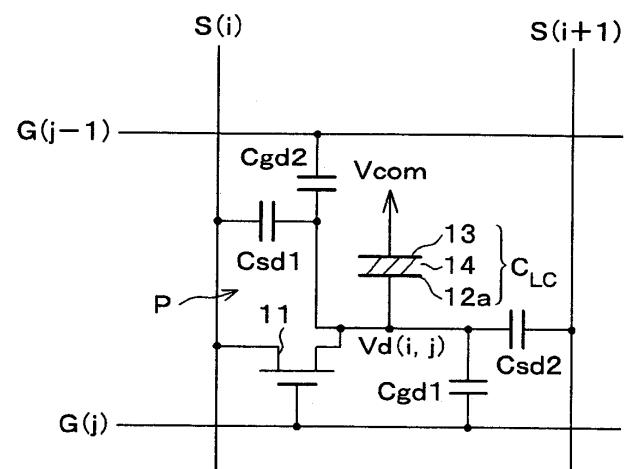
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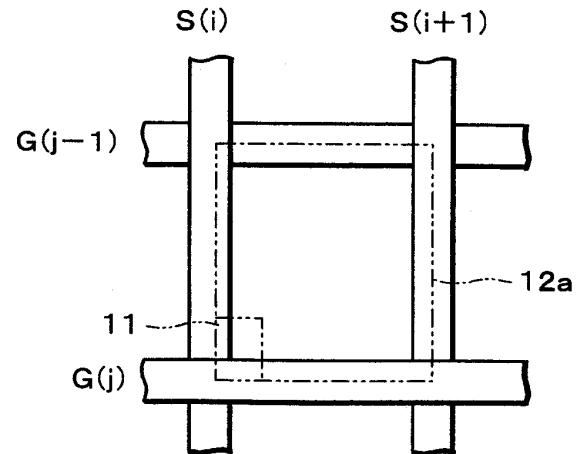
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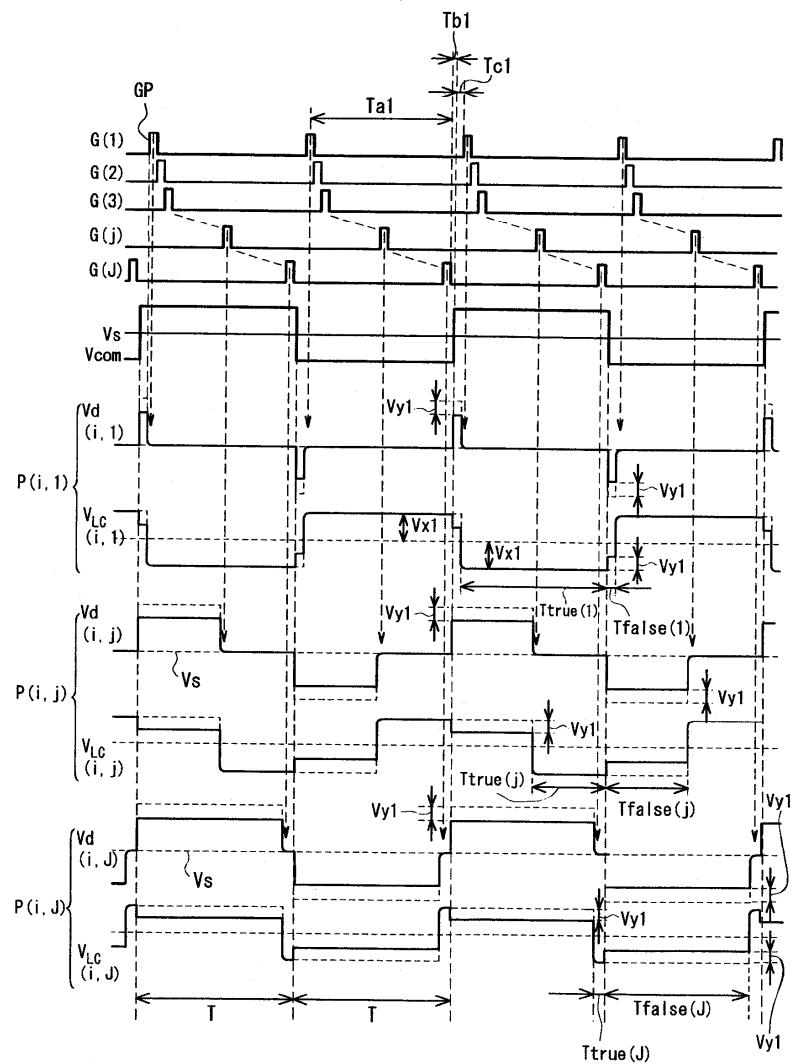
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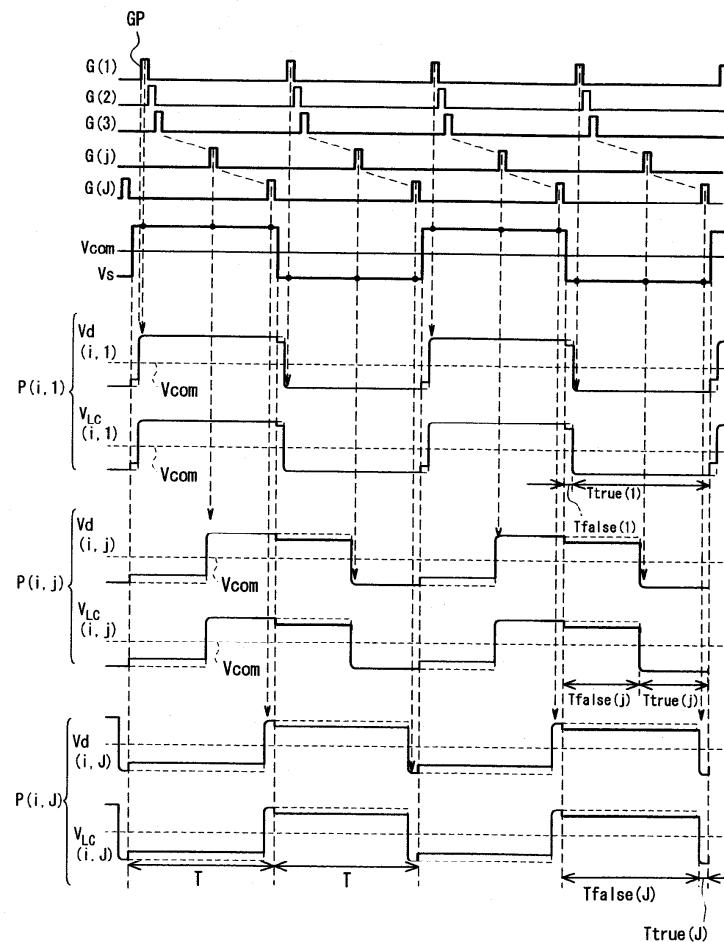


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专利名称(译)	有源矩阵型液晶显示装置及其驱动方法		
公开(公告)号	KR100464811B1	公开(公告)日	2005-01-05
申请号	KR1020010061490	申请日	2001-10-05
[标]申请(专利权)人(译)	夏普株式会社		
申请(专利权)人(译)	夏普株式会社		
当前申请(专利权)人(译)	夏普株式会社		
[标]发明人	YANAGI TOSHIHIRO 야나기토시히로 KUMADA KOUJI 쿠마다코우지 OHTA TAKASHIGE 오타타카시게 MIZUKATA KATSUYA 미즈카타카쓰야		
发明人	야나기토시히로 쿠마다코우지 오타타카시게 미즈카타카쓰야		
IPC分类号	G09G3/36 G09G3/20 G02F1/133		
CPC分类号	G09G3/3648 G09G3/3655 G09G3/3614 G09G2320/0223		
代理人(译)	LEE , 金泰熙		
优先权	2000308392 2000-10-06 JP 2001226740 2001-07-26 JP		
其他公开文献	KR1020020027275A		
外部链接	Espacenet		

摘要(译)

有源矩阵液晶显示器通过TFT等写入来自信号线驱动电路的源信号来驱动液晶，以在矩阵基板上的显示单元中显示电极，并将从公共信号发生器提供的公共信号施加到公共电极上。相对基板，公共信号在每帧中改变极性。在对应于一帧的扫描线完成扫描之后，控制器控制扫描周期之间的间隔和公共信号的极性变化周期，以便提供比扫描周期长的非扫描周期。非扫描周期的提供延长了显示单元保持指定电压的持续时间。这减小了由反射电极结构中产生的寄生电容引起的保持电压变化的影响，其中显示电极部分地忽略扫描线和信号线。因此，在帧反转驱动中，显示屏的顶部和底部之间的亮度差异减小，并且显示质量得到改善。

