

(19) (KR)
(12) (A)

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(21) 10-2002-0012453
(22) 2002 03 08

(71) 136-1

(72) 208-708

525-29

410-1306

210-50

(74) :

(54)

(VCR) , (TV) (DVD),
가 .

2

1

2 RC

3 2

4 2 RC
5 7
* *
1 : 1 2 : 1
3 : 4 :
5 : 6 :
7 : VCO 8 : 2
9 : 2 10 :
40 : 41 :
42 : 43 : (LSI)
50 : RC 53 : 가 (VR)

), (VCR) , (TV) (DVD
ling stsr) 가 (Horizontal samp

1
(6), VCO , 1 (1), 1 (2), (3), (4), (5),
(7), 2 (8), 2 (9) (10)
CO) 1 (1) (composite synchronous: Csync) VCO(Voltage Controlled Oscillator: V
(7) VCO 1 (2) 1 (1)
(3) 1 (2) Csync (3)
(6) VCO (7) (5) (4) (5)
2 (8) (V_IN) VCO (7) (VCO)
2 (9) , (10) 1 (2) 2 (9)
(HSYNC)

(setting) (user)가 (Jumper) 가 가
(real) (phase)

range) HD (composite synchronous: Csync) (shift

(DVD), (VCR) (TV) (Horizont

al sampling stsr) 가

1 1 RC

1 1

2 2

1 2

2 (40) RC (5

0) (CP_IN) (42) (40) (schmitt trigger) (CSYNC) (42) (41) , RC (50) (41) (A) (LSI)(43)

RC (CP_IN) (50) (41) (CP_OUT) (42) (

(CP_IN) RC (rising time) (50) (CP_IN) (falling time) LSI(43)

42) RC (horizontal timing) (4) (A) (1 '3') (A) 1 (2)

(Lock)

3 2 (42) , VIH
 , VIL .

4 2 RC (50) .

RC (50) (41) (CP_OUT) 가 (53) , 가
(53) (Nd1) (Vss) (C) , 가 (53)
(Nd1) (CP_IN)가 (42) .

RC (50) 가 (53) (CP_IN) (smooth)
, 5B k (CP-IN) (42)
(VIH) (VIL) (CP_IN_logic)

(CP_IN_Logic) (CSYNC) PLL (Lock)
(CP_IN) 5B ,
(CSYNC) 5A LCD 가
5A 6B ,

3 (42) (VIH VIL) 가 .
(42) (CP_IN_logic)

5A (CSYNC) 5A
(CSYNC) CP-IN-logic , (6)
VCO (7)

, LCD IC (CP_IN) (CP_OUT) (CP_OUT) 가 가
(53)

1 , 1 (1) 2 (8) (HD) (HD) 가
(CSYNC) 가 (Csync) 2 (42)

(CP-IN-logic)
가

(CRT) (TFT-LCD), (STN), (PDP)
가 .

(DVD), (VCR) , (TV)
al sampling stsrt) 가 (Horizont

(57)

1.

1

1

RC

1

2

2

1 2

2.

1

RC (CP_IN)

3.

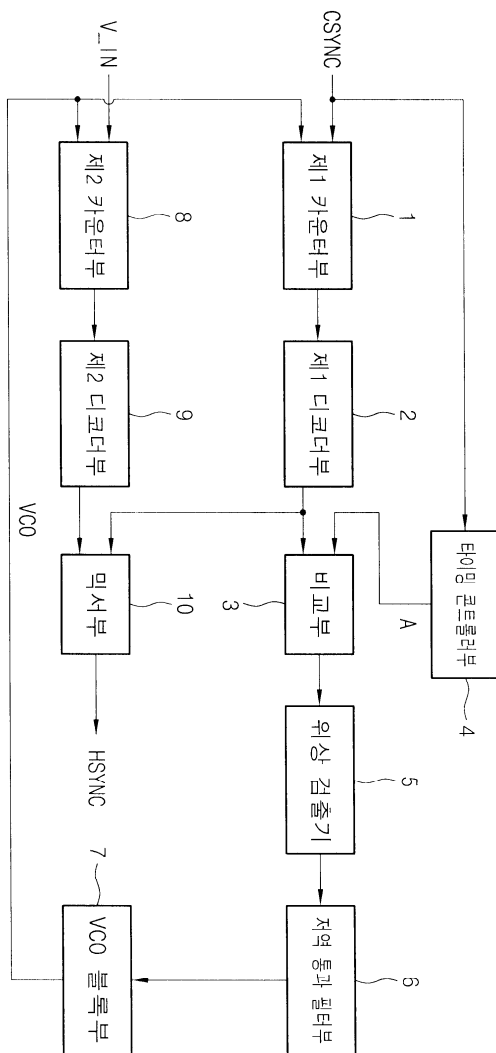
2 RC

(CP_OUT) RC 1 가

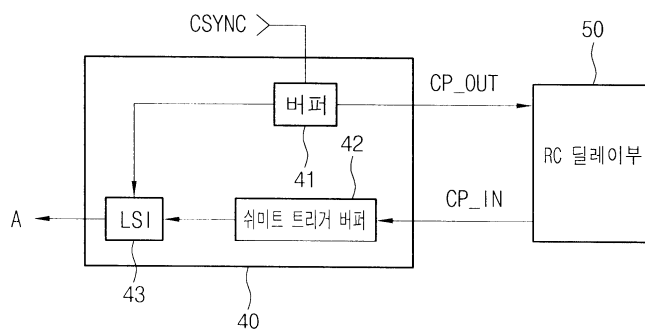
1

1

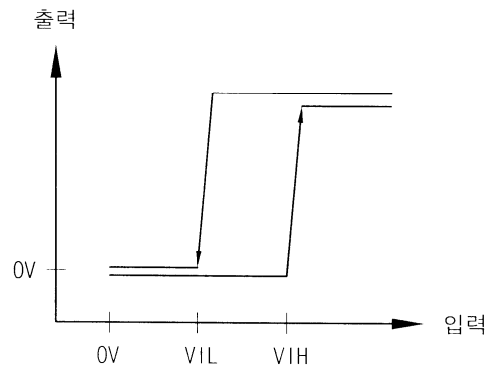
1



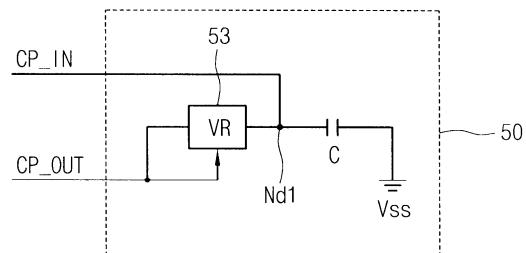
2



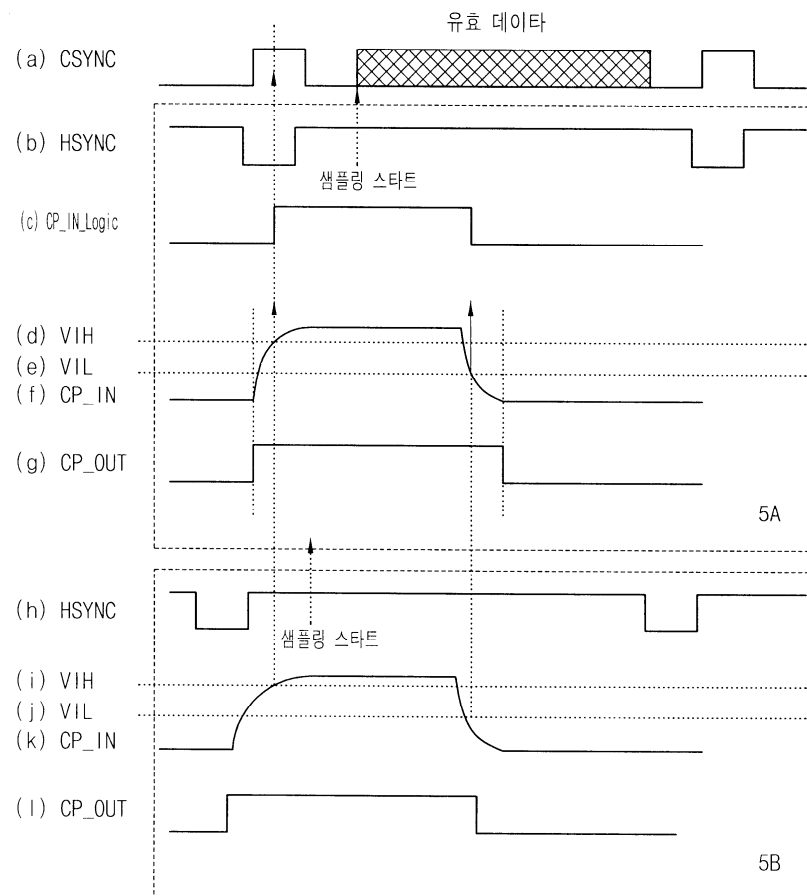
3



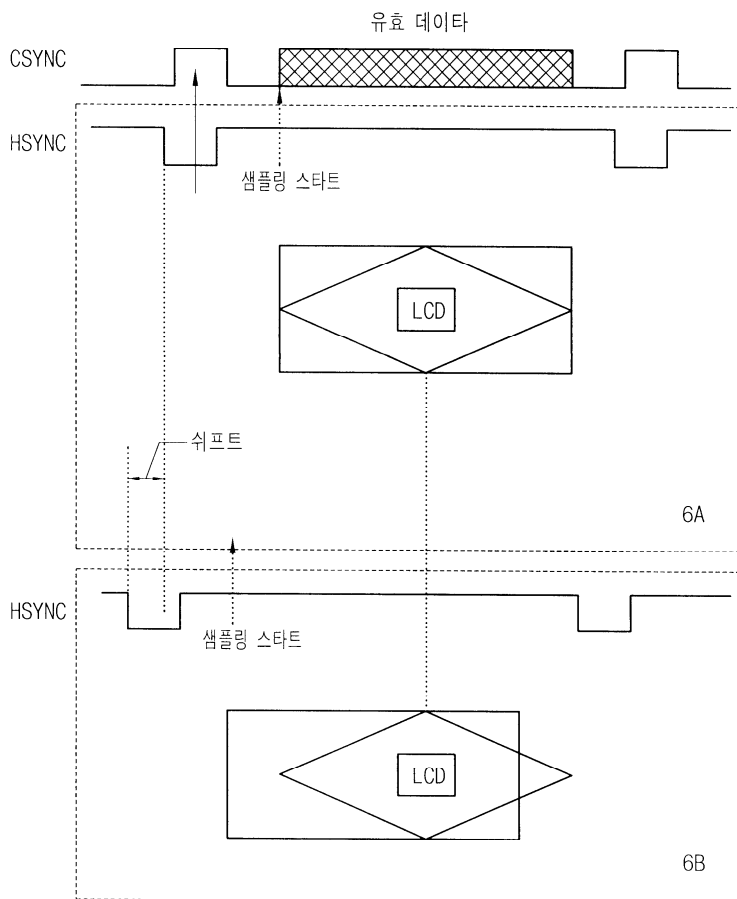
4



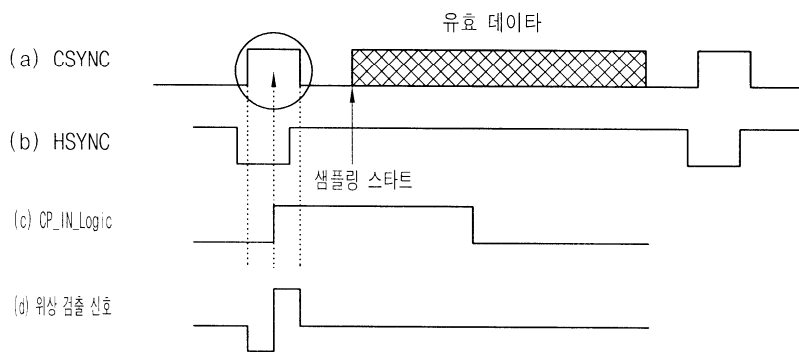
5



6



7



专利名称(译)	薄膜晶体管液晶显示装置的屏幕调整电路		
公开(公告)号	KR1020030073072A	公开(公告)日	2003-09-19
申请号	KR1020020012453	申请日	2002-03-08
[标]申请(专利权)人(译)	HYDIS TECH HYDIS技术有限公司		
申请(专利权)人(译)	하이디스테크놀로지주식회사		
当前申请(专利权)人(译)	하이디스테크놀로지주식회사		
[标]发明人	BAE YOUNGMIN 배영민 CHEGAL SEUNGWON 제갈승원 SUNG NAKHYUN 성낙현 MOON JOONIL 문준일		
发明人	배영민 제갈승원 성낙현 문준일		
IPC分类号	G02F1/133		
代理人(译)	赵龙HYUN		
其他公开文献	KR100812282B1		
外部链接	Espacenet		

摘要(译)

薄膜晶体管液晶显示装置的屏幕调整电路技术领域本发明涉及薄膜晶体管液晶显示装置的屏幕调整电路，更具体地说，涉及使用用于TV或VCR的DVD中的定时控制器的比较脉冲来改变水平采样开始位置的视频屏幕调整电路。对于薄膜晶体管液晶显示装置的屏幕调节电路，其中要显示的屏幕的位置左右变化，并且可以改变屏幕的范围。 2

