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10-2004-0052356
2004 06 23

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 10 : 12 :
 14 : 16 :
 18, 20, 40, 60 : 22, 42, 62 : PWM IC
 24, 44, 64 : PWM 26 :
 46, 66 : AND 28, 48, 68 :
 30, 50, 70 : 1 32, 52, 72 : 2
 45 :

, .
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 , .
 , (GL1 GLm) 1 (14) , (12) , (12) (DL1 DL
 n) (16) , (14) (12) (16) (VDD, VGH, VGL) ((18)
 (18) (12) (GL) (DL) (Clc) , (Clc)
 (TFT) .
 (TFT) (GL) (VGH) -
 (GL) (DL) (Clc) , (Clc) (TFT) 가
 (VGL) (Clc) - (Clc) 가
 (Clc) 가 (TFT)
 , (Clc) 가 가 (Clc)
 (TFT) (가 가 가 가
 (14) (18) (Gate Shift Clock; GSC) (GL1 GLm) (Gate Start Pulse; GSP) (18)

(VGH) (14) (GL) (VGL)

(VGH) 가 (18) (18)

(Gate Output Enable; GOE) (14)

(16) (18) (Source Start Pulse; SSP) (16) SSC

(Source Shift Clock; SSC) (RGB) (Source Output Enable; SOE) (R)

le; SOE) (16) (DL)

GB) () (18)

(POL) (16)

(SOE) 가 (DL) (16)

(18) (14) GSP, GSC, GOE (16)

SSP, SSC, SOE, POL (Data Enable; DE) (18) (Hsync), (Vsync),

(RGB) (Dot Clock; DCLK) GSP, GSC, GOE, SSP, S

SC, SOE, POL

(18) (VCC) (VDD), (VGH), (V

GL) (12) (18) (VDD) (VGL) (16), (14),

(18) (10) () (18)

(18) (VCC) 2

(18)가 VCC --> VGL --> VGH (VDD, VGH, VGL)

(VDD, VGH, VGL)

(Pulse Width Modulation; PWM) (VCC) (18) T

22 (VGL) , T25 (DC-DC) (VGH) (18)

(20V) (VGH) (VGL)

RC

가 (VCC) (18) (VGH, VGL)

(VCC) 70% T23 (VGL) 70%

T24 , T26

가 VCC --> VGL --> VGH (VCC, VGL, VGH) 70%

(VCC) 3

3 (VCC) (VGL) T31 (VGH) T32 T35 (VCC)

(18) (VCC) 10ms (T34) (VGL) 7

0% (VCC) 70% (CGL) (T33) 가

(VCC) (GL) 가

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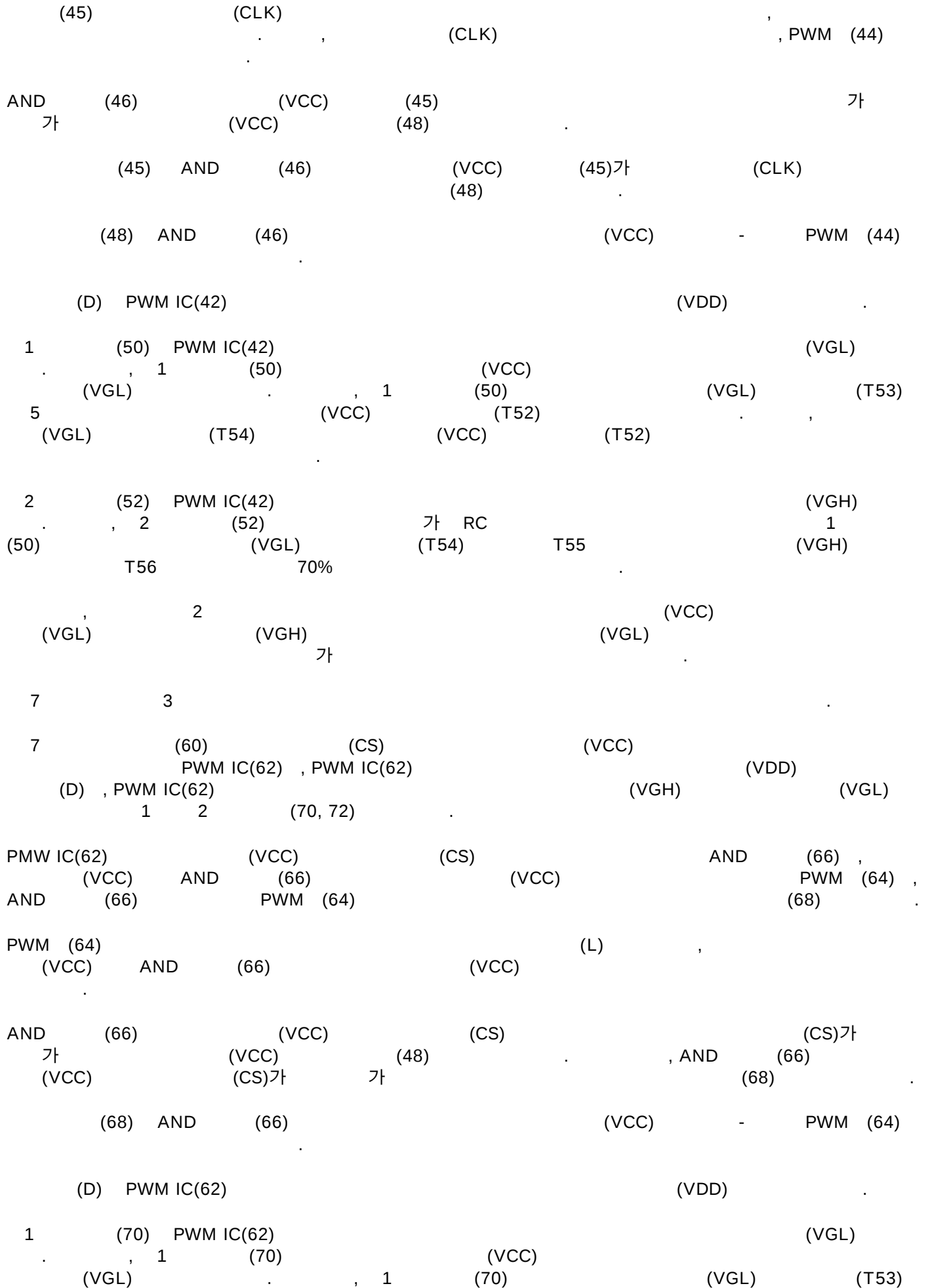
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4 (20) (VCC)
 PWM IC(22) , PWM IC(22) (VDD) (D) , PWM
 IC(22) (VGH) (VGL) 1
 2 (30, 32) .
 PMW IC(22) (VCC) (26) , (VCC)
 (VCC) PWM (24) , (26) PWM (24)
 (28) .
 PWM (24) (L) ,
 (VCC) (26) (VCC)
 .
 (26) (VCC) , (26)
 .
 (28) (26) (VCC) - PWM (24)
 .
 (D) PWM IC(22) (VDD) .
 1 (30) PWM IC(22) (VGL)
 . , 1 (30) (VCC) (VGL) 5
 (VGL) (VCC) , 1 (30) (VGL)
 (VGL) (T54) (VCC) (T52) . ,
 5 , T51 (VCC)
 (VCC) T52 - 70% (VCC) (VCC)
 (VGL) (T52) T53
 T54 70% .
 2 (32) PWM IC(22) (VGH)
 . , 2 (32) 가 RC 1
 (30) (VGL) 70% (T54) T55 (VGH)
 T56 .
 , 1 (VCC)
 (VGL) (VGH) 가 (VGL)
 .
 6 2 .
 6 (40) (CLK) (VCC)
 PWM IC(42) , PWM IC(42) (VDD)
 (D) , PWM IC(42) (VGH) (VGL)
 1 2 (50, 52) .
 PMW IC(42) (CLK) (45) , (45) (VCC)
 (VCC) AND (46) , (46) AND (46) PWM (44)
 PWM (44) , AND (46) PWM (44)
 (48) .
 PWM (44) (L) ,
 (VCC) AND (46) (VCC)



5 (VGL) (T54) (VCC) (T52) (VCC) (T52) ,
2 (72) PWM IC(62) (VGH)
(70) , 2 (72) 가 RC 1
(VGL) (T54) T55 (VGH)
T56 70%
(VGL) 3 (VGH) 가 (VGL) (VCC)

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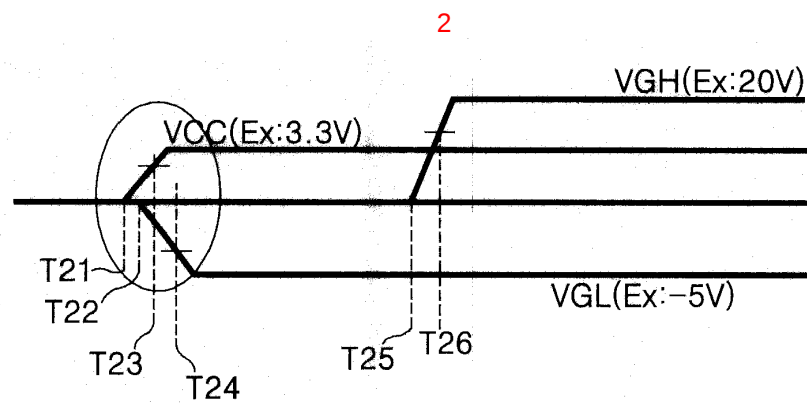
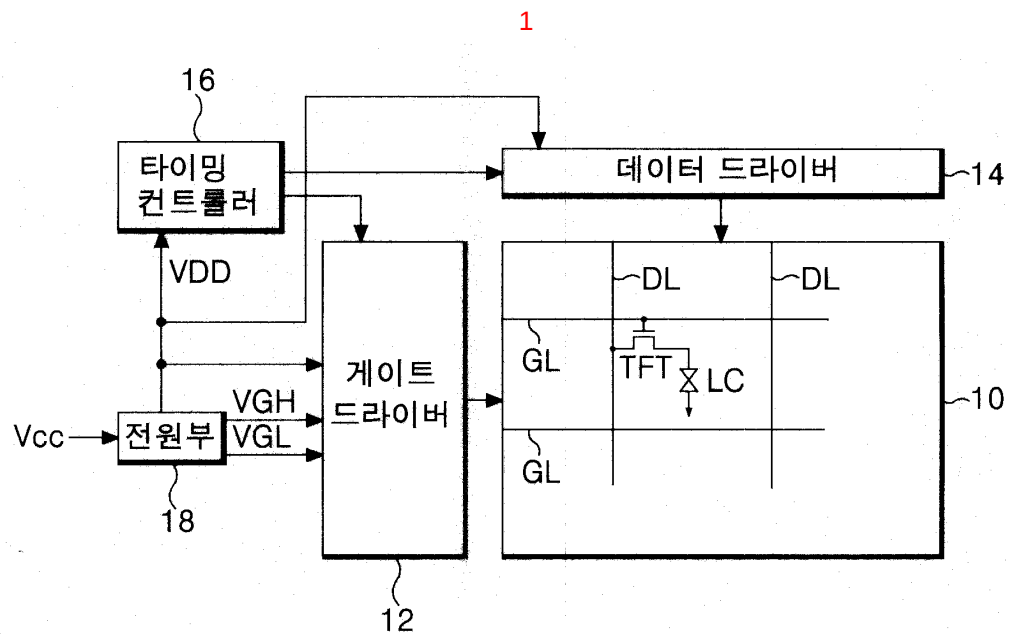
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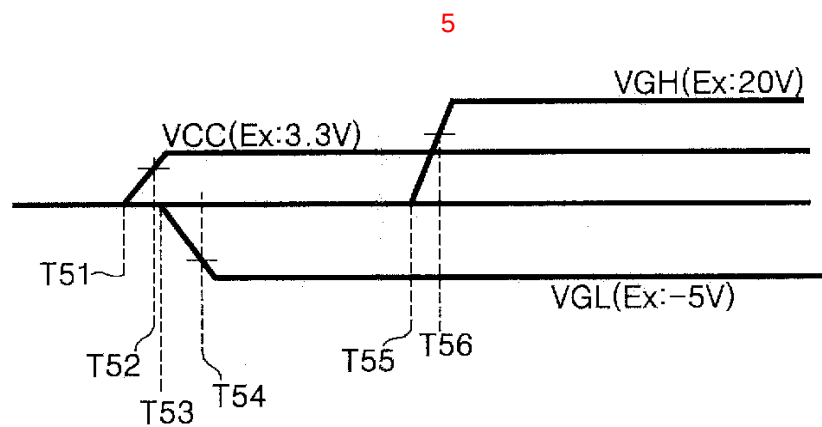
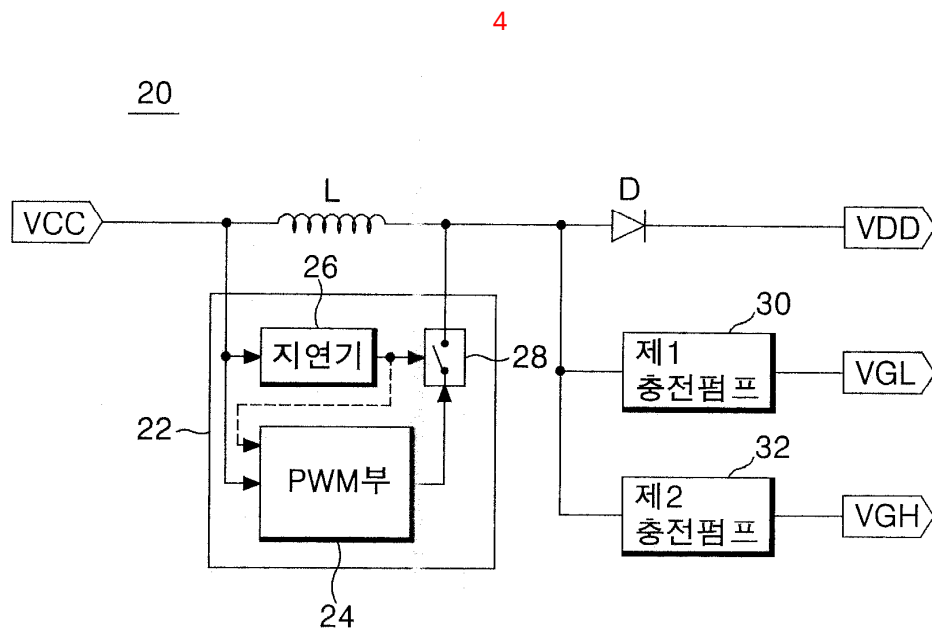
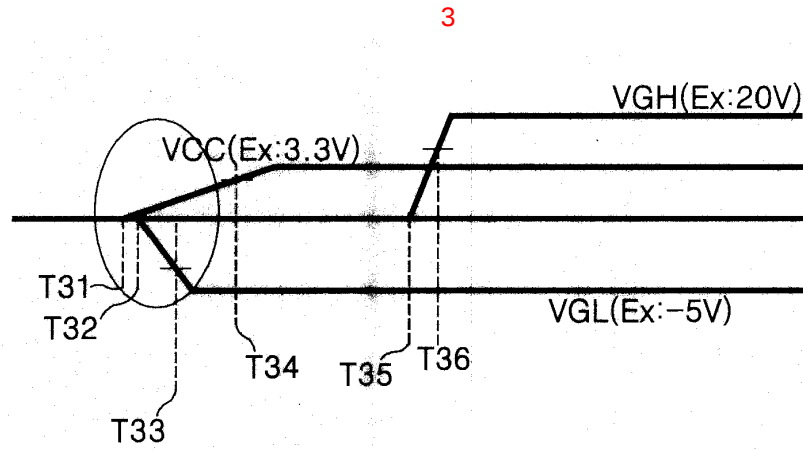
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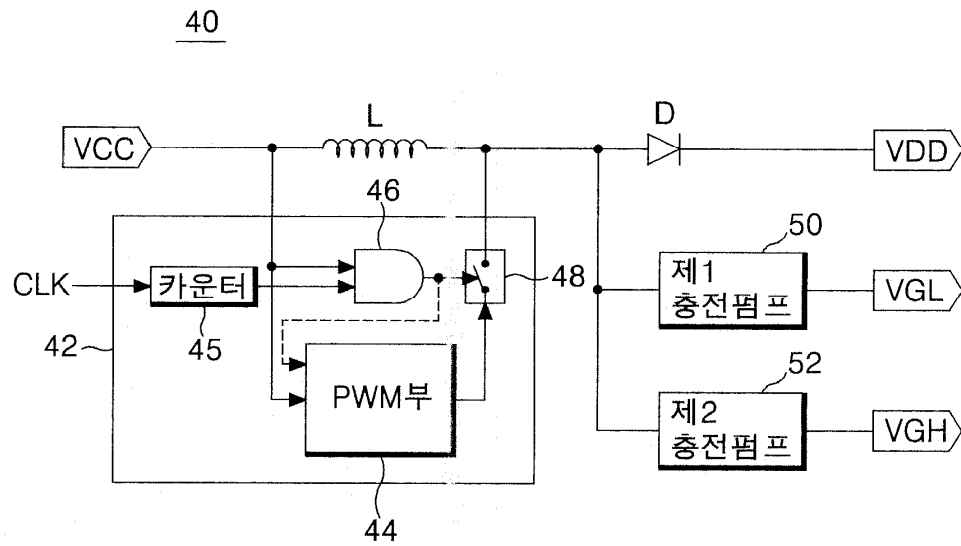
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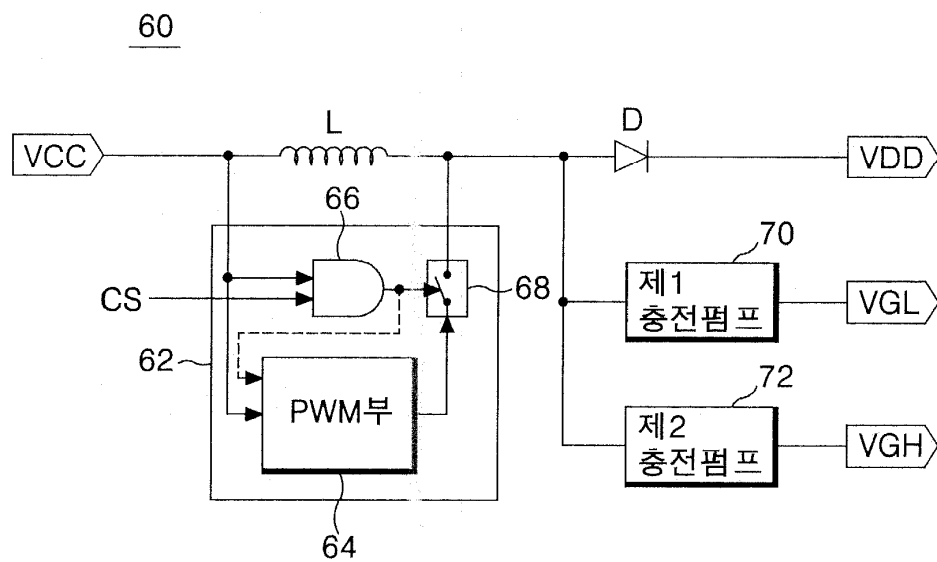




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专利名称(译)	用于液晶显示器的电源序列控制的方法和装置		
公开(公告)号	KR1020040052356A	公开(公告)日	2004-06-23
申请号	KR1020020080227	申请日	2002-12-16
[标]申请(专利权)人(译)	乐金显示有限公司		
申请(专利权)人(译)	LG显示器有限公司		
当前申请(专利权)人(译)	LG显示器有限公司		
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发明人	홍영기		
IPC分类号	G09G3/36		
其他公开文献	KR100942832B1		
外部链接	Espacenet		

摘要(译)

本发明涉及一种稳定维持电源序列的液晶显示器的电源序列控制装置和方法，包括计数输入时钟信号作为参考值的步骤;延迟输入驱动电压的步骤，它执行AND操作;以及使用延迟输入驱动电压产生驱动液晶显示器电压的步骤。

