

(19) (KR)
 (12) (A)

(51) . Int. Cl.⁷
 G02F 1/1339

(11)
 (43)

10-2004-0040159
 2004 05 12

(21) 10-2002-0068506
 (22) 2002 11 06

(71) 136-1

(72) 805 309

(74)

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(54)

, 가 ; ; ;

3a

1a 가

1b 1a A - A'

2 VA(Vertical Alignment)

3a

3b 3a B - B'

4a 4c

5 3a C - C'

()

100 : 200 :

200a : 200b :

300 : 400 :

400a : 400b :

600 :

TN- , STN- , MIN- ,

Alignment) 가 , VA(Vertical
 OCB(Optically Compensated Band) 16ms
 VA 가 LCD , 1a 1b
 (Post Spacer Patterned Spacer)(40)

RGB , 1 (40) , (10) ,

, 2 , VA (60) ,
 (50) .

(60) (50) (60) , (50) 가

, 가

()

3a , 3b 3a B - B' .
 3a (200b) , (200b) (200a) , (200a) , (400a)
 0a (150) (400b) 가

3b , (400b) 1 2 μ m (400a) 4 μ m

4a 4c

4a (100) (200) (300) (400)

, 1 (500a), 2 (500b) 3 (500c)
(500) .

, 2 (500b) 1 (500a) 가 , 3 (500c)

5 3a C-C'

5 , (600) (400b) (150)
(150) (200b)가

, (600) (200b) (200a) (150) (600) (400b)
, (600) (declination) (200b)가 (declination)가

가

가

가 가

(57)

1. 가

2.

1 , 가

3.

1 , 가

4.

1 ,

5.

1 , $4\mu\text{m}$

6.

1 , 1 $2\mu\text{m}$

7.

가

8.

7 , 1 , 2 , 3

9.

8 , 2 , 1 , 3 가

10.

9 , 2

11.

7 ,

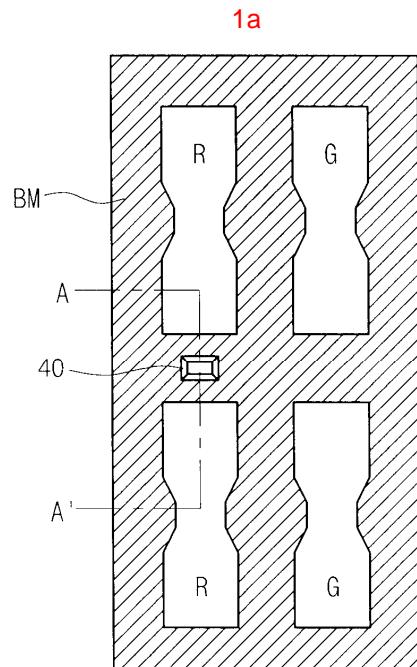
12.

7

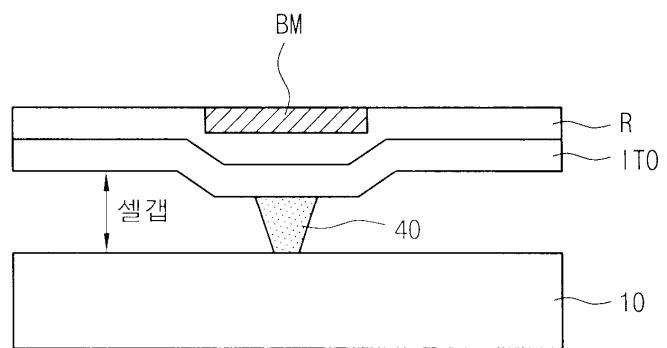
4 μ m

13.

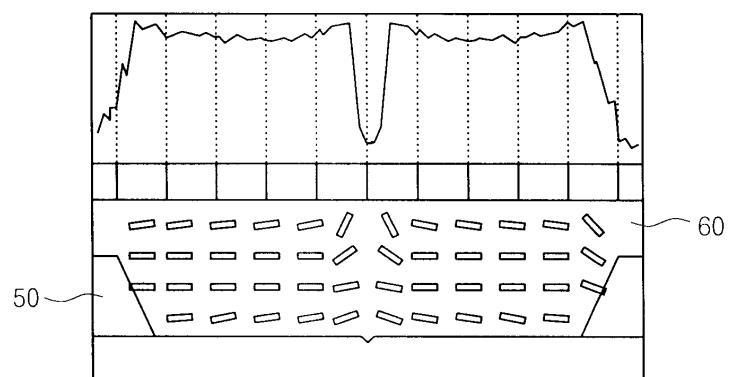
7

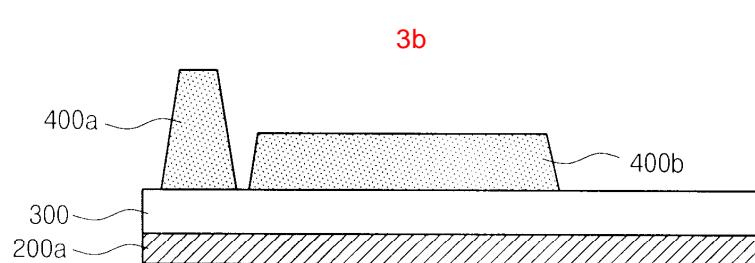
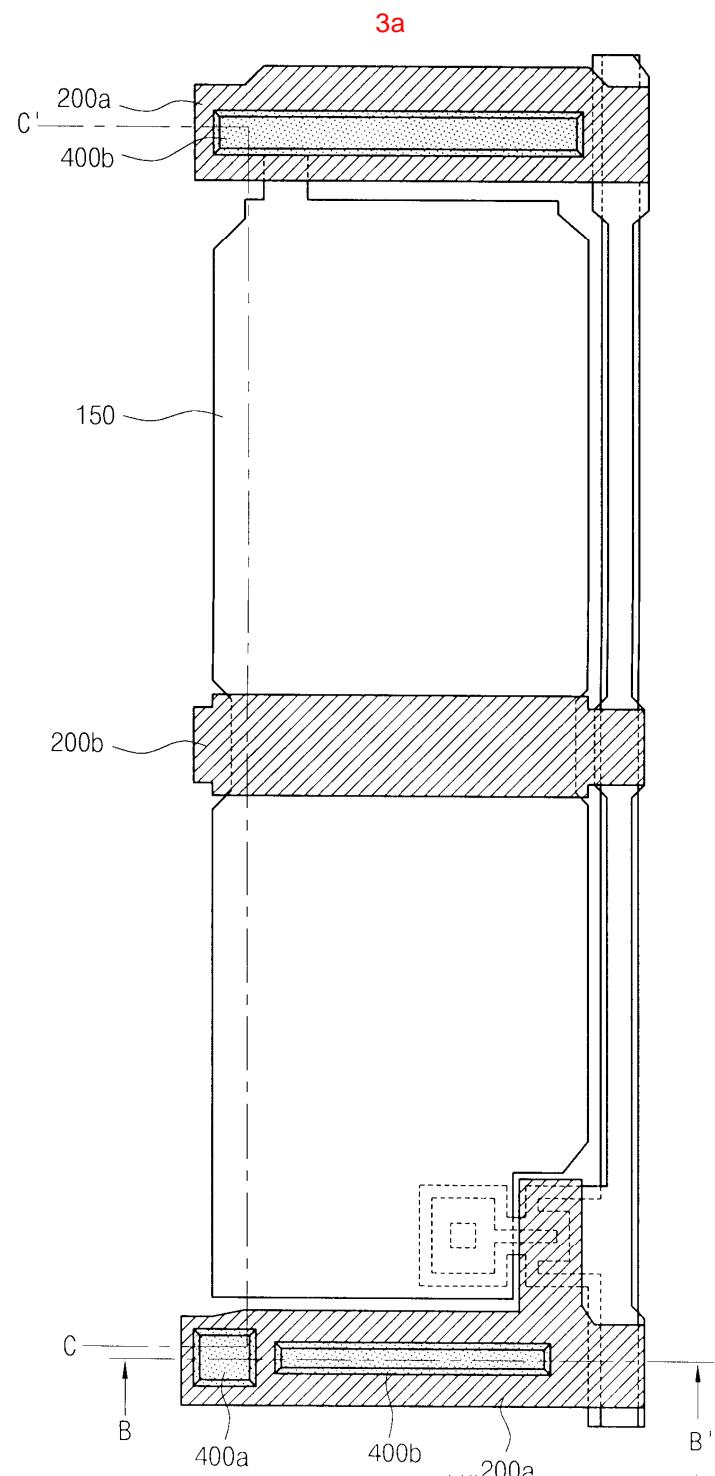
1 2 μ m

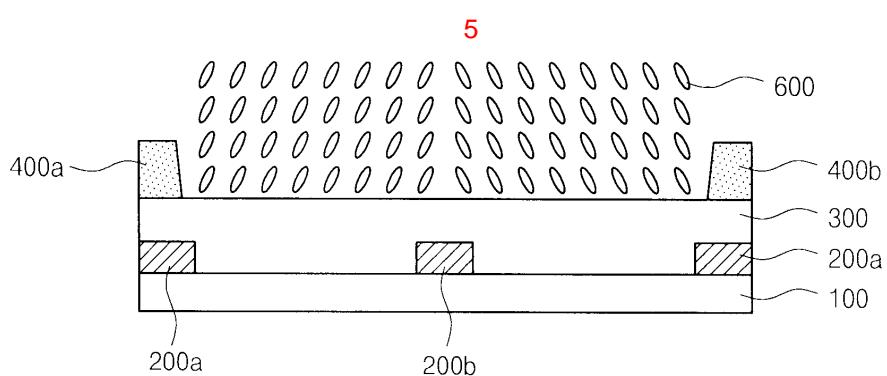
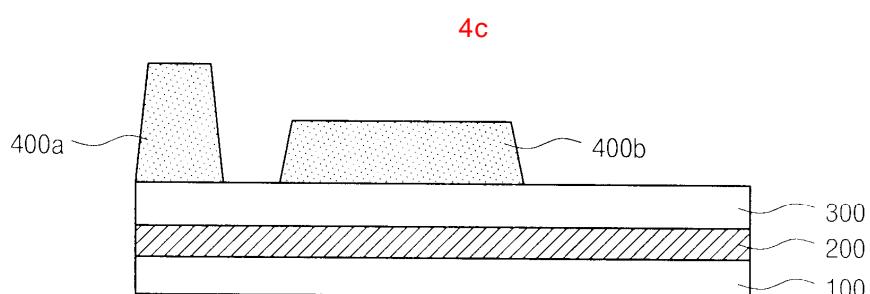
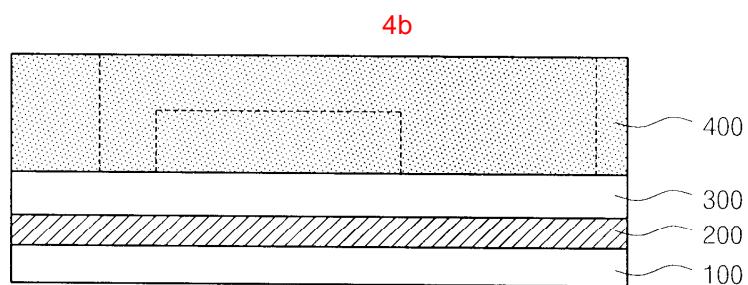
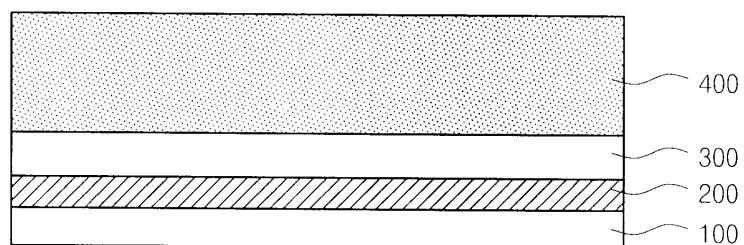
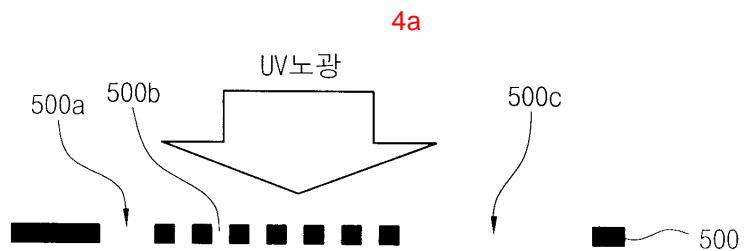
1b



2







专利名称(译)	液晶显示器及其制造方法		
公开(公告)号	KR1020040040159A	公开(公告)日	2004-05-12
申请号	KR1020020068506	申请日	2002-11-06
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IPC分类号	G02F1/1339		
CPC分类号	G02F1/13394 G02F1/136213 G02F1/136286 G02F2001/136222 G02F2001/13625 G02F2201/42 G03F7/20		
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摘要(译)

本发明涉及液晶显示器及其制造方法。并且它包括注入存储电极线之间的液晶层：柱状间隔物，设置在配备有滤色器的上板之间，下板：设置为与上板分离的栅极线和数据线：栅极线其中，互易垂直交叉布置在下板上的屏障上，形成在上板和下板的栅线上。

