



1

1a , 1b

2 .

3a , 3b 3c

4 - .

5 - , -

6 .

7a , 7b

8a , 8b

9a , 9b

10a , 10b

11 .

12 .

13a , 13

14a , 14b

15a , 15b

16a , 16b

17 .

18 .

19 .

20a , 20b

21a , 21b

22 .

&lt; &gt;

1

1a

1b

2

3, 3a, 3b, 3c, 3d, 3r0, 3g0, 3b0

4, 4a, 4b, 4c, 4r0, 4g0, 4b0

5

6

7

8

8r

8g

8b

9, 9a, 9b

10

11  
12  
13  
14  
15  
15a  
15b  
16b, 16g, 16r  
17b, 17g, 17r

2 .  
( , TN )  
가  
(In-P  
lane Switching , IPS ) 63-21907 (USP 4,345,249 ), WO91/10936  
6-160878  
IPS 20a  
(1a) (4) (3) . TFT (7)  
(6) (5) (4) - (on-off) (11)  
(3) (5) (12) TFT (7)  
(1a) (1b) (2) (7)가  
(4) 가 (4)  
(3) 가 (2)  
TN  
, IPS  
IPS 가  
( 가 )  
가  
IPS 가  
, IPS - ( , T-V ) TN  
가 가 19  
가 가  
가 가  
가 가  
가 2

[illegible]

(1) (6) (7)  
 (5) (1a) a-Si (5) (6)  
 (3) (4) ITO  
 (1) (8r), (8g), (8b) (10)가  
 (1a) (1b) (2) (9a) (9b) (2)  
 가 (1a) (1b) (9a) (9b)가  
 (1a) (1b) (1a) (1b) (2)  
 (7) (6) (7)가 (4)  
 (3) (4) 가 (2)  
 가  
 1 (3a) (4a) (A) (3b)  
 (3c) (4b) (3a) (4a) (w<sub>1</sub>) (3b) (3c) (4b) (w<sub>2</sub>) (l<sub>2</sub>)  
 (3a) (4a) (1<sub>1</sub>) (3b) (3c) (4b) (l<sub>2</sub>)

[ 1 ]

	( $\mu\text{m}$ )	( $\mu\text{m}$ )	d( $\mu\text{m}$ )
A	10	6	4
B	4	4	

A B 가 (3b) (4b)  
 A B 가 d 가  
 (1a) (1b) 가 B  
 (2) B  
 B  
 A B (A B)  
 2 550nm n · d( n , d )  
 B A 480nm B  
 가  
 가 A B  
 B A B가 A B가 A B (A B)  
 7 $\mu\text{m}$ , 5 $\mu\text{m}$ ) 가

, A B  
 < 2> 가 .  
 (1b) (1a) 3a (1a) 1 (14) . , (14)  
 3) (14) (5) (3) (4) (3) 가 (14)  
 (4) 가 가 3b 3c (3) (4)  
 (2) 가 (14) (4)  
 , A( (3a) (4a)) B( (3b) (4b))  
 가 A B  
 (2) ,  
 < 3>  
 1  
 1 , 2 (3) (4)  
 C, D E , T-V .

[ 2]

	( $\mu\text{m}$ )	( $\mu\text{m}$ )
C	4	8
D	6	6
E	8	4

4

5

H

가

6

가

T-V

F

T-V

G가

가

T-V

(3)

(4)

2

(2)

(15a)

(15)가

7a

7b

(15)

(15a)

(15b)

(15)

(15a)

(15)

(1a)

(15)

b)

(4)

(3)

< 4 >

8a

8b



[ 3 ]

	( $\mu\text{m}$ )	( $\mu\text{m}$ )	( )	( $\mu\text{m}$ )
F	10	6	2,000	4
G	6	10	8,000	

가  
가  
가  
(  
가  
가 2,000 F  
가 G  
가 8,000  
F G  
가 5,000  
가 F G  
< 10>  
14a 14b  
(3) (4)  
(4) (2) (3) (1) 15°  
20° (B) A  
A, B, C D  
(  
4 ( 7  
가 ( 1- 2)  
(flicker) 가  
± 10°  
(2) 가 (3))  
(5) 가 가  
< 11>  
RGB  
15a 15b  
가  
RGB 0nm 19 R 700nm, G가 55  
2, B가 440nm  
10 $\mu\text{m}$  4 $\mu\text{m}$  4 $\mu\text{m}$  6 $\mu\text{m}$  A 550nm  
B 480nm  
8g A (8b)  
B (8r)  
700nm  
가  
< 12>  
, RGB ( ) RGB  
16a 16b  
(R) 가 ( )  
가 가 (3)



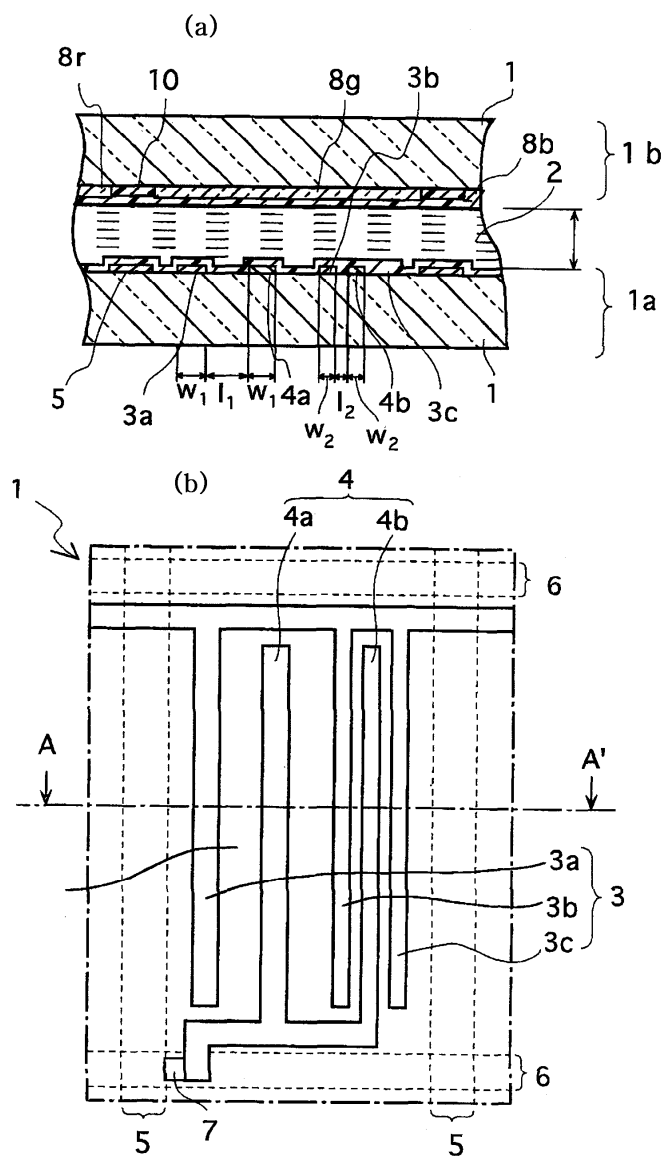
r0) (4r0) ( r) (3g0) (4g0) ( g) (3b0) (4b0)  
 ( b)  
 < 13>  
 RGB RGB  
 17  
 (4g0) (16g) (4b0) (16b) (17b) (4r0)  
 (16r) (17r) (17g) (4b0), (4g0) (4r0) (17b)  
 ), (17g) (17r)  
 11 가 , , , ,  
 (d) (13) (17b), (17g) (17r) (13) (17b), (17g) (1a) (17r) (1a)  
 (13) (d)  
 가 18 (10) (1b)

(57)

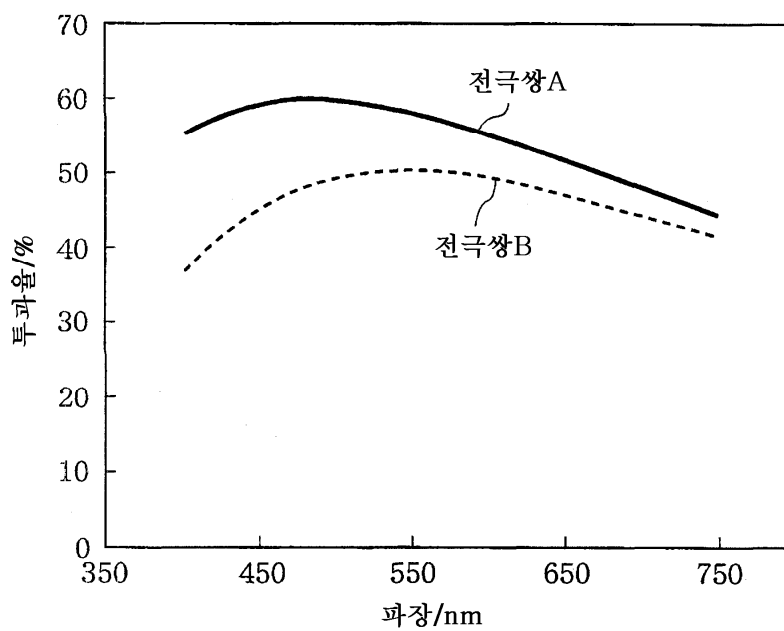
- 1.
- 2.
- 3.
- 4.

- 9
- 10.
- 9
- 11.
- 9
- 1
- 2
- 10
- 12.
- 13.
- 14.
- 15.
- 16.
- 17.
- 18.
- 19.
- 20.
- 가,
- 21.
- 22.
- 23.
- 24.
- 25.

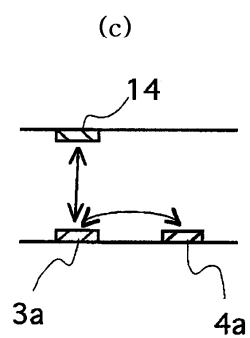
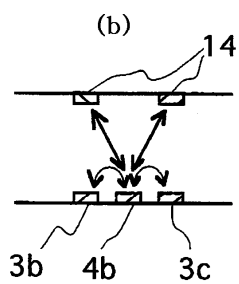
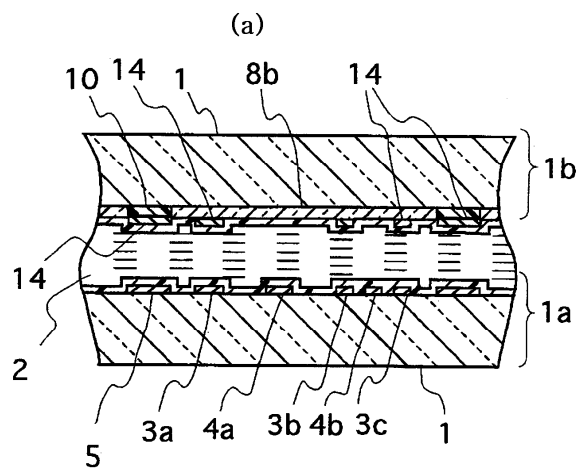
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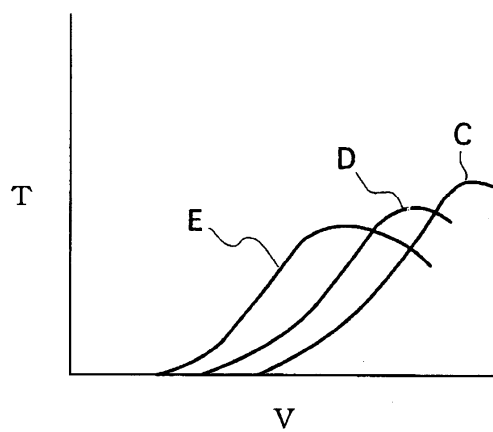
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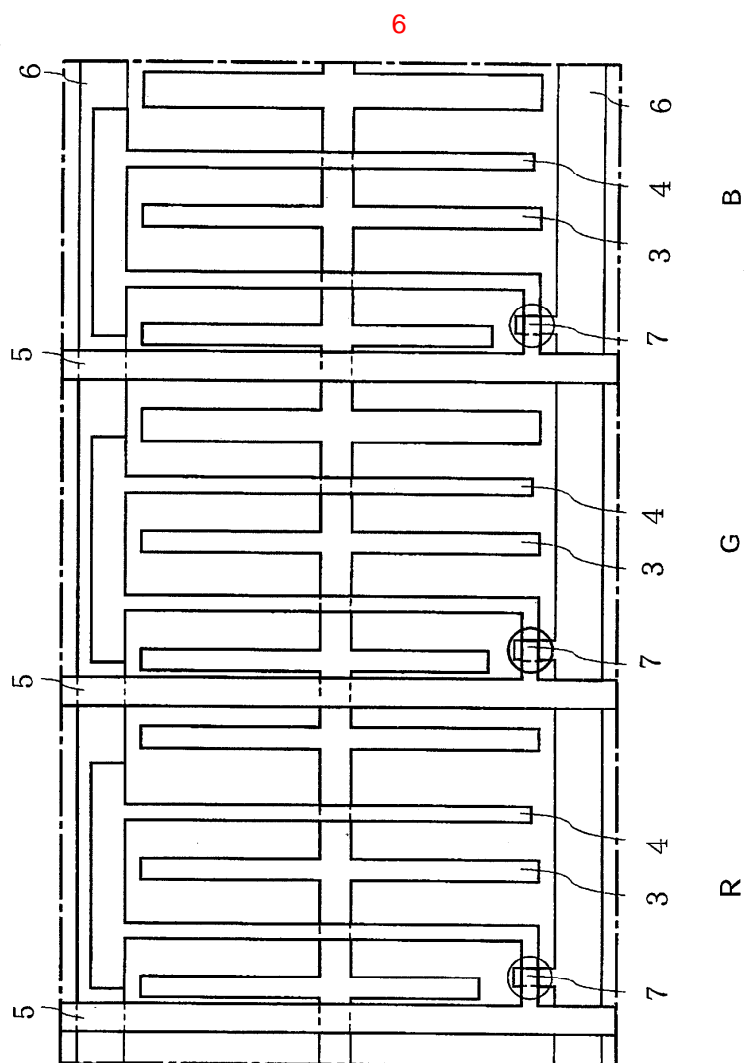
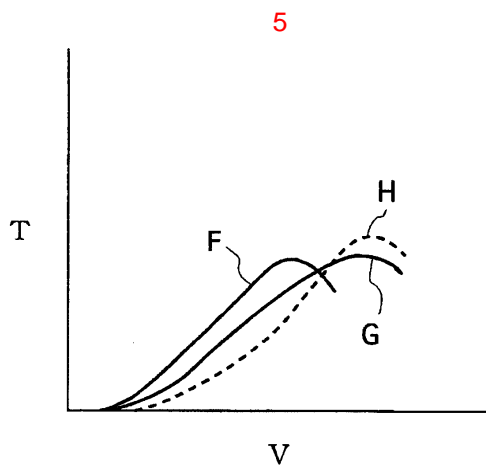


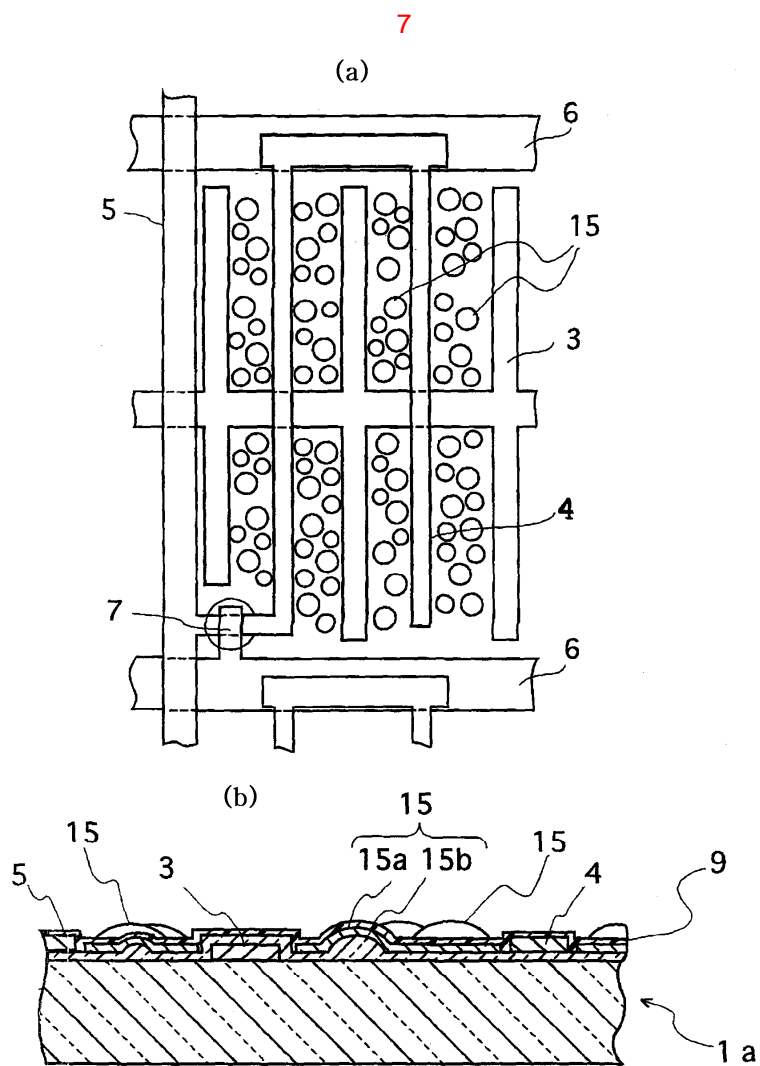
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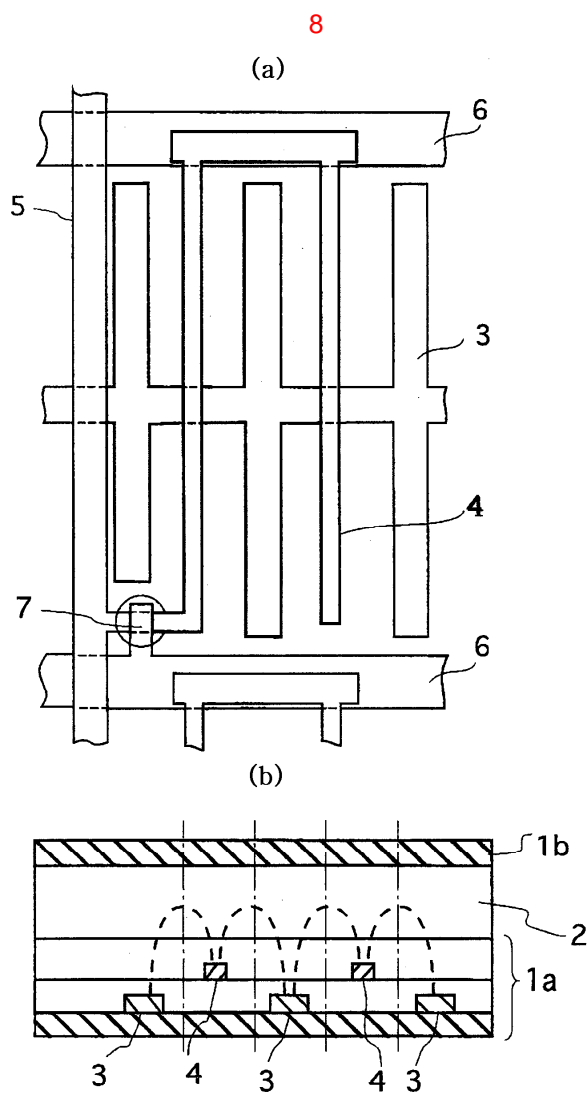


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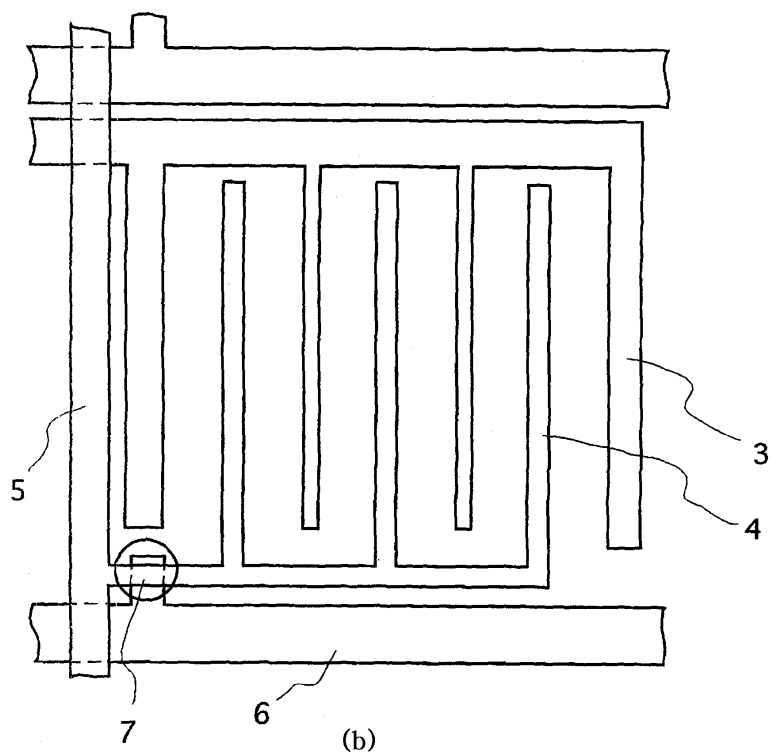




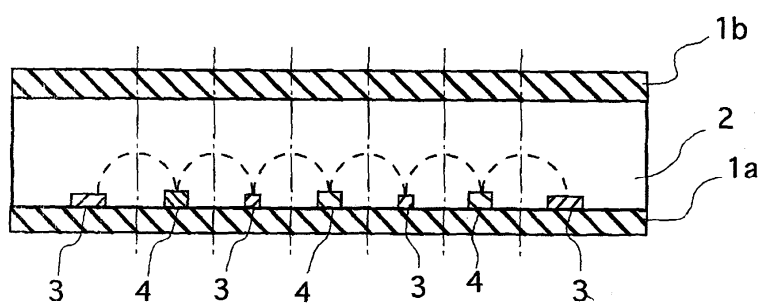


9

(a)



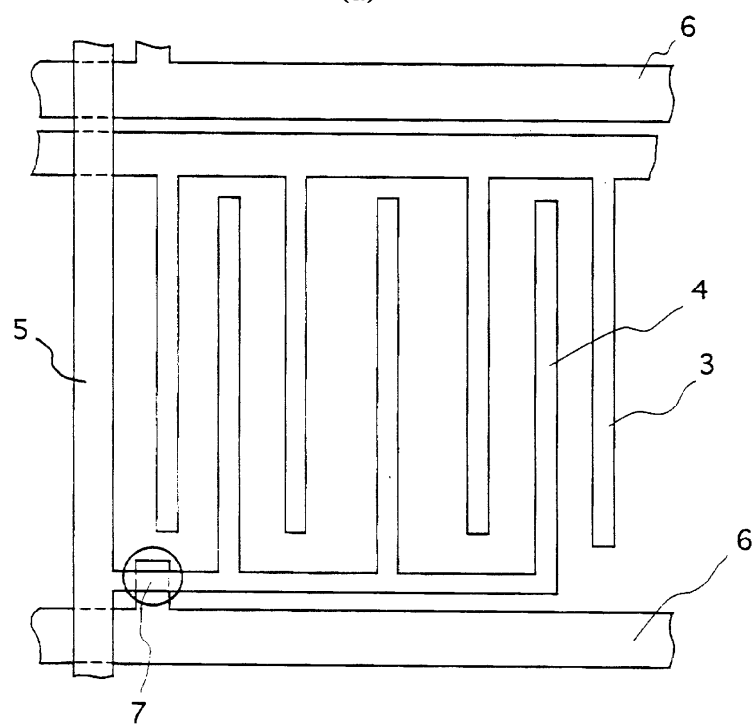
(b)



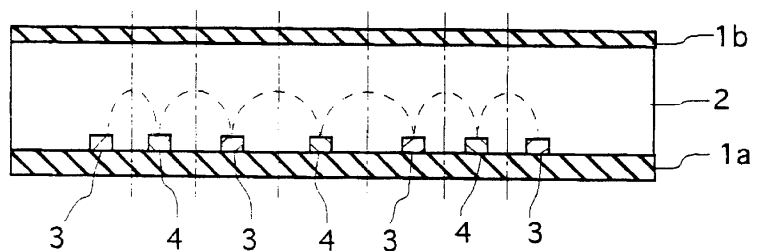


10

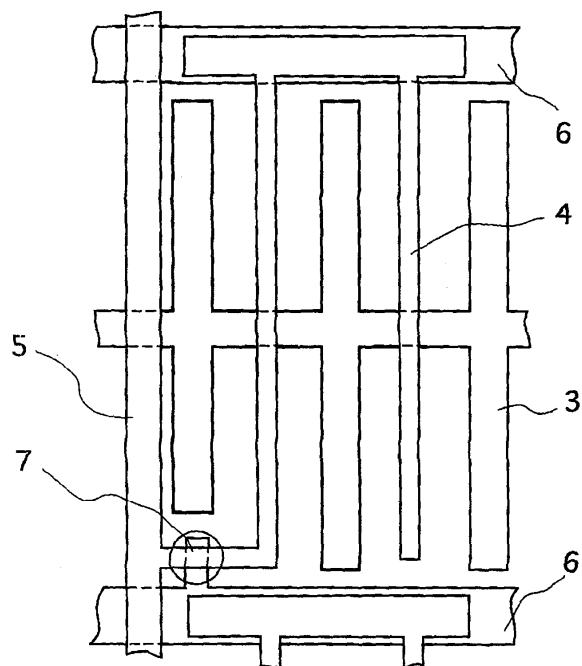
(a)



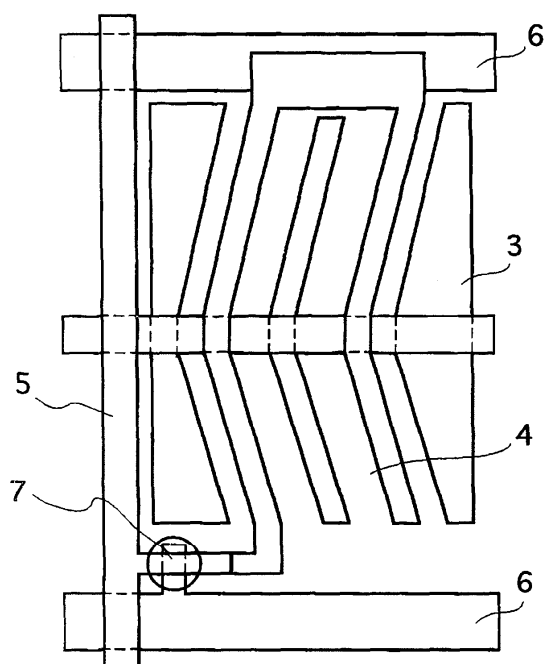
(b)



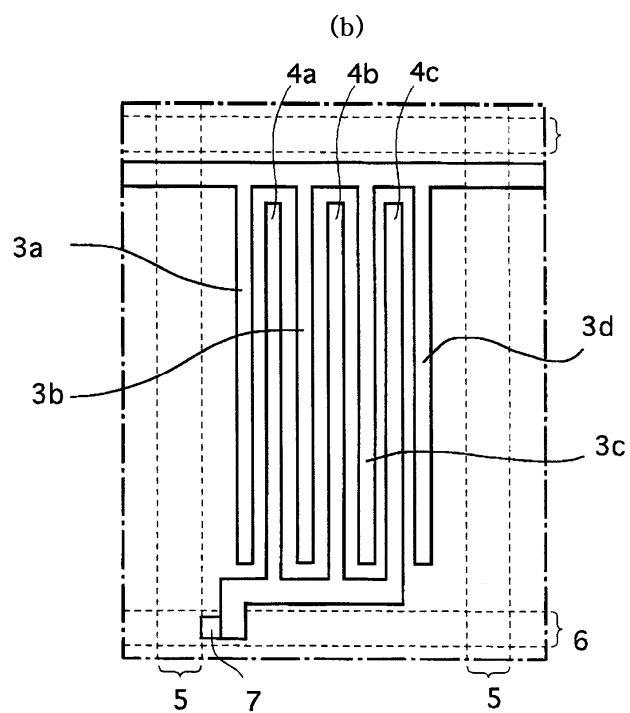
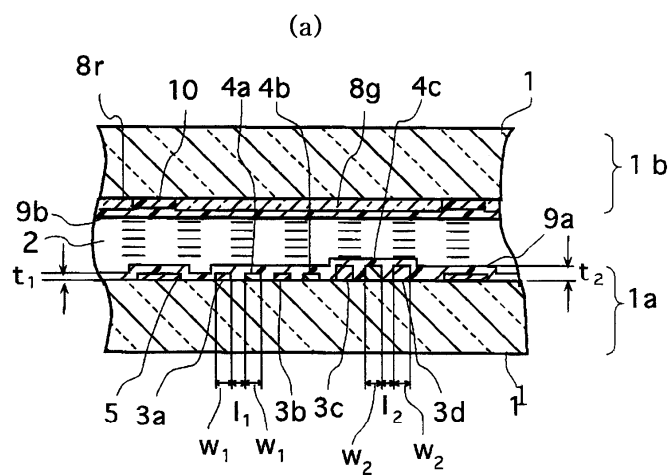
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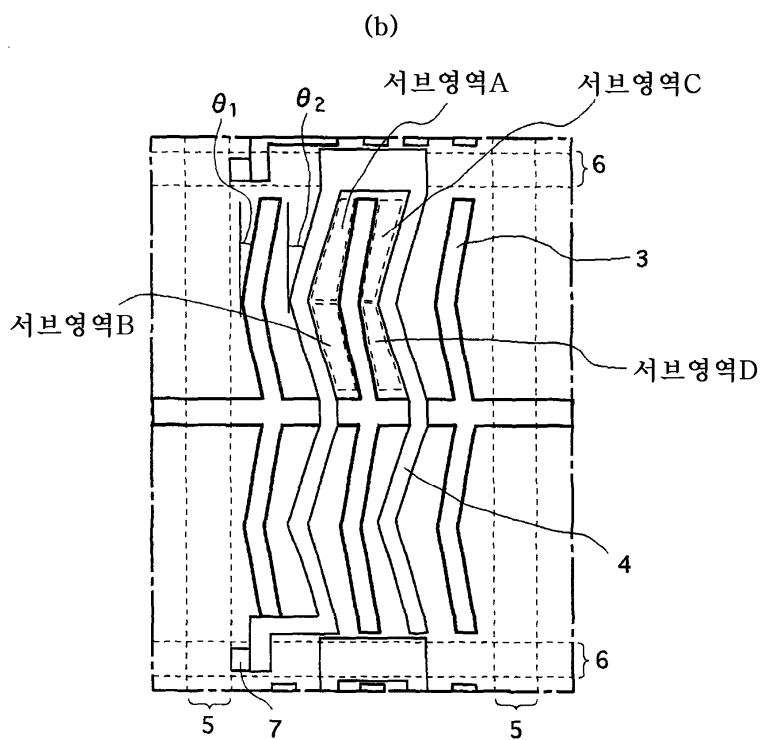
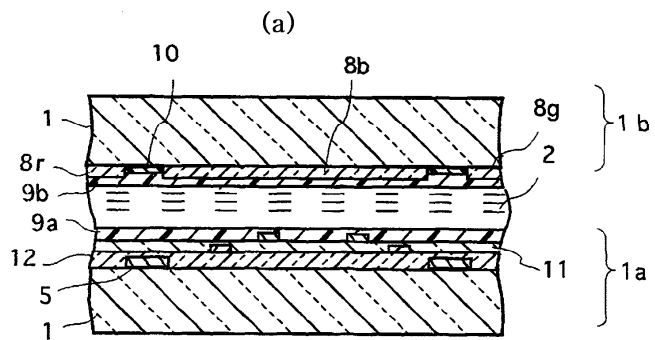
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13

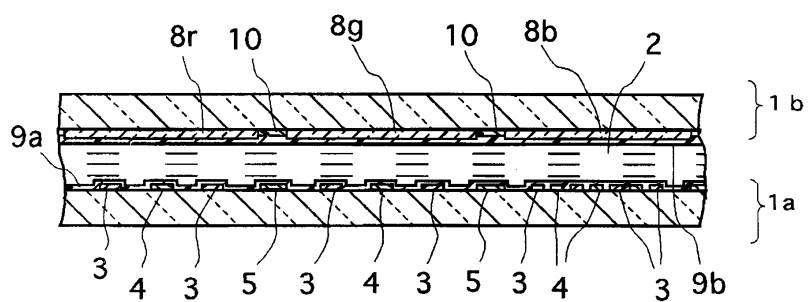


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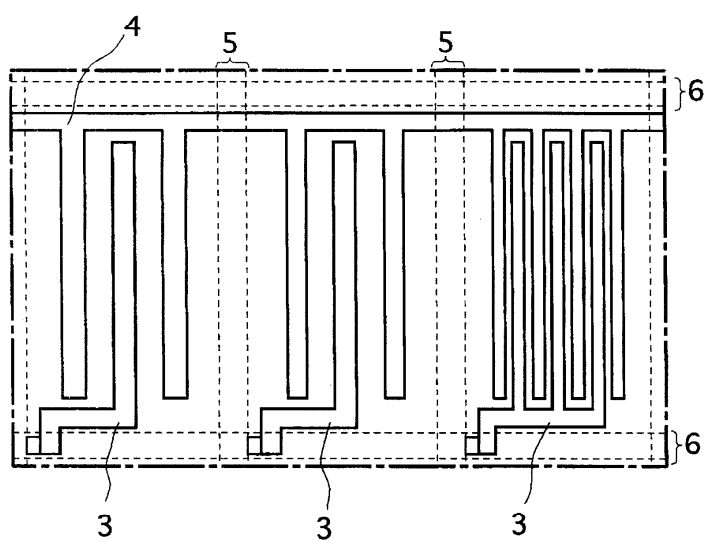


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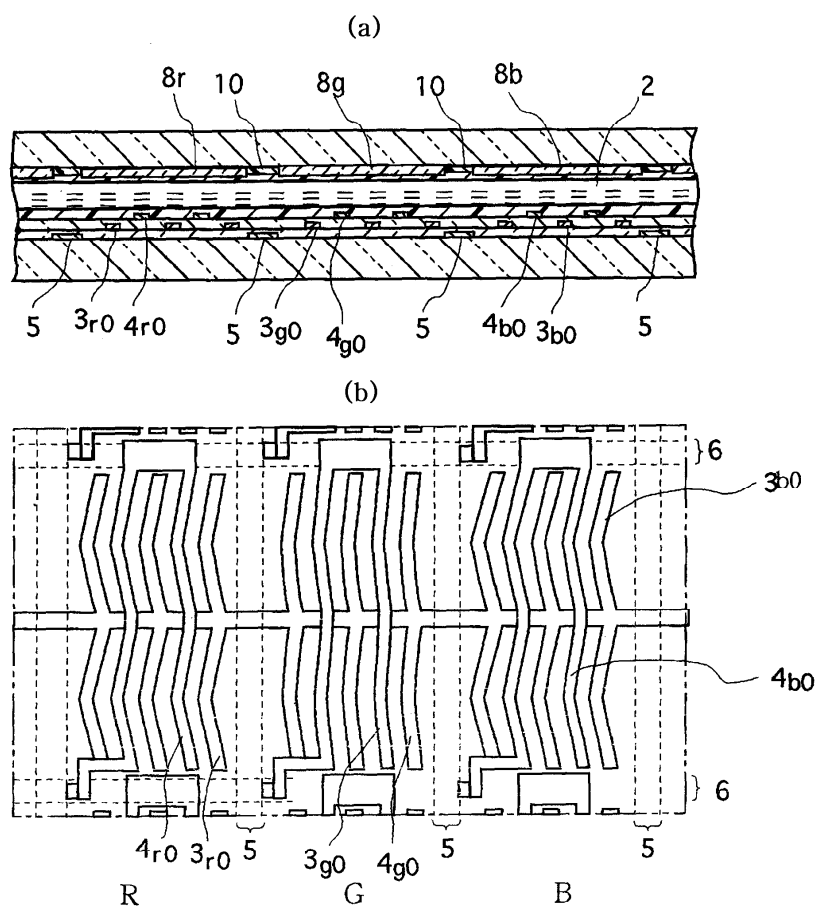
(a)

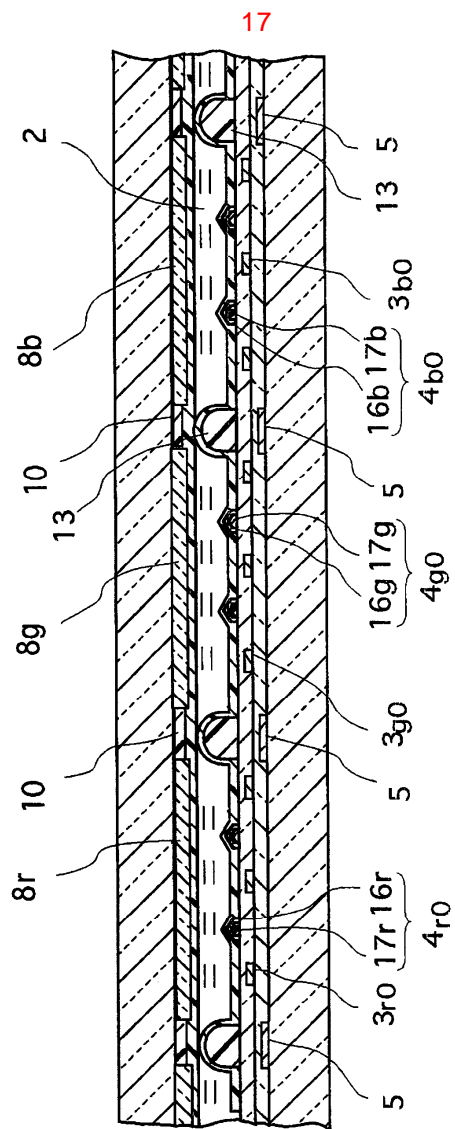


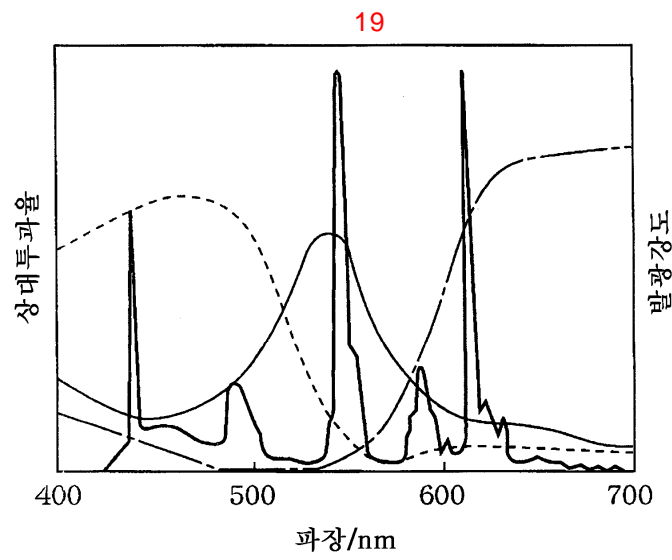
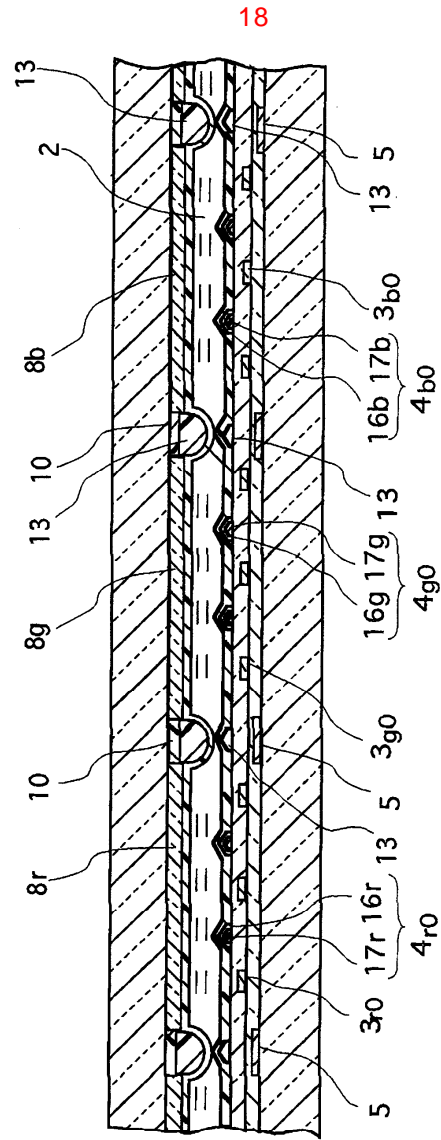
(b)



16

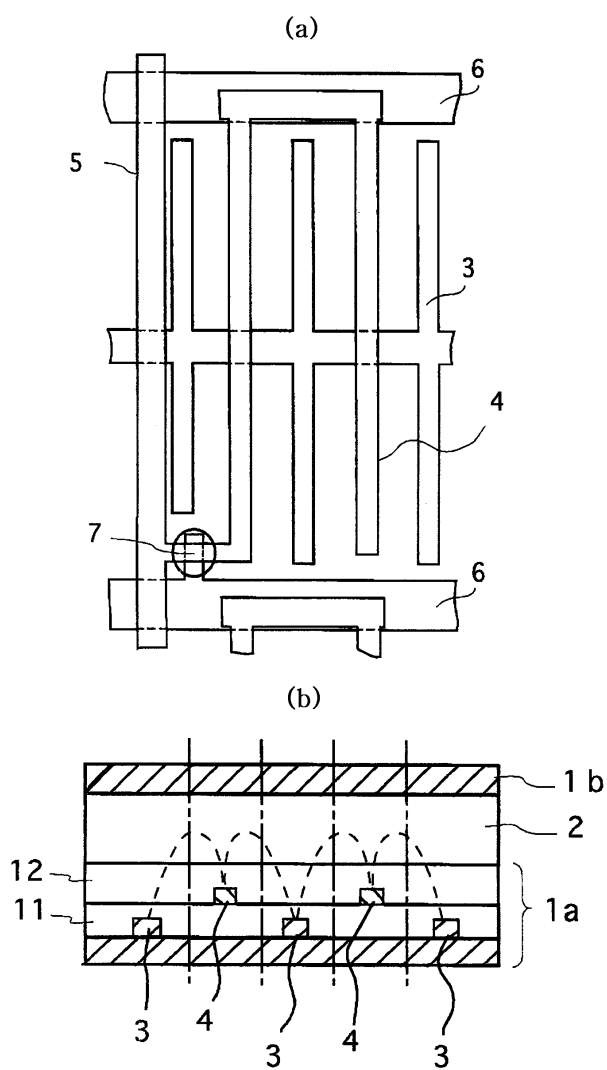




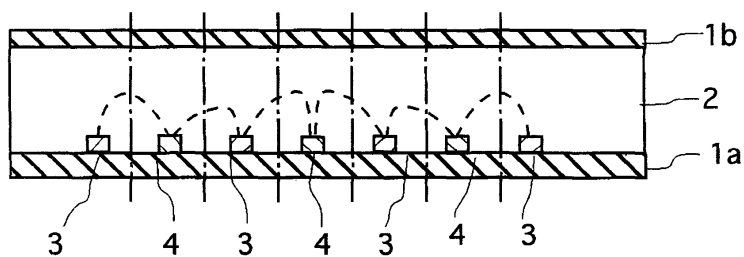
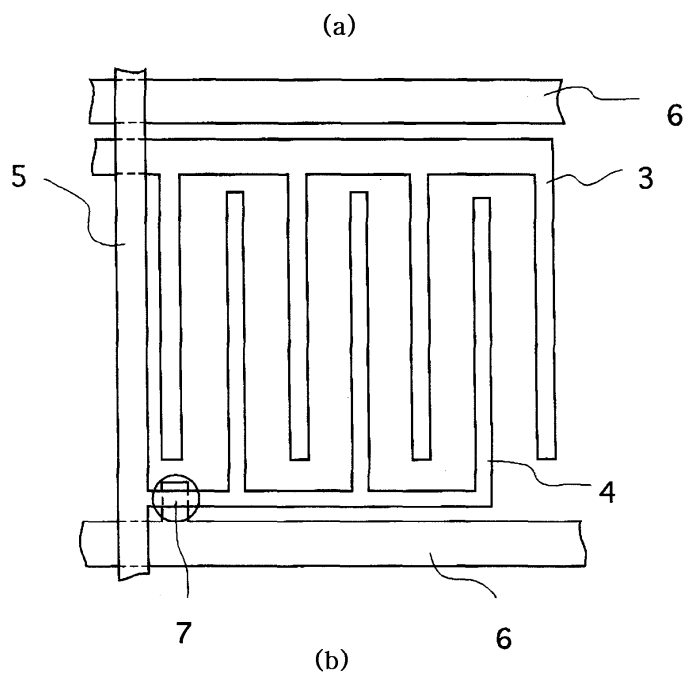




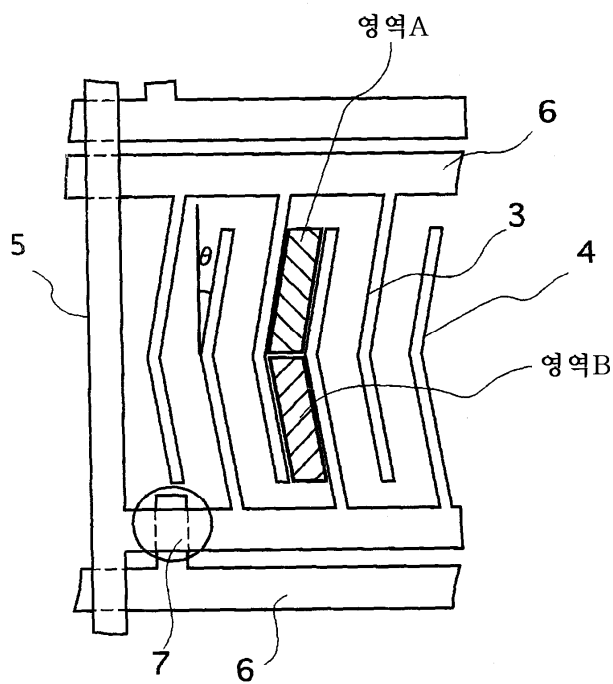
20



21



22



专利名称(译)	液晶显示器		
公开(公告)号	<a href="#">KR100433596B1</a>	公开(公告)日	2004-05-31
申请号	KR1020000061200	申请日	2000-10-18
申请(专利权)人(译)	松下电器产业株式会社		
当前申请(专利权)人(译)	松下电器产业株式会社		
[标]发明人	YAMAKITA HIROYUKI 야마키타히로유키 KUMAGAWA KATSUHIKO 쿠마가와카쓰히코 SHIOTA AKINORI 시오타아키노리 SATO ICHIRO 사토이치로 TAKIMOTO AKIO 타키토아키오		
发明人	야마키타히로유키 쿠마가와카쓰히코 시오타아키노리 사토이치로 타키토아키오		
IPC分类号	G02F1/1343 G02F1/136		
CPC分类号	G02F1/134363		
优先权	2000084345 2000-03-24 JP 1999299658 1999-10-21 JP 1999299659 1999-10-21 JP 1999299660 1999-10-21 JP		
其他公开文献	KR1020010040114A		
外部链接	<a href="#">Espacenet</a>		

## 摘要(译)

一种液晶显示单元，包括多个像素，每个像素包括多个公共电极，多个像素电极和半导体开关元件，多个扫描信号线，多个视频信号线，用于向像素电极输出信号。该显示器还包括阵列基板，该阵列基板具有布置在其表面上的像素，扫描信号线和视频信号线，与阵列基板相对设置的对向基板，以及夹在阵列基板和对向基板之间的液晶层其中每个像素包括多个电极对，每个电极对包括一个公共电极和相邻的一个像素电极，并且至少一个电极对与其它电极对的厚度不同电极或其像素电极的厚度。

