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(12)

(KR)  
(A)

(51) 。 Int. Cl.<sup>7</sup>  
G09G 3/36  
G09G 3/20

(11)  
(43)

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2005 01 24

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(22) 2004 07 09

(30) JP-P-2003-00195992 2003 07 11 (JP)

(71) 4 1-8 ,

(72) 61 - - 218

61 -106

가

61 - - 412

2-2-16

(74)

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(54)

CS1 CSn (6)  
NMOS (8) , CS1 CSn 1 VcsH  
PMOS (9) , CS1 CSn 2 VcsL(<VcsH)  
ND (10) , CS1 CSn (4) A  
( 1 ) , CS1 CSn  
, 가 ,

1

, PMOS , ,



가 . , ( 2001-255851 ).

, 가 , 가 가 . , 가 2 , 1 , 2 , 1 , 1 2 , 가 . , 1 2 , 가 1 가 ( 1 )

1 1 1 2 S1 Sn G1 Gn , TFT(1)(Thin Film Transistor) , TFT(1) C1 C2 , (2) , (2) (3) (5) , ( 2 ) CS1 CSn , CS1 CSn (6) (7) 가 . ( 5) (7) CS1 CSn , 1 CS1 CSn (6)가 .

2  
 (6) , CS1 CSn 1 VcsH NMOS  
 (8) , CS1 CSn 2 VcsL(<VcsH) PMOS  
 (9) , (8, 9) / (4) AND (10)

AND (10) , CS1 CSn s2  
 s1 , (8, 9) /

3 2 VcsH, VcsL, C2 (6) 3 (5) 1  
 2 CS1 CSn

, 3 (6)가 2 가 , CS1 CSn 가

3 A , 3 , 2

(0V) OS (9)가 , 2 ( A-B ), s1 , PM  
 (6) AND (10) VcsH가

1 CS1 CSn VcsH , 2 VcsL CS1 CSn (2)  
 , C2 ( 가 (3) (2) ) 가  
 , 가

, B가 , 2 (6) s1 , NMOS  
 (8) PMOS (9) s2 / 가 , AND (10) 가

, VcsL CS1 CSn , 1 VcsH 2

가 ( 1 ) , CS1 CSn

, CS1 CSn (3) 가 가 가

( 2 )

2 , 가

4 2 , 4 , 1

4 (5) , 1 가 가 (11) , (20)  
 (12) , (12) 3 가 (12) (5)  
 , 가 (5) (12) 3 가 (12) (5)

(12) 3, 2, 4

(11) TFT(1), TFT(1) C2 C1 TFT(1)

1) CS1 C1 TFT(1) (5) (20) COG(Chip On Glass) 5 (5) (20)

(4) (13) (5) TFT(1)가 가 (4)

(5) TFT가

(12) (5) (1)

2) TFT(1)가 C2 ( ) (13) (4) (14) (13) (13)

CC(Capacitively Coupled Driving) . CC TFT(1) CS1 CS1 CS1 . CC (15)가

6 4 t<sub>1</sub> t<sub>1</sub> (5) (12)

(4) (5) (4) 가 (4) (13) C2 TFT(1)가 가 0V

t<sub>2</sub> 가 (4) CS1 (13) C2 C1 가

t<sub>3</sub> (4) (13) t<sub>4</sub> 가

2 가 C2 가 C1 0V TFT(1)

( 3 )

3 (20)

7 3 , , 7 , 1

7 , FPC(Flexible Print Circuit) (20) (7) (20) (7)  
 C1, (4) (5)가 , (20) , TFT(1), C2, (7)  
 (4) (21)가 (21) , (5) (20) IC (20)  
 (4) , (7) FDON FDON ,  
 가

8 , (4) (13) (20) (7)  
 , NAND (22) , NAND (22) (縱續) 2 (23, 24)  
 , NAND (22) , FDON FDON ,  
 , FDON , NAND (22) TFT(1)가 ,  
 FDON , (4) NAND (22) FDON  
 , (11) TFT(1)가 ,  
 (7) , FDON ,  
 TFT(1)가

(21) , PMOS PMOS  
 ( , ) FDON 가 , PMOS PMOS  
 FDON , (21) PMOS 가 , 9 ,  
 (11) PMOS (26) 가 ,  
 (25) PMOS 가 ,

10 , A A , (5) (4)  
 , C (4) , FDON , FDON B ,  
 , 가 , 0V , , 가 , C2 , TFT(1)  
 A C , 1 , , C , FDON  
 , (4) , (5) , 11 (27) ,  
 (4) (5) , (4) , D

11 FDON , (5) (4) , FDON  
 TFT(1)가 , (4) (13) PMOS 가 ,  
 0V , 가 , (21) C2 가 ,  
 , E , (4) (5) , ,  
 E , 가 , 가 0V , ,

, 2 , (20) , FDON ,

.  
, , 가 , , .

, , 가 .

(57)

1.

1 2 , ,

2

1

1

2.

1

1

3.

1

1

가 가

1

4.

3



8 11.

8 12.

13. 1 2

13 14.

13 15.

CC

가 , 가 1 , 가 1

가 , 1

가 1 2 , 가 1 2 ,

13 16. ,

, 가 1 ,

16 17. ,

, ,

13 18. ,

가 1 ,

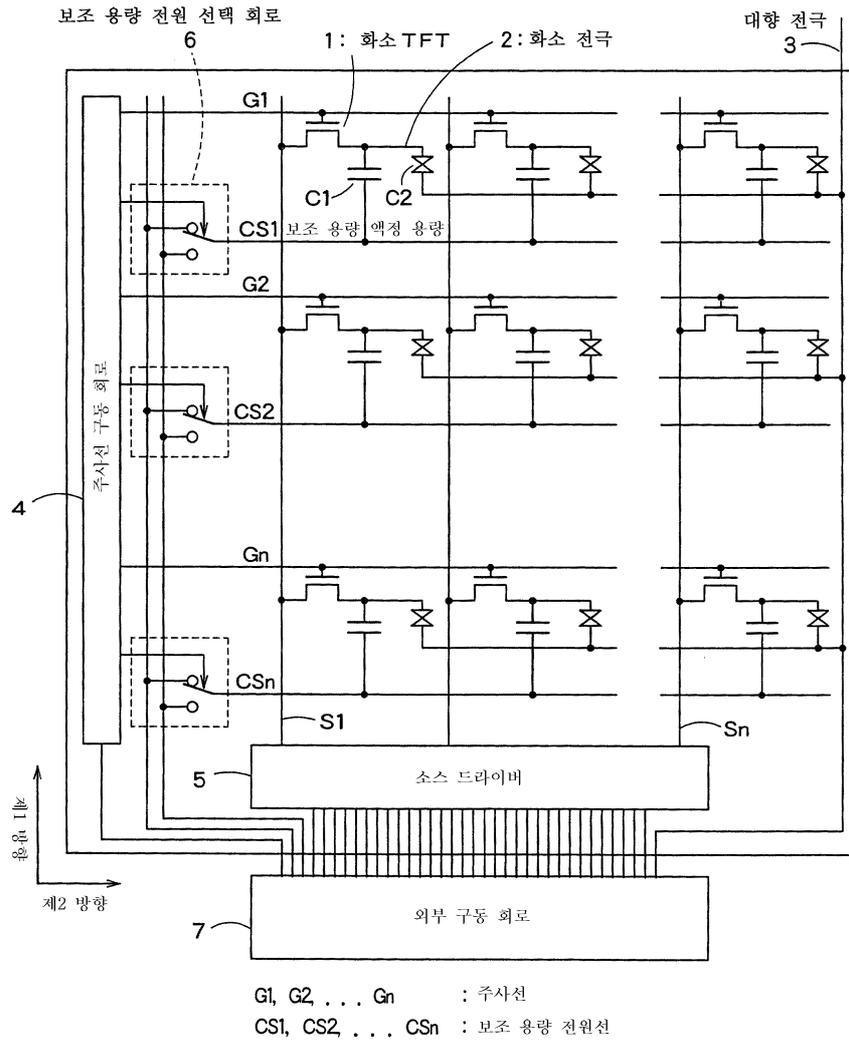
18 19. ,

, 가 ,

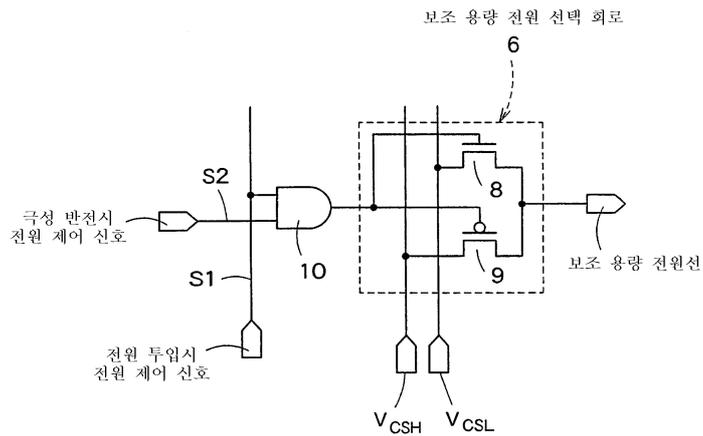
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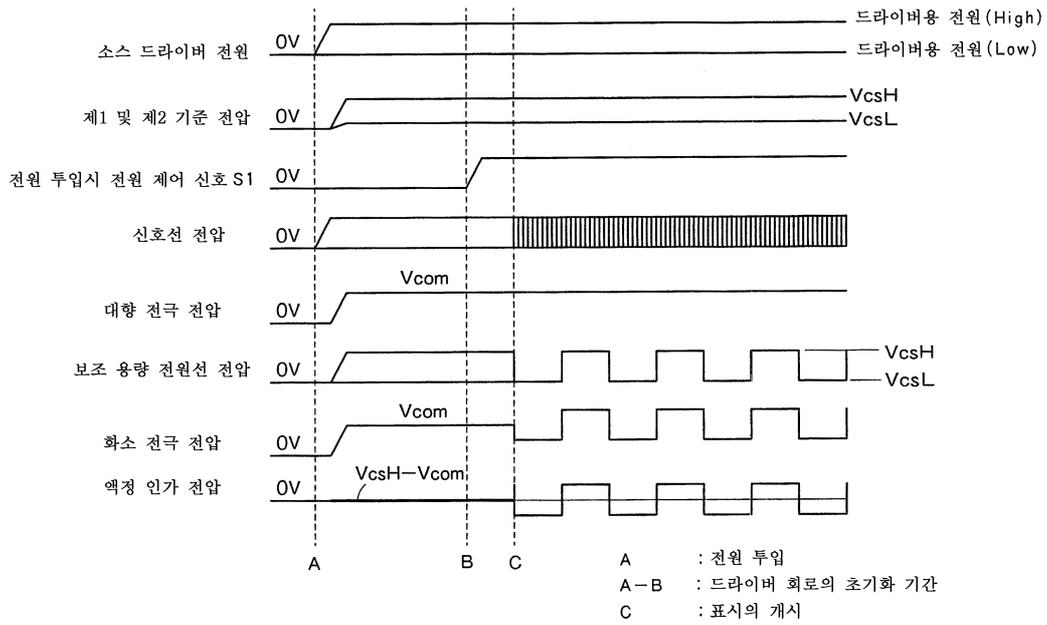
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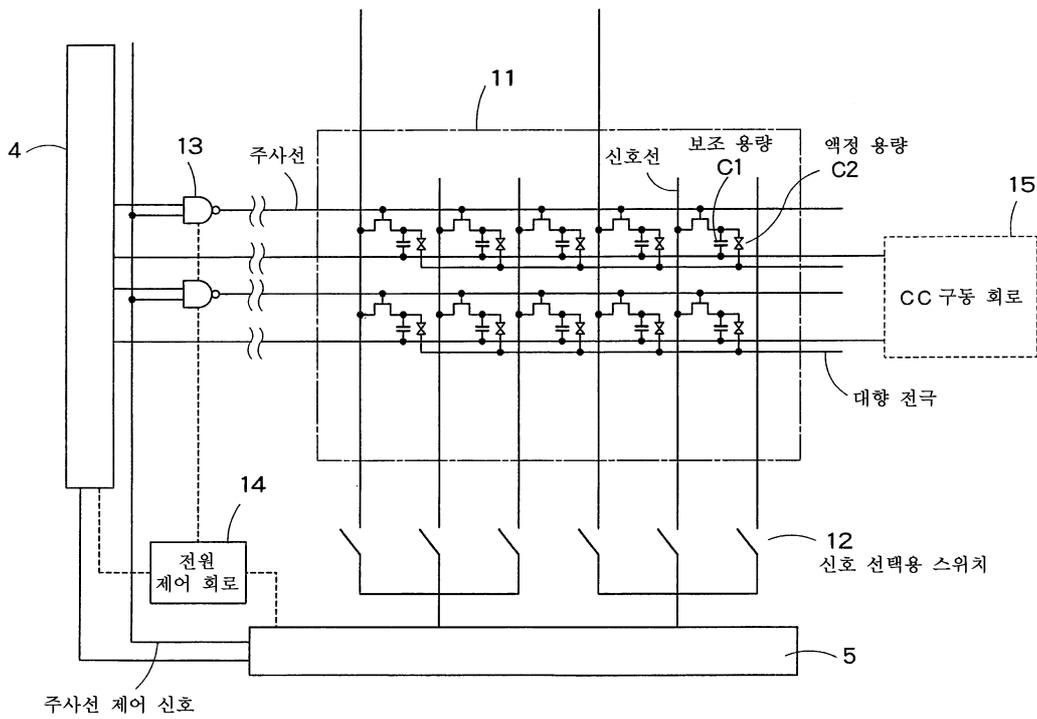
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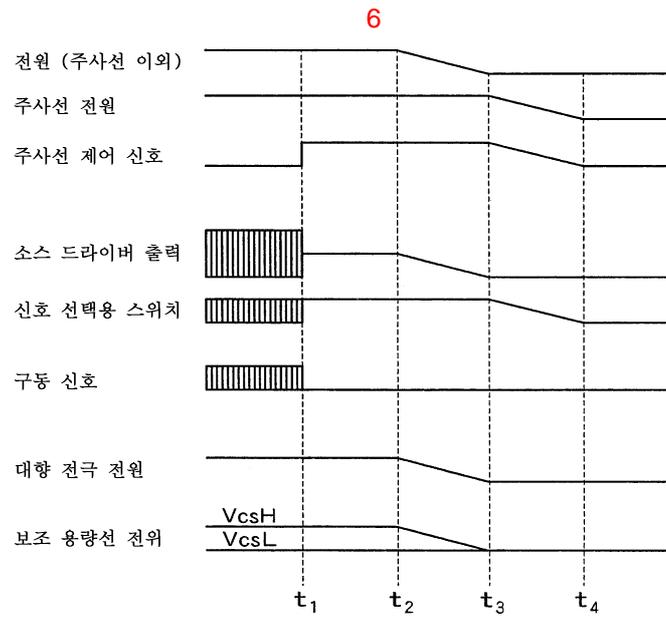
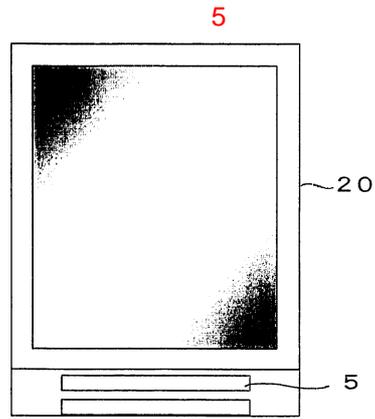


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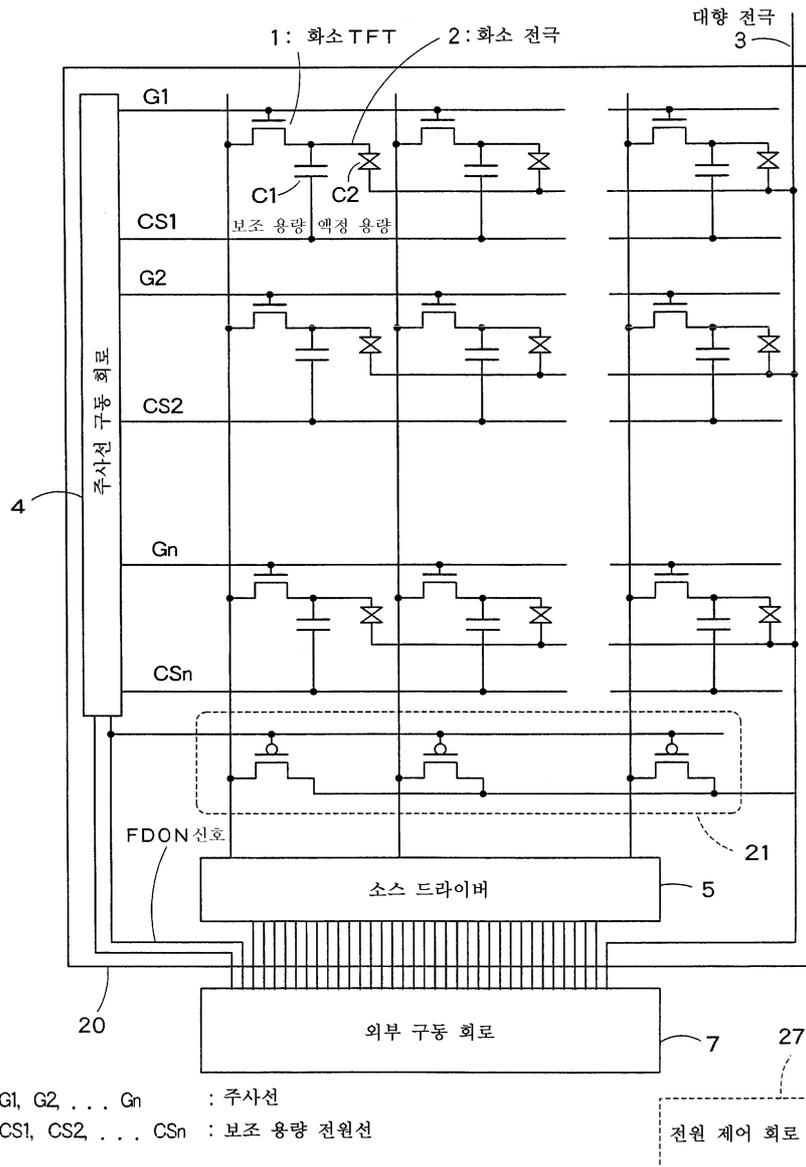


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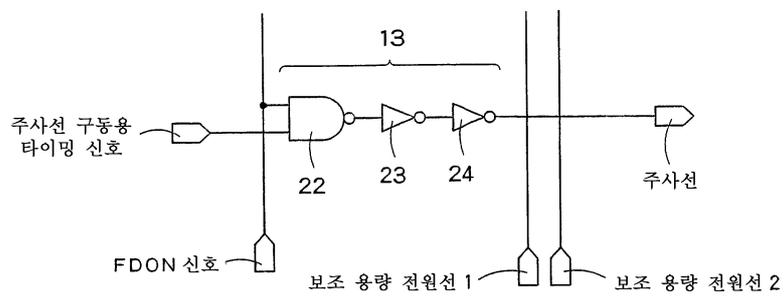




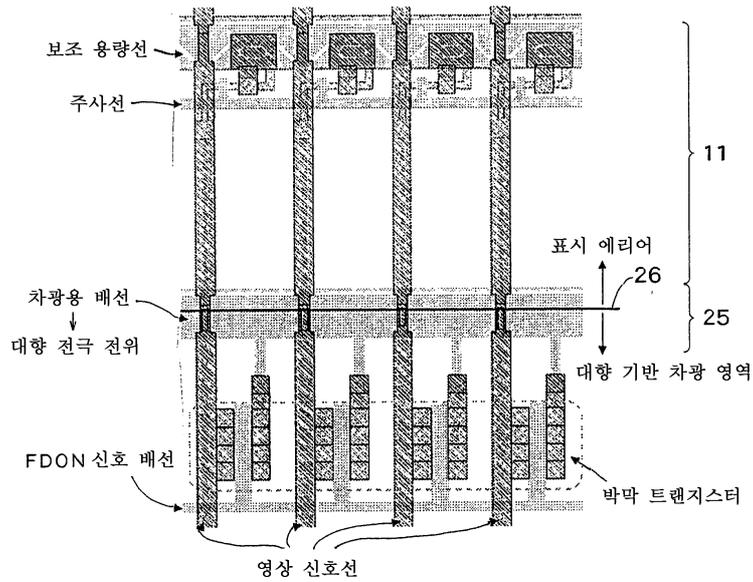
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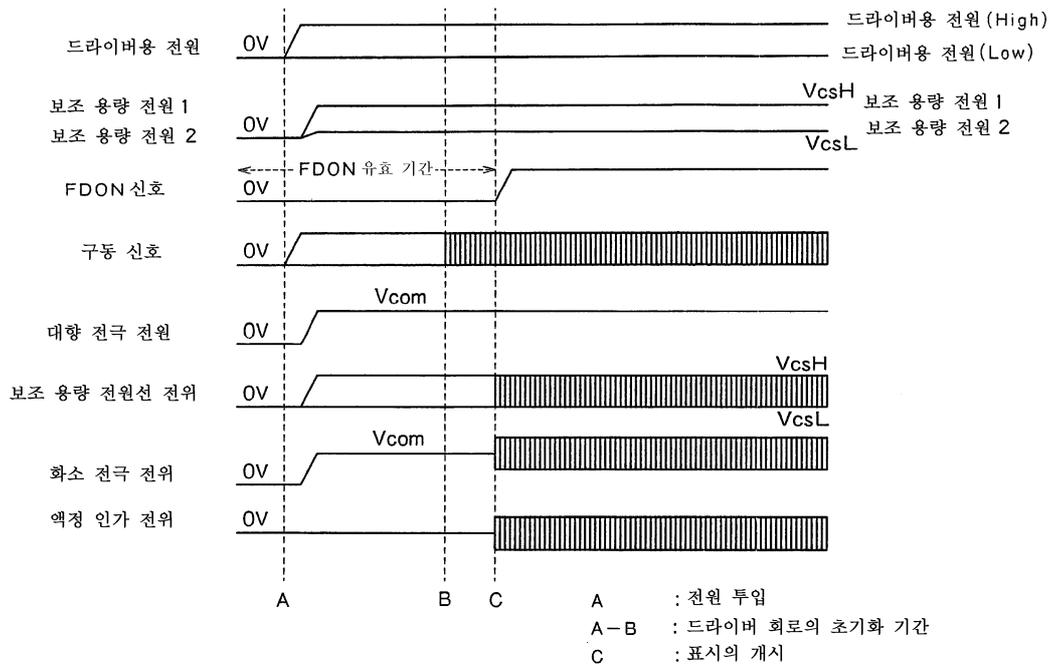
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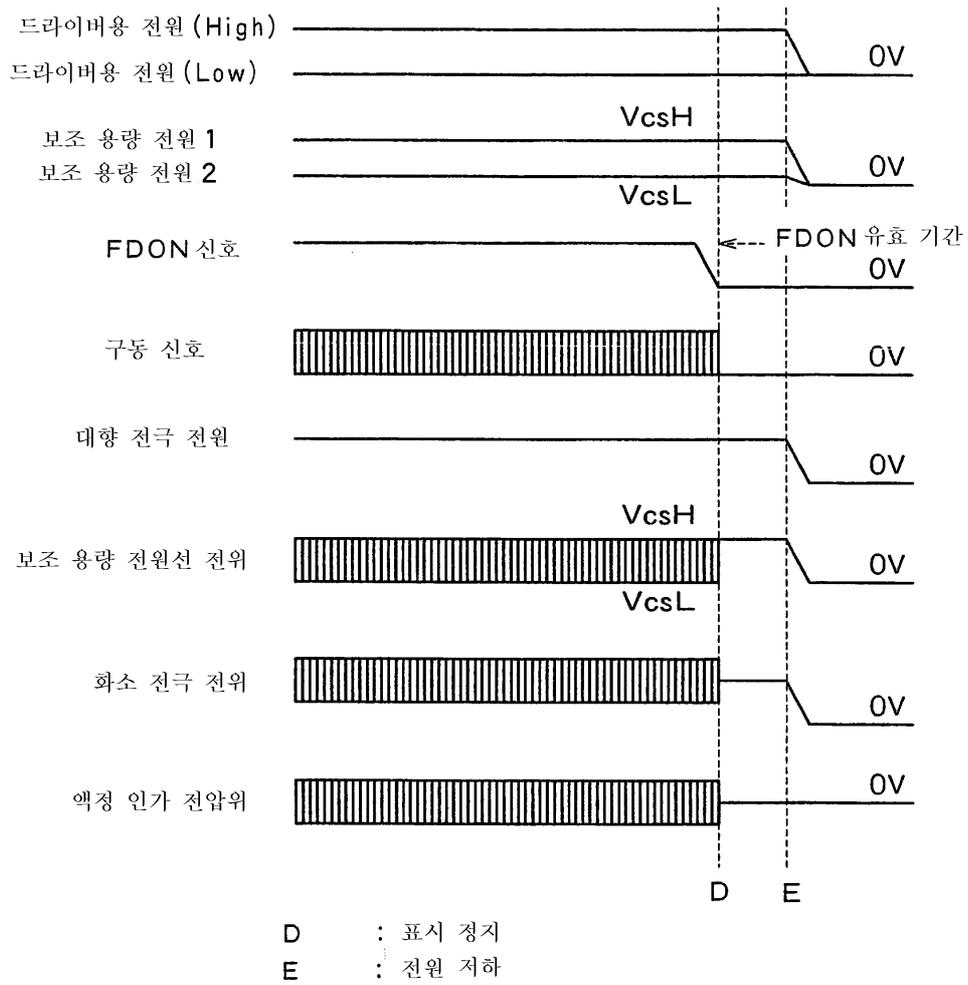
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专利名称(译)	液晶显示器		
公开(公告)号	<a href="#">KR1020050009154A</a>	公开(公告)日	2005-01-24
申请号	KR1020040053318	申请日	2004-07-09
申请(专利权)人(译)	可否让我这个小粉丝展示中心		
当前申请(专利权)人(译)	可否让我这个小粉丝展示中心		
[标]发明人	TSUNASHIMA TAKANORI 썩나시마다까노리 KIMURA HIROYUKI 기무라히로유키 KARUBE MASAO 가루베마사오 FUJIWARA HISAO 후지와라히사오		
发明人	썩나시마다까노리 기무라히로유키 가루베마사오 후지와라히사오		
IPC分类号	G09G3/36 G09G3/20 G02F1/133 G02F1/1343		
CPC分类号	G09G2330/02 G09G3/3614 G09G3/3655 G09G2300/0876		
代理人(译)	Jangsugil Yijunghui		
优先权	2003195992 2003-07-11 JP		
其他公开文献	KR100608191B1		
外部链接	<a href="#">Espacenet</a>		

摘要(译)

根据本发明的液晶显示装置包括辅助电容电源选择电路6，用于设置辅助电容电源线CS1至CSn的电压。辅助容量电源选择电路6包括用于选择是否向辅助容量电源线CS1至CSn提供第一基准电压VcsH以及向辅助容量电源线CS1至CSn提供第二基准电压的NMOS晶体管8。它具有用于选择是否提供VcsL ( $< VcsH$ ) 的PMOS晶体管9，并且这些晶体管8和9的导通/截止由扫描线驱动电路4中的与门10控制。由于从接通电源起的预定时间内子容量电源线CS1至CSn被设置为相同的电源电压（第一参考电压），因此子容量电源线CS1至CSn的电压电平不变，并且水平线形状的亮线不可见。图1索引词 辅助容量电源线，PMOS晶体管，电压电平，亮线

