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(43)

2003-0061683
2003 07 22

(21) 10-2003-0002415
(22) 2003 01 14

(30) JP-P-2002-00006101 2002 01 15 (JP)

(71)	가	가	가	4	6
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(72) 3300 가 가

3300 가 가

3300 가 가

(74)

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(54)

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2	TFT	2-2'		
3	TFT	3-3'		
4	TFT	4-4'		
5	TFT	TFT	1	
6	TFT	TFT	2	
7	TFT	TFT	3	
8	TFT	TFT	4	
9	TFT	TFT	5	
10	TFT	CF	LCD	
11	LCD	PCB	TAB	
12	LCD	TAB		
13	TFT-LCD	가		
14	TFT-LCD			
15				
16				
17		TFT		
18			가	, 17 18-18'
19			가	, 17 18-18'
20		TFT		
21		20 21-21'		

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CJ :

CLT :

CPAD :

CNT1 : Si

CNT2 : Si

CNT3 :

CNT4 :

CNT5 : Si

CNT6 : Si

CNT7:

Cstg :

DDC :

DL :

EPX :

CF :

FIL :

FPAS :

SPT :

STM :

LCB :

RM

(TFT)

IPS , 7-36058 , 2
가 ,

TN

, , , LCD

, 9-230378 ,

, TFT ,

TFT 가

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1 5
2-2', 3-3', 4-4'

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1 GL PSI GL, TFT DL 1
PSI PSI TFT, TFT
가 () 1 GL
PSI, PAD SPT, 가
DL 1 CNT1 PSI
2 CNT2 , PAD , PAD
3 CNT3 SPT SPT H

IPS , , ,
1 CLT SPT TN
, , ,
(IZO), CLT (ITO)
, , ,
(ITZO)

DL,	STM, 1	PAD	(8).
SiH ₄	NH ₃ N ₂ 가	CVD 3.5μm	300nm Si ₃ N ₄ PAS
,	,	2.0μm	,
,	,	FPAS	230 20
N ₄	5 CNT5 Si ₃ N ₄ 1 CF ₄ 3 CNT3	FPAS 가	FPAS 가 Si ₃ N ₄
2	FPAS	가	,
3). 6	ITO CLT 70nm SPT TFT가	,	(
1	(AR) 10 GLS1, GLS2	,	
10	GLS1 Tg, Td가 (GLS1, GLS2) TFT Tg, Td GSCL TFT GLS2 GLS1	가	가
AR	AR TFT GLS1 TFT CHI가 DDC		
TCP(11)	TCP Td DDC TCP		
SL	GLS1, GLS2 INJ LC	,	
2	ORI ORI SL SL LC		
,	SL TFT GLS2 GLS1 INJ LC CF INJ SUB1 GLS2 GLS2		
FT 11, 10	DDC TFT IC GLS1 TCP TFT TFT GLS1 GSCL T		
TCP	IC TCON, (TAB) PCB1 CJ TCP		
12	TCP TCP Td ACF Td TCP FPAS		
SL GLS2 LS1	TCP CLT EPX () TCP CF GLS2, G		
LC	가 SPC 가		

Cstg Cadd
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 L , SPT CLT , G
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 , GL CLT SPT
 , SPM 20 , SPM
 CLT SPT . .
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 IPS

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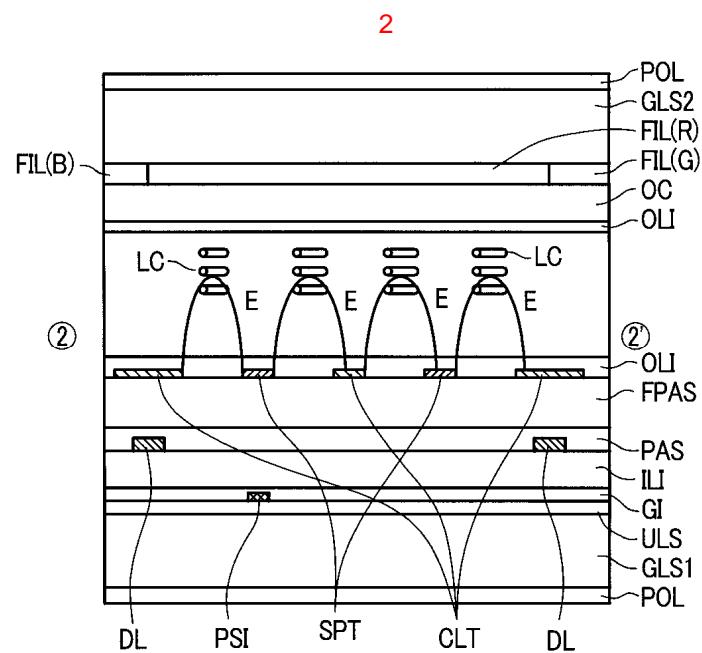
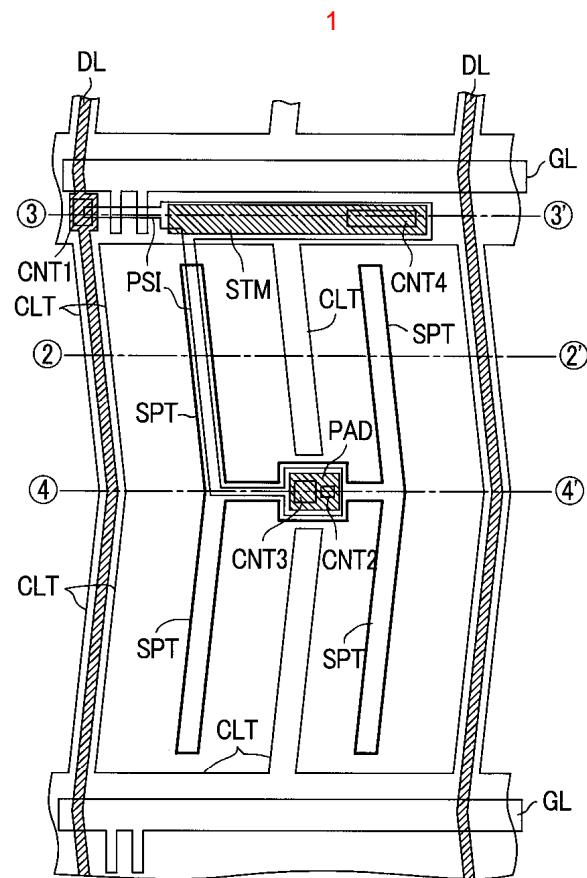
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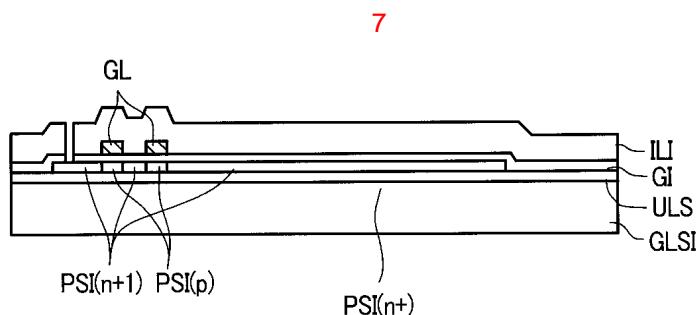
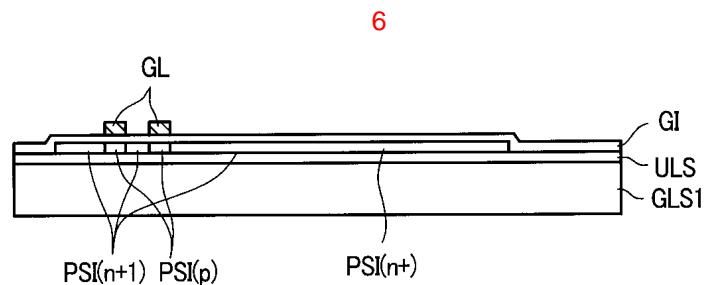
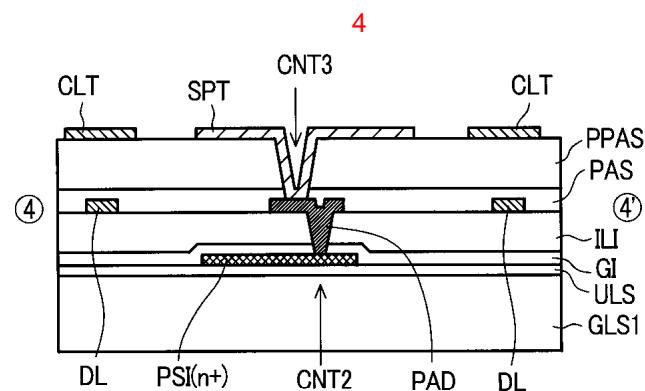
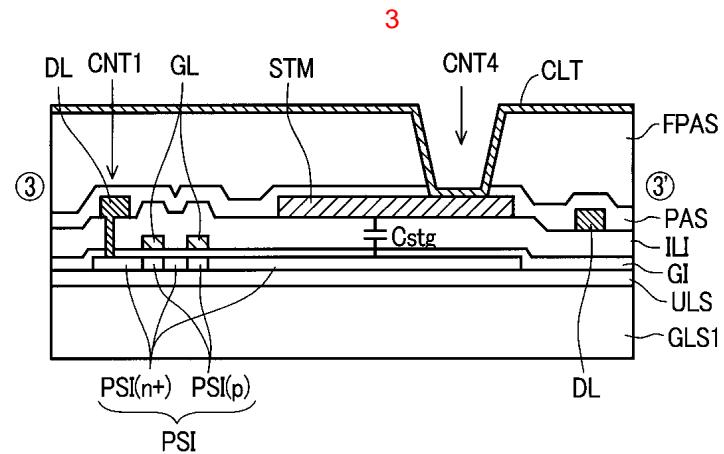
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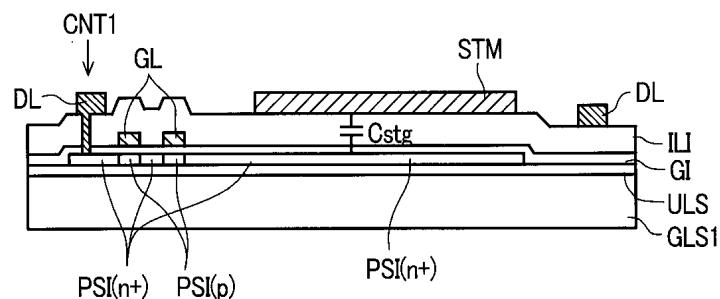
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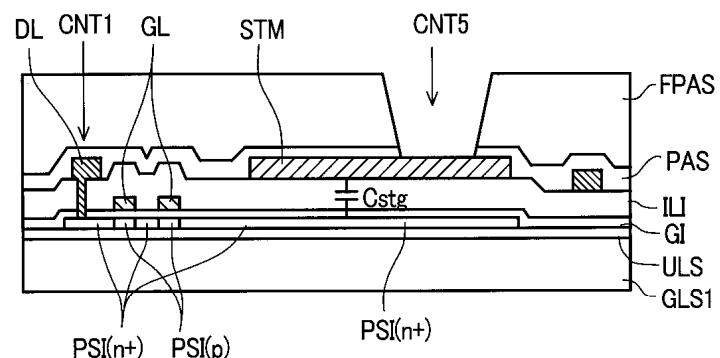




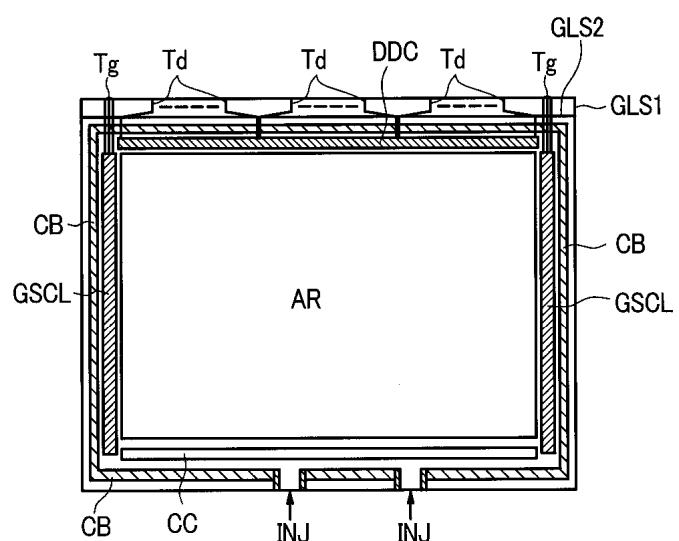
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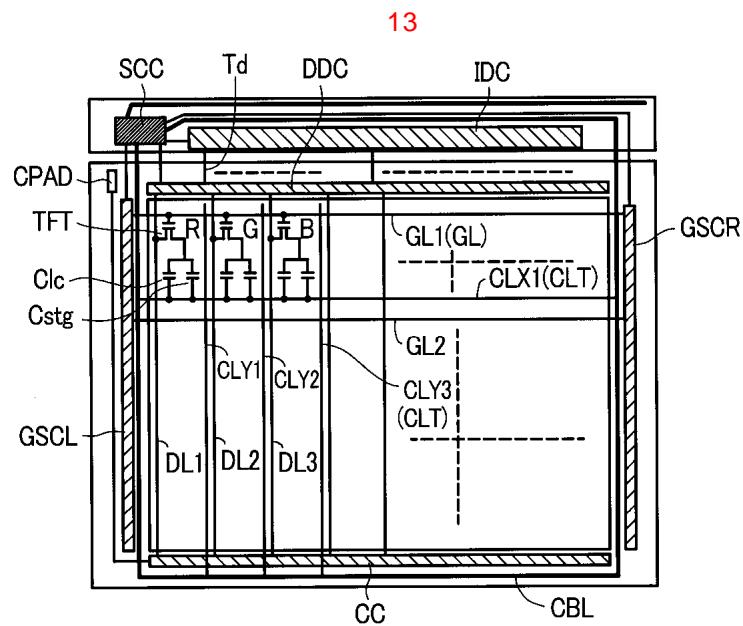
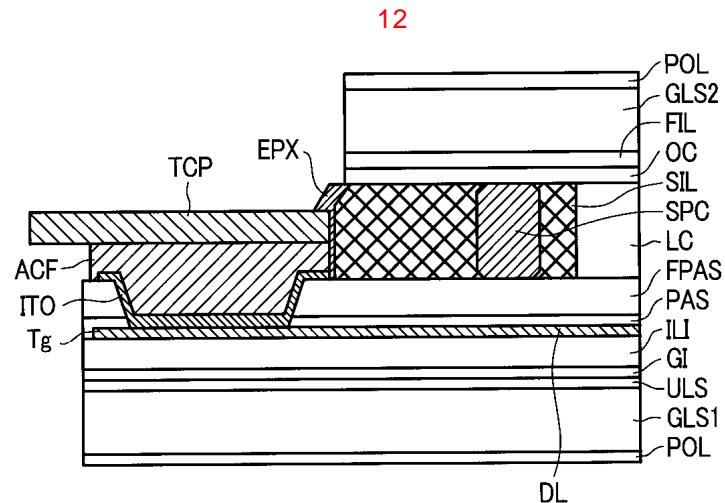
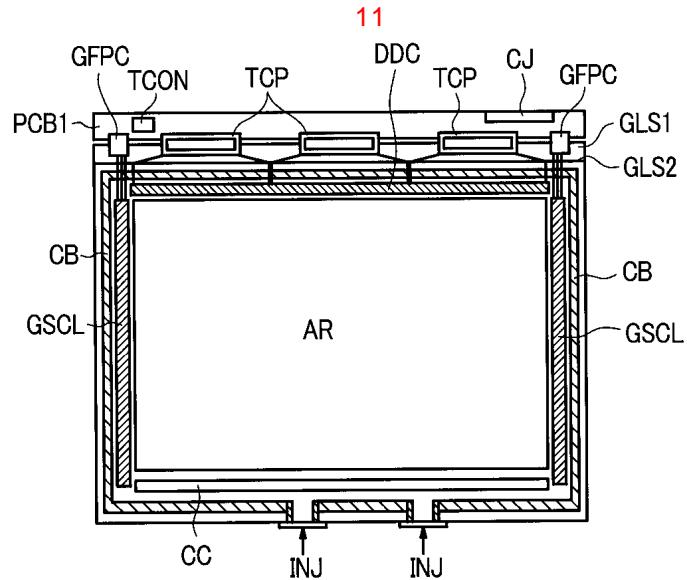


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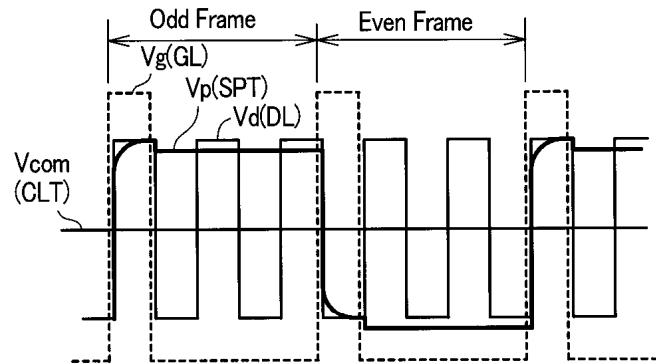


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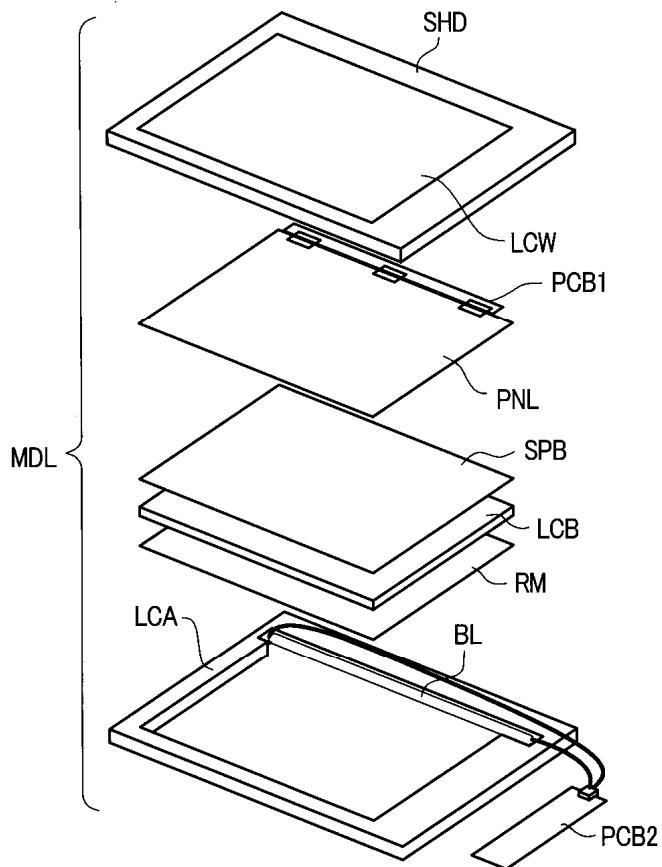




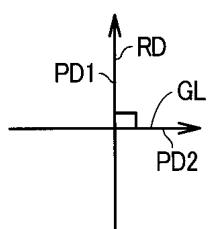
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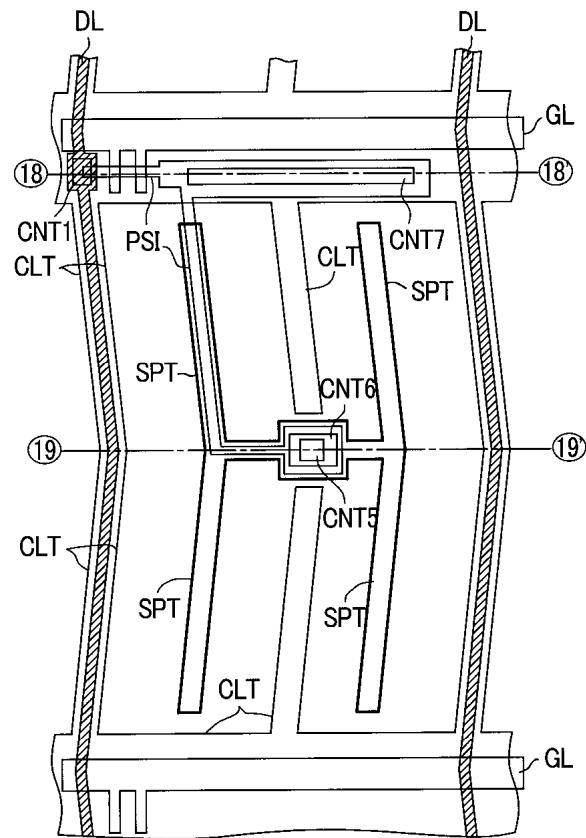
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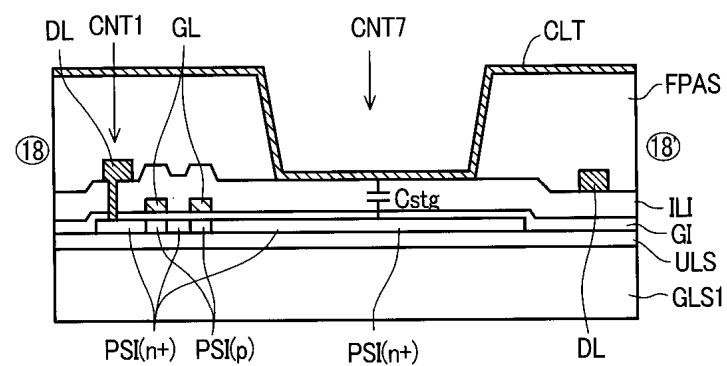
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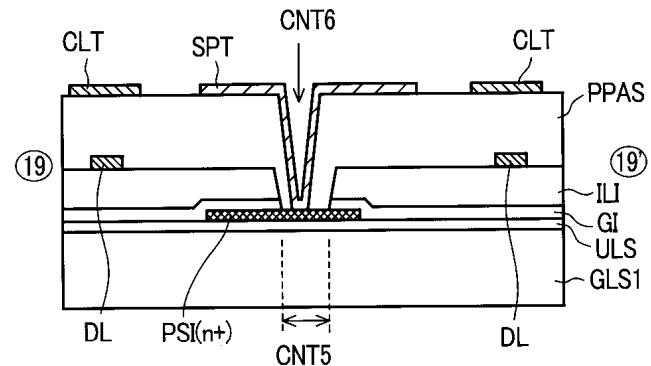
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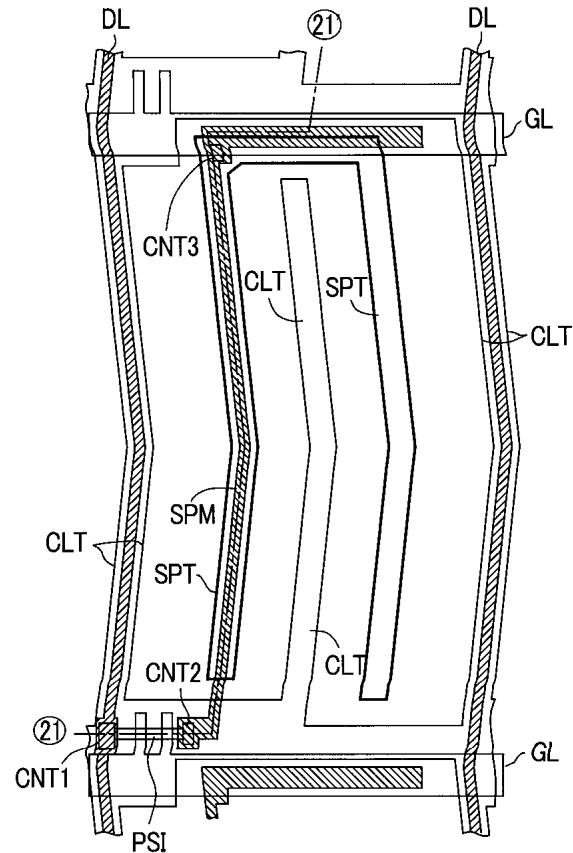
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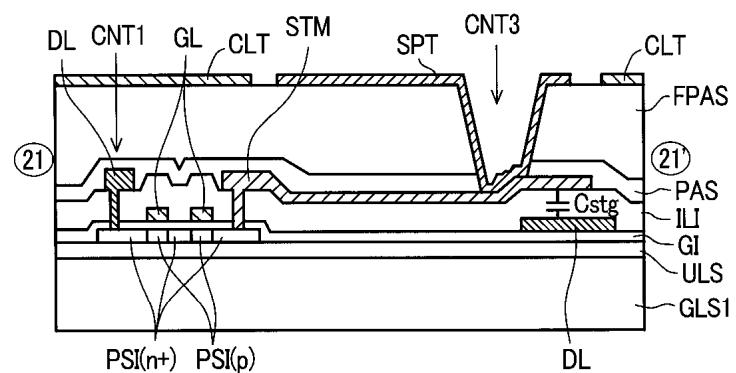
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专利名称(译)	液晶显示器		
公开(公告)号	KR1020030061683A	公开(公告)日	2003-07-22
申请号	KR1020030002415	申请日	2003-01-14
[标]申请(专利权)人(译)	日立HITACHI SEISAKUSHODBA		
申请(专利权)人(译)	株式会社日立制作所		
当前申请(专利权)人(译)	株式会社日立制作所		
[标]发明人	ONO KIKUO 오노기꾸오 OKE RYUTAROU 오께류따로 OCHIAI TAKAHIRO 오찌아이다까히로		
发明人	오노기꾸오 오께류따로 오찌아이다까히로		
IPC分类号	G02F1/1333 G02F1/1368 G02F1/1343 G02F1/1362 G09F9/30 H01L29/786 G09F9/35		
CPC分类号	G02F1/136213 G02F1/134363		
代理人(译)	CHU, 晟敏		
优先权	2002006101 2002-01-15 JP		
其他公开文献	KR100511817B1		
外部链接	Espacenet		

摘要(译)

在有源矩阵型液晶显示器中，形成在第一基板上形成的薄膜晶体管的半导体层上形成的公共布线，在半导体层上形成的第一绝缘膜，在第一绝缘膜上形成的第二绝缘膜，以及第二绝缘膜第二绝缘膜形成位于半导体层形成区域中的去除区域，并且存储电容器由公共布线提供的公共电位和去除区域中的半导体层形成的像素电位形成。1指针方面 半导体层形成区域，去除区域，公共布线，像素电位

