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G02F 1/1343(11) 2001 - 0087345
(43) 2001 09 15(21) 10 - 2001 - 0011403
(22) 2001 03 06

(30) 2000 - 061059 2000 03 06 (JP)

(71) 가 가
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가 4 6

(72) 1 5 - 1 가 가

1 5 - 1 가 가

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7 4

8 7 VIII - VIII

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15 14 XV - XV

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HTV :

MPX :

DDR :

CCD :

LST :

GDR :

GOV : -

GFV : -

BF :

가

1

2

(TN; Twisted Nematic)

IPS(In Plane - Switching type)

TN

2

2

TN

(rubbing)

, 90 °

(가) ,

가

TN

가 가

가

(white)

가

, 가 .
() , , (black) 가 .
1991 , , 「 」 .

, 11 - 231341 (1999 8 27), 2000 - 221527
(2000 8 11), 2536632 (1997 2 21)

13 . . , GL
DL AR
TFT가 GL DL , STL
GL . ITO2 .
01) IT
TFT (,
Clc IT01 IT02 ,
, cstg가 IT01 STL .
IT02 CDR (,) ,
STL STR (,) . GDR
, DDR , CONT

14
SUB1
14

14 , 2 GL 2 DL
 , TFT . TFT GL,
 ()
 (100), (1000) SD1 SD2

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1 , 2 1 II - II
 , 14 15 1 . 2 , ALC
 TL 1 2 , STL 14 15 ALC 가 . , S
 01 ALC , STL 2 PAS 1 SiN , IT
 STL ITO1 ALC , 2 PAS 1 STL SiN STL
 가 ITO1 , ALC 2 가 ALC1
 1TO1 ALC , STL , ITO1 () 2 STL , ITO1 , ITO1
 , , ALC , ITO1 1 SiN 2 PAS STL
 STL ITO1 2 (PAS, SiN) STL ITO1 ITO1 ,
 , , , ()
 , , ,
 ALC ITO1, 1 SiN 2 PAS STL
 , , Cstg 2 1 2 SiN PAS STL
 , , ITO1 ALC Cstg ITO1 ALC 가 .
) , , ITO1 , ITO1 (PAS, SiN , ALC
 , , ,
 , , Cstg , , STL ALC
 , , STL 가 , , STL 가가 . , 1
 , , IT01 , , IT01 ALC
 , , ,
 cstg , , ,
 , , ,
 3 2 GL , , 4
 DL

1 , ITO2 가 , 2
 , , .
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9

, PNL; PNL
 DDR (IC); PNL), (GDR (IC); DDR
 GDR ,
 CRL; PWU ,

, (),
 , , 가 CRL .

CRL , TCON ,
 PNL

GDR DDR 가 9
 DDR ,

PNL , GL STL
 STL , PWU

10 9
 DDR () () CVD, CDD,
 HTV, MPX, GOV, GFD, DC - DC D/D 9
 LST, - - - -
 CRL, PWU

11 () CRL CRL
 DDR GDR CRL CRL
 (,) , , DDR
 D1(CL1), D2(CL2) , G(CL3) GDR
 FLM,

LVDS (LVDS) LVDS
 CRL () LVDS
 GDR DDR

11 , DDR D2(CL2)
(DCLK) , XGA 40MHz
가

12

PNL

(57)

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가

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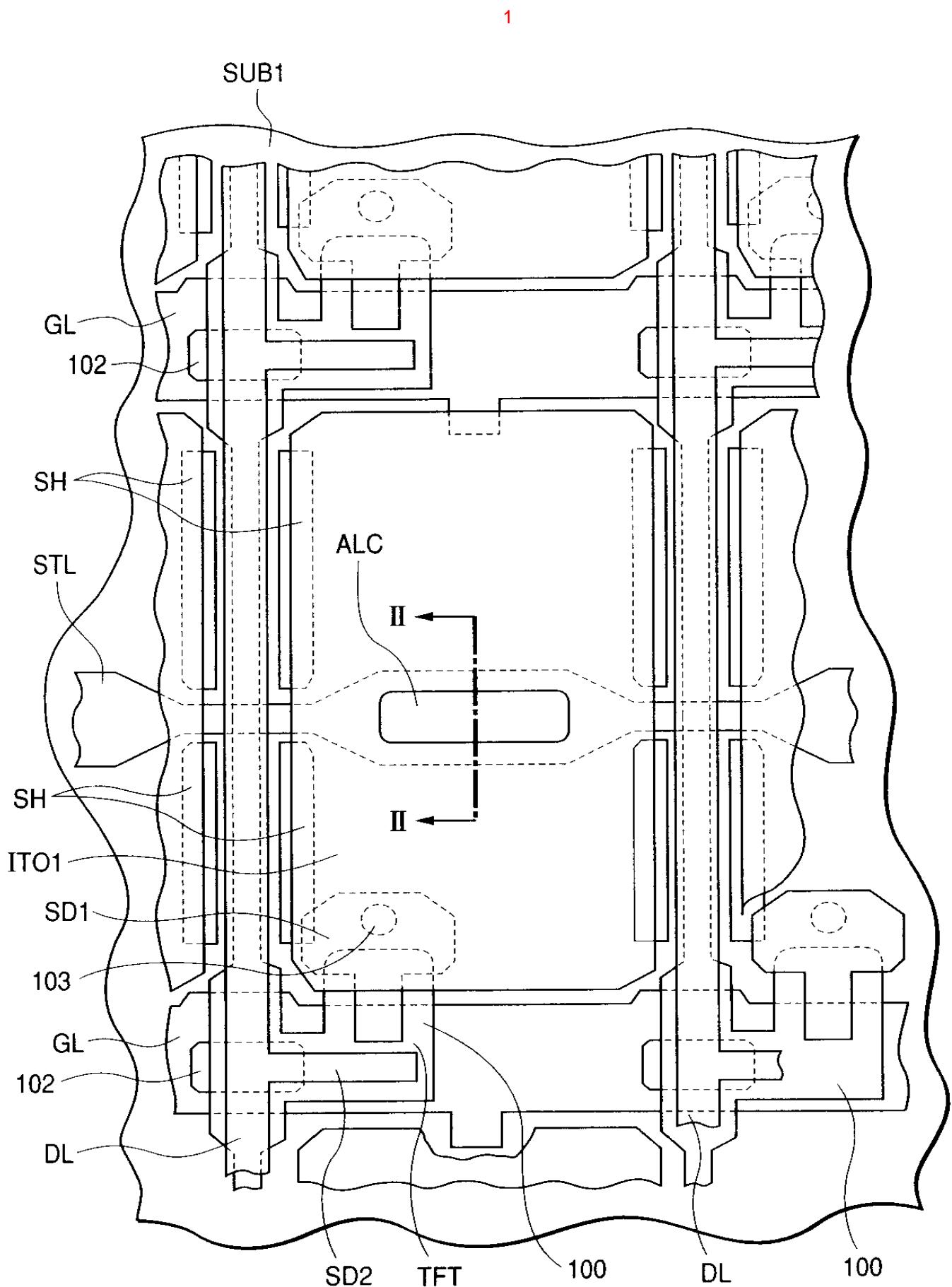
7

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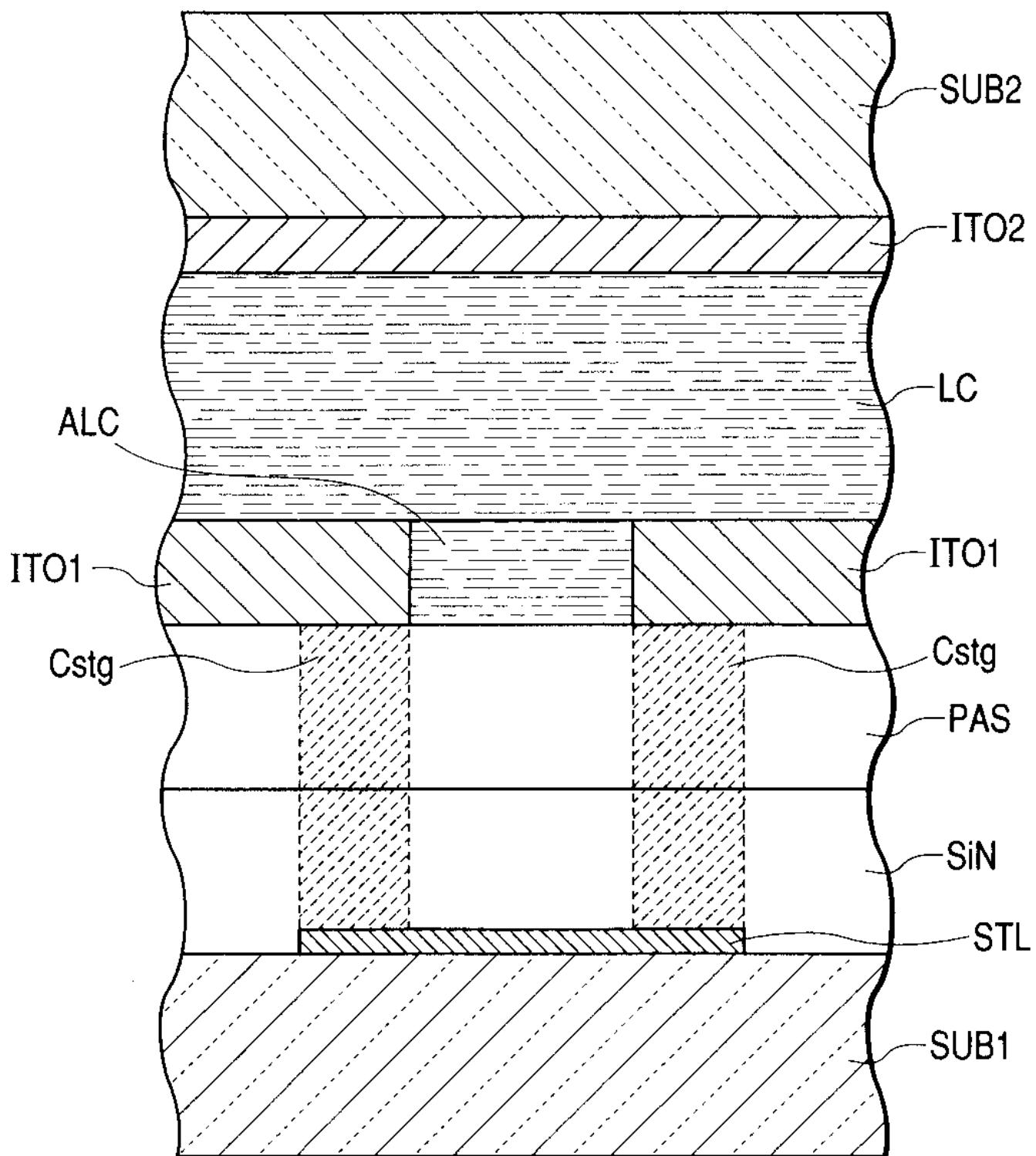
12.

8

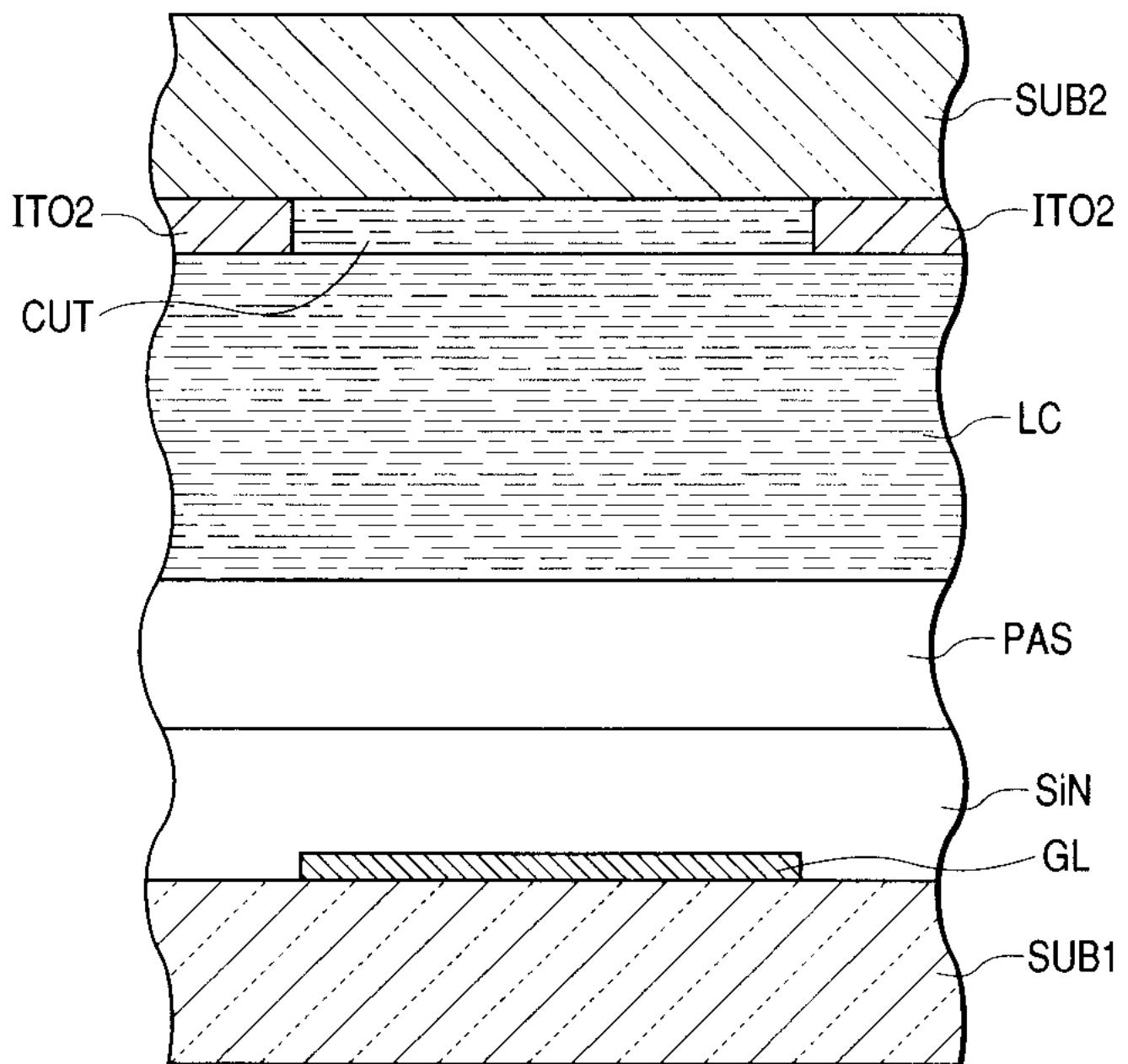
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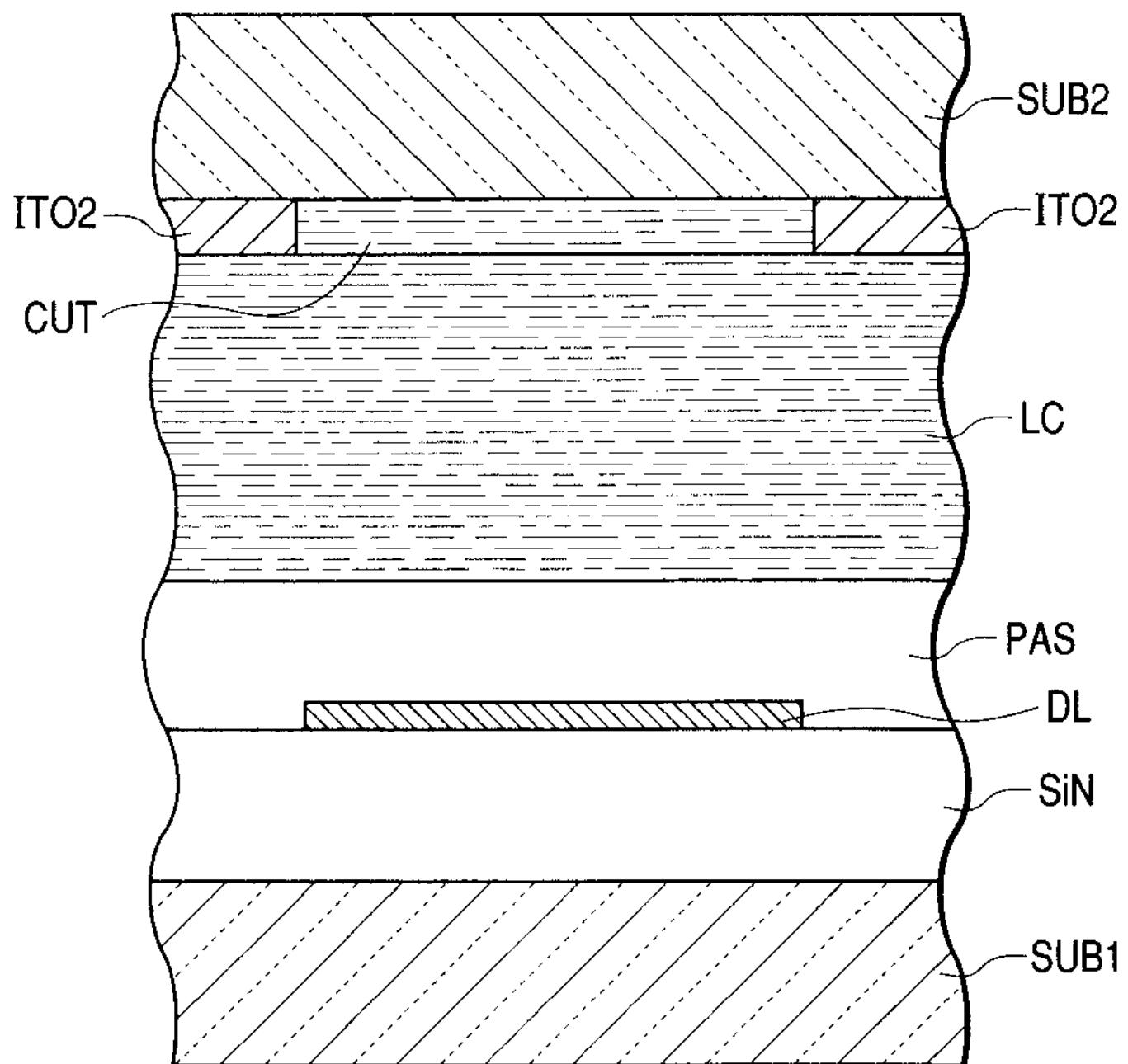
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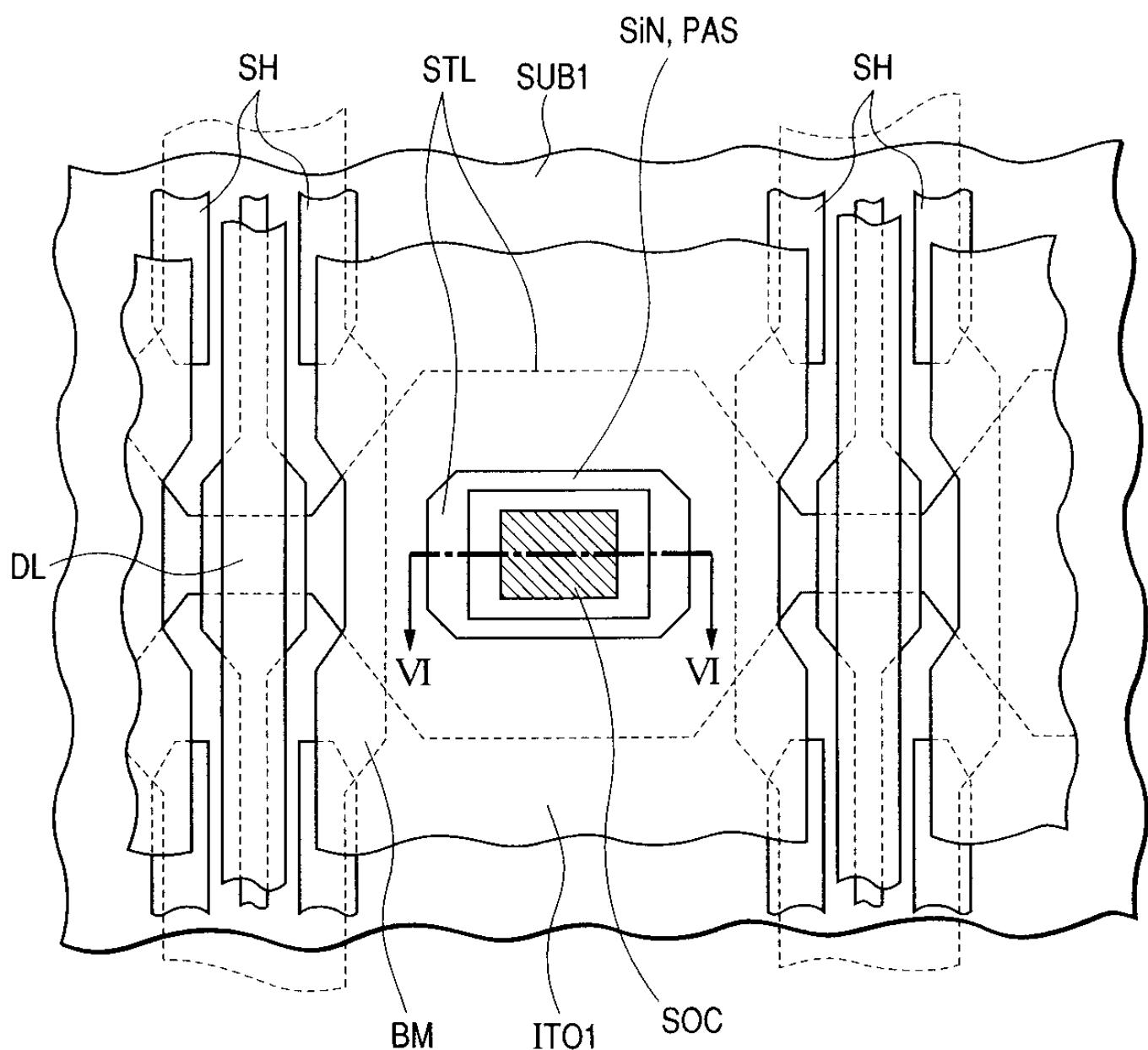
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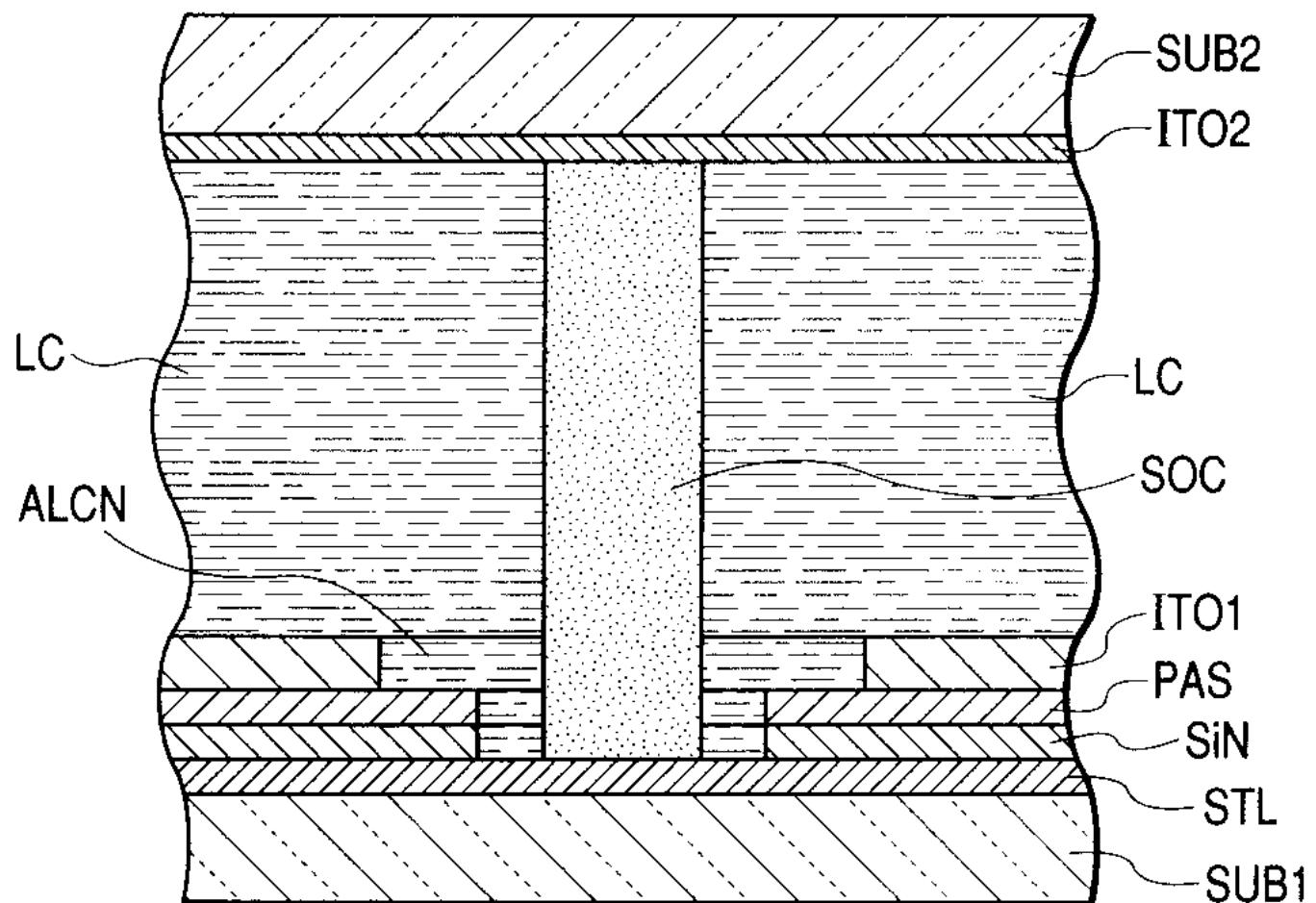
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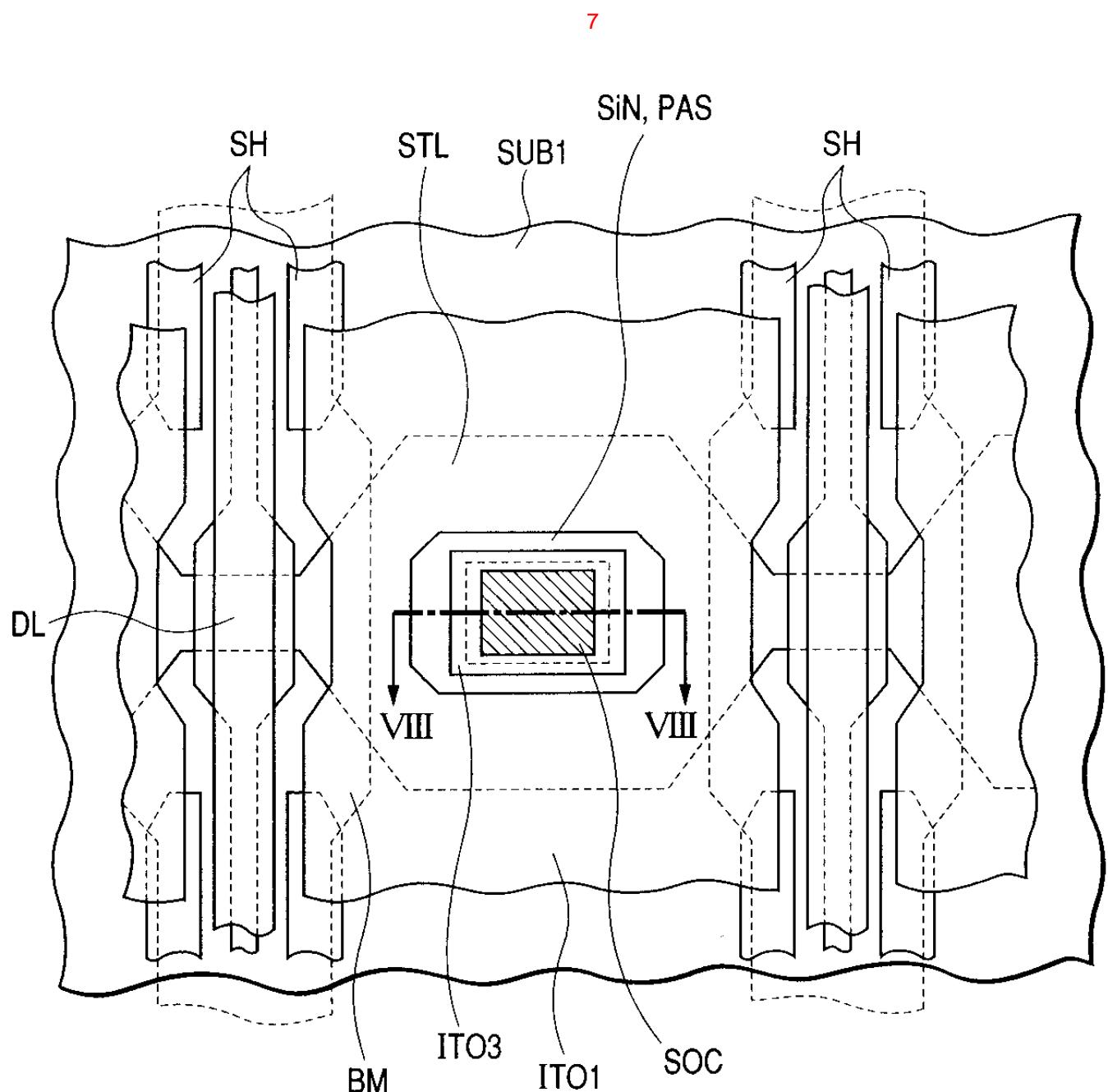


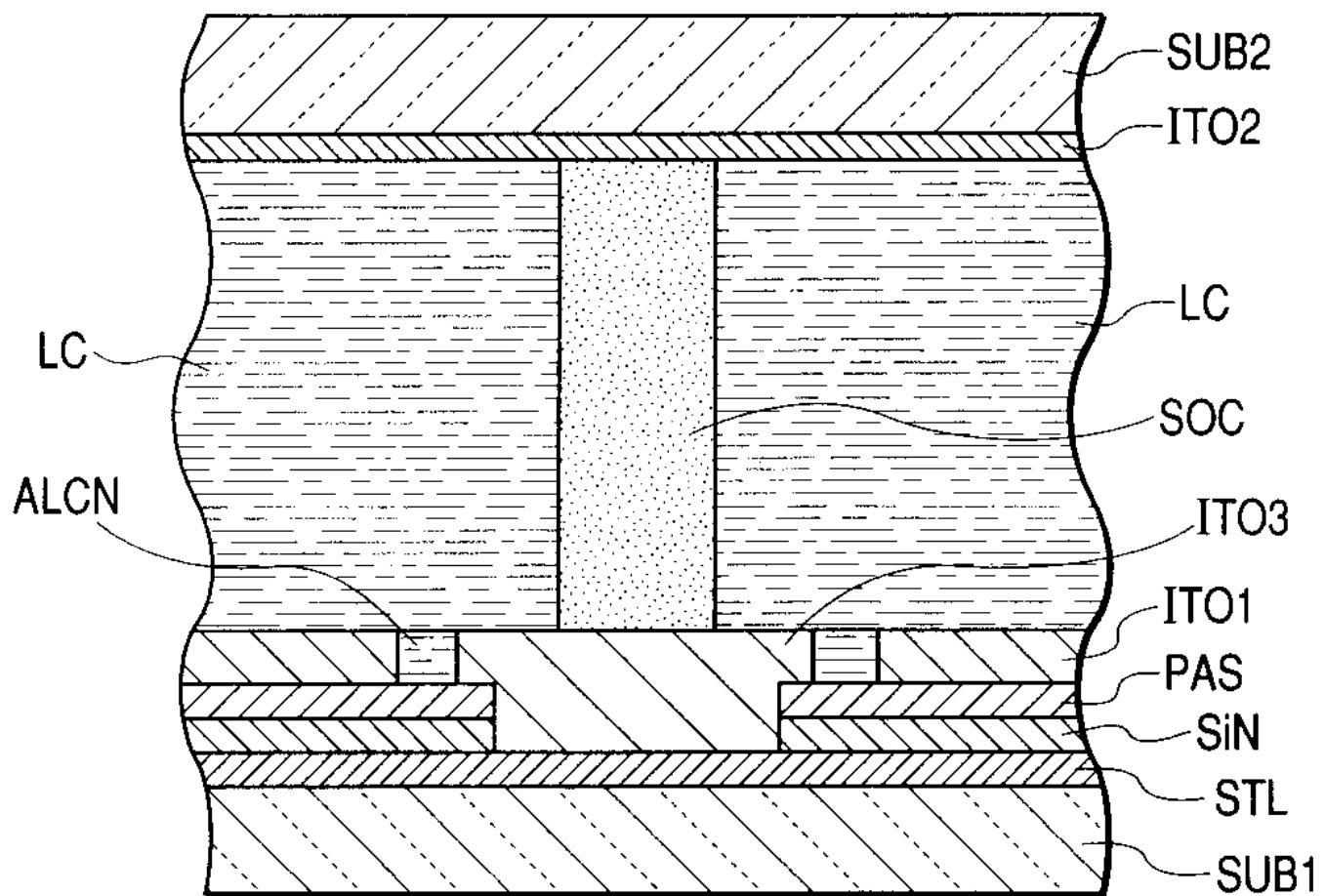
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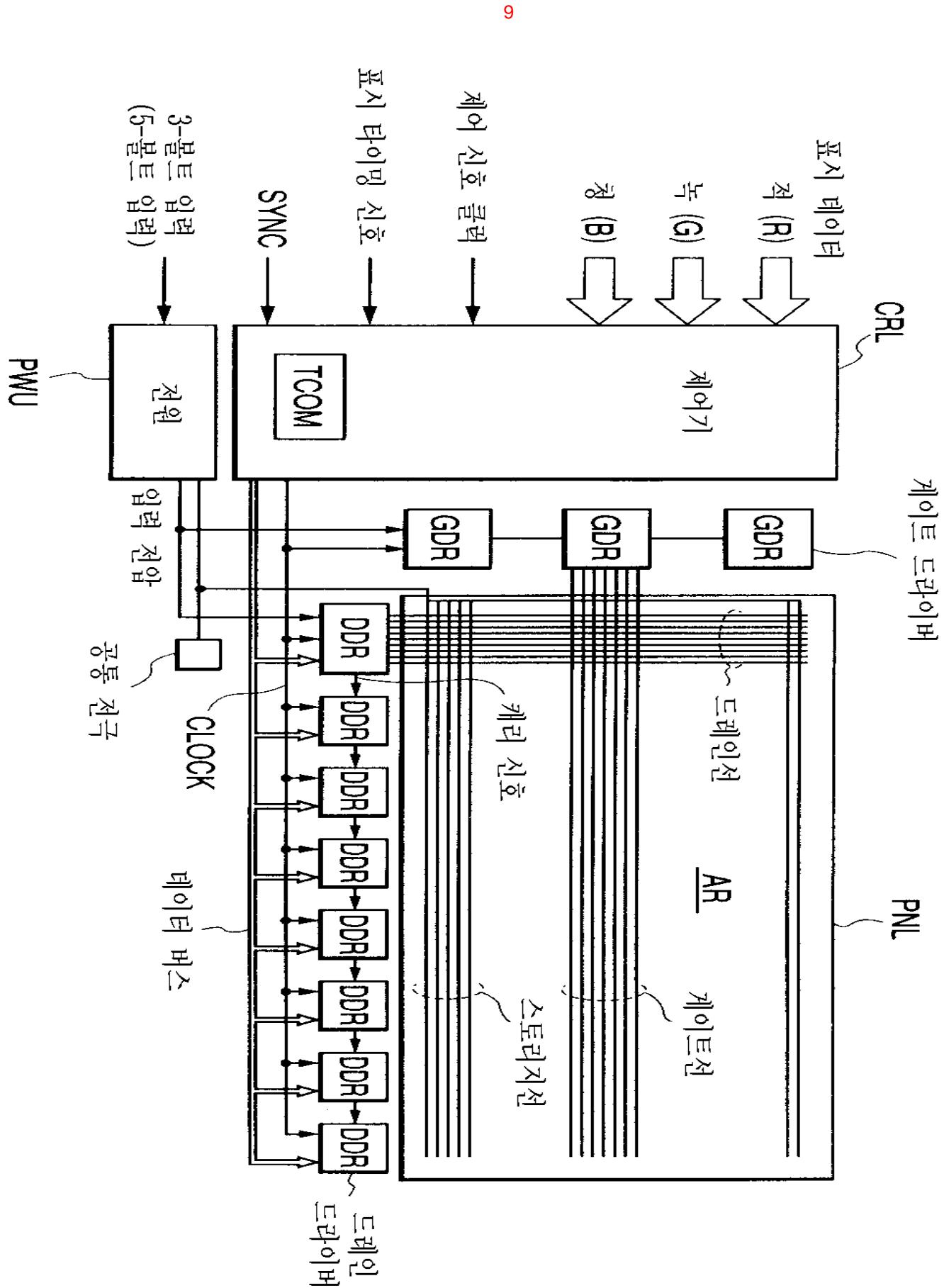


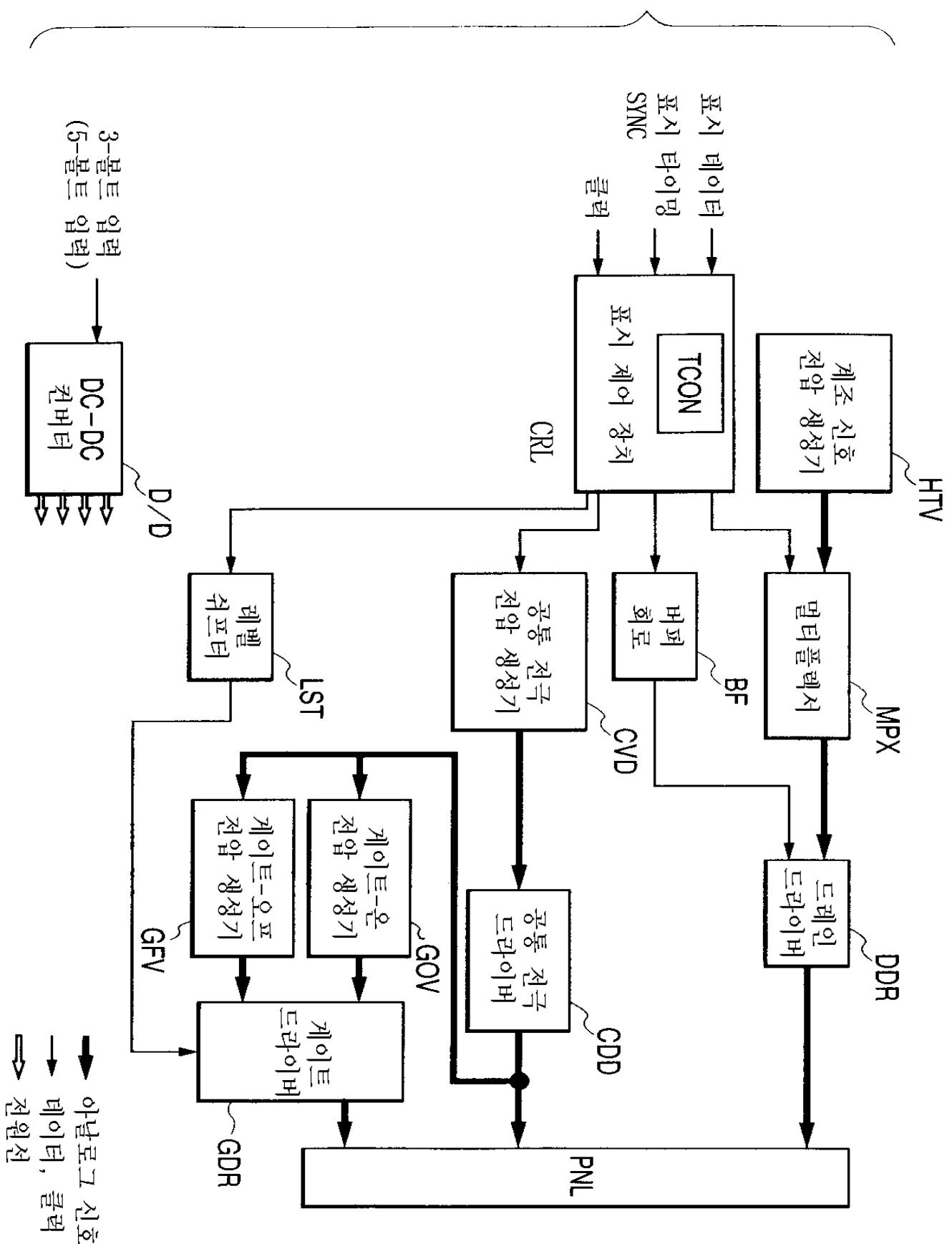
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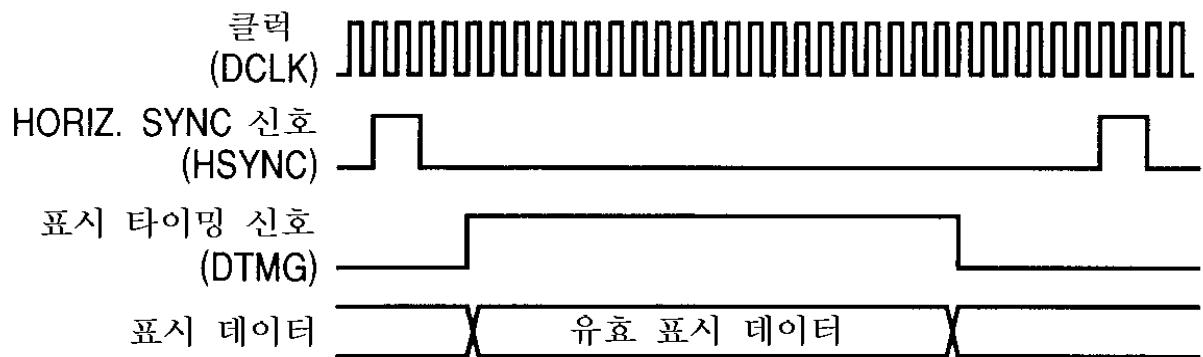
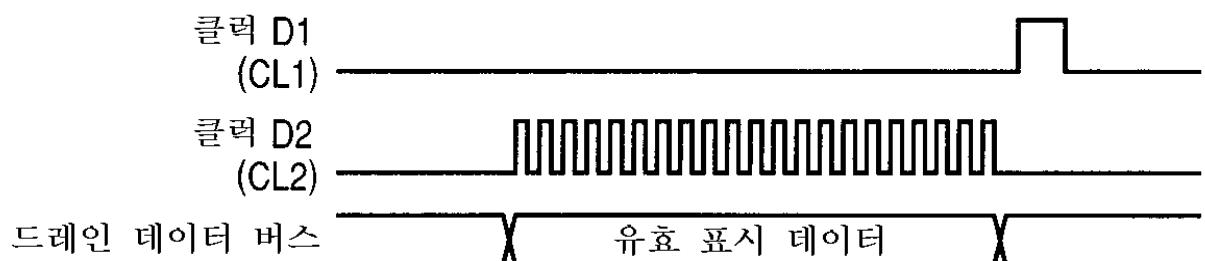
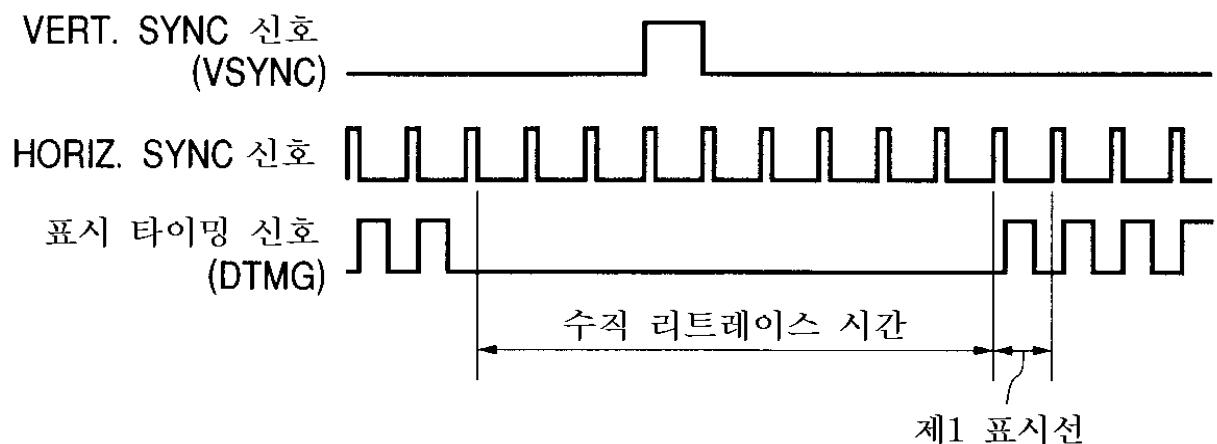
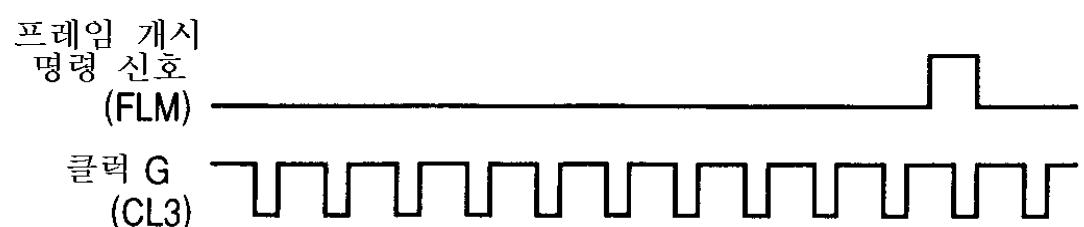




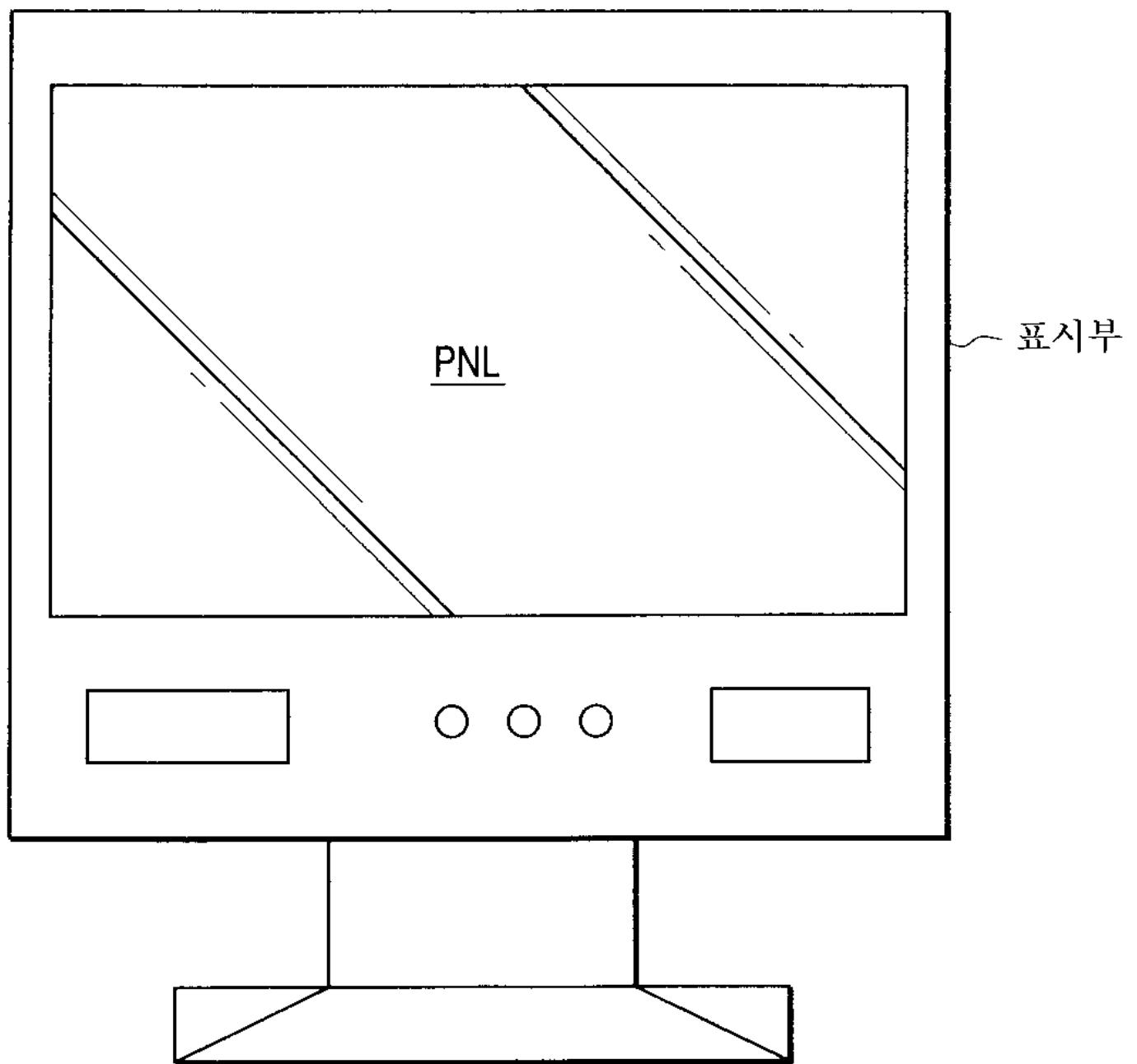


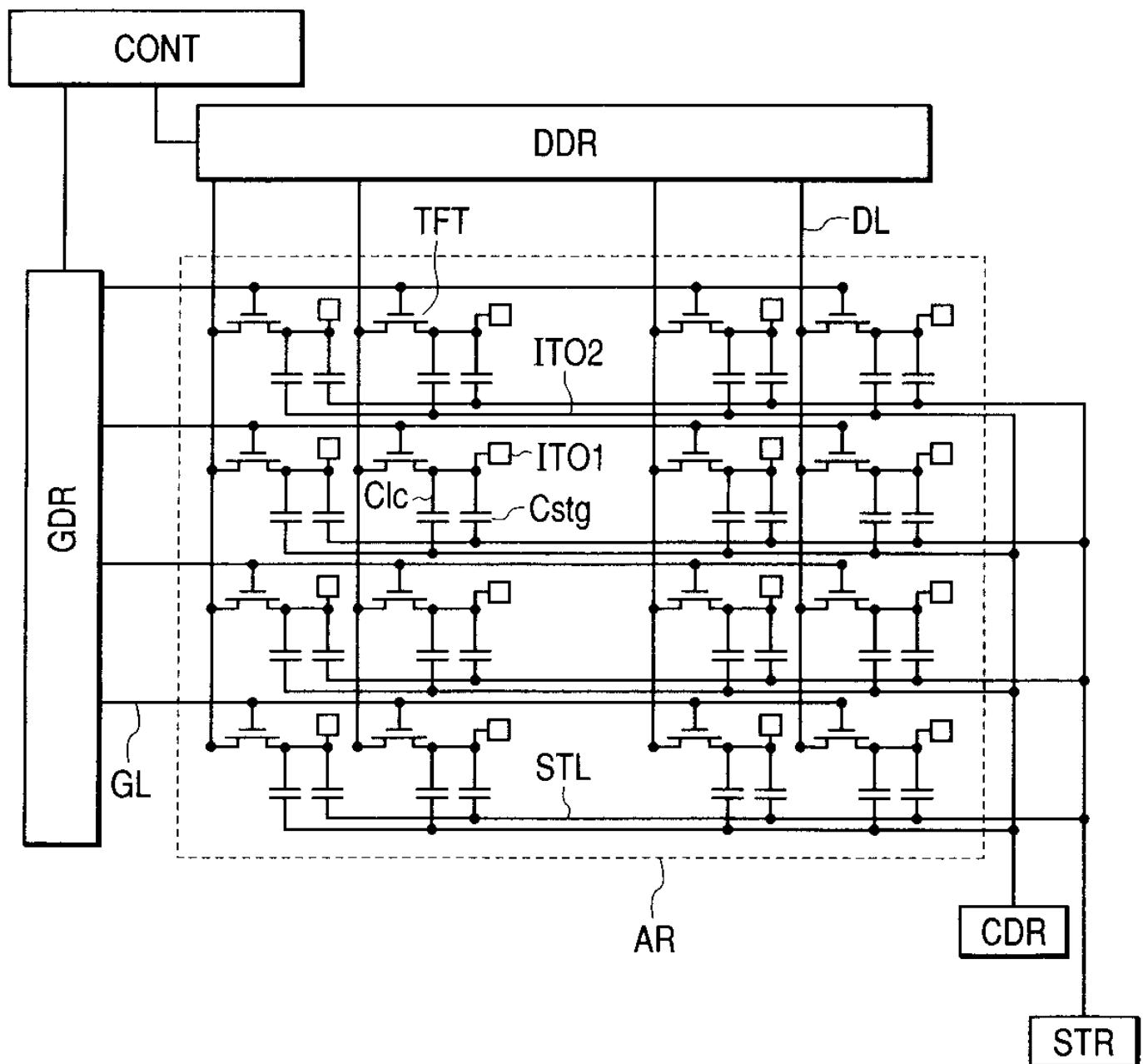


→ 아날로그 신호
→ 데이터, 클릭
⇒ 전원선

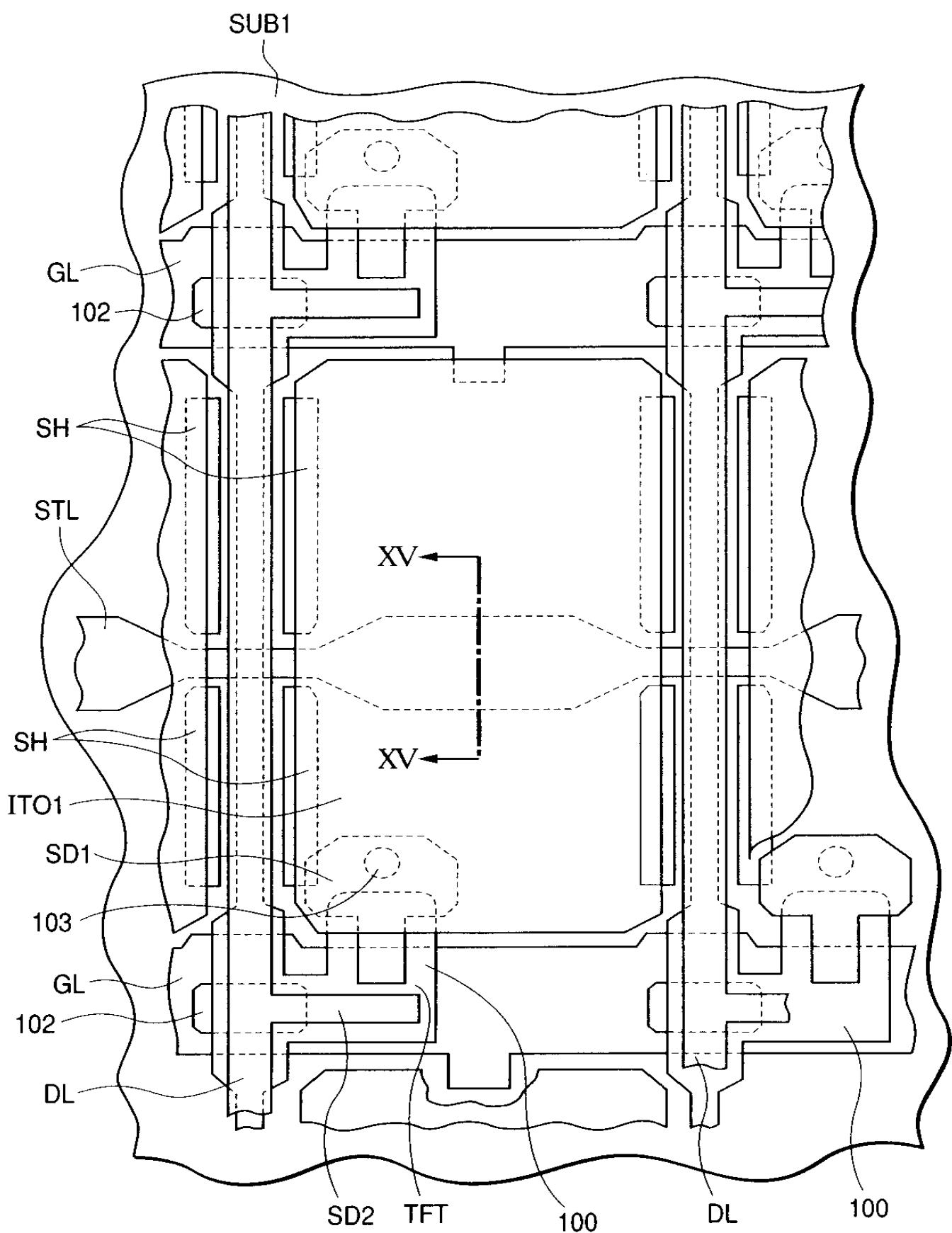
메인 바디로부터의 신호드레인 드라이버로의 출력메인 바디로부터의 신호게이트 드라이버로의 출력

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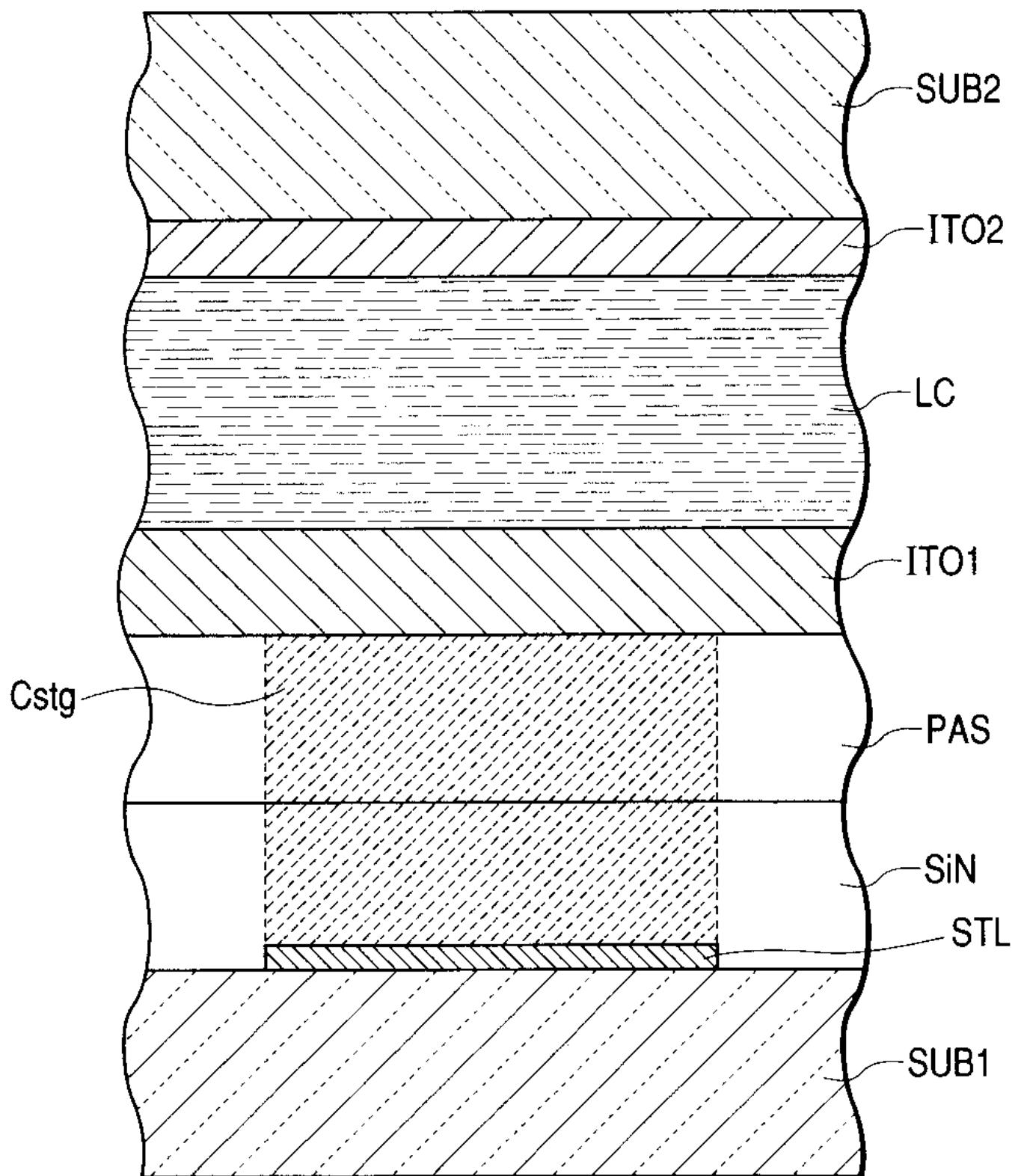




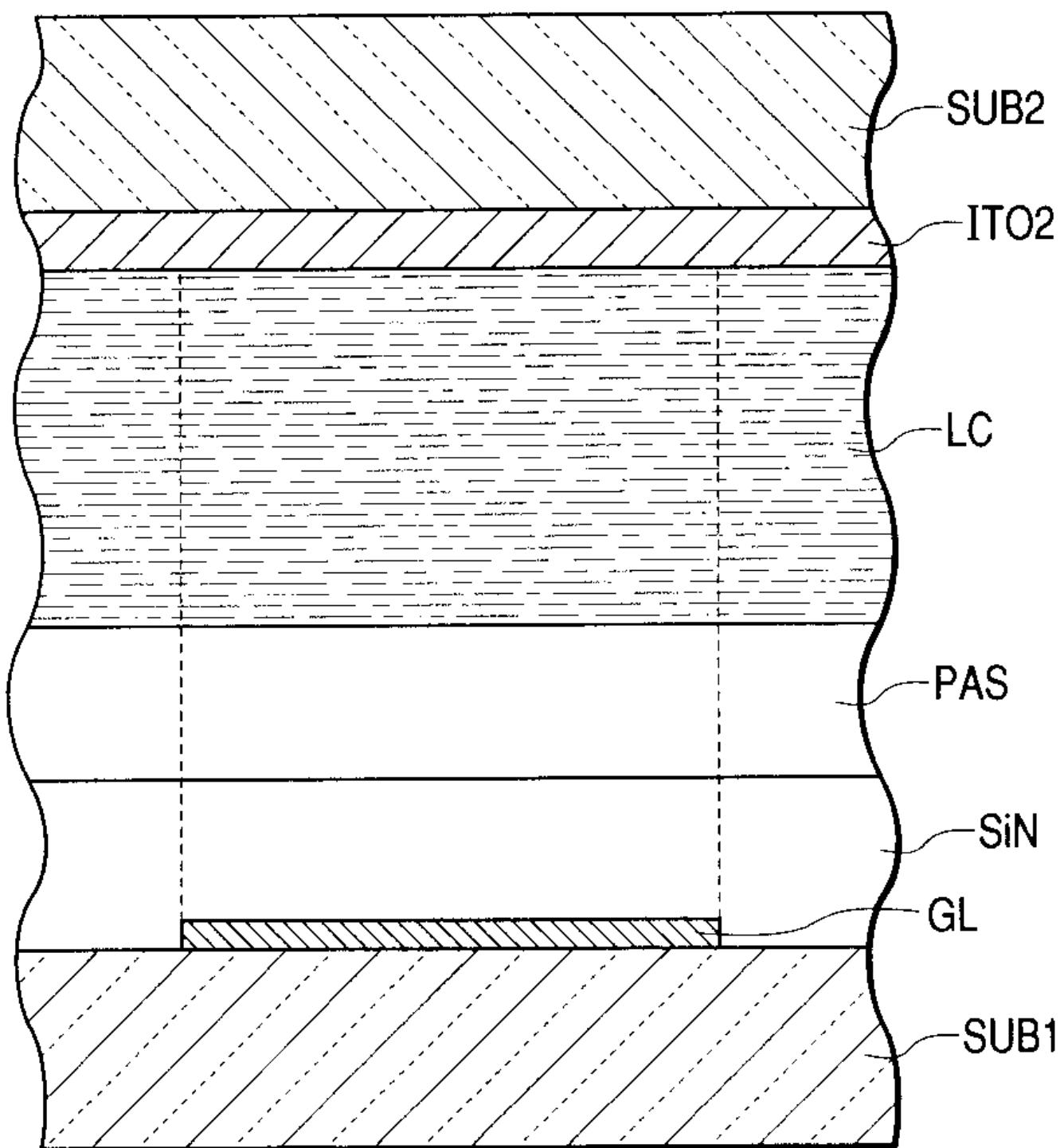
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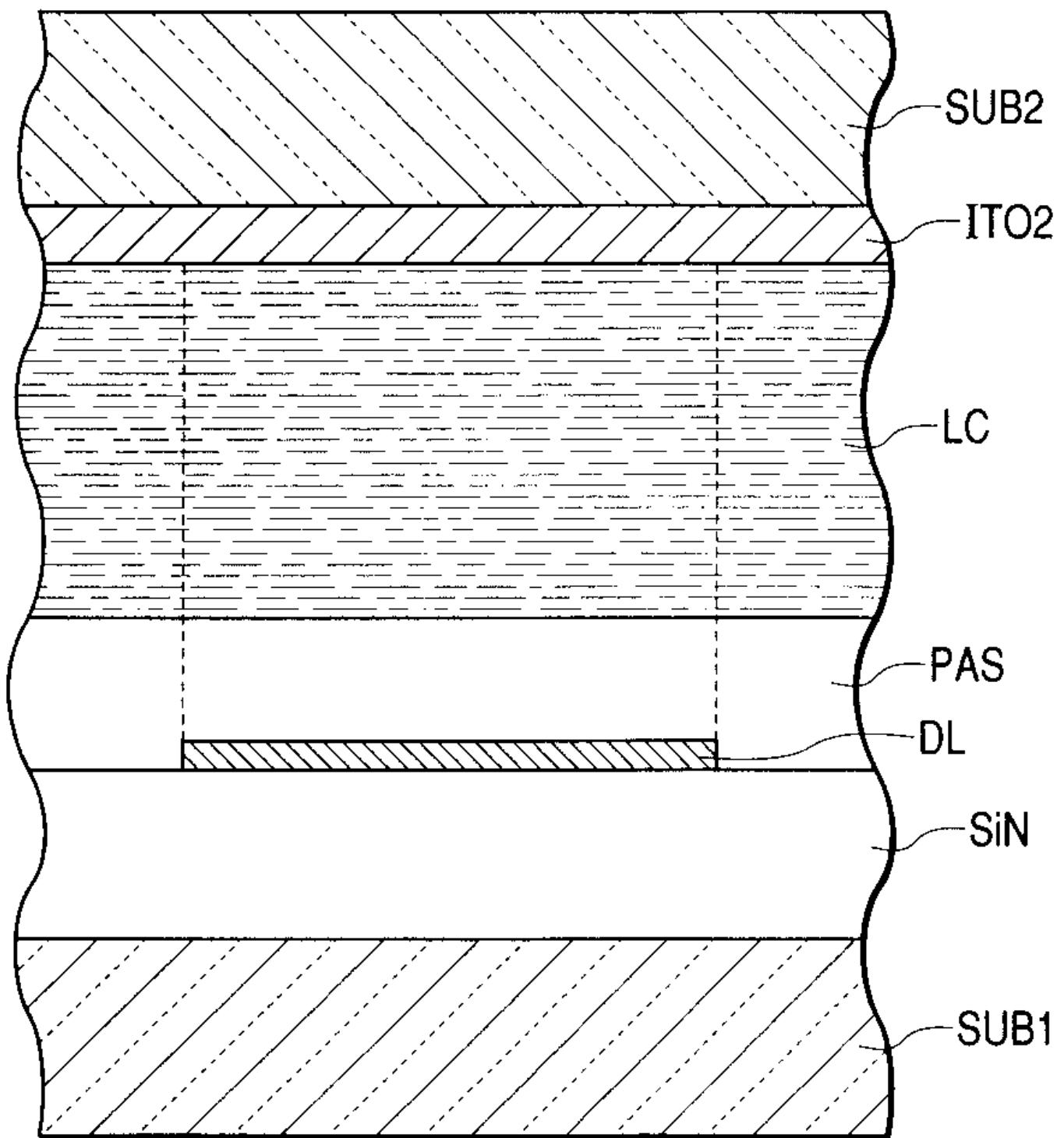
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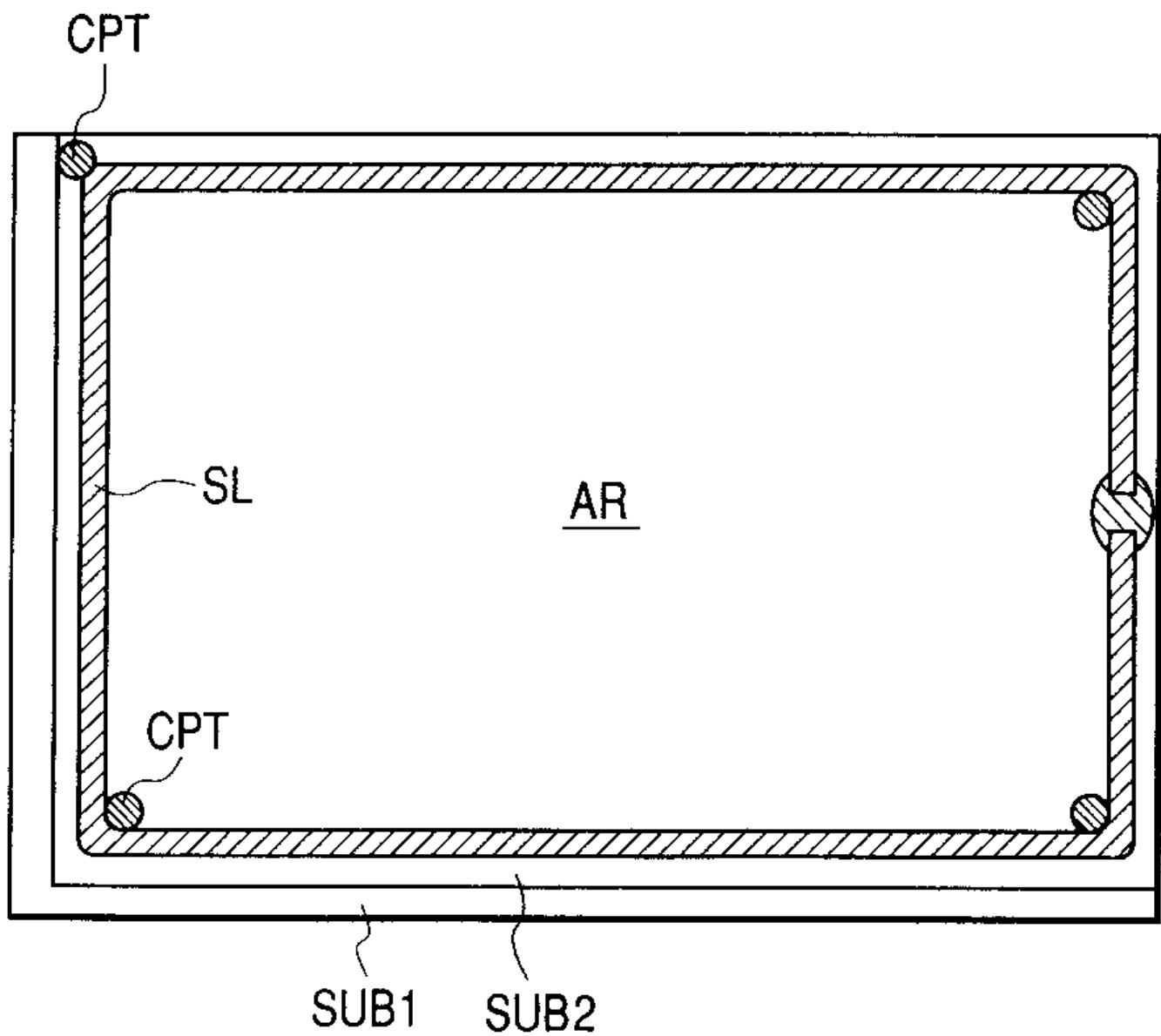


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专利名称(译)	具有可控存储容量的液晶显示装置		
公开(公告)号	KR1020010087345A	公开(公告)日	2001-09-15
申请号	KR1020010011403	申请日	2001-03-06
[标]申请(专利权)人(译)	日立HITACHI SEISAKUSHODBA		
申请(专利权)人(译)	株式会社日立制作所		
当前申请(专利权)人(译)	株式会社日立制作所		
[标]发明人	OHGIICHI KIMITOSHI 오기이찌기미또시 OOTSU RYOUICHI 오쯔료우이찌		
发明人	오기이찌기미또시 오쯔료우이찌		
IPC分类号	G02F1/13 G02F1/1362 G02F G02F1/1343		
CPC分类号	G02F2201/123 G02F1/134336 G02F1/136213		
代理人(译)	CHANG, SOO KIL		
优先权	2000061059 2000-03-06 JP		
其他公开文献	KR100356877B1		
外部链接	Espacenet		

摘要(译)

一种液晶显示装置，包括：一对基板，夹在所述一对基板之间的液晶层，设置在所述一个基板上的栅极线，以及与所述栅极线交叉的漏极线，像素电极布置在由与两条相邻栅极线相邻的两条漏极线围绕的区域中，并且公共电极布置在另一基板上。液晶显示元件还在像素电极下方设置有存储线，该存储线设置有插入其间的绝缘层以在它们之间形成电容。在像素电极的与存储线相对的部分中形成开口。 1 指数方面 - 1 - 液晶显示装置，存储式液晶显示装置，薄膜晶体管型有源矩阵法，存储容量

