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2002 - 0032790
2002 05 04

(21) 10 - 2000 - 0063451
(22) 2000 10 27

(71)

3 416

(72)

2 202 1203

3 1 809

551 1702

104 104

1 5 521 1107

(74)

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(54)

가

1

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1 1

2 1 - ' ,

3 2 ,

4 3 - ' .

(Coplanar Electrode, CE)

(Coplanar Electrode, CE)

(R), (G), (B)

가

가

가

가

가

가

가

200 μ m

1 1 , 2 1 - ' , (100) (200) , " " " " (26) (29) " " (22) (62) (TFT) (67, 68) (27, 28) (TFT)가 (29) (22) 가 (27, 28) (26)가 가 (22) (22) 가 (100) (201) (202) (22, 24, 26) (27, 28, 29) (22, 24, 26) 가 (22), (22) (24) (22) (22) (22) (26) (26) 가, (27, 28, 29) (27) (27) 가 (28) (27) (29) (22, 24, 26) (27, 28, 29) (30) (30) (42) (42) (55, 56) (62, 65, 66) (67, 68) (42)

(62, 65, 66) (22) 가 (62), (62) (62)
 (55) (65), (56) (66)

7) (67, 68) (66) (27) (6)
 (67) 가 (68)

(62, 65, 66) (67, 68) (70)

(C) (22) (26) (C)
 (70), (30) (22) (22)
 (26)

" " (26) (29) 1 2 (72, 74)
 (202) 1 (72) (70), (30) (26)
 , 2 (74) (70), (30) (29)
 (202)

(70) 1 2 (72, 74) (26) (29)
 (82)

(220)가 (100) (200) (210)
 (220) (200) (210)

(100) (200) (300)

(22) 가 가 (29) (27, 28, 29) 5V
 가

(26)가 (22) (29) (26)
 (29) 5V (26, 29) (200)
 (210) (26) (29) 5V

(210)
 1

(26) (22) (22) (C), 1 2 (72,
 (26) (82) (26) (22) (29)

(26) (22)

(29) (10) (26)가 (22)
 (30) (70)

(C), 1 2 (72, 74)
 (70) (30) (26) (202)
 (29) (202) (26) (29) (201)

1 2 (72, 74) (26) (29) (82)
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(26) (22)

3 2 , 4 3 - ' (29) 2 (84) (22) (26) (26) (84)
 (26) (26) (210)

1 " " " " (100) " " (22) (26) " " (27) (29) (22)
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(29) (30) (70) (29) (202) (74) (70) (74)
 (29) (29) (84) (84) (70)
 (30) (26) (26)

(22) 가 가 , (29) () (27, 28, 29) 5V
 가

(22) (26) -7V (29)

5V (26) (26) (26) -7V (26) (29) (84) (84) (210)
 (26) (210) (210) (26) (84) 5V (29)
 5V (210) 1
 (26)가 (22) (26) (22) 가 가
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 , 200 μ m

(57)
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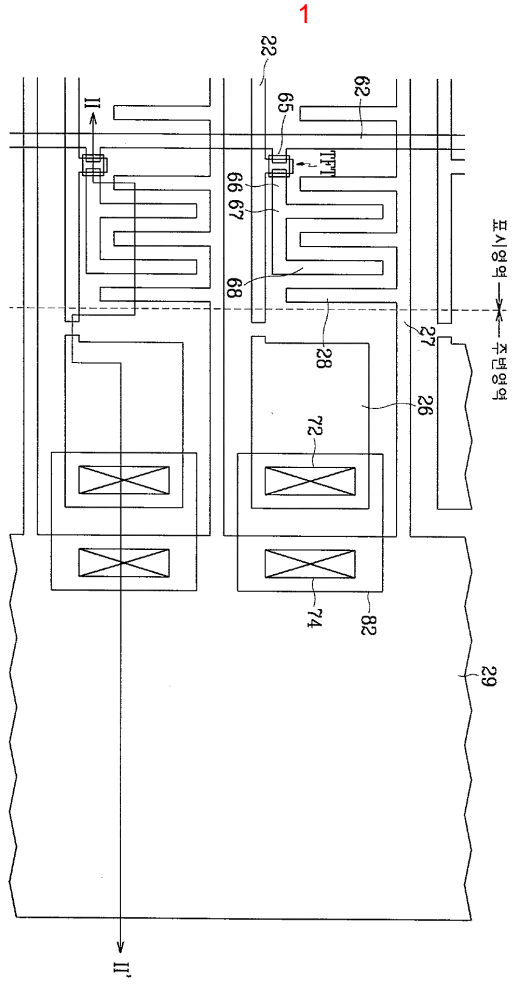
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8.

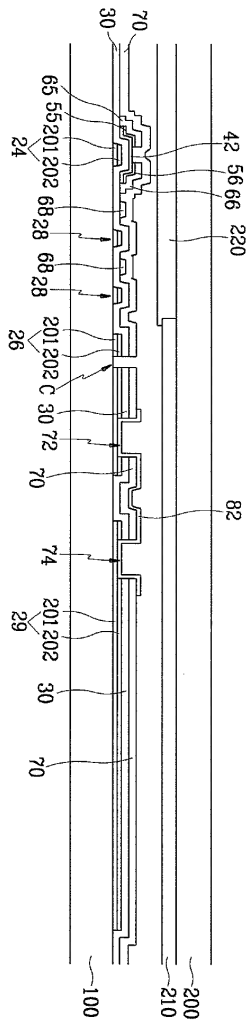
7 ,

200 μ m

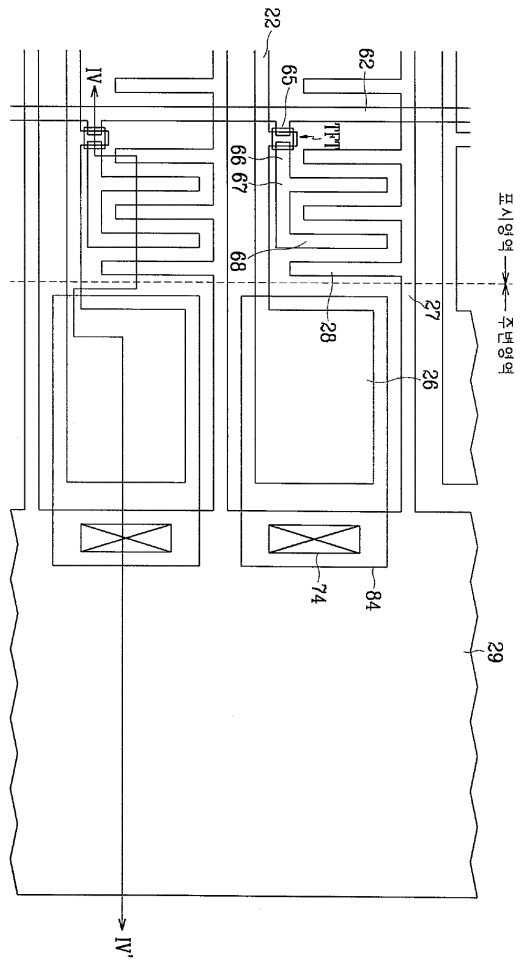
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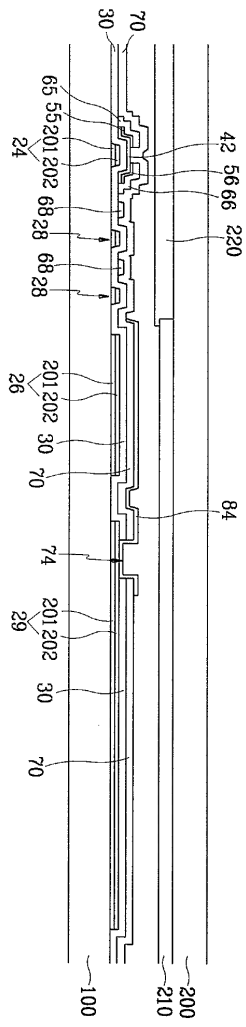
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专利名称(译)	液晶显示器		
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[标]申请(专利权)人(译)	三星电子株式会社		
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CPC分类号	G02F1/1309 G02F1/13458 G02F1/1345		
代理人(译)	KIM , WON GUN		
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摘要(译)

本发明涉及液晶显示器。并且为了使漏光现象最小化并且实现稳定的图像质量，测试焊盘与栅极线分离并且它连接到用于公共电压的图案或者屏蔽到用于公共电压的图案。在根据本发明的液晶显示器中，限定了在基板上布置多个像素区域的显示区域。像素区域包括栅极线，与栅极线交叉并形成的数据线，栅极线，薄膜晶体管以数据线电连接，以及像素电极线电连接在薄膜晶体管中，以及公共像素区域在像素电极线之间形成电极线。并且显示区域的外部公共电压零图案共同连接多个像素区域的公共电极线可以设置有测试焊盘，该测试焊盘连接到公共电压的图案并且与栅极线分离。光源，测试垫，横向电场，公共电压的模式。

