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(12) (B1)

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(11)  
(24)

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2003 06 25

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(22) 2000 12 13

(65)  
(43)

2002-0047535  
2002 06 22

(73) .  
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(72) 1-18202

876 348-1002

(74)

:

(54)

， ，  
1 1 ， 1 2  
1 .

4a

，

1  
2 1 - '  
3

4a			1	
4b	4a			
5a	4b	-	'	
5b	4b	-	'	
5c	4b	-	'	
6a	6c			1
7			2	
8a	7	-	'	
8b	7	-	'	
9			3	
41 :	42 :			
45 :	47 :			
100 :				100a :

가 CRT(cathod ray tube) CRT (flat panel display) (liquid crystal display : LCD), (electro luminescent display : ELD), (field emission display : FED), (plasma display panel : PDP) (cost) CRT (trace) LCD (LCD) TFT-LCD (parameter) 1 2 1 (C) (P) (G1,G2,...,Gn) (D1,D2,...,Dn) (G1,G2,...,Gn) (D1,D2,...,Dn) IC IC (Dp1,Dp2,...,Dpn) R( ), G( ), B( ) (3) 가 2 (P) 1 (1) (C) (D1,D2,D3) (Dp1,Dp2,Dp3)가 (2) (D

p1,Dp2,Dp3)  
6)

(P) Package)

IC (D1,D2,D3)  
가 50μm

(4)

(Dp1,Dp2,Dp3)  
TCP COF D2  
(6)

(Pitch)  
TCP(Tape Carrier

("W")  
, 200PPI(Pixel Per Inch)  
42μm 가 2 200PPI  
가

(Double Bank)

(Dp1,Dp3,...,Dpn - 1 ;n=2,4,6,...  
(Dp2,Dp4,...,Dpn) ( )

(P)

(G1,G2,...,Gn) (D1,D2,...,Dn)  
(D1,D2,...,Dn) (Dp1,Dp2,Dp3,...,Dpn - 1,Dpn)

1 (Single Bank)  
(Pitch) 가 200PPI

3 (Double Bank)  
(Modu

le)

(Compact) 가가 가

1 2 1 1 1 1

가 1, 2, 3 1

1 2 3 3

가 1 가 가

), ( )가

1 2

1 2 가

1 1 2 가

4a 1 ("A" ) 5a 4b 4a

5b 4b 5c 4b (C) (P) 1

(41) (C) (G1,G2,...,Gn) (G1,G2,...,Gn) (Gp1,Gp2,...,Gpn) (D1,D2,...,Dn)

(D1,D2,...,Dn) (D1,D3,...,Dn-1) (D2,D4,...,Dn) 1

(Dp1,Dp3,...,Dpn-1) (D1,D2,...,Dn) 1 (

Dp1,Dp3,...,Dpn-1) (Dp2,Dp4,...,Dpn) ,

TCP(Tape Carrier Package) COF(Chip On Film)

(51) "51a" 1 "51b" 2 (61) (61)

"51c" 3

4b (D2) (61) (D2) (C) (D1) (P)

4a 1 (Dp1,Dp3,...,Dpn-1) (Dp1) 2 (Dp2,Dp4,...,Dpn) ,

(D1,D2,...,Dn) 1 (Dp1,Dp3,...,Dpn-1) (Pitch)

가 가

1 (Dp1,Dp3,Dpn-1) (D1,D3,...,Dn-1) 가 2

(D2,D4,...,Dn) (100) 1 (51a) (C) (61) (Dp1,

Dp3,...,Dpn-1) (D2,D4,...,Dn) (100a) (overlap) 1

(D2,D4,...,Dn) (100a) (61) 2 (51b)

(61) 3 (51c) (47)

1, 2, 3 (51a,51b,51c)

5a 5c (41) (Gn)

(61) (61) 1 (41) (43) , (D

2) (D2) (D1) (Dp1) (D2)

(Dp1) (100) 1 (45) (45) (D2)

(51a) , (D2)

(100a) (61) 2 (51b) (61)

3

1

6a 6c

6a , 1 (41) (G1,G2,...,Gn) (71),

(61) (61) (P) (P) (C)

6b , (G1,G2,...,Gn) (61)

( (G1,G2,...,Gn) (71) (D1,D2,D3,...,Dn) / (72) (S/D)

(D1,D2,D3,...,Dn), / (S/D)

(D1,D3,...,Dn-1) (100) , (

100a) (61) , (61) 가 (

6c (G1,G2,...,Gn) (D1,D2,...,Dn)

(47) , (D1,D3,...,Dn-1) (100) 1

(51a) , (D2,D4,...,Dn) (100a) (61) (61) 3

(51b) , (61)

(51c) 2 1

7 2 , 1

1 (61) (D1,D3,...,Dn-1) (61) 1

(Dp1,Dp3,...,Dpn-1) 가 , 2 (61) 1

(Dp1,Dp3,...,Dpn-1) (61)

1 (Dp1,Dp3,...,Dpn-1) 2 (Dp2,Dp4,...,Dpn) 1 (Dp1

,Dp3,...,Dpn-1) (61) 2 1 (Fine Pitch) 2

8a 7 - ' 8b 7 - '

8a 8b , 1 (41) , (C)

(g1,G2,...,Gn) (61) (P) (61) (42) , (42)

(D1) (45) , (D1) (100)

1 (51a) (61) (61) 3 (51c) (D2) 2 (51

b) 3 - 9 3 1, 2 (

3

TCP(Tape Carrier Package) COF(Chip On Film)

3

(Fine Pitch)  
가

(57)

1. ; ; 1 ; 1 ;

2. 1 , 2 , , .

3. 1 , 2 , , .

4. 3 , 1

5. 3 , .

6. 2 3 , .

7. 2 3 , .

8. 2 , 1 .

9. 2 , .

10. 1 , 1 2

11. 1 , 가

12. 1 , .

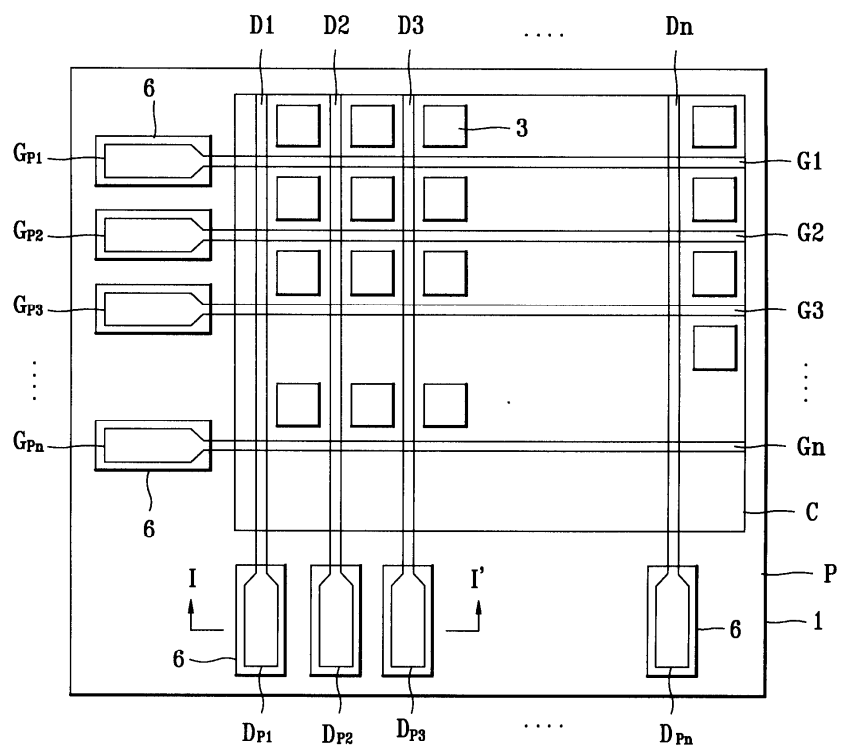
13. ; ; 가 가

14. .

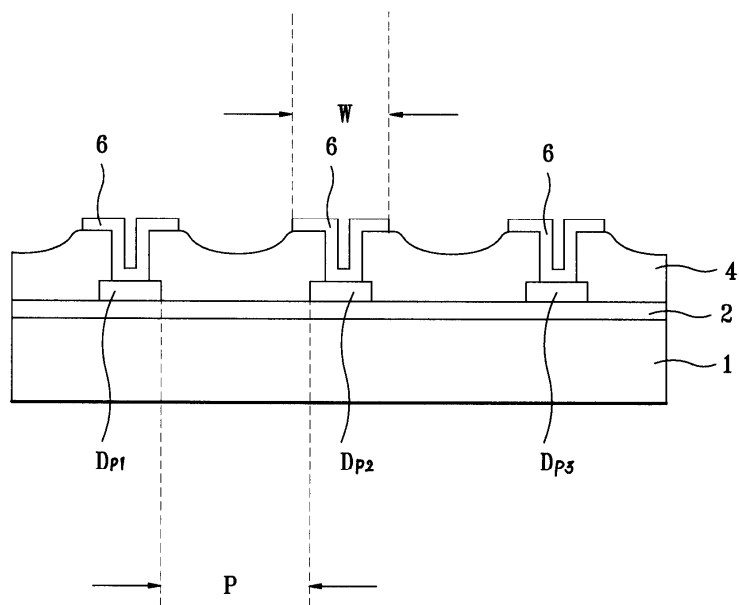
15. 13 , 2

16. 13 , 가 가 .
17. 13 , 가 .
18. 17 , .
19. 1 1 ; ; ; ; 가 1, 2, 3 ; 1 2 1 2 3 3 .
- 20.
- 21.
22. 19 , .
23. 19 , , .
24. 19 23 , 1, 2 3 .
25. 1 가 ; 가 ; ;
26. 25 , 가 가 .
27. 25 , , / , .

1

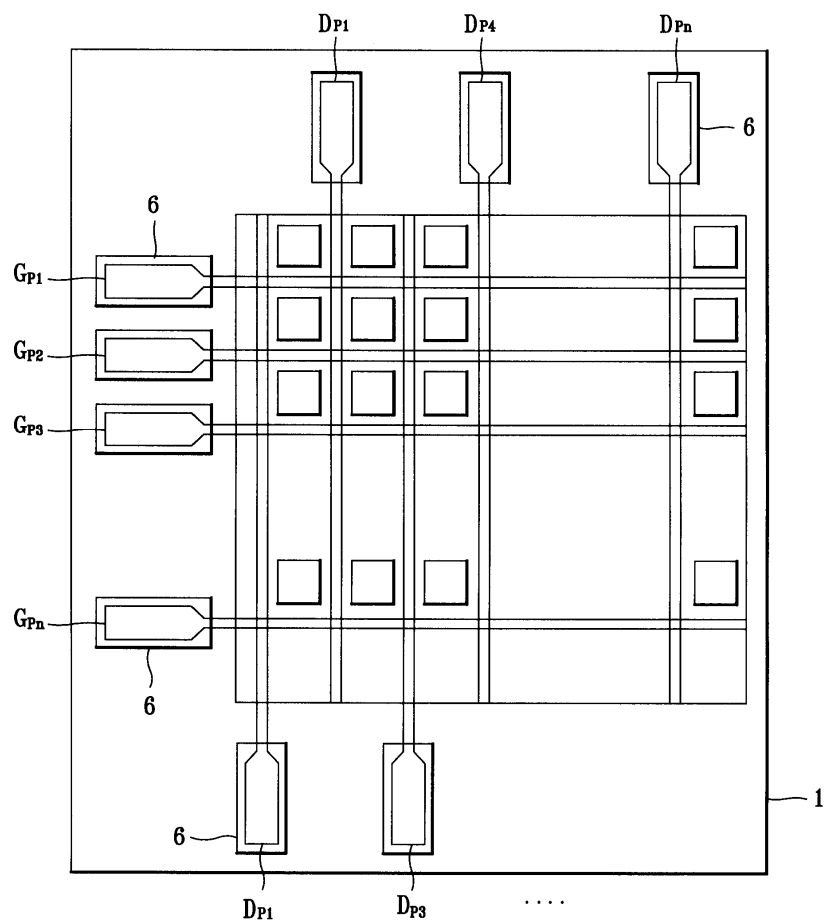


2

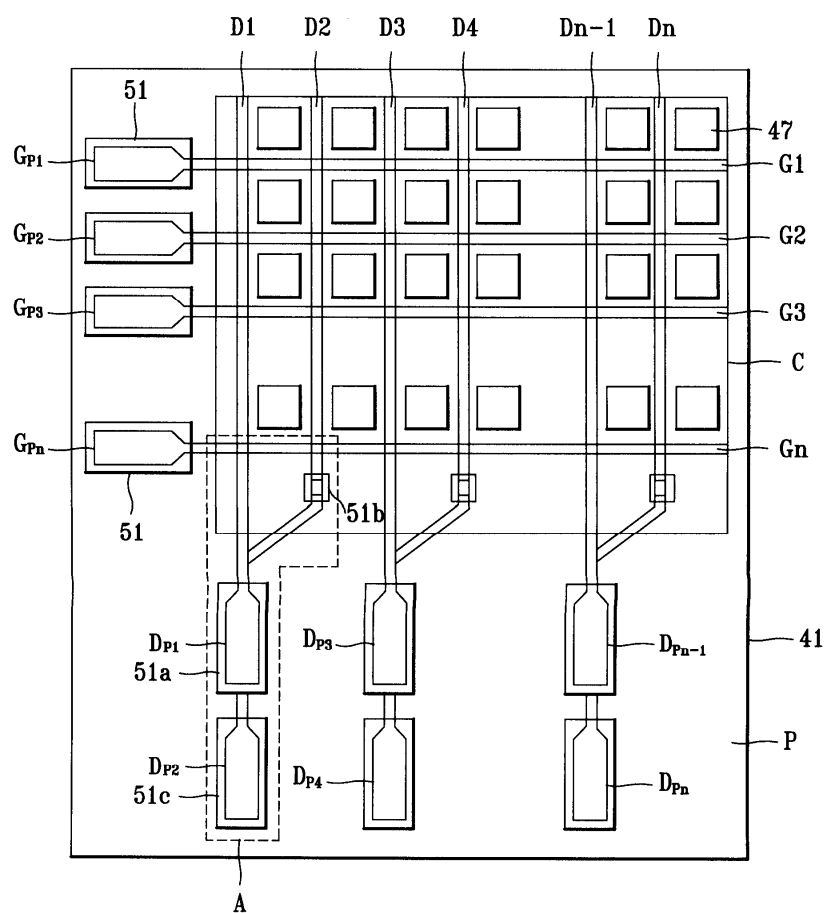


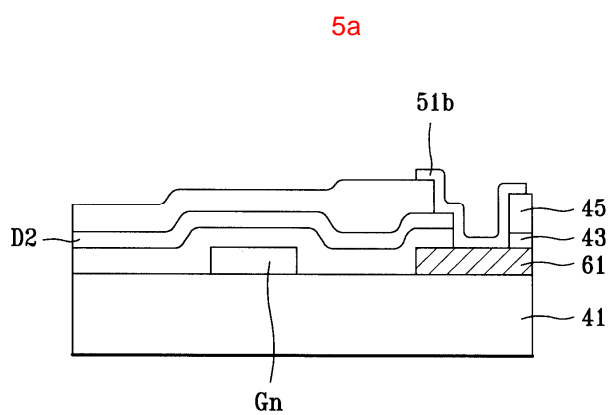
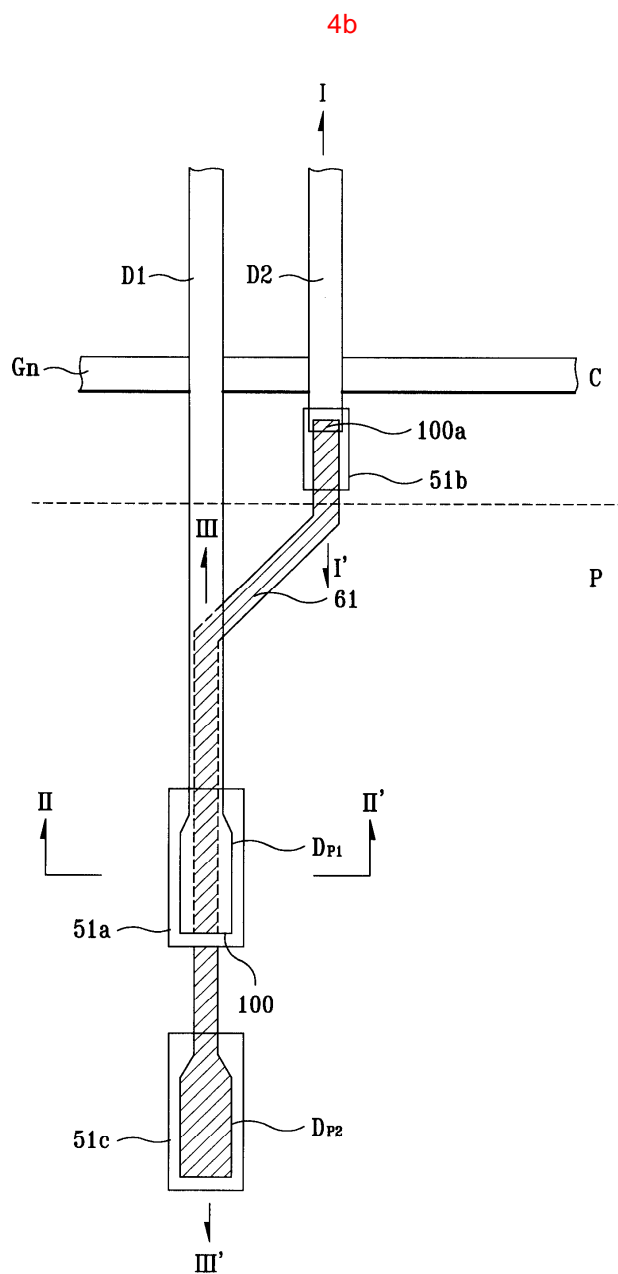


3

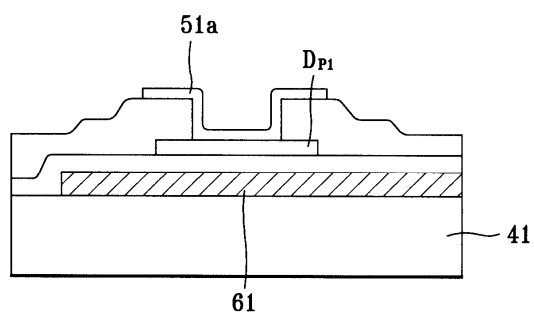


4a

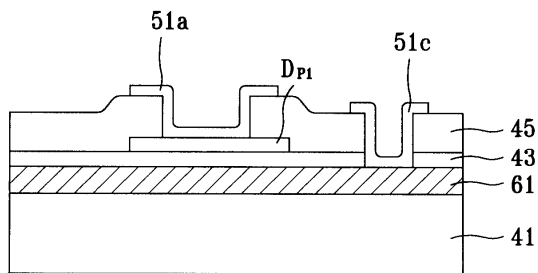




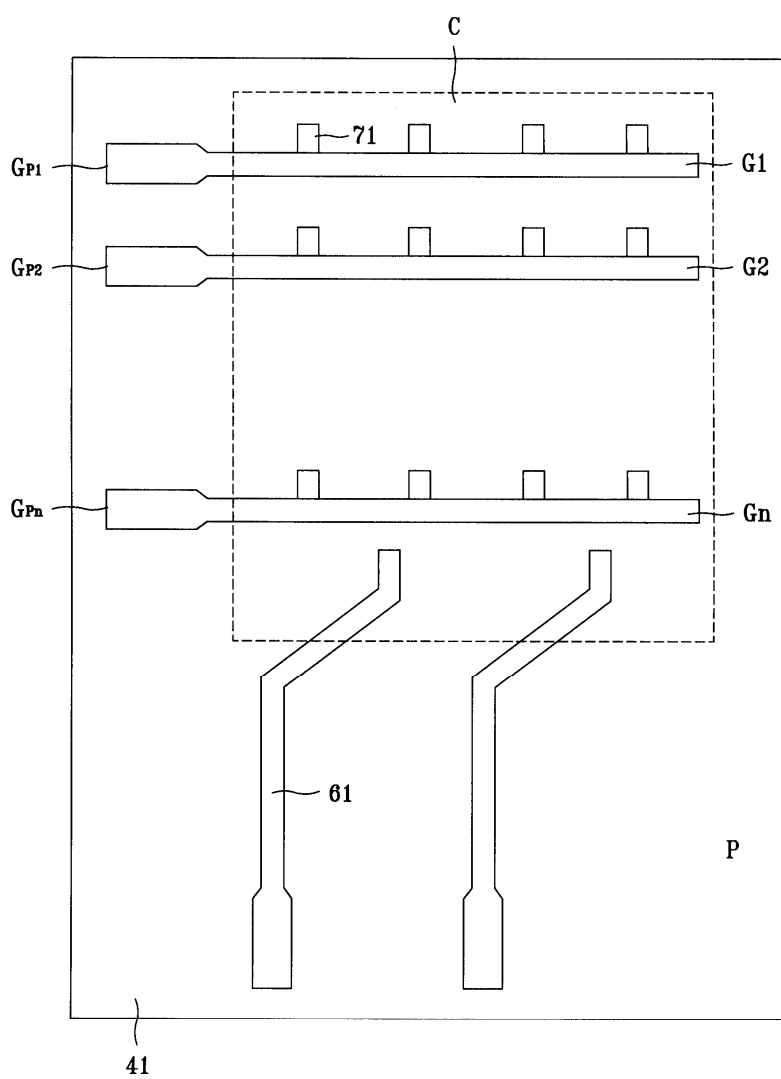
5b



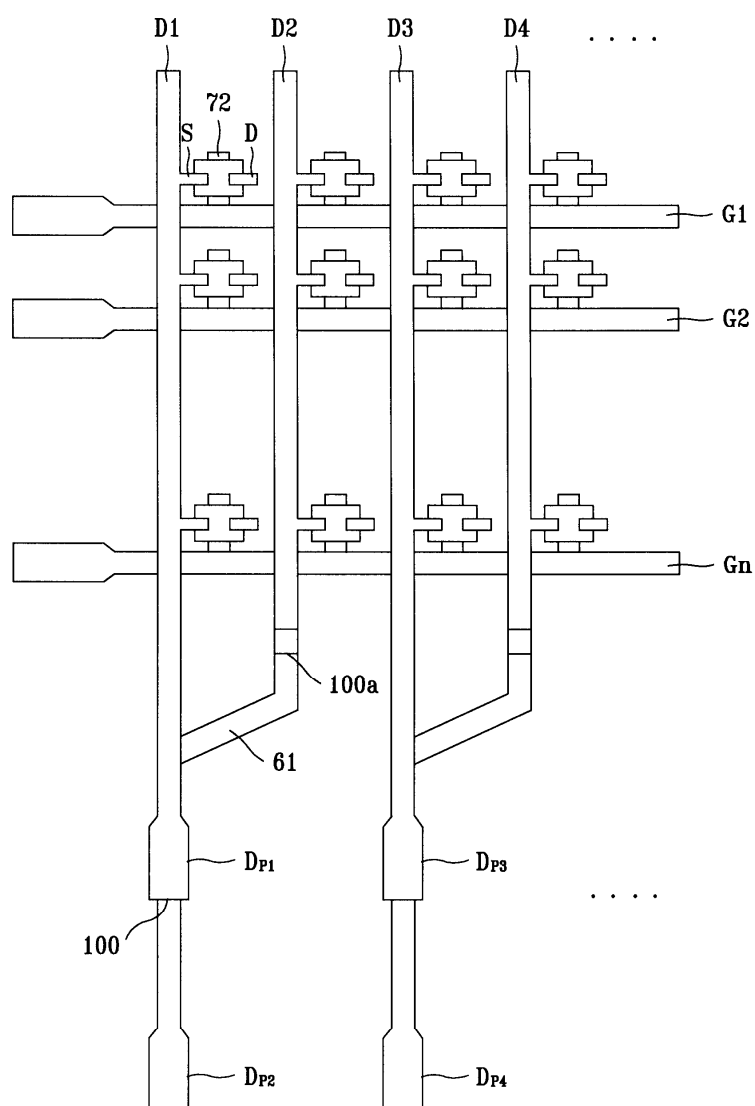
5c



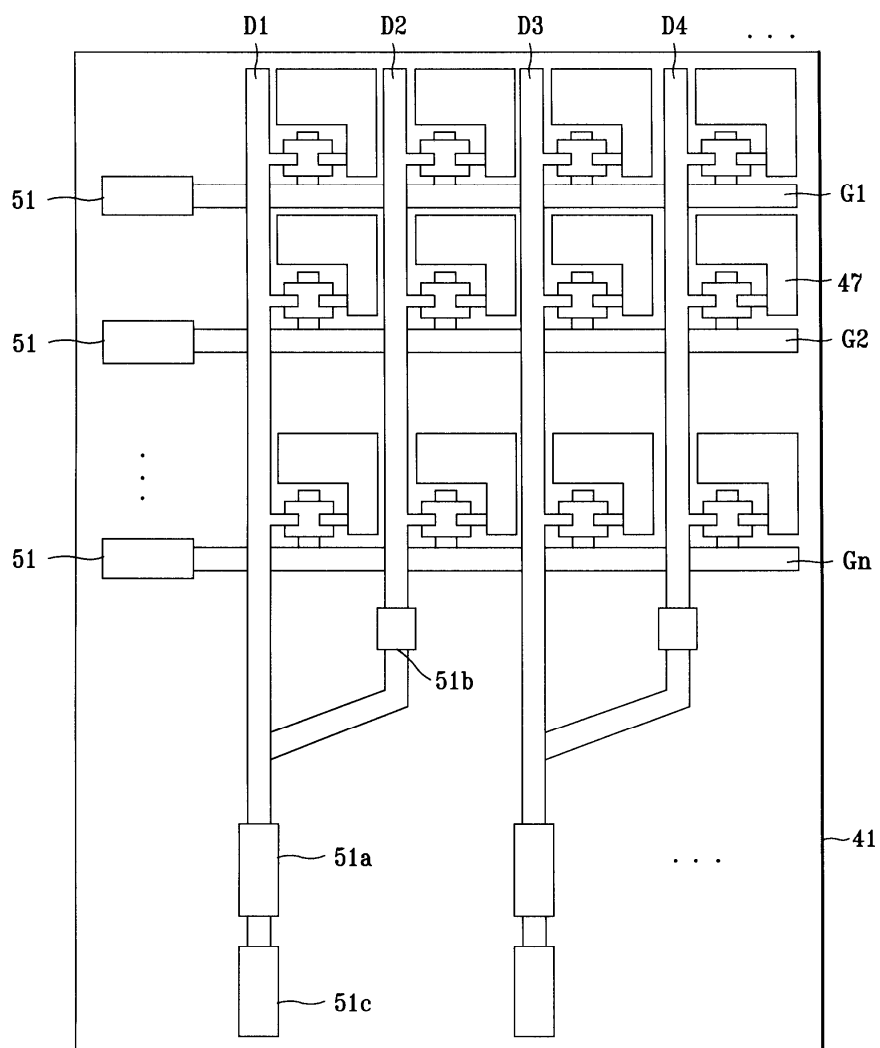
6a



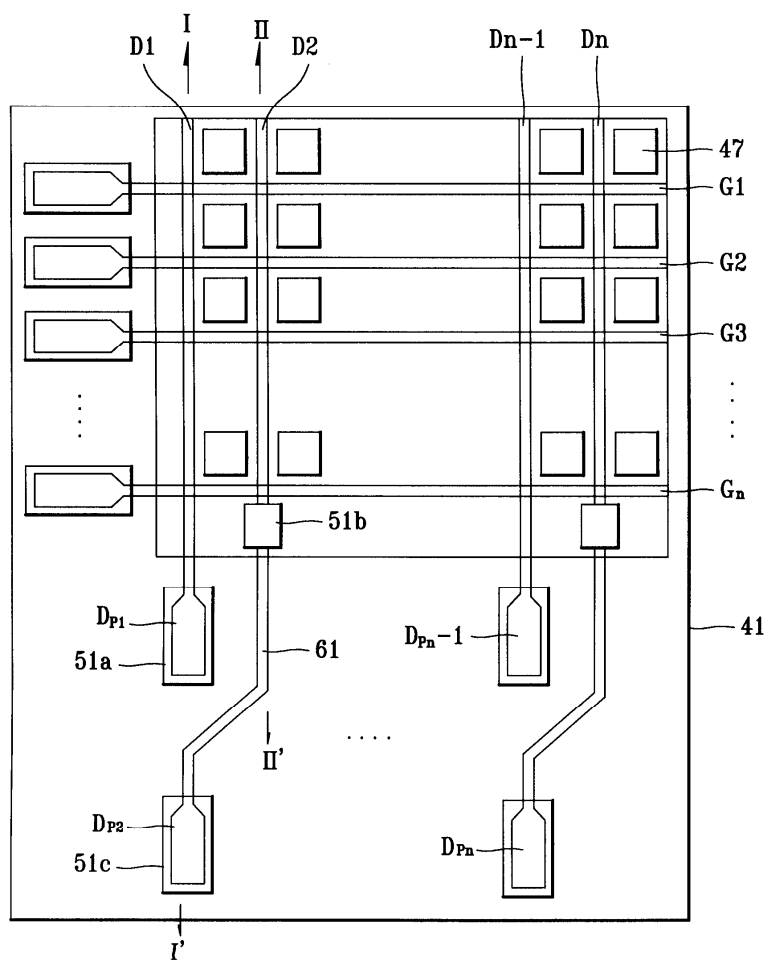
6b

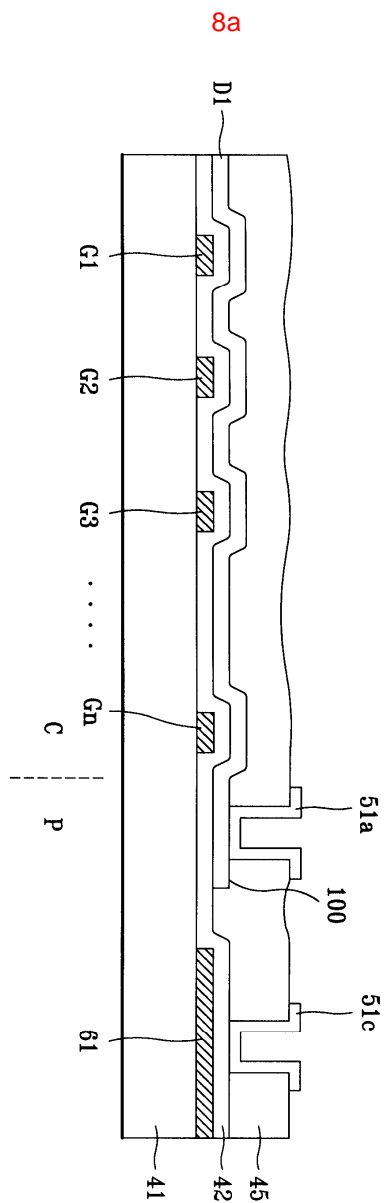


6c



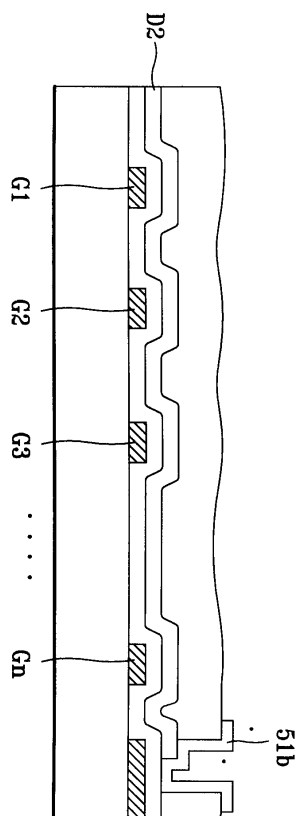
7



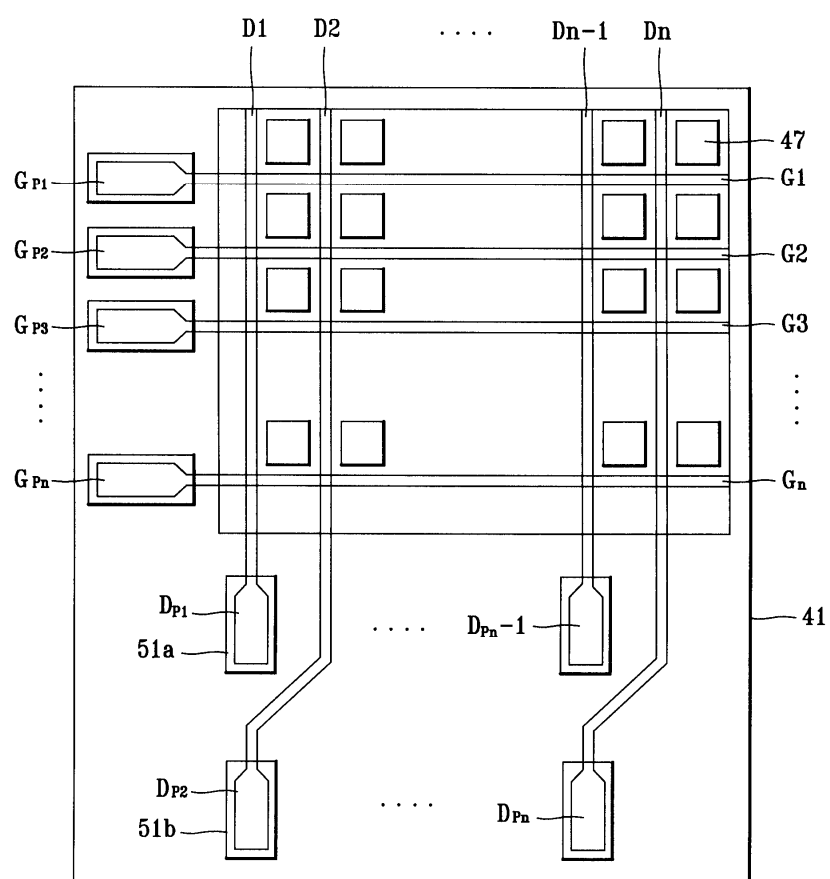




8b



9



专利名称(译)	液晶显示面板及其制造方法		
公开(公告)号	<a href="#">KR100390456B1</a>	公开(公告)日	2003-07-07
申请号	KR1020000076006	申请日	2000-12-13
[标]申请(专利权)人(译)	乐金显示有限公司		
申请(专利权)人(译)	LG显示器有限公司		
当前申请(专利权)人(译)	LG显示器有限公司		
[标]发明人	KIM WOOHYUN 김우현 YOO HONGSUK 유홍석		
发明人	김우현 유홍석		
IPC分类号	G02F1/1362 G02F1/1368 G02F1/1345 G09F9/30 G02F1/133 G09F9/35		
CPC分类号	G02F1/13458 G02F1/1345		
代理人(译)	金勇 新昌		
其他公开文献	KR1020020047535A		
外部链接	<a href="#">Espacenet</a>		

#### 摘要(译)

焊盘区域 ( P ) 中的一组奇数数据焊盘 ( Dp1 , Dp3 , ... , Dpn-1 ) 从单元区域 ( C ) 中的相应奇数数据线 ( D1 , D3 , Dn-1 ) 延伸。 。 一组偶数数据焊盘 ( Dp2 , Dp4 , ... Dpn ) 沿奇数数据线的方向形成在奇数数据焊盘的下方并与其平行，并与相应的偶数数据线 ( D2 , D4 , ... ) 连接。 。 DN ) 。 布置在焊盘区域中的数据焊盘形成单个堤结构。 包括用于液晶显示面板制造方法的独立权利要求。

