

6

7

8a
가

8b

,

< >

130 : 136 :

138 : 140 :

142 : 200 :

T : 1 T2 : 2

P : C_{LC} :

C_{ST} :

가 (display)

play : LCD)가 (Cathode-Ray Tube : CRT) (Liquid Crystal Dis

가 가 가 가

가 1 가 2 가 (Active Matrix LCD : AM-LCD)

(10) (24) (20) (32)
(30) (50) 가
(26) (20) (1) (22)
(50) 가 (24) (22)

(26) (cover) (30)

36) (30) (1) (32) (40), (T) (matrix) (P) (36) (40) (32) (24) (T) (32) (C_{LC}) (P) (storage capacitor : C_{ST})가 (C_{LC}) (P)

(20) (30) 1 (28) 2 (34)

(30) 가 (36) (38)가 (40) (scan) , 가 (42)가 (40) (T) (on) , (40)

3 , 1 2

(P) (s) , (T) (36) (g) , (40) (C_{LC}) (d) (C_{LC}) (T)

(30) (10) (frame) ,

(38) G1 Gm D1 (scan) (42) (40) (全) (40) (P) T1 Tm (C_{LC}) D1 Dm 가 (on) D1 Dm Gm-1 Gm-1 가 (P) (C_{LC}) , 1 2 (28, 3)

4) (P) (C_{LC}) , (22) , , 1 2 (28, 3)

60 가 (10) , (10) (60) , (10) (20) , (50) (30) (50) 가 (封函) , (36) (30) (10) (36) 가 (36)

가 4a 4b 3 Gm-1

T1 Tm Gm-1 (G(N))가 (T) T1 Tm

D(N) T1 Gm-2 Tm D(N+1) Gm-1 D(N-1) Gm-1 Gm

(rising) (G(N)) (D(N)) (falling)

Gm-1 (G(N))가 (Vth) T1 Tm
 가 (on) (D(N)) (C_{LC}) (Vth)
 가 (off) (C_{LC}) (G(N))가 (D(N)) T1 Tm

4a 4b Ta Tb (D(N)) (Vth) (C_{LC}) T1
 (charging time) (off time)

Tm (G(N)) (Vth) (D(N)) (D(N)) (D(N+1)) (G(N))가 T1
 Tm (noise)

(G(N)) (Vth) T1 Tm (slightly turn on)
 가 (Vth) 가

가 (G(N)) (D(N)) Gm-1 T1 Tm
 (C_{LC}) Gm (D(N), D(N+1))가 (D(N+1))가

(G(N))가 (D(N)) T1 Tm 가

가 4a 4b Gm-1 T1 Tm (36)
 (G(N))

(N)가 T1 (G(N)) Gm-1 Tm (G
 (RC Delay)

Gm-1 Gm (D(N+1))가
 (G(N))가 (D(N)) (Vth) (D(N))가

)
)

Tb (G(N)) Ta 가 Gm (Vth (D(N+1))

Ta 가 (C_{LC}) (D(N))가

가 (flicker)

(36) (36) (gate modulation) 가 가 가

가

(matrix)

1 2 1 2

2 2

1 2

5 6 가 (Active Matrix LCD : AM-LCD)

(130) (110) (124) (150) (120) (132)

가 (120) (124) (1) (122) (122),
 , , (130) (12)

6) (124) (Vcom) 가 .

(P) (130) (136) 1 (T) (1) (140) (132) (P) ,
 (150) , (124) (132) (C_{LC})
) (P) 1 (C_{ST})가 2 (C_{LC}
) ,, (110) (128) (134)
 1 2 (128, 134) (film) .
 1 (130) 가 (136) (138)가
 (T) (on) , (off) 가 (140)
 (142)가 ,
 , (150) (110) 가 (160)가 (封函) ,
 , (200) (130) 1 2 (T) (200) ,
 (200) (136) 2 (T')가 .
 (P) (130) 1 (T) , 가 (200) (136)
 2 (T') .
 (130) (200) 가 (136) 2 (T') (138)
 .
 1 (T)
) 2 (T') (g) , (200)
 (d) (s) , (138)
 2 2 .
 , 7 가 5 6 Gm-1
 ,
 Gm-1 , 1 (T)
 T1 Gm-1 Tm .

D_m , T_1 , T_m , G_{m-1} , (C_{LC}) , (s) , (d) , (g) , D_1 , (C_{LC})

T'_m , (200) , G_{m-1} , G_{m-2} , (200) , (d) , G_{m-1} , (200) , (s) , (g) , G_{m-2}

(138) 1 (scan) (T) , G_1 , D_1 , G_m , D_m

m , G_{m-1} , (C_{LC}) , D_1 , D_m , T_1 , T , (P)

G_{m-1} , T'_m , T_m , G_{m-2} , T'_m

G_{m-2} , 1 , 가, 가, G_{m-2}

1

8a 가, 8b, 7, G_{m-1} , 1

G_{m-1} , T_1 , T_m , 가, T_m , 가, 8a, T_1 , 가

가, $(G(N))$, $(G(N))$, $(G(N))$, $(D(N))$, $(D(N))$, $(D(N))$, $(D(N))$, $(D(N))$, 가, 8b, T_m , 가

G_m , $D(N-1)$, $D(N+1)$, 가, G_{m-1} , 가, G_{m-2} , G_{m-1} , $(G(N+1))$

$(G(N))$, $(D(N))$, $(D(N))$, (falling), (rising)

G_{m-1} , 가, (C_{LC}) , $(D(N))$, $(G(N))$ 가, (V_{th}) , $(D(N))$, T_1 , T_m , 가, $(D(N))$, $(G(N))$ 가, (C_{LC}) , (V_{th}) , T_1 , T_m

8a, 8b, T_a , $(G(N))$, $(D(N))$, (V_{th}) , T_1 , (C_{LC}) , T_m

가, T_b , 가

, (G(N))가 (G(N+1)) (G(N)) (Vth) (D(N+1)) (D(N)) (D(N))

1 Gm-1 Gm-2 1 (G(N))가 가 T'm 2

(G(N)) 8a 8b

, Gm-1 (G(N)) 1 T'm 가 Gm-2

G1 Gm

/ 가

1 2 가

1 2

(flicker) 가

가

가 2 가

(57)

1.

;

;

(matrix)

;

;

;

;

1

1

2

2.

1

2

3.

2

2

4.

1

2

5.

1

6.

1

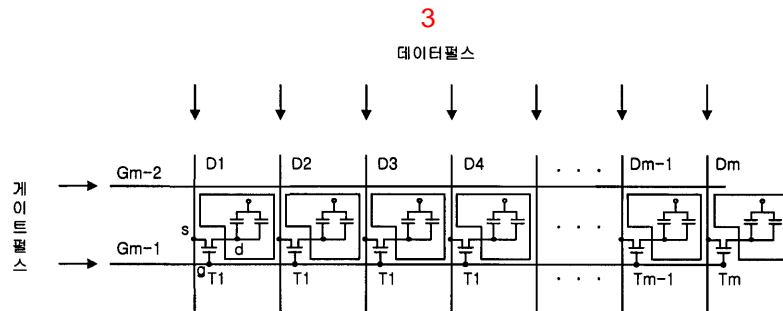
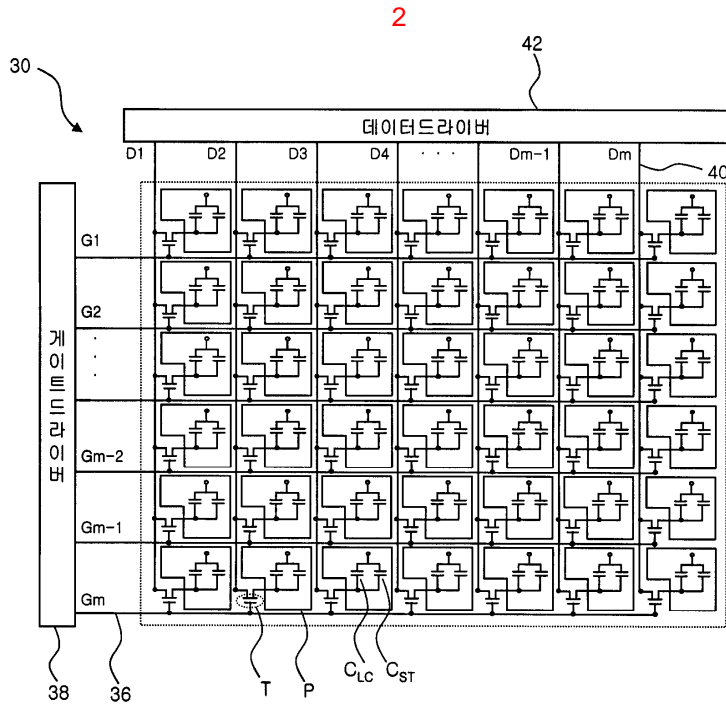
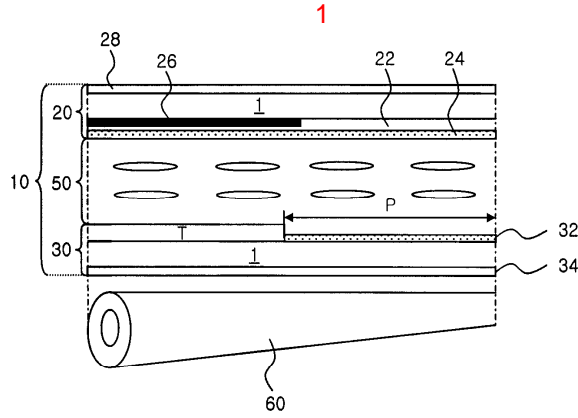
7.

1

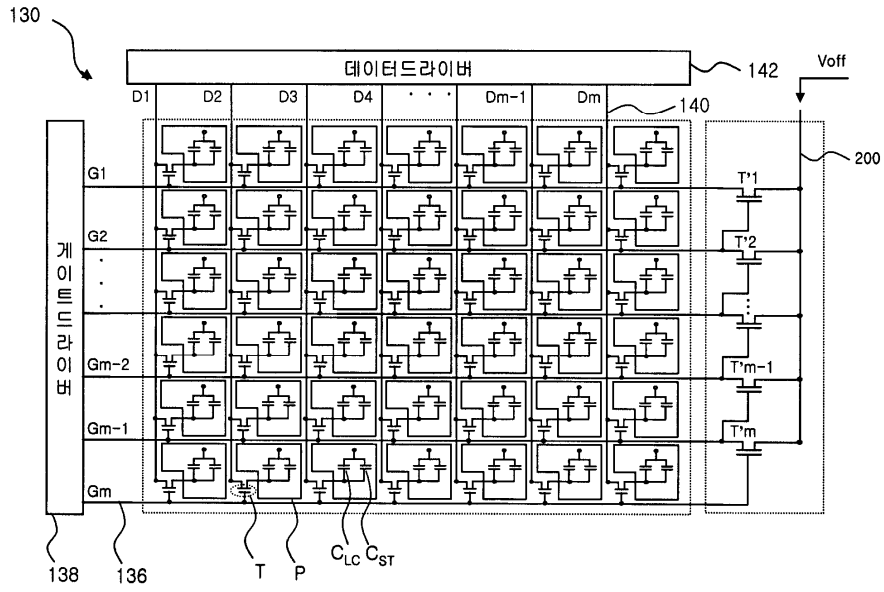
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1

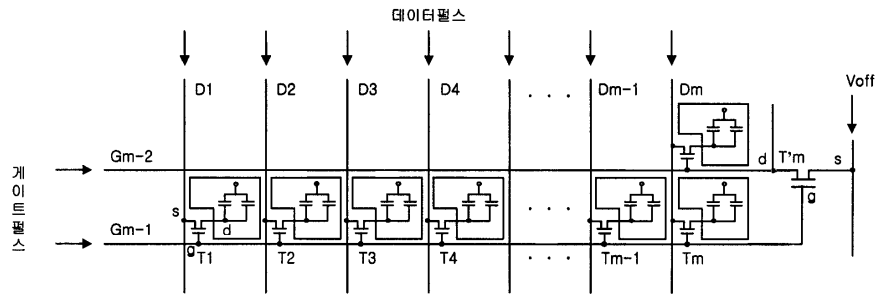
8.



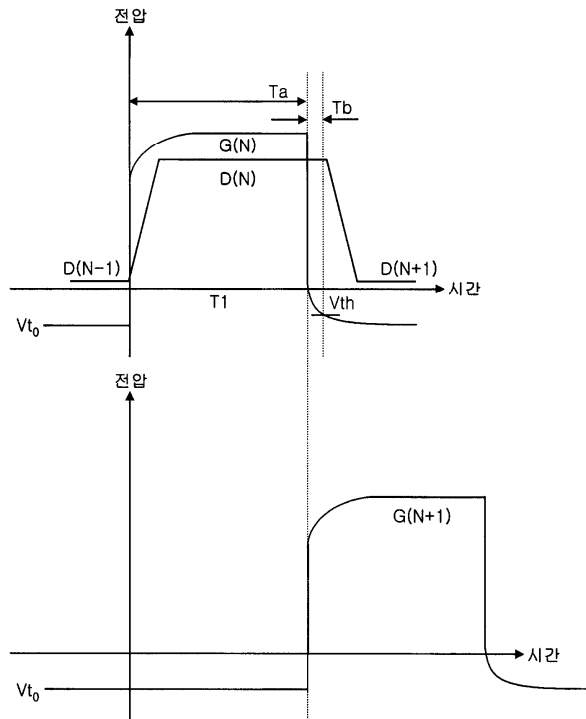
6



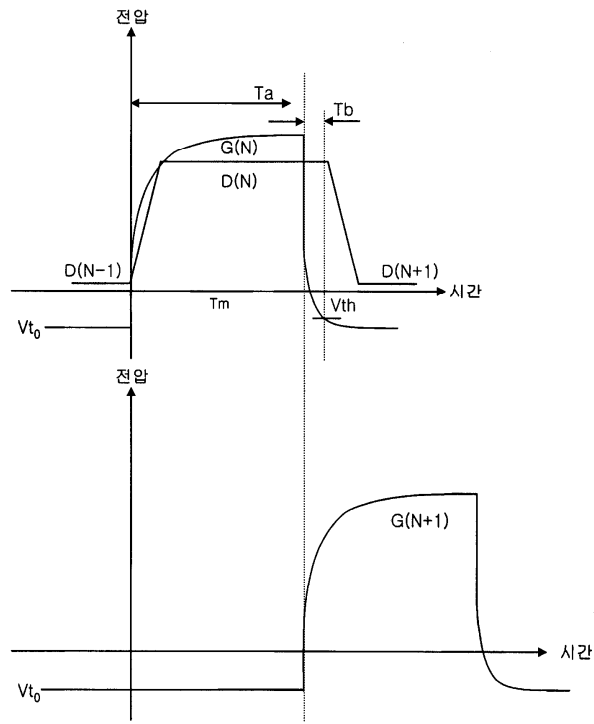
7



8a



8b



专利名称(译)	一种用于液晶显示器的液晶面板的下阵列基板		
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[标]申请(专利权)人(译)	乐金显示有限公司		
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外部链接	Espacenet		

摘要(译)

本发明提供的阵列面板包括相应多个的第二薄膜晶体管，连接透明基板的像素和平行于透明基板布置的多条栅极线，矩阵形成相应的多个定义的多个并行的多个并行数据线每个像素具有多个栅极线一端的薄膜晶体管包括传送栅极驱动器连接的数据驱动器，并且通过单向扫描将栅极脉冲连续地传送到每个栅极线。并且多个数据线数据脉冲多个数据线一端连接，每个像素具有的像素电极和栅极连接到栅极线，源极连接到数据线和连接到像素电极的漏极和馈线输出薄膜晶体管的截止电源和互连状态馈线和多条栅极线它设置在透明基板中，它与栅极线和长度和宽度一致，它是包括液晶面板的下阵列基板用于液晶显示器。

