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(19) **United States**(12) **Patent Application Publication**
CHANG(10) **Pub. No.: US 2013/0242215 A1**(43) **Pub. Date: Sep. 19, 2013**(54) **LIQUID CRYSTAL DISPLAY HAVING
SHIELDING CONDUCTOR****Publication Classification**(75) Inventor: **Jong Woong CHANG**, Asan-si (KR)(51) **Int. Cl.**
G02F 1/136 (2006.01)(52) **U.S. Cl.**
USPC **349/41**(73) Assignee: **Samsung Display Co., Ltd.**,
Yongin-City (KR)(57) **ABSTRACT**

Provided is a liquid crystal display including, on an insulation substrate having a polygonal display area and a peripheral area surrounding the display area a first signal line, a second signal line crossing the first signal line, a plurality of switching elements connected to the first signal line and the second signal line and disposed in the display area, a plurality of pixel electrodes each connected to the switching element and disposed in the display area, and a shielding conductor disposed in the peripheral area and extending along at least one side of the polygonal display area.

(21) Appl. No.: **13/551,372**(22) Filed: **Jul. 17, 2012**(30) **Foreign Application Priority Data**

Mar. 19, 2012 (KR) 10-2012-0027723

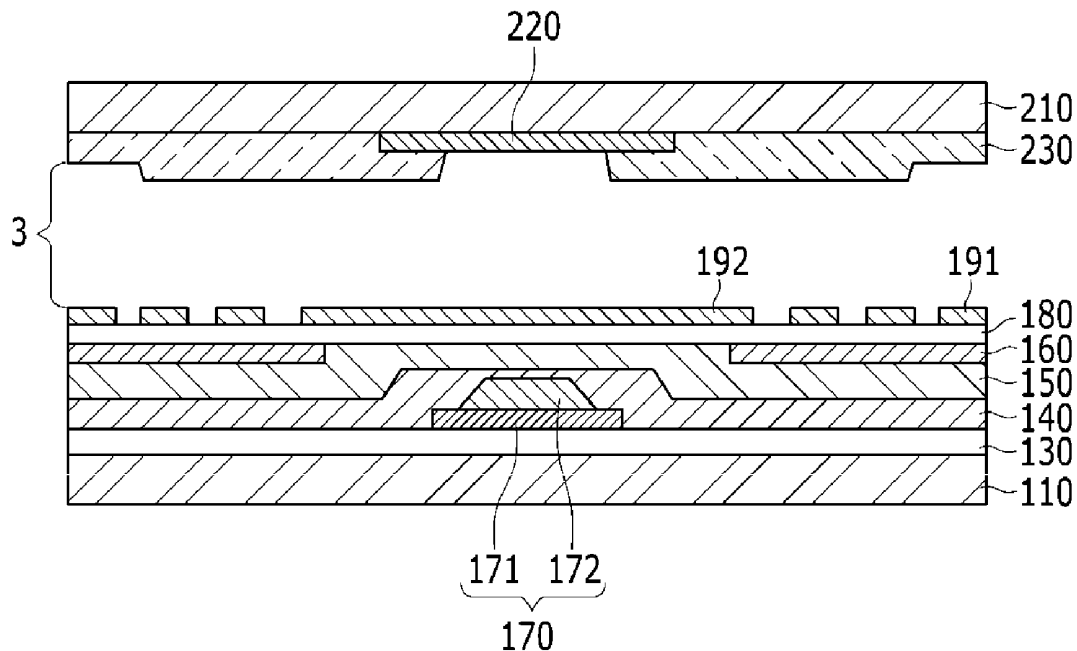


FIG. 1

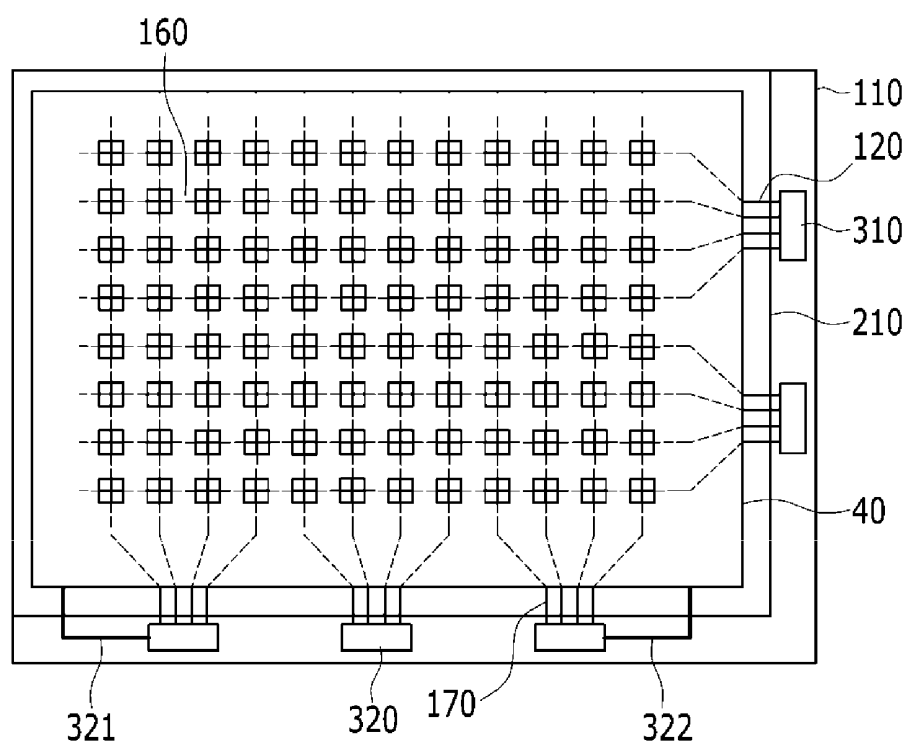


FIG. 2

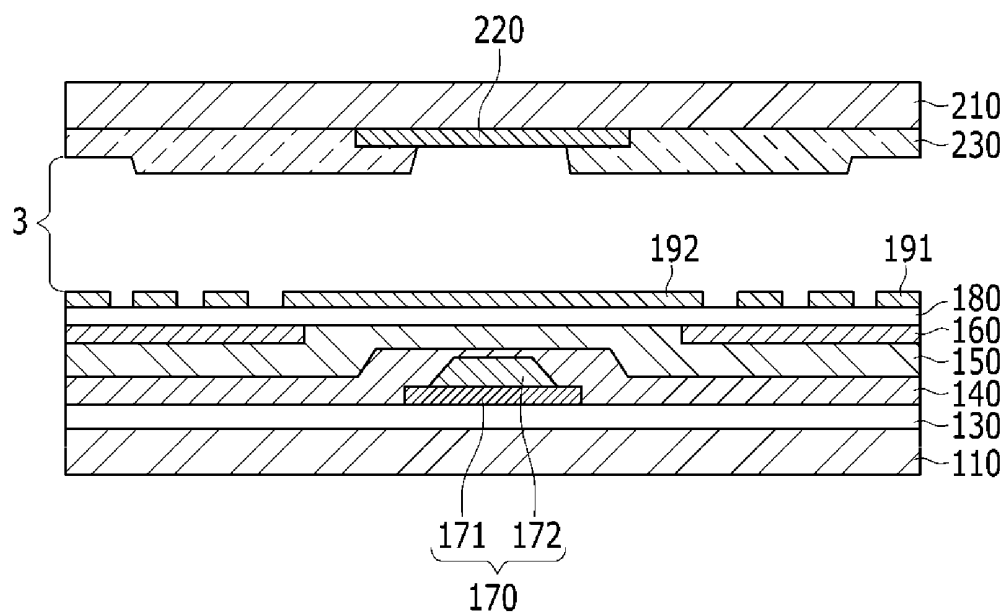


FIG. 3

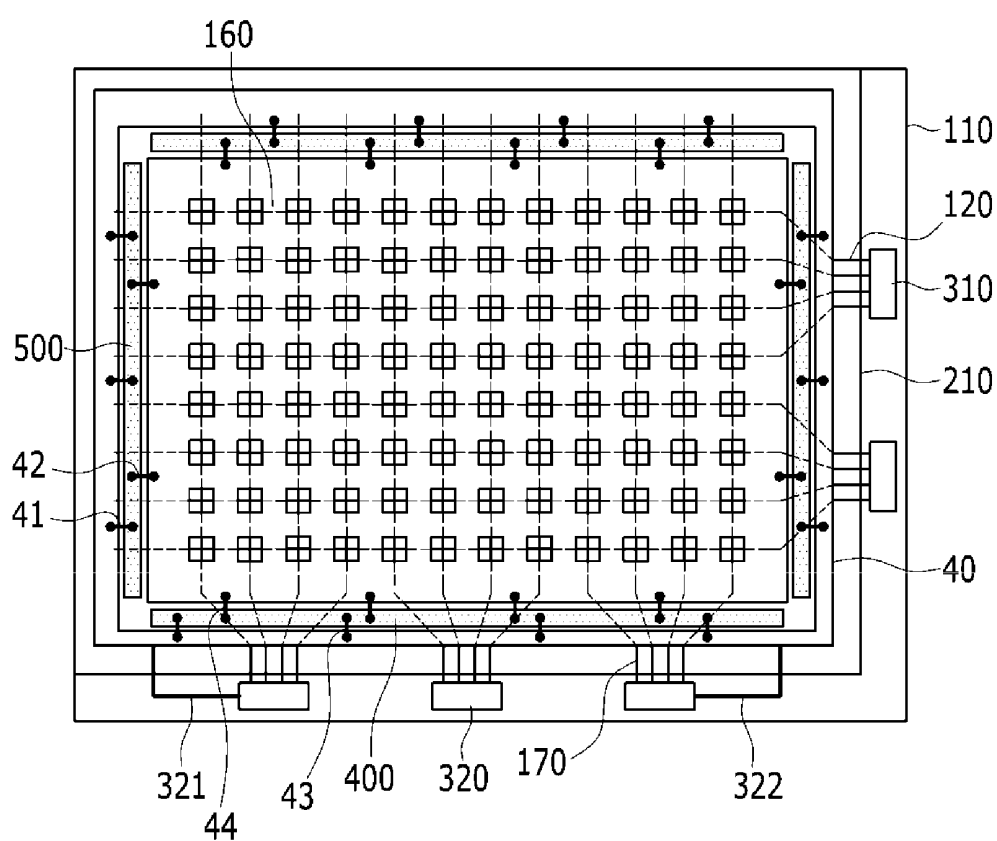


FIG. 4

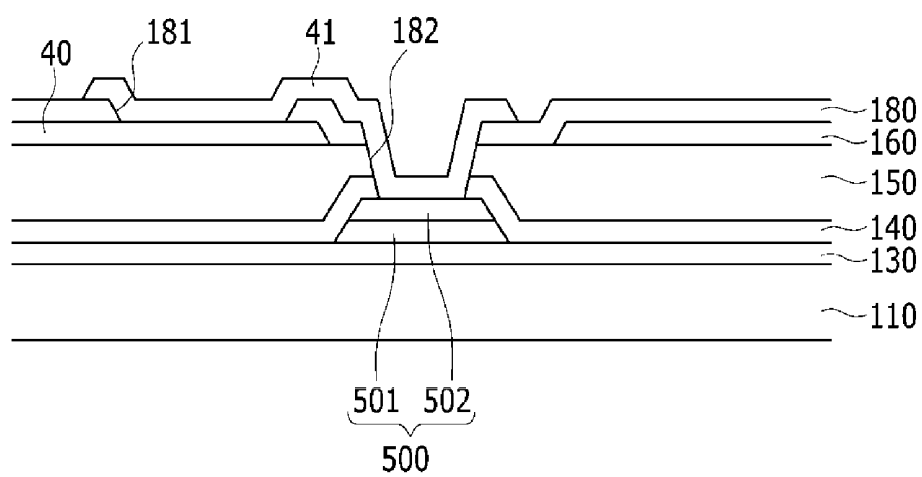


FIG. 5

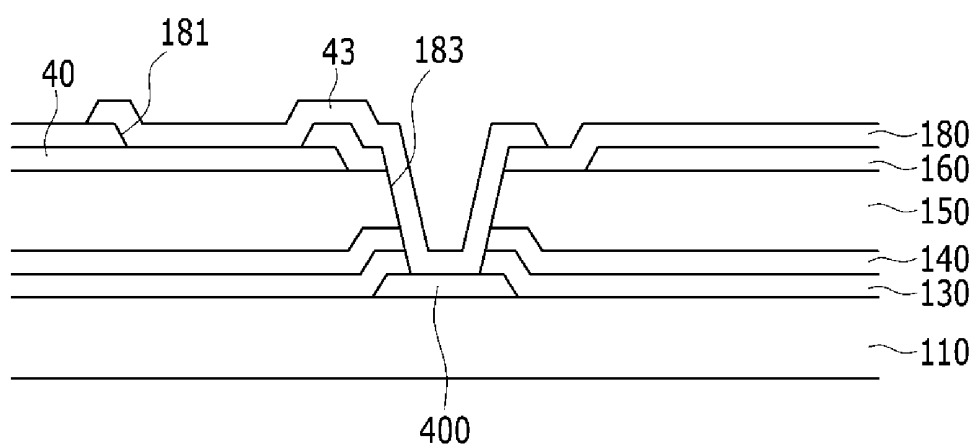


FIG. 6

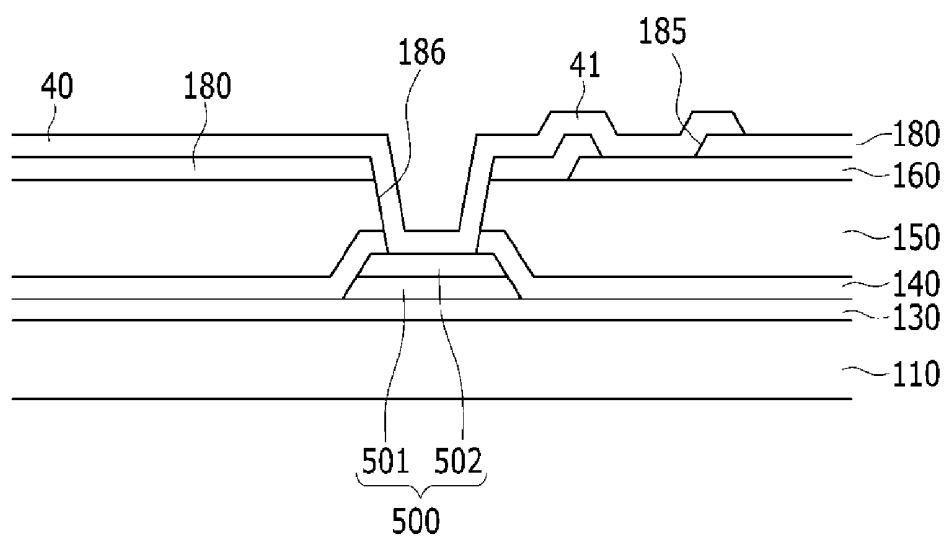


FIG. 7

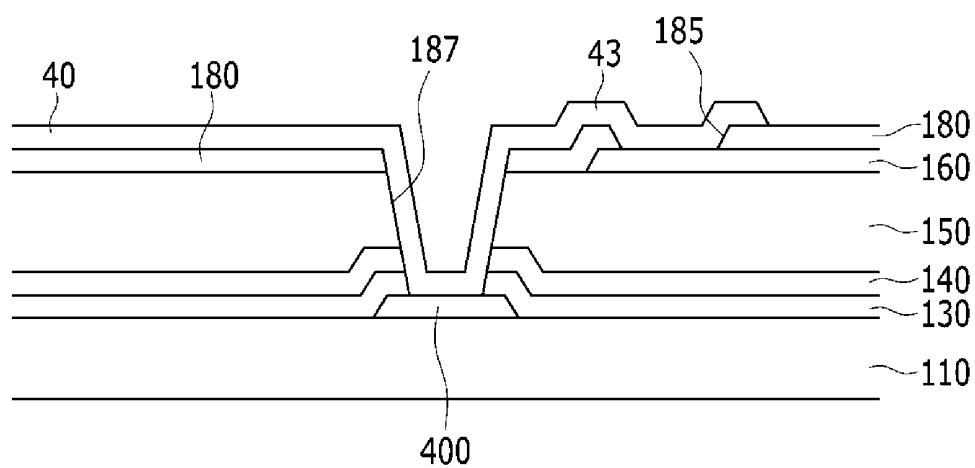


FIG. 8

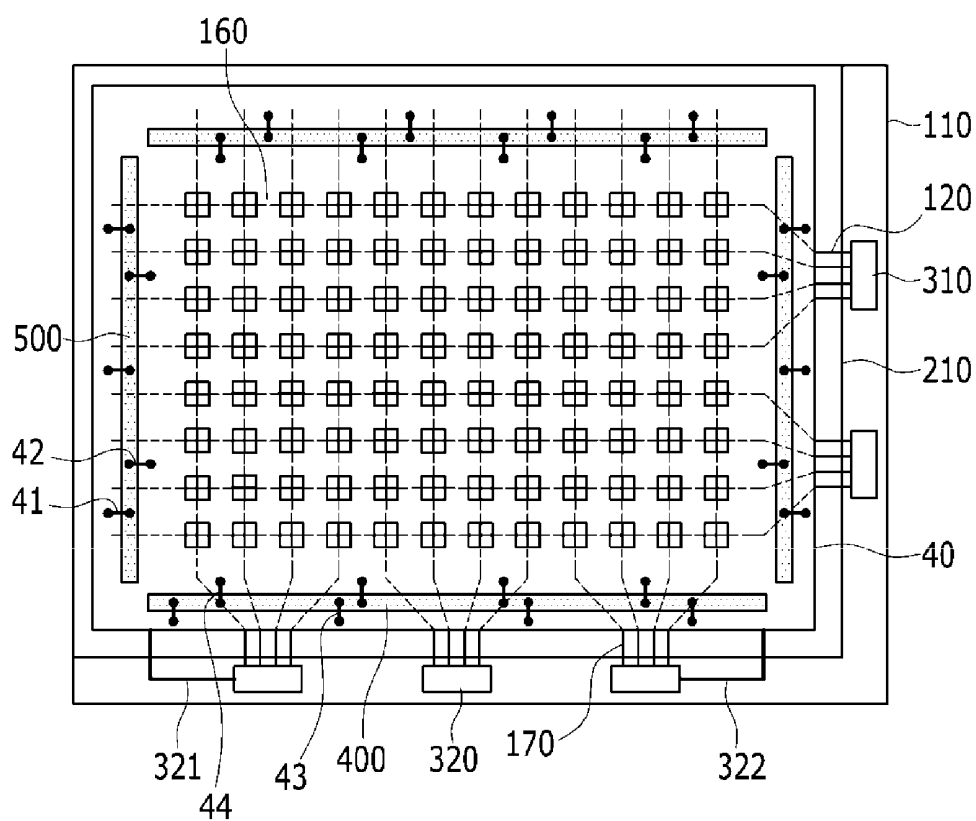


FIG. 9

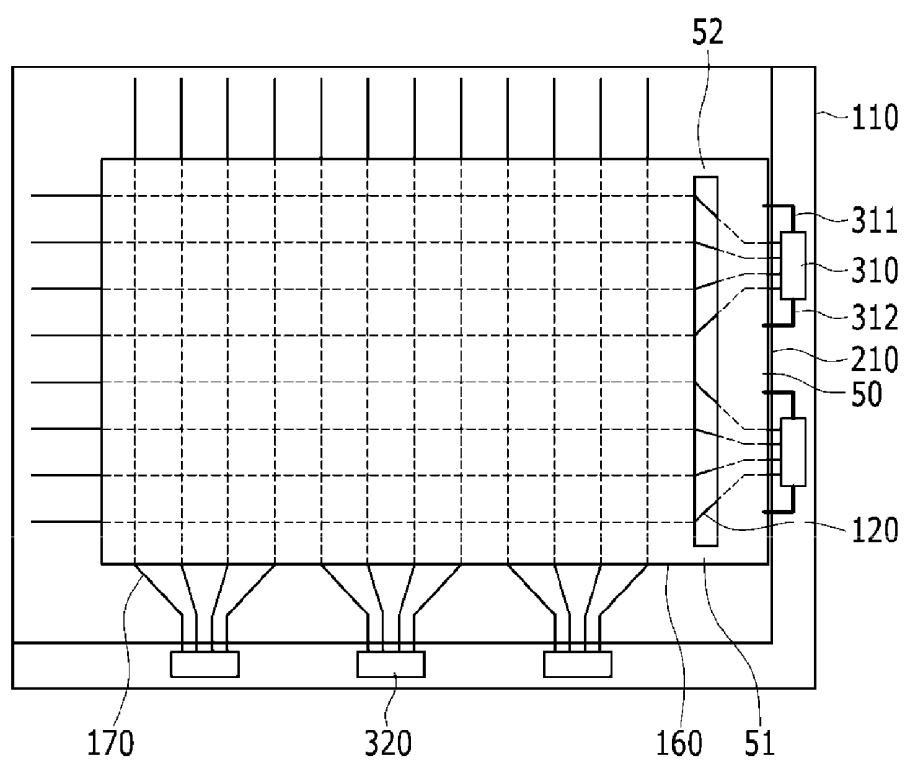


FIG. 10

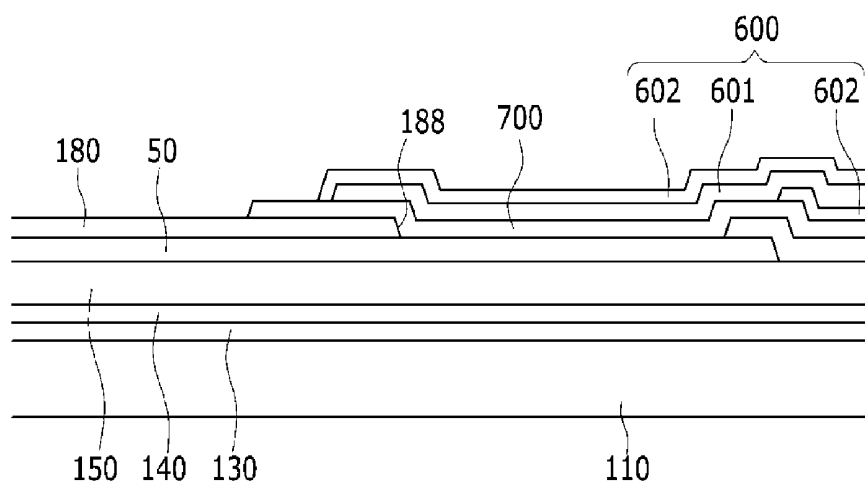


FIG. 11

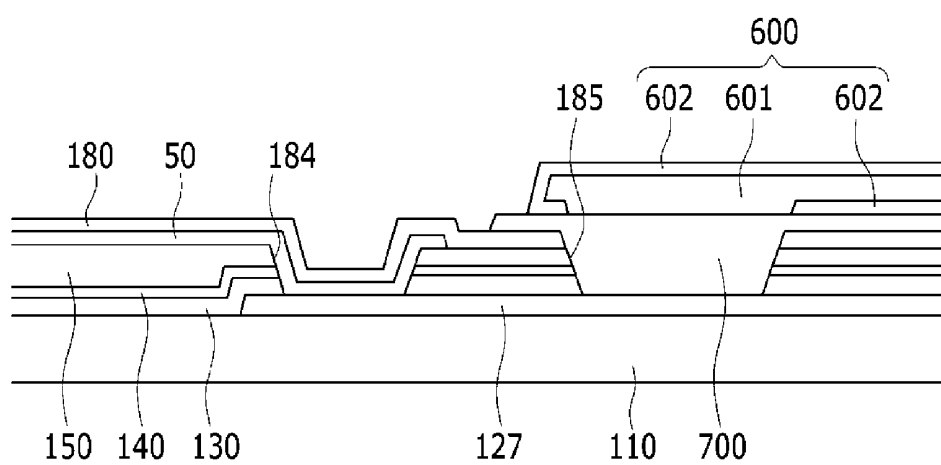


FIG. 12

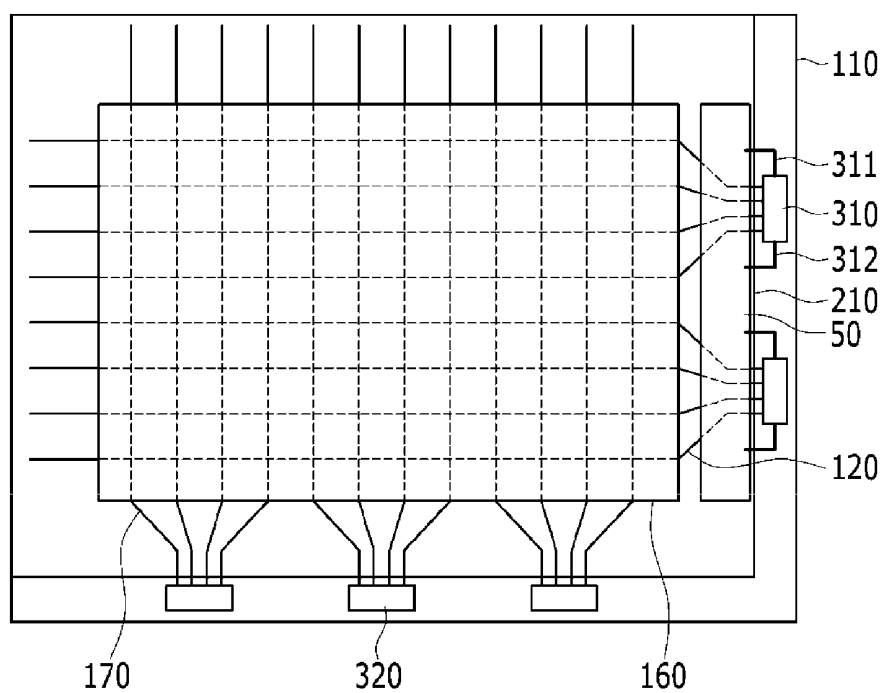


FIG. 13

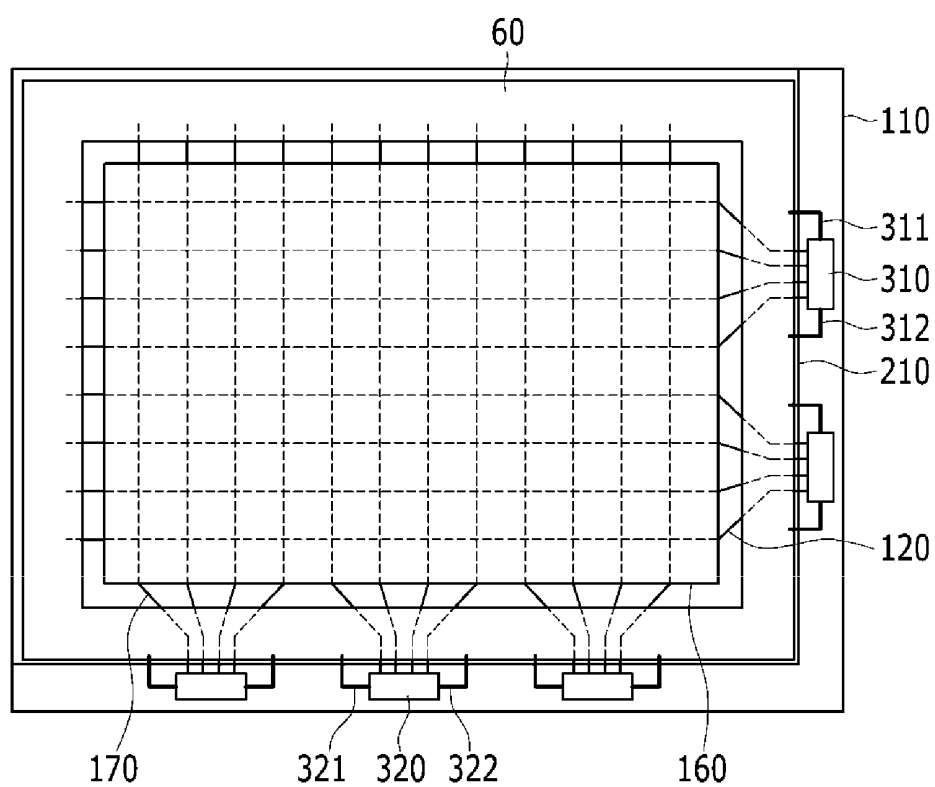


FIG. 14

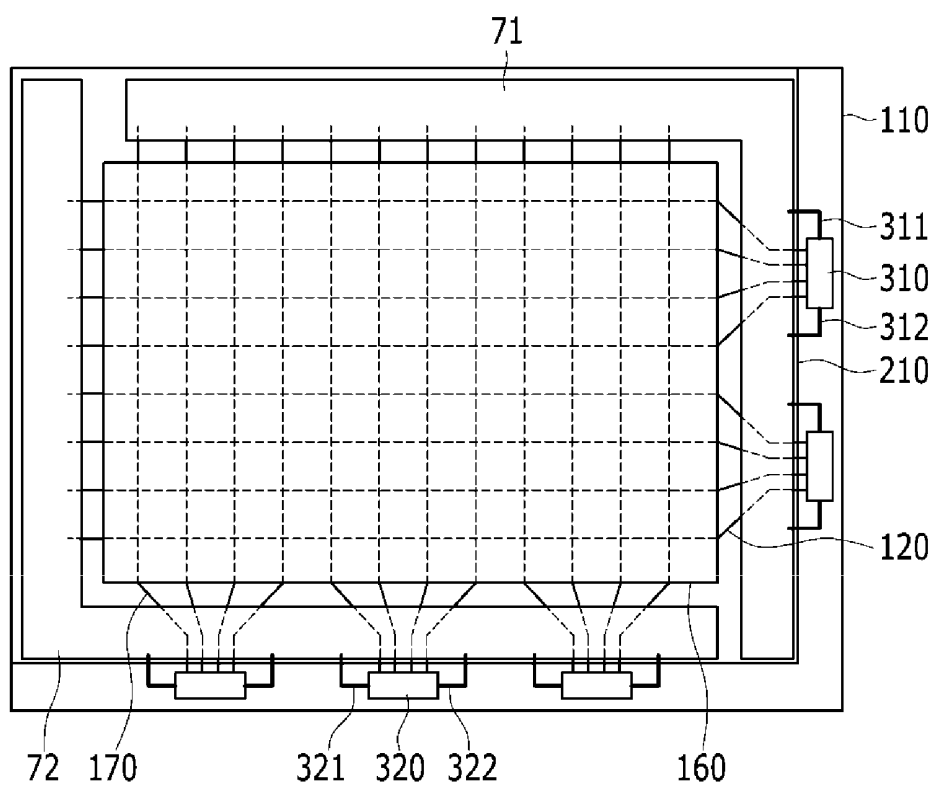
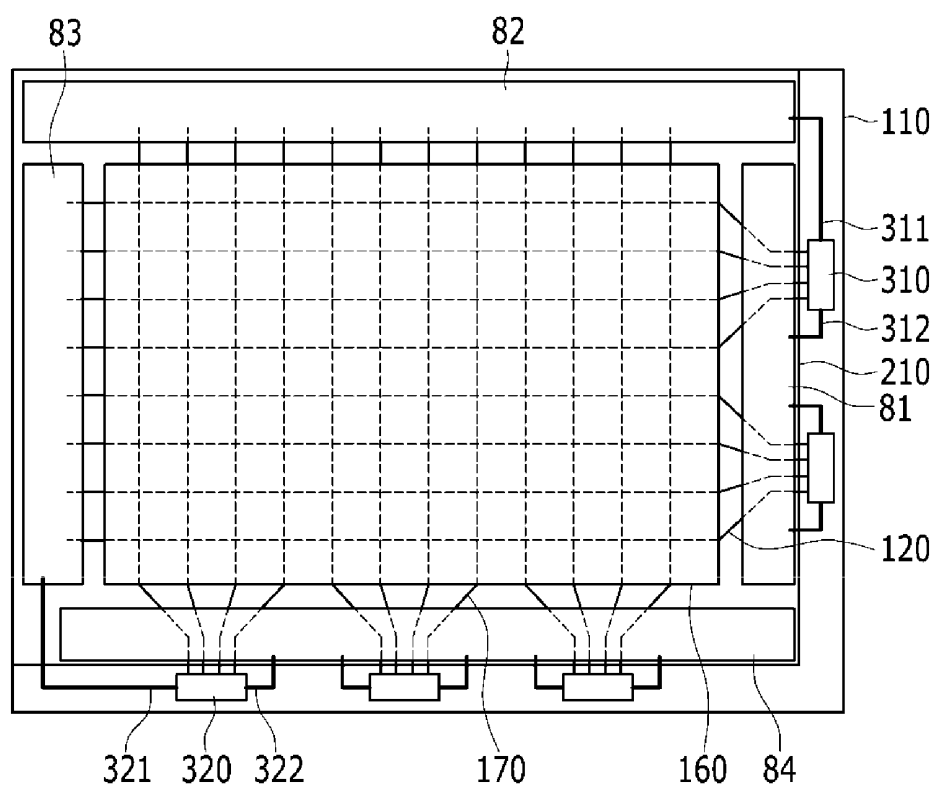


FIG. 15



LIQUID CRYSTAL DISPLAY HAVING SHIELDING CONDUCTOR

CROSS-REFERENCE TO RELATED APPLICATION

[0001] This application claims priority from and the benefit of Korean Patent Application No. 10-2012-0027723 filed on Mar. 19, 2012, which is hereby incorporated by reference for all purposes as if fully set forth herein.

BACKGROUND OF THE INVENTION

[0002] 1. Field of the Invention

[0003] Exemplary embodiment of the present invention relate to a liquid crystal display having a shielding conductor formed to prevent static electricity.

[0004] 2. Discussion of the Background

[0005] A liquid crystal display (LCD) is a display device that is most often used as a television, a monitor and the like. The LCD displays an image by using a liquid crystal layer is between two substrates, on which an electrode and a switching element are formed, and applying an electric field to the liquid crystal layer to control the liquid crystals direction thereby controlling transmittance of light through the liquid crystals.

[0006] The manufacturing of LCD displays includes a process of forming several layers of thin films on an insulation substrate and patterning the layers, a process of rubbing an alignment layer, a process of testing a formed wiring, and other processes. During these processes, it can happen that static electricity inflows or arises causing damage to devices such as the thin film transistors and the wiring formed on an insulation substrate.

[0007] The following are examples of ways in which static electricity can appear and cause damage to devices: scratches may form on the wiring, for example, caused by a probe during a wiring test, wherein the scratched portion acts as a lightning rod causing sparks may break insulating layers; sparks may be generated because of an insulating layer that includes a portion with poor insulating properties due to, for example, defects of an organic insulating layer, and the sparks break the insulating layer; and static electricity may arise due to friction during the process of rubbing an alignment layer to break a weak portion in a display area and other processes.

[0008] The above information disclosed in this Background section is only for enhancement of understanding of the background of the invention and therefore it may contain information that does not form any part of the prior art.

SUMMARY OF THE INVENTION

[0009] Exemplary embodiments of the present invention provide a liquid crystal display having a structure that can prevent device damage due to static electricity.

[0010] Additional features of the invention will be set forth in the description which follows, and in part will be apparent from the description, or may be learned by practice of the invention.

[0011] An exemplary embodiment of the present invention provides a liquid crystal display, including: a first insulation substrate having a polygonal display area and a peripheral area surrounding a peripheral side thereof, a first signal line formed on the first insulation substrate, a second signal line formed on the first insulation substrate and crosses the first signal line, a plurality of switching elements connected to the

first signal line and the second signal line and disposed in the display area, a plurality of pixel electrodes each connected to the switching element and disposed in the display area, and a shielding conductor disposed in the peripheral area and extending along at least one side of the polygonal display area.

[0012] It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

BRIEF DESCRIPTION OF THE DRAWINGS

[0013] The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in and constitute a part of this specification, illustrate embodiments of the invention, and together with the description serve to explain the principles of the invention.

[0014] FIG. 1 is a top plan view of a liquid crystal display according to a first exemplary embodiment of the present invention.

[0015] FIG. 2 is a partial cross-sectional view of the liquid crystal display according to is the first exemplary embodiment of the present invention.

[0016] FIG. 3 is a top plan view of a liquid crystal display according to a second exemplary embodiment of the present invention.

[0017] FIG. 4 is a cross-sectional view of a connection portion of a shielding conductor and a shorting bar in the liquid crystal display according to the second exemplary embodiment of the present invention.

[0018] FIG. 5 is a cross-sectional view of a connection portion of a shielding conductor and a shorting bar in the liquid crystal display according to the second exemplary embodiment of the present invention.

[0019] FIG. 6 is a cross-sectional view of a connection portion of a shielding conductor, a shorting bar, and a medium electrode in a liquid crystal display according to a third exemplary embodiment of the present invention.

[0020] FIG. 7 is a cross-sectional view of a connection portion of a shielding conductor, a shorting bar, and a medium electrode in the liquid crystal display according to the third exemplary embodiment of the present invention.

[0021] FIG. 8 is a layout view of a liquid crystal display according to a fourth exemplary embodiment of the present invention.

[0022] FIG. 9 is a layout view of a liquid crystal display according to a fifth exemplary embodiment of the present invention.

[0023] FIG. 10 is a cross-sectional view of a connection portion of a shielding conductor and a dummy pin of a gate driving circuit in the liquid crystal display according to the fifth exemplary embodiment of the present invention.

[0024] FIG. 11 is a cross-sectional view of a connection portion of a shielding conductor and a dummy pin of a gate driving circuit in a liquid crystal display according to a sixth exemplary embodiment of the present invention.

[0025] FIG. 12 to FIG. 15 are each a layout view of liquid crystal displays according to seventh to tenth exemplary embodiments of the present invention.

DETAILED DESCRIPTION OF THE ILLUSTRATED EMBODIMENTS

[0026] The invention is described more fully hereinafter with reference to the accompanying drawings, in which embodiments of the invention are shown. This invention may, however, be embodied in many different forms and should not be construed as limited to the embodiments set forth herein. Rather, these embodiments are provided so that this disclosure is thorough, and will fully convey the scope of the invention to those skilled in the art. In the drawings, the size and relative sizes of layers and regions may be exaggerated for clarity. Like reference numerals in the drawings denote like elements.

[0027] It will be understood that when an element or layer is referred to as being “on” or “connected to” another element or layer, it can be directly on or directly connected to the other element or layer, or intervening elements or layers may be present. In contrast, when an element or layer is referred to as being “directly on” or “directly connected to” another element or layer, there are no intervening elements or layers present. It will be understood that for the purposes of this disclosure, “at least one of X, Y, and Z” can be construed as X only, Y only, Z only, or any combination of two or more items X, Y, and Z (e.g., XYZ, XYY, YZ, ZZ).

[0028] A liquid crystal display according to a first exemplary embodiment of the present invention will be described in detail with reference to FIG. 1 and FIG. 2.

[0029] FIG. 1 is a top plan view of a liquid crystal display according to the first exemplary embodiment of the present invention, and FIG. 2 is a partial cross-sectional view of the liquid crystal display according to the first exemplary embodiment of the present invention.

[0030] The liquid crystal display illustrated in FIG. 1 is a PLS (plane to line switching) type liquid crystal display where liquid crystals are aligned in parallel to a surface of a display panel, and may include a lower panel, an upper panel facing the lower panel, a liquid crystal layer 3 interposed between the lower panel and the upper panel and the like.

[0031] The lower panel includes an insulation substrate 110, a thin film transistor (not shown) disposed on the insulation substrate 110, and a pixel electrode 191 connected to the thin film transistor. The lower panel further includes signal lines such as a gate line 120 extending in a horizontal direction and a data line 170 extending in a vertical direction and crossing the gate line 120 such that the gate line and the data line are insulated from each other. The lower panel further includes a medium electrode 160 acting as an opposed electrode of the pixel electrode 191 and integrally formed over the entire display area of the insulation substrate 110, a shielding electrode 192 formed on the data line 170 and the like, and a shielding conductor 40 formed in a peripheral area outside the display area, surrounding the display area, and connected to the medium electrode 160. Further, the lower panel includes a gate insulating layer 130, a first passivation layer 140, an organic insulating layer 150, a second passivation layer 180 and the like that insulate the conductors such as the signal lines, the thin film transistor, the pixel electrode 191, the medium electrode 160, the shielding electrode 192, and the shielding conductor 40. In addition, an alignment layer (not shown) for aligning liquid crystals is formed on the pixel electrode 191. A gate driving circuit 310 for applying a scanning signal to the gate line 120 and a data driving circuit 320 for applying an image data signal to the data line 170 may be mounted on the lower panel.

[0032] The thin film transistor, acting as a switching element, independently applies an image voltage to each pixel electrode 191 and the drain electrode, the gate electrode, and the source electrode are respectively connected to the pixel electrode 191, the gate line 120, and the data line 170. The medium electrode 160 and the pixel electrode 191 form an electric field that drives the liquid crystals. The medium electrode 160 is integrally formed over the entire display area except for partially removed portions through which contact holes (not shown) are formed to connect the pixel electrode 191 and the thin film transistor and the like. The function of the shielding electrode 192 is to prevent the voltage of the data line 170 from affecting the liquid crystal layer 3. The shielding electrode 192 is formed along the data line 170 at a position overlapping the data line 170 on the second passivation layer 180 and applied with a common voltage or the like. The shielding electrode 192 is formed of the same material as the pixel electrode 191 and on the same layer as the pixel electrode 191. The shielding electrode 192 may be formed of a transparent conductive material such as ITO (indium tin oxide), IZO (indium zinc oxide) or the like. The shielding conductor 40 is formed in a quadrangle loop shape and surrounds the quadrangle display area. The shielding conductor may be formed by any suitable material, including non-transparent conductive materials and transparent conductive materials. The shielding conductor 40 is formed on the same layer as the medium electrode 160 and is physically connected to the medium electrode 160. Alternatively, the shielding conductor 40 may be formed on the same layer as the pixel electrode 191. That is, the shielding conductor 40 may be formed of a transparent conductive material such as ITO (indium tin oxide), IZO (indium zinc oxide) or similar materials. The shielding conductor 40 receives a predetermined voltage such as a common voltage through dummy pins 321 and 322 of the data driving circuit 320. Alternatively the shielding conductor 40 may receive a predetermined voltage through the dummy pin of the gate driving circuit 310 or may receive a predetermined voltage such as a common voltage through both the dummy pins 321 and 322 of the data driving circuit 320 and the dummy pin of the gate driving circuit 310. The shielding conductor 40 includes a portion extending in a horizontal direction and a portion extending in a vertical direction, the portions having uniform width, such that the shielding conductor 40 may extend across both a gate fanout region and a data fanout region. The gate fanout region is the region where the interval between the gate lines 120 becomes narrower so that the gate lines 120 are connected to the gate driving circuit 310, whereas the data fanout region is the region where the interval between the data lines 170 becomes narrower so that the data lines 170 are connected to the data driving circuit 320.

[0033] The upper panel includes an insulation substrate 210, a black matrix 220, and a color filter 230 disposed on a lower surface of the insulation substrate 210. An alignment layer (not shown) for aligning liquid crystals is formed on the black matrix 220 and the color filter 230. The black matrix 220 is formed both in the peripheral area and in the display area. The black matrix 220 formed in the peripheral area may be disposed at a position overlapping the shielding conductor 40 of the lower panel.

[0034] Regarding the liquid crystal layer 3, the long axes of the liquid crystal molecules are parallel to the surfaces of the

insulation substrates **110** and **210**, and for a PLS type liquid crystal display the liquid crystal molecules are aligned to be parallel to each other.

[0035] Regarding the liquid crystal display of this invention, even if static electricity inflows or is generated in the liquid crystal display, it is possible to prevent the devices of the display area from being damaged by ensuring that the static electricity is dispersed to the shielding conductor **40** formed in the peripheral area.

[0036] The first exemplary embodiment describes the present invention as used in a PLS type of liquid crystal display. The present invention structure and method for protecting the devices of the display area from static electricity by disposing the shielding conductor **40** in the peripheral area may be used in liquid crystal displays employing other liquid crystal display modes such as a TN (Twisted Nematic) mode, a VA (vertical alignment) mode, and an IPS (In Plane Switching) mode.

[0037] FIG. 3 shows a top plan view of a liquid crystal display according to a second exemplary embodiment of the present invention. FIG. 4 shows a cross-sectional view of a connection portion of a shielding conductor and a shorting bar in the liquid crystal display according to the second exemplary embodiment of the present invention, and FIG. 5 is a cross-sectional view of a connection portion of a shielding conductor and a shorting bar in the liquid crystal display according to the second exemplary embodiment of the present invention.

[0038] The liquid crystal display according to the second exemplary embodiment of the present invention further includes a first shorting bar **400** and a second shorting bar **500** as compared to the liquid crystal display according to the first exemplary embodiment. The first shorting bar **400** and the second shorting bar **500** are formed in the peripheral area outside the display area. The first shorting bar **400** is formed of the same material as the gate line **120** and on the same layer as the gate line **120**. The second shorting bar **500** is formed of the same material as the data line **170** and on the same layer as the data line **170**. The second shorting bar **500** has a lower layer **501** and an upper layer **502** similar to the data line **170**. The first shorting bar **400** is formed in parallel to the gate line **120**, is connected through the first connection portion **43** to the shielding conductor **40**, and is connected through the second connection portion **44** to the medium electrode **160**. The second shorting bar **500** is formed in parallel to the data line **170**, is connected through the third connection portion **41** to the shielding conductor **40**, and is connected through the fourth connection portion **42** to the medium electrode **160**. The shielding conductor **40** includes a portion extending in a horizontal direction and a portion extending in a vertical direction and the portions may be each formed to have uniform width.

[0039] Referring to FIG. 4 and FIG. 5, the structure of the first connection portion **43** and the third connection portion **41** is described in more detail.

[0040] A first shorting bar **400** is formed on the insulation substrate **110**, where the shorting bar **400** is formed of the same material as the gate line **120** and on the same layer as the gate line **120**. The gate insulating layer **130** is formed on the first shorting bar **400** and the second shorting bar **500** is formed on the gate insulating layer **130**. The first passivation layer **140** and the organic insulating layer **150** are formed on the second shorting bar **500**. The medium electrode **160** and the shielding conductor **40** are formed on the organic insulat-

ing layer **150**. The second passivation layer **180** is formed on the medium electrode **160** and the shielding conductor **40**. The second passivation layer **180** has a contact hole **181** exposing the shielding conductor **40**. The gate insulating layer **130**, the first passivation layer **140**, the organic insulating layer **150**, and the second passivation layer **180** have a contact hole **183** formed therethrough to expose the first shorting bar **400**. In addition, the first passivation layer **140**, the organic insulating layer **150**, and the second passivation layer **180** have a contact hole **182** formed therethrough to expose the second shorting bar **500**. The first connection portion **43** connecting the shielding conductor **40** and the first shorting bar **400** through the contact holes **181** and **183** is formed on the second passivation layer. The third connection portion **41** connecting the shielding conductor **40** and the second shorting bar **500** through the contact holes **181** and **182** is formed on the second passivation layer **180**.

[0041] Since the connection structure of the second connection portion **44** and the fourth connection portion **42** is the same as the connection structure of the first connection portion **43** and the third connection portion **41**, except that the contact hole **181** of the second passivation layer exposes the medium electrode **160** instead of the shielding conductor **40**, a description thereof will be omitted.

[0042] The shielding conductor **40** may be formed on various layers of the liquid crystal display. For example, the shielding conductor **40** may be formed, like the pixel electrode **191**, on the second passivation layer **180**. The third exemplary embodiment presents such a situation.

[0043] FIG. 6 shows a cross-sectional view of a connection portion of a shielding conductor, a shorting bar, and a medium electrode in a liquid crystal display according to a third exemplary embodiment of the present invention. FIG. 7 shows a cross-sectional view of a connection portion of a shielding conductor, a shorting bar, and a medium electrode in the liquid crystal display according to the third exemplary embodiment of the present invention.

[0044] A first shorting bar **400** is formed on the insulation substrate **11**. The shorting bar **400** is formed of the same material as the gate line **120** and on the same layer as the gate line **120**. The gate insulating layer **130** is formed on the first shorting bar **400** and the second shorting bar **500** is formed on the gate insulating layer **130**. The first passivation layer **140** and the organic insulating layer **150** are formed on the second shorting bar **500**. A medium electrode **160** is formed on the organic insulating layer **150** and a second passivation layer **180** is formed on the medium electrode **160**. The second passivation layer **180** has a contact hole **185** exposing the medium electrode **160**. The gate insulating layer **130**, the first passivation layer **140**, the organic insulating layer **150**, and the second passivation layer **180** have a contact hole **187** formed therethrough to expose the first shorting bar **400**. Further, the first passivation layer **140**, the organic insulating layer **150**, and the second passivation layer **180** have a contact hole **186** is formed therethrough to expose the second shorting bar **500**. The shielding conductor **40**, the first connection portion **43**, and the second connection portion **41** are formed on the second passivation layer **180**. The first connection portion **43** connects the shielding conductor **40**, the first shorting bar **400**, and the medium electrode **160** through the contact holes **185** and **187**. The third connection portion **41** connects the shielding conductor **40**, the second shorting bar **500**, and the medium electrode **160** through the contact holes **185** and **186**. The first connection portion **43** and the second

connection portion 41 are formed on the same layer as the shielding conductor 40 to be directly connected without a separate contact hole. As described above, the first connection portion 43 may simultaneously connect the shielding conductor 40, the first shorting bar 400, and the medium electrode 160. Further, the second connection portion 41 may simultaneously connect the shielding conductor 40, the second shorting bar 500, and the medium electrode 160.

[0045] FIG. 8 shows a layout view of a liquid crystal display according to a fourth exemplary embodiment of the present invention.

[0046] When compared to the second exemplary embodiment, the difference between the fourth exemplary embodiment and the second exemplary embodiment is that for the fourth exemplary embodiment the shielding conductor 40 is integrated with the medium electrode 160. As described above, in the case where the shielding conductor 40 and the medium electrode 160 are connected to each other, some of the connection portions 41, 42, 43, and 44 may be omitted.

[0047] For example, only one out of the first connection portion 43 and the second connection portion 44 may be formed, and only one of the third connection portion 41 and the fourth connection portion 42 may be formed.

[0048] FIG. 9 shows a layout view of a liquid crystal display according to a fifth exemplary embodiment of the present invention. FIG. 10 shows a cross-sectional view of a connection portion of a shielding conductor and a dummy pin of a gate driving circuit in the liquid crystal display according to the fifth exemplary embodiment of the present invention.

[0049] The fifth exemplary embodiment is described with reference to FIG. 9 and FIG. 10.

[0050] When compared to the first exemplary embodiment, the difference between the liquid crystal display according to the fifth exemplary embodiment and that of the first exemplary embodiment is that, for the fifth exemplary embodiment, the shielding conductor 50 is disposed only at a portion overlapping the fanout portion of the gate line 120, and the shielding conductor 50 and the medium electrode 160 are not connected in overall but partially connected through the connection portions 51 and 52. The shielding conductor 50 is connected through the dummy pins 311 and 312 of the gate driving circuit 310 to a voltage such as a common voltage Vcom. Herein, the number of connection portions 51 and 52 may be increased. The shielding conductor 50 may be formed with a uniform width.

[0051] As described above, if the shielding conductor 50 is formed, even if static electricity inflows, occurs or is generated in the liquid crystal display, it is possible to prevent the devices of the display area from being damaged by dispersing static electricity to the shielding conductor 50 formed in the peripheral area. Consequently, it is possible to prevent defects such as the instantaneous generation of an abnormal image both when the power of the liquid crystal display is on and off.

[0052] The dummy pins 311 and 312 of the gate driving circuit 310 and the connection portion of the shielding conductor 50 are described in more detail by reference to FIG. 10.

[0053] The gate driving circuit 310 may be IC (Integrated Circuit) chip, and the IC chip is mounted on a tape carrier package (TCP) 600 on which a metal wiring 601 is formed. Connection pins of the IC chip are connected to the metal wiring 601 formed on the tape carrier package (TCP) 600. Most of the metal wiring 601 is surrounded by an insulating film 602, and the metal wiring 601 is partially exposed.

[0054] The gate insulating layer 130, the first passivation layer 140, and the organic insulating layer 150 are formed on the insulation substrate 110 of the liquid crystal panel. The shielding conductor 50 is formed on the organic insulating layer 150 and the second passivation layer 180 is formed on the shielding conductor 50. A contact hole 188 exposing a portion of the shielding conductor 50 is formed in the second passivation layer 180.

[0055] The tape carrier package (TCP) 600 on which the gate driving circuit 310 is mounted is further mounted on the passivation layer 180. In this case, an opening through which the metal wiring 601 is exposed of the tape carrier package (TCP) 600 is disposed at a position corresponding to the contact hole 188. An anisotropic conductive film 700 is disposed between the tape carrier package (TCP) 600 and the passivation layer 180 to electrically connect the metal wiring 601 exposed through the opening and the shielding conductor 50 exposed through the contact hole 188.

[0056] The dummy pins 311 and 312 of the gate driving circuit 310 and the connection portion of the shielding conductor 50 may be connected through another metal wiring layer formed on the insulation substrate 110. Such an exemplary embodiment will be described referring to FIG. 11.

[0057] FIG. 11 shows a cross-sectional view of a connection portion of a shielding conductor and a dummy pin of a gate driving circuit in a liquid crystal display according to a sixth exemplary embodiment of the present invention.

[0058] The tape carrier package (TCP) 600 on which the gate driving circuit 310 is mounted has the same structure as that of the fifth exemplary embodiment.

[0059] A connection wiring 127 is formed on the insulation substrate 110 of the liquid crystal panel, and the gate insulating layer 130, the first passivation layer 140, and the organic insulating layer 150 are formed on the connection wiring 127. A portion of the connection wiring 127 is exposed through a contact hole 184 formed through the gate insulating layer 130, the first passivation layer 140, and the organic insulating layer 150. The shielding conductor 50 is formed on the organic insulating layer 150 to be connected through the contact hole 184 to the connection wiring 127. The second passivation layer 180 is formed on the shielding conductor 50, and another portion of the connection wiring 127 is exposed through a contact hole 185 formed through the gate insulating layer 130, the first passivation layer 140, the organic insulating layer 150, and the second passivation layer 180.

[0060] The tape carrier package (TCP) 600 on which the gate driving circuit 310 is mounted is mounted on the passivation layer 180. In this case, an opening through which the metal wiring 601 is exposed of the tape carrier package (TCP) 600 is disposed at a position corresponding to the contact hole 185. An anisotropic conductive film 700 is disposed between the tape carrier package (TCP) 600 and the passivation layer 180 to electrically connect the metal wiring 601 exposed through the opening and the wiring connection portion 127 exposed through the contact hole 185.

[0061] Referring to FIG. 12, a seventh exemplary embodiment of the present invention is described.

[0062] When compared to the fifth exemplary embodiment, the difference between the liquid crystal display according to the seventh exemplary embodiment and that of the fifth exemplary embodiment is that there are no connection portions 51 and 52 connecting the shielding conductor 50 and the medium electrode 160. This way, it is possible to prevent charges accumulated in the shielding conductor 50 from

affecting the voltage of the medium electrode **160** by separating the shielding conductor **50** from the medium electrode **160** and separately applying voltage thereto. As a result, static electricity generated during the process of manufacturing the liquid crystal display may be accumulated in the shielding conductor **50** having high capacitance and then move to the medium electrode **160** disposed in the display area, thereby preventing an influence on the image. Since the shielding conductor **50** is connected through the dummy pin and the like to the gate driving circuit **310** to independently receive a common voltage, a ground voltage or the like, the voltage of the shielding conductor **50** is very stably maintained. Further, it is possible to prevent the problem of charge accumulation in the shielding conductor **50** by discharging the charge accumulated in the shielding conductor **50** during the manufacturing process through the dummy pin to the outside. Herein, the shielding conductor **50** may be formed such as to have a uniform width.

[0063] Referring to FIG. 13, an eighth exemplary embodiment of the present invention is described.

[0064] When compared to the seventh exemplary embodiment, the difference between the liquid crystal display according to the eighth exemplary embodiment and that of the seventh exemplary embodiment is that the shielding conductor **60** has a loop form surrounding the medium electrode **160** and a voltage such as a common voltage, a ground voltage or the like may be applied through the dummy pins **321** and **322** of the data driving circuit **320** to the shielding conductor **60**. If the shielding conductor **60** is formed on all four sides of the quadrangle display area, the shielding conductor **60** has a capacitance which is more suitable for preventing the problems caused by static electricity.

[0065] Referring to FIG. 14, a ninth exemplary embodiment of the present invention is described.

[0066] When compared to the eighth exemplary embodiment, the difference between the liquid crystal display according to the ninth exemplary embodiment and that of the eighth exemplary embodiment is that the shielding conductors **71** and **72** are separated into two portions. The first shielding conductor **71** is disposed in a “ $\neg$ ” shape and connected to the dummy pins **311** and **312** of the gate driving circuit **310**, and the second shielding conductor **72** is disposed in a “ $\perp$ ” shape and connected to the dummy pins **321** and **322** of the data driving circuit **320**. As described above, if the first shielding conductor **71** and the second shielding conductor **72** are separated from each other and the voltage is applied through the gate driving circuit **310** and the data driving circuit **320**, if necessary, different voltages may be applied to the first shielding conductor **71** and the second shielding conductor **72**. In the shielding conductors **71** and **72**, a portion extending in a horizontal direction and a portion extending in a vertical direction may be each formed such as to have uniform width.

[0067] Referring to FIG. 15, a tenth exemplary embodiment of the present invention is described.

[0068] When compared to the ninth exemplary embodiment, the difference between the liquid crystal display according to the tenth exemplary embodiment and that of the ninth exemplary embodiment is that the shielding conductors **81**, **82**, **83**, and **84** are separated into four portions. The first shielding conductor **81** and the second shielding conductor **82** are connected to different dummy pins **311** and **312** of the gate driving circuit **310**, and the third shielding conductor **83** and the fourth shielding conductor **84** are connected to dif-

ferent dummy pins **321** and **322** of the data driving circuit **320**. Accordingly, if necessary, different voltages may be applied to the four shielding conductors **81**, **82**, **83**, and **84**. The shielding conductors **81**, **82**, **83**, and **84** may be each formed such as to have uniform width.

[0069] In exemplary embodiments of the present invention, the gate driving circuit **310** and the data driving circuit **320** may be implemented as an IC (integrated chip) or a circuit directly formed on the insulation substrate **110**.

[0070] Although exemplary embodiments of the present invention are described in relation to liquid crystal displays the invention may be applied to other types of displays, such as organic light emitting diode displays, electro-wetting displays, and others.

[0071] It will be apparent to those skilled in the art that various modifications and variations can be made in the present invention without departing from the spirit or scope of the invention. Thus, it is intended that the present invention cover the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.

What is claimed is:

1. A display, comprising:

a first insulation substrate comprising a display area and a peripheral area outside the display area;

a plurality of first signal lines disposed on the first insulation substrate;

a plurality of second signal lines disposed on the first insulation substrate and crossing the first signal lines;

a plurality of switching elements connected to the first signal lines and the second signal lines and disposed in the display area;

a plurality of pixel electrodes disposed in the display area, wherein each pixel electrode is connected to a switching element; and

a shielding conductor disposed in the peripheral area and extending along at least one side of the display area.

2. The display device of claim 1, further comprising:

a medium electrode disposed in the display area, wherein the medium electrode and the pixel electrodes are configured to form an electric field to drive liquid crystals.

3. The display device of claim 2, wherein:

the shielding conductor is electrically connected to the medium electrode.

4. The display device of claim 3, wherein:

the shielding conductor is disposed on the same layer as the medium electrode and the medium electrode extends such that it is directly connected to the shielding conductor.

5. The display device of claim 3, wherein:

the shielding conductor and the medium electrode are disposed on different layers, and the shielding conductor and the medium electrode are connected to each other through a connecting member.

6. The display device of claim 1, further comprising:

a shorting bar disposed in the peripheral area and electrically connected to the shielding conductor.

7. The display device of claim 6, wherein:

the shorting bar comprises a first shorting bar disposed on the same layer as the first signal lines and parallel to the first signal lines, and a second shorting bar disposed on the same layer as the second signal line and parallel to the second signal line.

8. The display device of claim 6, further comprising:
a medium electrode disposed in the display area, wherein the medium electrode and the pixel electrodes are configured to form an electric field to drive the liquid crystals,
wherein at least one of the shorting bar and the shielding conductor is electrically connected to the medium electrode.
9. The display device of claim 8, wherein:
the shorting bar, the shielding conductor, and the medium electrode are disposed on different layers and are connected to each other through a connecting member.
10. The display device of claim 6, wherein:
the shielding conductor forms a loop surrounding the display area such that the shorting bar is disposed on all sides of the display area.
11. The display device of claim 1, further comprising:
a first driving circuit disposed in the peripheral area and configured to apply a signal voltage to the first signal lines; and
a second driving circuit disposed in the peripheral area and configured to apply a signal voltage to the second signal lines;
wherein the shielding conductor is configured to receive a voltage through at least one dummy pin of the first driving circuit and the second driving circuit.
12. The display device of claim 11, further comprising:
a medium electrode disposed in the display area, wherein the medium electrode and the pixel electrodes are configured to form an electric field to drive the liquid crystals; and
a connection portion partially connecting the shielding conductor and the medium electrode.
13. The display device of claim 11, further comprising:
a medium electrode disposed in the display area, wherein the medium electrode and the pixel electrodes are configured to form an electric field to drive the liquid crystals, and wherein the medium electrode is electrically separated from the shielding conductor.
14. The display device of claim 13, wherein:
the shielding conductor forms a loop surrounding the display area.
15. The display device of claim 13, wherein:
the shielding conductor comprises a first shielding conductor and a second shielding conductor separated from each other,
the first shielding conductor is connected to a dummy pin of the first driving circuit, and the second shielding conductor is connected to a dummy pin of the second driving circuit.
16. The display device of claim 15, wherein:
the first shielding conductor together with the second shielding conductor surround the display area.
17. The display device of claim 13, wherein:
the shielding conductor comprises a first, second, third, and fourth shielding conductors separated from each other,
the first and the second shielding conductors are connected to a dummy pin of the first driving circuit, and the third and the fourth shielding conductors are connected to a dummy pin of the second driving circuit.
18. The display device of claim 17, wherein:
the first shielding conductor and the second shielding conductor are connected to different dummy pins of the first driving circuit, and
the third shielding conductor and the fourth shielding conductor are connected to different dummy pins of the second driving circuit.
19. The display device of claim 18, wherein:
the display area is quadrangle, and the first, second, third, and fourth shielding conductors are respectively disposed on the first, second, third and fourth sides of the display area.
20. The display device of claim 1, further comprising:
a second substrate facing the first substrate and a black matrix disposed on the second substrate,
wherein the shielding conductor overlaps a portion of the black matrix disposed in the peripheral area.
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专利名称(译)	具有屏蔽导体的液晶显示器		
公开(公告)号	US20130242215A1	公开(公告)日	2013-09-19
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[标]申请(专利权)人(译)	常JONG WOONG		
申请(专利权)人(译)	常, JONG WOONG		
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摘要(译)

一种液晶显示器，包括：在具有多边形显示区域的绝缘基板和围绕显示区域的外围区域，第一信号线，与第一信号线交叉的第二信号线，连接到第一信号的多个开关元件线和第二信号线设置在显示区域中，多个像素电极分别连接到开关元件并设置在显示区域中，屏蔽导体设置在外围区域中并沿多边形的至少一侧延伸展示区。

