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Shiomi(10) **Pub. No.: US 2011/0194039 A1**(43) **Pub. Date: Aug. 11, 2011**(54) **LIQUID CRYSTAL DISPLAY APPARATUS
HAVING AN INPUT GRADATION SET TO
HAVE RELATIONSHIP ALONG A GAMMA
CURVE****Publication Classification**(51) **Int. Cl.**
G02F 1/1335 (2006.01)
(52) **U.S. Cl.** **349/15**(76) Inventor: **Makoto Shiomi, Tenri-shi (JP)**(21) Appl. No.: **13/064,791**(22) Filed: **Apr. 15, 2011****Related U.S. Application Data**(63) Continuation of application No. 12/224,854, filed on
Sep. 8, 2008, now Pat. No. 7,948,566, filed as applica-
tion No. PCT/JP2006/325212 on Dec. 18, 2006.**Foreign Application Priority Data**

(30) Mar. 27, 2006 (JP) 2006-086207

ABSTRACT

In a liquid crystal display apparatus realizing a dual view display by bonding a liquid crystal panel and a parallax barrier, the parallax barrier separates display images by treating three pixels including R, G, and B pixels as one unit (one picture element). At this time, luminance variation due to crosstalk concentrates on a right-end pixel among the three pixels constituting the one picture element (in a case where each pixel receives data from a source line immediately on the left of the pixel). Accordingly, the right-end pixel is arranged to be a B pixel that has a low correlation with luminance information and in which influence of crosstalk is hard to be viewed. Further, an applied voltage to be supplied to the display pixel of the B (blue) color and an input gradation are set to have a relationship along a γ curve that makes luminance variation difficult to occur in a low luminance area.

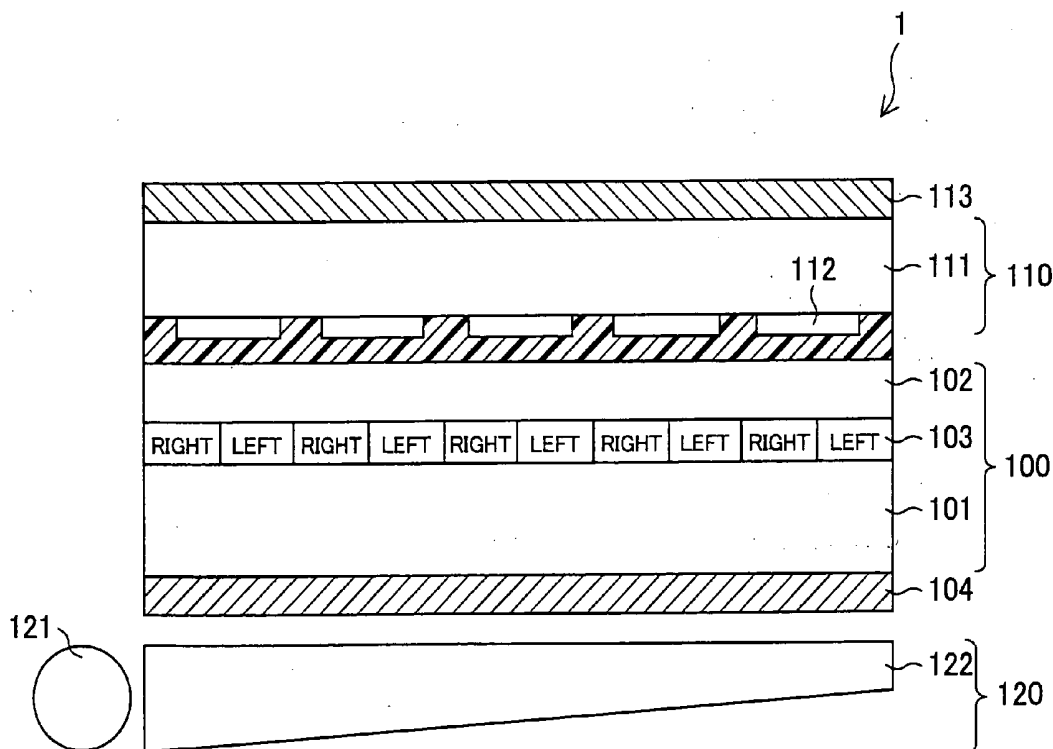


FIG. 1(a)

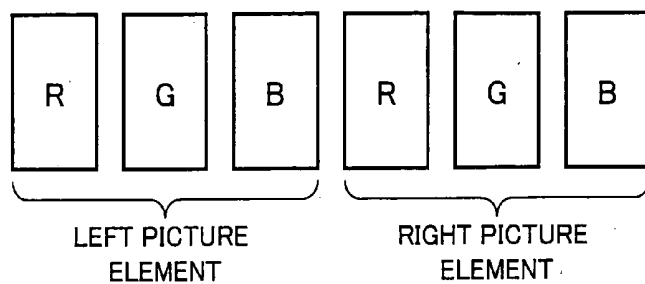


FIG. 1(b)

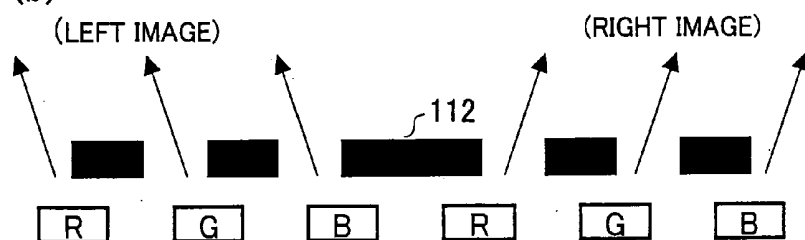


FIG. 2

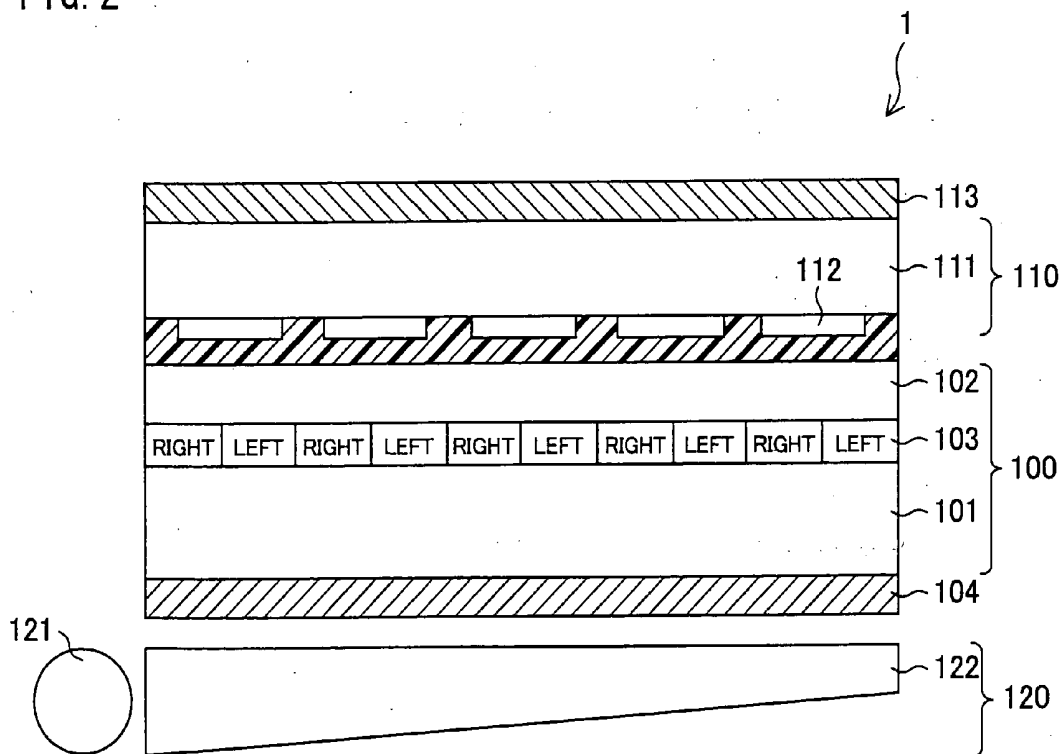


FIG. 3

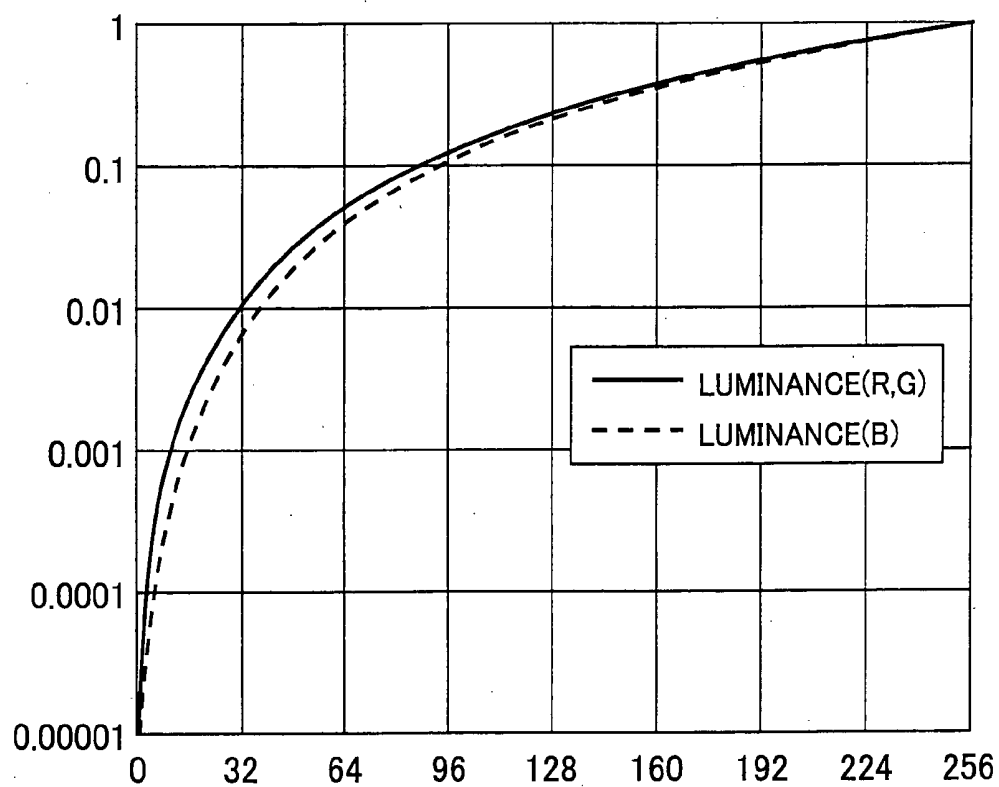


FIG. 4

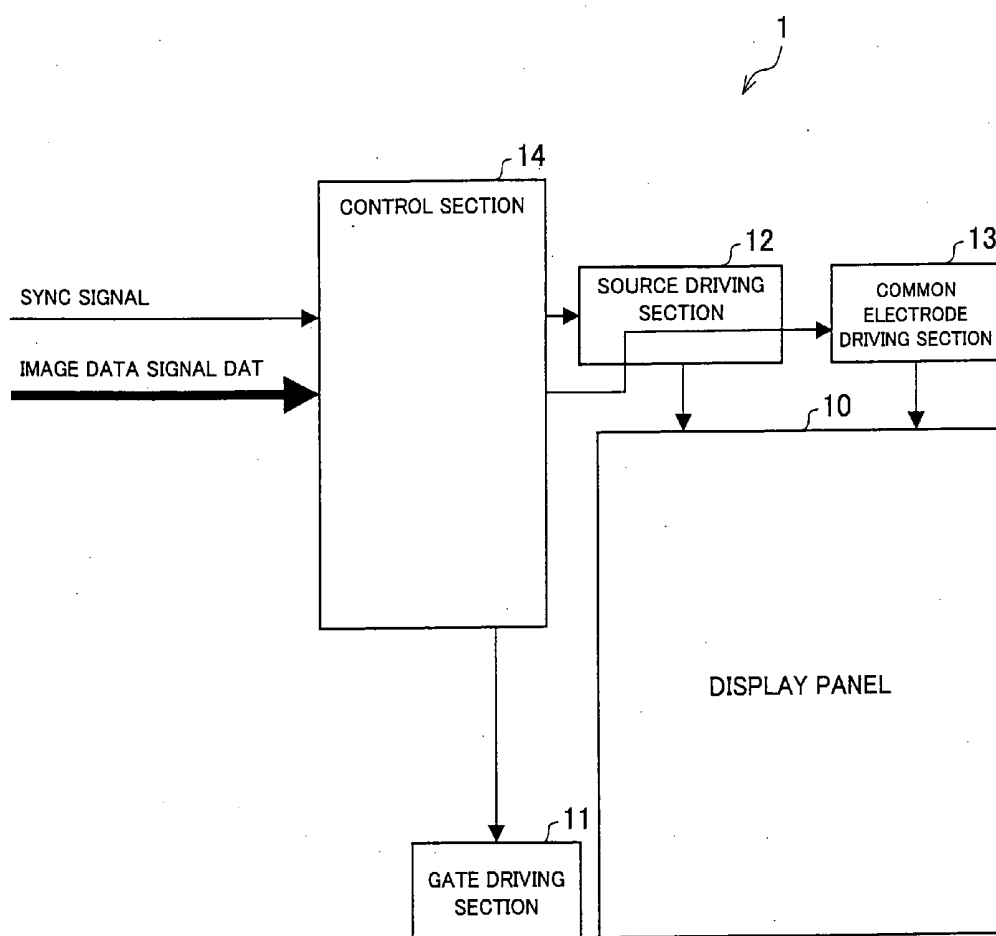


FIG. 5

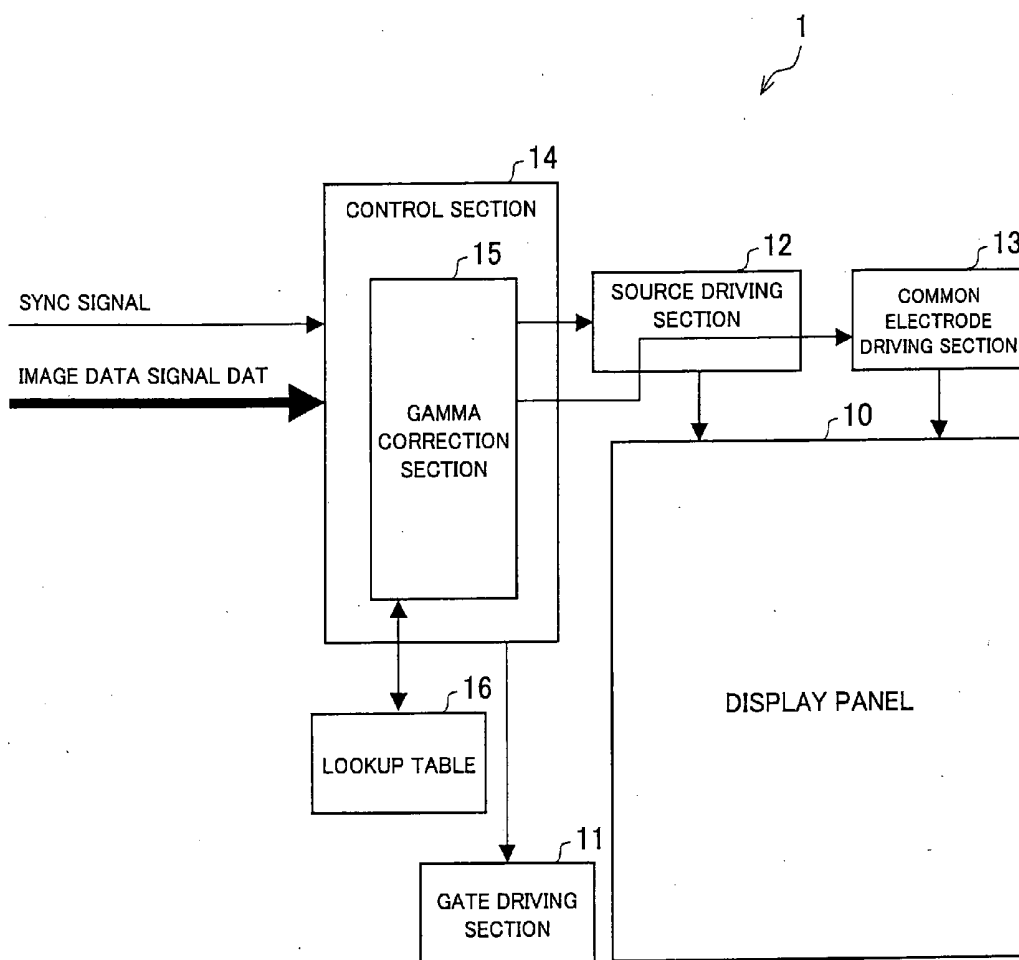


FIG. 6(a)

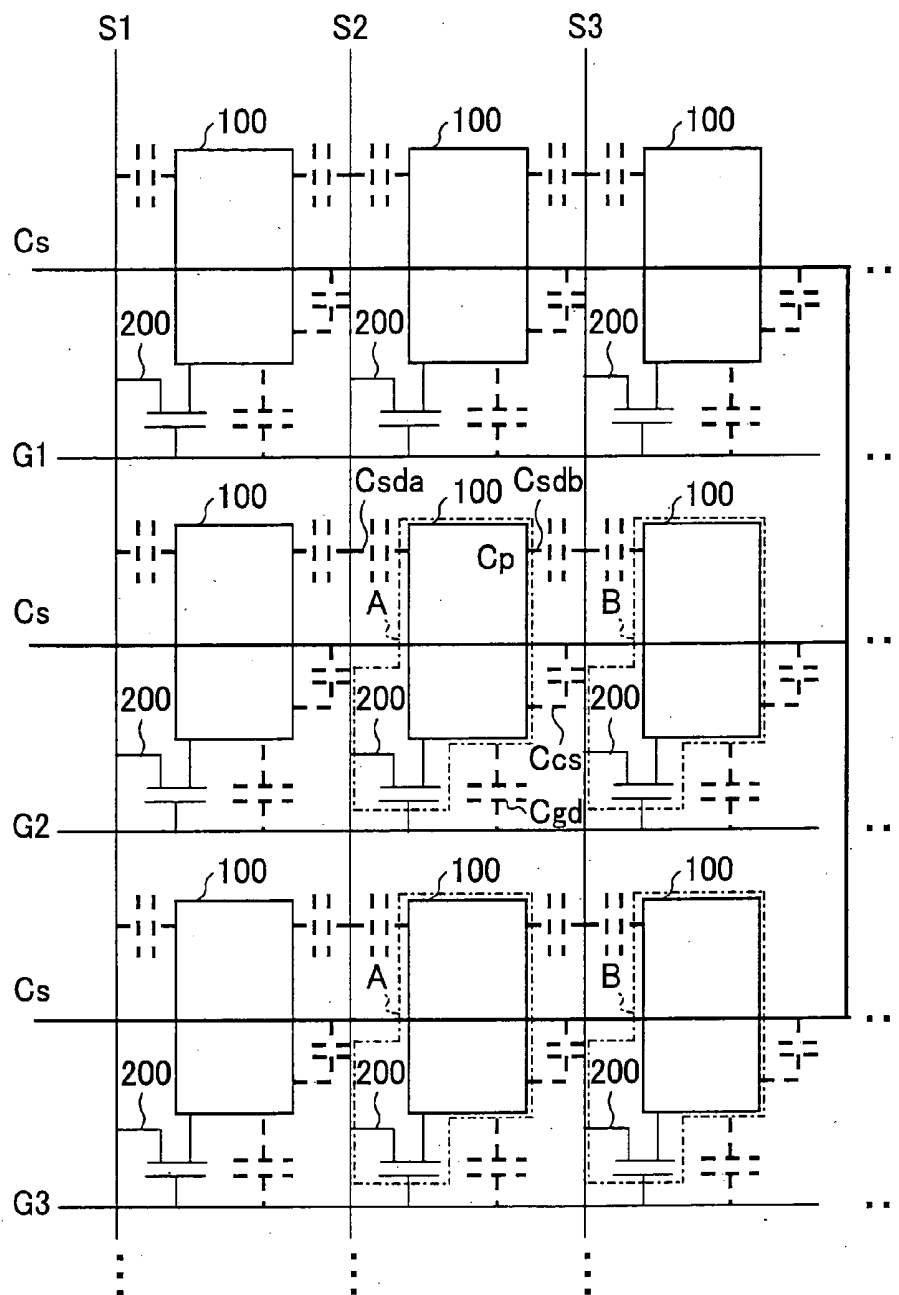


FIG. 6(b)

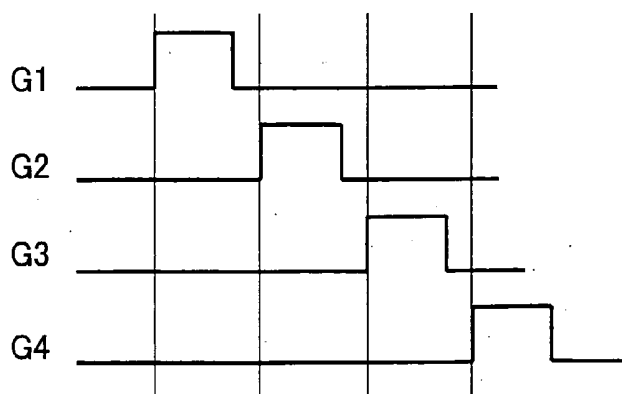


FIG. 7

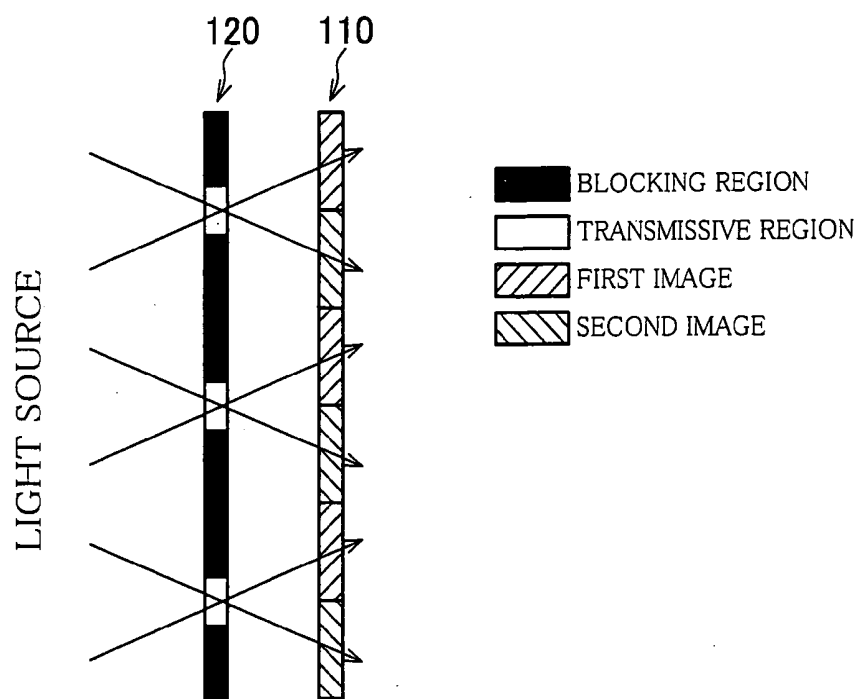
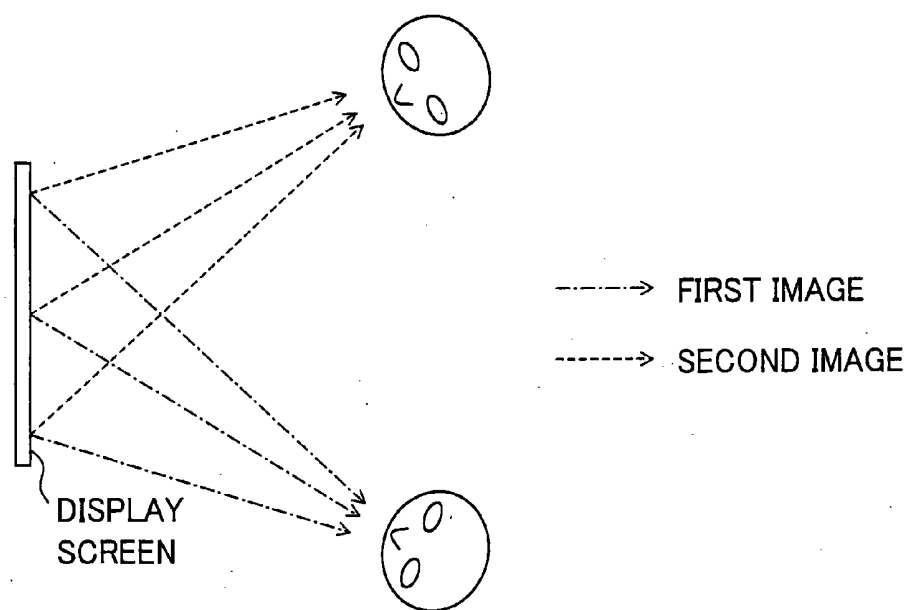


FIG. 8



LIQUID CRYSTAL DISPLAY APPARATUS HAVING AN INPUT GRADATION SET TO HAVE RELATIONSHIP ALONG A GAMMA CURVE

PRIORITY STATEMENT

[0001] This application is a continuation under 35 U.S.C. §120 of application Ser. No. 12/224,854 filed Sep. 8, 2008, which is a national stage Application of PCT/JP2006/325212 filed Dec. 18, 2006 and claims priority under 35 U.S.C. §119 to Japanese Patent Application No. 2006-086207 filed on Mar. 27, 2006, the contents of each of which are hereby incorporated herein by reference in their entirety and for all purposes.

TECHNICAL FIELD

[0002] The present invention relates to a liquid crystal display apparatus that performs a dual view display, in particular, to a liquid crystal display apparatus that improves color reproducibility by reducing color crosstalk.

BACKGROUND ART

[0003] A problem of crosstalk is pointed out as a specific problem in a TFT-LCD. The crosstalk occurs because adjacent pixels are connected via a parasitic capacitance. In other words, when an insulating film intervenes between a transparent electrode and a source line, a parasitic capacitance is produced between the transparent electrode and the source line. In the same manner, parasitic capacitances are produced between a gate line and the transparent electrode and between the source line and a common electrode, respectively. Due to influence of these parasitic capacitances and a capacitance of a liquid crystal itself, an electric potential of a display pixel becomes different from a desired voltage when a gate is turned OFF. Consequently, a display gradation becomes different from a desired gradation.

[0004] In other words, at the moment a gate is high, a desired voltage is applied to a display pixel that is connected to a TFT. However, when the gate is low, the pixel is connected to many peripheral electric circuits via parasitic capacitances. Because many of these peripheral electric circuits are related to panel design, a driving voltage can be set in advance in consideration of parasitic capacitances between the display pixel and the peripheral electric circuits. Therefore, the crosstalk caused by the parasitic capacitances that are formed between the display pixel and the peripheral electric circuits can be compensated in advance. However, an electric potential of a source line that drives other display pixel cannot be determined in advance. Therefore, it is difficult to compensate, in advance, crosstalk that is caused by other source line.

[0005] As illustrated in FIG. 6(a), in a liquid crystal display apparatus, source lines Si ("i" is an integer) and gate lines Gj (j is an integer) are provided to be orthogonal. At each intersection of source lines Si and gate lines Gj, a display pixel 100 and a switching element 200 are provided. Regarding a display pixel (A) among the display pixels 100, parasitic capacitances Csda, Csdb, Cgd, and Ccs are formed as follows. A display pixel (B) indicates a display pixel that is adjacent to the display pixel (A) in a direction along which a gate line is provided.

[0006] The details of the parasitic capacitances Csda, Csdb, Cgd, and Ccs are as follows:

[0007] the parasitic capacitance Csda: a parasitic capacitance that is formed between a source line S2 for driving display pixels (A) and the display pixel (A);

[0008] the parasitic capacitance Csdb: a parasitic capacitance that is formed between a source line S3 for driving display pixels (B) and the display pixel (A);

[0009] the parasitic capacitance Cgd: a parasitic capacitance that is formed between a gate line G2 for driving display pixels (A) and the display pixel (A); and

[0010] the parasitic capacitance Ccs: a parasitic capacitance that is formed between a common electrode line and the display pixel (A).

[0011] A capacitance of the display pixel (A) itself is Cp and a voltage which is applied to each gate line varies as shown in FIG. 6(b). Furthermore, while the display pixel (A) displays a G color, the display pixel (B) displays an R color or B color. In addition, in a case where a gradation of the display pixel (A) is LA and a gradation of the display pixel (B) is LB, LA # LB.

[0012] In this case, at the time at which the gate is high, when a drain voltage +V(A) is applied to a liquid crystal part of the display pixel (A), a drain voltage -V(B) is applied to a liquid crystal part of the display pixel (B). Then, when a next gate line is turned ON, -V(A) is applied to the source line that drives the display pixel (A) and +V(B) is applied to a source line that drives the display pixel (B).

[0013] However, in the reality, the above-mentioned drain voltage is not applied directly to the display pixel (A). A drain voltage that is varied due to the influence of the parasitic capacitances is applied to the display pixel (A). Specifically, an effective value Va of a voltage that is applied to the display pixel (A) is represented by

$$V_a = V(A) + (Csda * V(A) + Cgd * V_g + Csdb * V(B) + Ccs * V_c) / C_p,$$

[0014] where: Vg is a voltage that is applied to the gate line; and Vc is a voltage that is applied to an opposed electrode.

[0015] In this way, a voltage different from a desired drain voltage (A) is applied to the display pixel (A).

[0016] The parasitic capacitances Csda, Cgd, and Ccs that are formed between the display pixel (A) and the respective lines as mentioned above are predictable at a stage of designing. Therefore, a drain voltage can be set in consideration of values of the parasitic capacitances. Accordingly, these parasitic capacitances do not have much influence on a display gradation of the display pixel (A).

[0017] However, the calculation formula of the effective voltage Va above includes the parasitic capacitance Csdb and a drain voltage V(B). In other words, the voltage Va is influenced by the source line that is connected to the display pixel (B). This causes color crosstalk that changes the gradation of the display pixel (A) according to a display gradation of the display pixel (B). For example, Patent Document 1 discloses a method of solving the problem of the color crosstalk by correcting a display signal.

[0018] [Patent Document 1] Japanese Unexamined Patent Publication No. 202377/2005 (Tokukai 2005-202377 (published on Jun. 28, 2005))

DISCLOSURE OF INVENTION

[0019] However, in a conventional arrangement, a circuit and a process for correction become complicated.

[0020] Further, in a normal display mode in which the same image is displayed with respect to all display directions, color crosstalk mentioned above does not occur prominently for the following reason. That is, in a normal display state, image data of adjacent source lines are of the same image. In regard

to luminances of the image data of the adjacent source lines, the image data that relate to R, G, and B colors are highly correlated to one another. Therefore, even if crosstalk occurs, influence of the crosstalk is hard to appear in a visible image.

[0021] On the other hand, recently, a display mode (hereinafter, referred to as a dual view display) in which different images can be displayed with respect to a plurality of display directions, respectively, is realized. In such a mode, a display panel is combined with a parallax barrier. In this dual view display, the problem of the crosstalk caused by other source line becomes particularly prominent.

[0022] That is, in the dual view display, as illustrated in FIG. 7, a specific viewing angle is given, by a parallax barrier **120** that is provided outside a display panel **110**, to each of first and second images that are produced by the display panel **110**. This allows, as illustrated in FIG. 8, displaying different images to a plurality of observers at different observation points, respectively.

[0023] In the dual view display, data of a different image is provided to each source line. The display is performed by separating, with the use of the parallax barrier, the different images into different directions, respectively. Accordingly, the image data of the adjacent source lines relate to different images, respectively. As a result, the influence of the crosstalk to a visible image becomes large.

[0024] The present invention is attained in view of the above problem. The object of the present invention is to reduce, by a simple method, color crosstalk in a liquid crystal display that performs a dual view display.

[0025] In order to achieve the object above, in a liquid crystal display apparatus of the present invention allowing a display mode in which a different image can be displayed with respect to each of a plurality of display directions to be realized by bonding a liquid crystal panel and a parallax barrier, the liquid crystal panel being provided with a display pixel including a switching element and a pixel electrode which display pixel corresponds to each intersection of a plurality of gate lines and a plurality of source lines: the parallax barrier separates display images viewed in different directions, respectively, by treating, as one unit, three pixels including R, G, and B pixels provided in a direction in which a gate line is extended; in a case where, among the three pixels constituting the one unit, a pixel that is present at one end in the direction in which the gate line is extended is a first display pixel and a pixel that is adjacent to the first display pixel and belongs to a display image that is separated into a display direction different from that of the first display pixel is a second display pixel, a source line connected to the second display pixel is adjacent to the first display pixel, the first display pixel is a display pixel of a B (blue) color, and an applied voltage to be supplied to the display pixel of the B (blue) color and an input gradation are set to have a relationship along a γ curve that makes luminance variation difficult to occur in a low luminance area, compared with applied voltages supplied to display pixels of R (red) and G (green) colors, respectively.

[0026] According to the arrangement, in a pixel other than the first display pixel, influence of crosstalk from other source line (other than a source line that supplies data to the pixel other than the first display pixel) is hard to appear because the pixel other than the first display pixel and a pixel that is connected to the other source line relate to the same image and are highly correlated to each other. On the other hand, influence of crosstalk that is caused by other source line (other

than a source line that supplies data to the first display pixel) easily appears in the first display pixel because the first display pixel and a pixel that is connected to the other source line relates to images different from each other and are not correlated.

[0027] In other words, influence of the crosstalk is concentrated in the first display pixel, by treating three pixels including R, G, and B pixels as one unit in separation of display images with the use of a parallax barrier at the time when a dual view display is performed. By arranging the first display pixel to be a B pixel that has a low correlation with luminance information, a change in a luminance due to the crosstalk can be suppressed. Accordingly, influence of the crosstalk to a display screen can be reduced.

[0028] Further, the applied voltage to be supplied to the display pixel of the B (blue) color and the input gradation are set to have a relationship along a γ curve that makes luminance variation difficult to occur in a low luminance area (dark area), compared with applied voltages supplied to display pixels of R (red) and G (green) colors, respectively (γ of B is set large in the dark area).

[0029] This is for two reasons. A ratio of luminance variation due to crosstalk becomes larger in a darker area. However, in an area that is darker than a certain level, even if the luminance changes at a large ratio, the luminance variation becomes insensible. In a bright area, because the ratio of the luminance variation is small, it is clearly not necessary to care about color crosstalk. That is, influence of the color crosstalk is the largest in a half-tone area that is relatively dark. The relatively dark area can be avoided by setting γ so that γ on a low gradation side is relatively large. γ may be separately set such that, for example, γ of a gradation not more than a certain level is 2.5 and γ of a gradation not less than the certain level is 2.2. In particular, it is preferable to set γ so that γ of a gradation not less than 128^{th} gradation is 2.2 and γ of a gradation not more than 128^{th} gradation varies sequentially from 2.2 towards 2.5 that is γ at 0^{th} gradation. This is for the purpose of realizing a gradation display characteristic that is relatively sequential and smooth even in a case where the crosstalk tends to influence the gradation display. The γ of 2.5 that is set here is one example. The γ may be set as appropriate according to an application, as long as a video image does not deviate largely from the image in a case where γ is set to 2.2 as in a general case.

BRIEF DESCRIPTION OF DRAWINGS

[0030] FIG. 1(a) is a plan view showing an embodiment of the present invention and a positional relationship of a picture element and R, G, and B pixels in a color liquid crystal display apparatus.

[0031] FIG. 1(b) is a diagram showing the embodiment of the present invention and illustrating an example of a structure in a case where separation is carried out by providing a barrier light-blocking layer and treating three pixels including R, G, and B pixels as one unit.

[0032] FIG. 2 is a cross sectional view schematically illustrating an arrangement of the color liquid crystal display apparatus.

[0033] FIG. 3 is a graph illustrating an example of a setting of a γ curve.

[0034] FIG. 4 is a block diagram schematically illustrating a structure of the color liquid crystal display apparatus.

[0035] FIG. 5 is a block diagram schematically illustrating an example of an arrangement of the color liquid crystal display apparatus which example is different from the arrangement of FIG. 4.

[0036] FIG. 6(a) is a diagram illustrating an arrangement of a display panel in a conventional liquid crystal display apparatus.

[0037] FIG. 6(b) is a diagram illustrating a state in which a voltage is applied to a gate line.

[0038] FIG. 7 is a diagram illustrating an effect of giving a viewing angle with the use of a viewing barrier in a dual view display.

[0039] FIG. 8 is a diagram illustrating a relationship between a display screen and observers in a case where a dual view display is performed.

BEST MODE FOR CARRYING OUT THE INVENTION

[0040] One embodiment of the present invention is explained with reference to drawings.

[0041] First, FIG. 2 schematically illustrates an arrangement of a liquid crystal display apparatus 1 of the present embodiment. The liquid crystal display apparatus 1 is a color liquid crystal display apparatus that is capable of performing a dual view display. As illustrated in FIG. 2, roughly, the liquid crystal display apparatus 1 includes a display panel 100, a parallax barrier 110, and a backlight 120.

[0042] The backlight 120 includes a light source 121 and a reflecting section 122. The reflecting section 122 reflects light that is emitted from the light source 121, so that light is irradiated on the display panel 100. Examples of the light source 121 are an LED (Light Emitting Diode), a CCFT (Cold Cathode Fluorescent Tube), and a CCFL (Cold Cathode Fluorescent Lamp).

[0043] The display panel 100 is an active matrix type liquid crystal display panel in which a liquid crystal layer 103 made of a nematic liquid crystal is sandwiched between a TFT (Thin Film Transistor) substrate 101 and a CF (Color Filter) substrate 102 that are provided so as to face each other.

[0044] The TFT substrate 101 is provided with a plurality of source lines and a plurality of gate lines each intersecting each of the source lines. At each intersection of a source line and a gate line, a pixel is provided. As illustrated in FIG. 2, the pixels are provided along a direction in which a data signal line (not shown) is extended so that pixels of a left picture element line for an image display to a left side (an image display to a left side of the display apparatus) is provided alternatively with pixels of a right picture element line for an image display to a right side (an image display to a right side of the display apparatus). As illustrated in FIG. 1(a), each of the left picture element and the right picture element are formed so as to have R, G, and B pixels as one set.

[0045] On the CF substrate 102, a color filter layer (not shown) is provided. In the color filter layer, R, G, and B filters are provided so as to correspond to respective pixels.

[0046] On opposed surfaces of the TFT substrate 101 and the CF substrate 102 are provided with alignment films (not shown), respectively. The alignment films have been subjected to alignment treatments in directions that are substantially perpendicular to each other, respectively. A surface of the TFT substrate 101 on a side provided with the backlight 120 is provided with a polarizer 104.

[0047] The parallax barrier 110 is made of a barrier glass 111 and a barrier light-blocking layer 112. The barrier light-

blocking layer 112 is formed by patterning a metal layer or a resin layer on the barrier glass 111. On a display surface side of the barrier glass 111 (an opposite side with respect to a side provided with the backlight 120), a polarizer 23 is provided.

[0048] The barrier light-blocking layer 112 is provided so as to form, for example, striped lines in a direction parallel to a direction in which the picture element lines are extended. A material of the barrier light-blocking layer 112 is not specifically limited. The barrier light-blocking layer 112 may be formed, for example, by using a photosensitive resin in which black pigments are dispersed or by patterning a metal thin film.

[0049] Further, each line of the barrier light-blocking layer 112 is provided so as to correspond to each picture element line of the display panel 100. Namely, the barrier light-blocking layer 112 separates a right image and a left image by treating three pixels including R, G, and B pixels as one unit. FIG. 1(b) illustrates an example of a structure in a case where separation is performed by providing the barrier light-blocking layer 112 and treating three pixels including R, G, and B pixels as one unit.

[0050] In this way, as illustrated in FIGS. 1(a) and 1(b), the separation of a right image and a left image with the use of the barrier light-blocking layer 112 is performed by treating the three pixels (corresponding to R, G, and B pixels) as one unit. In such a case, in a structure in which data is supplied to each pixel from a source line on the left side of the pixel, crosstalk that is caused by other source line largely influences only a pixel at the right end among the three pixels constituting one unit.

[0051] In other words, in the above-mentioned structure, a left-end pixel (R pixel in FIGS. 1(a) and 1(b)) among the three pixels constituting one unit is influenced by crosstalk from a source line that supplies data to a pixel (a center pixel among the three pixels constituting one unit: G pixel in FIGS. 1(a) and 1(b)) immediately on the right of the left-end pixel. However, the left-end pixel and the center pixel relate to an identical image. Therefore, the left-end pixel and the center pixel are highly correlated to each other. Accordingly, even when crosstalk occurs, influence of the crosstalk is hard to appear in a visible image. In the same manner, although the center pixel is influenced by crosstalk from a source line that supplies data to a right-end pixel (B pixel in FIGS. 1(a) and 1(b)) immediately on the right of the center pixel, the crosstalk is hard to appear in a visible image.

[0052] On the other hand, the right-end pixel is influenced by crosstalk from a source line that supplies data to another left-end pixel immediately on the right of the right-end pixel. Here, the right-end pixel and the another left-end pixel relates to different images, respectively. Therefore, display data of the right-end pixel and the another left-end pixel are not correlated to each other. Therefore, the right-end pixel is influenced by crosstalk more than the left-end pixel and the center pixel.

[0053] The above explanation assumes a structure in which data is supplied to each pixel from a source line that is provided on the left of the pixel. Accordingly, the right-end pixel is largely influenced by crosstalk. However, in the present invention, it is assumed that, among three pixels constituting one unit, (i) a first display pixel is a pixel provided to one end in a direction in which the gate line is extended and (ii) a second display pixel is a pixel that is adjacent to the first display pixel and belongs to a display image separated into a display direction different from that of a display image to

which the first display pixel belongs. On this assumption, the first display pixel is a pixel that is largely influenced by crosstalk. In such a case, an end pixel on a side adjacent to a source line that connects to the second display pixel is the first display pixel.

[0054] Here, the present invention has a feature such that the first display pixel is arranged to be a B pixel, as illustrated in FIGS. 1(a) and 1(b), so that influence of crosstalk to the first display pixel is reduced.

[0055] That is, the larger a change in luminance due to crosstalk that is caused by other source line is, the more easily the crosstalk is viewed. On the other hand, in regard to correlation of each of R, G, and B colors and a luminance, R and G colors have a high correlation with luminance information while B color has a low correlation with luminance information. Accordingly, by arranging the first display pixel to which the influence of crosstalk is large to be a B color pixel whose correlation with luminance information is low, a change in the luminance due to crosstalk can be suppressed and influence of the crosstalk to a display screen can be reduced.

[0056] In other words, in the liquid crystal display apparatus 1 of the present embodiment, separation of display images with the use of a parallax barrier is performed by treating the three pixels including R, G, and B pixels as one unit, when a dual view display is performed. This concentrates the influence of the crosstalk in the first display pixel. Further, by arranging the first display pixel to be a B pixel that has a low correlation with luminance information, a change in the luminance can be suppressed, thereby reducing the influence to the display screen.

[0057] Further, in the liquid crystal display apparatus 1, γ correction capable of further suppressing influence of crosstalk is carried out in the first display pixel in which the influence of the crosstalk is large.

[0058] Specifically, when the γ correction is carried out with respect to data of a B color, a γ curve of the data is set deep so that influence of crosstalk in a dark area becomes hard to see. This is because a color shift due to a change in luminance is more easily seen in a dark area than in a bright area. By setting a γ value of B in a dark area to be small, a change in luminance in the display region of B becomes hard to be produced even when a potential changes due to an adjacent source line (R).

[0059] FIG. 3 as follows illustrates an example of a setting of the γ curve. A curve illustrated by a solid line in FIG. 3 shows a general γ curve that is generally used. However, in the liquid crystal display apparatus 1 of the present embodiment, the curve illustrated by the solid line in FIG. 3 is used for γ correction with respect to data of R and G colors. Moreover, a curve illustrated by a dotted line in FIG. 3 shows a curve that is used for the γ correction with respect to data of a B color.

[0060] The following explains an example of a circuit configuration that carries out γ correction with respect to an input gradation signal and generates a write signal for a liquid crystal panel in the liquid display apparatus 1 of the present embodiment.

[0061] As illustrated in FIG. 4, the liquid crystal display apparatus 1 includes a display panel 10, a gate driving section 11, a source driving section 12, a common electrode driving section 13, and a control section 14.

[0062] The display panel 10 (detailed illustration thereof is omitted here) includes m gate lines that are parallel to each other, n source lines that are parallel to each other, and pixels arranged in a matrix. Each pixel is formed in a region that is

surrounded by two of the gate lines adjacent to each other and two of the source lines adjacent to each other.

[0063] The gate driving section 11 sequentially generates scanning signals that are to be provided to the gate lines to which pixels in each line are connected, according to gate clock signals and gate start pulses that are outputted from the control section 14.

[0064] The source driving section 12 samples image data signals DAT in accordance with the source clock signals and the source start pulses that are outputted from the control section 14, and outputs image data obtained to the source lines to which pixels in each line are connected.

[0065] The control section 14 is a circuit that generates and outputs, in accordance with a sync signal and an image data signal DAT that are inputted, various kinds of control signals that control operations of the gate driving section 11 and the source driving section 12. As mentioned above, clock signals, start pulses, the image data signals DAT, and the like are prepared as control signals that are outputted from the control section 14.

[0066] Each pixel of the display panel 10 includes a switching element such as a TFT, and a liquid crystal capacitor. In such a pixel, a gate of the TFT is connected to a gate line. Moreover, a source line is connected to one electrode of the liquid crystal capacitor via a drain and a source of the TFT. Further, the other end of the liquid crystal capacitor is connected to a common electrode line that is common to all the pixels. The common electrode driving section 13 is arranged to supply a voltage to be applied to the common electrode line.

[0067] In the liquid crystal display apparatus 1, the gate driving section 11 selects a gate line. Then, the source driving section 12 outputs, to each source line, an image data signal DAT for a pixel corresponding to a combination of the gate line and a source line that are being selected. This allows each image data to be written into the pixel that is connected to the gate line. Further, the gate driving section 11 sequentially selects the gate lines and the source driving section 12 outputs image data to the source lines. As a result, the respective image data is written into all of the pixels of the display panel 10, and an image corresponding to the image data signals DAT is displayed on the display panel 10.

[0068] Here, the image data sent from the control section 14 to the source driving section 12 is transmitted as image data signals DAT in a time-sharing manner. The source driving section 12 extracts each image data from the image data signals DAT according to timing on the basis of a source clock signal, an inversion source clock signal, and a source start pulse that become timing signals, and transmits the extracted image data to each pixel.

[0069] Next, the γ correction in the liquid crystal display apparatus 1 is explained.

[0070] For example, in the liquid crystal display apparatus that performs multi-gradation display including 256 gradations, 256 kinds of applied voltage values are required. However, in practice, it is not possible to include power source voltages that correspond to all of these gradation voltages, respectively. Therefore, in general, several kinds of reference voltages are prepared as the power source voltages. By dividing these reference voltages by resistance dividing means into partial voltages, applied voltages (gradation quasi-voltages) that correspond to all the gradations, respectively, are generated.

[0071] The resistance dividing means are composed of many resistors connected in series. An applied voltage that is obtained at each of connecting points of respective resistors is extracted according to switching control in accordance with the image data signal DAT. That is, the image data signal DAT is, for example, an 8-bit digital signal (in case where the number of gradations is 256). A desired applied voltage can be extracted from the 256 kinds of applied voltages, by carrying out 8-stage switching control with the use of each bit signal. Note that such resistance dividing means is a publicly known arrangement that is conventionally used in a liquid crystal display of a voltage modulation system. The source driving section 12 includes a gradation voltage generating circuit (not shown) that employs the resistance dividing means as mentioned above.

[0072] Here, a relationship between the gradation and the applied voltage in the liquid crystal apparatus is not proportional, but has a specific γ curve. Accordingly, a resistance value of each resistor is set so that the resistance dividing means of the gradation voltage generating circuit obtains a gradation voltage along the γ curve. In this way, the resistance value of each resistor is not set so that the reference voltages are proportionally distributed.

[0073] In the liquid crystal display apparatus 1, the gradation voltage generation circuit that supplies a gradation voltage to a source line that is connected to a B-color pixel should be set so that it becomes possible to obtain a gradation voltage along γ curve with respect to the B-color data in FIG. 3.

[0074] A method of the γ correction in the liquid crystal display apparatus 1 is not limited to this. The γ correction may be carried out by carrying out data conversion with respect to image data of a B-color. The γ correction in such a case is explained with reference to FIG. 5.

[0075] In this case, as illustrated in FIG. 5, the liquid crystal display apparatus 1 further includes a γ correction section 15 and a lookup table 16. In other words, the γ correction section 15 carries out data conversion with reference to the lookup table 16 so that image data of a B-color pixel stays along the γ curve with respect to the B-color data in FIG. 3.

[0076] Specifically, among data signal's DAT that are inputted into the control section 14, image data of R and G colors are directly sent to the source driving section 12. However, image data of a B color is subjected to data conversion with the use of the γ correction section 15 and the lookup table 16, and then sent to the source driving section 12.

[0077] The lookup table 16 stores an input gradation level and an output gradation level so that the input gradation level and the output gradation level correspond to each other. When an input gradation level of the image data signal DAT is inputted from the γ correction section 15, an output gradation level corresponding to this input gradation level is read out. The γ correction section 15 outputs the output gradation level that is read out from the lookup table 16 to the source driving section 12.

[0078] For example, in the liquid crystal display apparatus 1, an ideal applied voltage along the γ curve with respect to the data of a B color in FIG. 3 is in a range of $V'0$ to $V'255$ (in the case of 256-gradation display). The following Table 1 is shows a relationship between (i) each of the applied voltages $V'0$ to $V'255$ and (ii) an applied voltage among the applied voltages $V0$ to $V255$ that are generated by the resistance dividing means for carrying out the 256 gradation display which applied voltage is the closest to the each of the applied voltages $V'0$ to $V'255$. Note that the Table 1 below shows an

example in which a gradation equal to or less than 128^{th} gradation is set to γ 2.5. Moreover, in the Table 1, the output gradation indicates a voltage that corresponds to the output gradation.

TABLE 1

IG	OG(B)
0	0
1	0
2	1
3	1
4	2
5	2
6	3
7	4
8	5
9	5
10	6
11	7
12	8
13	9
14	9
15	10
16	11
17	12
18	13
19	14
20	14
21	15
22	16
23	17
24	18
25	19
26	20
27	21
28	22
29	23
30	23
31	24
32	25
33	26
34	27
35	28
36	29
37	30
38	31
39	32
40	33
41	34
42	35
43	36
44	37
45	38
46	39
47	40
48	41
49	42
50	43
51	44
52	45
53	46
54	47
55	48
56	49
57	50
58	51
59	52
60	54
61	55
62	56
63	57
64	58
65	59
66	60
67	61
68	62

TABLE 1-continued

IG	OG(B)
69	63
70	64
71	65
72	66
73	67
74	68
75	69
76	71
77	72
78	73
79	74
80	75
81	76
82	77
83	78
84	79
85	80
86	81
87	83
88	84
89	85
90	86
91	87
92	88
93	89
94	90
95	91
96	92
97	93
98	95
99	96
100	97
101	98
102	99
103	100
104	101
105	102
106	103
107	104
108	106
109	107
110	108
111	109
112	110
113	111
114	112
115	113
116	114
117	115
118	117
119	118
120	119
121	120
122	121
123	122
124	123
125	124
126	125
127	126
128	128
129	129
130	130
131	131
132	132
133	133
134	134
135	135
136	136
137	137
138	138
139	139
140	140
141	141
142	142

TABLE 1-continued

IG	OG(B)
143	143
144	144
145	145
146	146
147	147
148	148
149	149
150	150
151	151
152	152
153	153
154	154
155	155
156	156
157	157
158	158
159	159
160	160
161	161
162	162
163	163
164	164
165	165
166	166
167	167
168	168
169	169
170	170
171	171
172	172
173	173
174	174
175	175
176	176
177	177
178	178
179	179
180	180
181	181
182	182
183	183
184	184
185	185
186	186
187	187
188	188
189	189
190	190
191	191
192	192
193	193
194	194
195	195
196	196
197	197
198	198
199	199
200	200
201	201
202	202
203	203
204	204
205	205
206	206
207	207
208	208
209	209
210	210
211	211
212	212
213	213
214	214
215	215
216	216

TABLE 1-continued

IG	OG(B)
217	217
218	218
219	219
220	220
221	221
222	222
223	223
224	224
225	225
226	226
227	227
228	228
229	229
230	230
231	231
232	232
233	233
234	234
235	235
236	236
237	237
238	238
239	239
240	240
241	241
242	242
243	243
244	244
245	245
246	246
247	247
248	248
249	249
250	250
251	251
252	252
253	253
254	254
255	255

(IG: INPUT GRADATION, OG(B): OUTPUT GRADATION (B))

[0079] In other words, in case where γ correction of the image data of a B color is to be performed along the γ curve with respect to the data of a B color in FIG. 3, for example, an applied voltage V1 that is generated by the resistance dividing means becomes the closest to an ideal applied voltage V'3 with respect to display of the input gradation level 3. Accordingly, the lookup table 16 should be set to have an output gradation level of 1 in a case where the input gradation level of the image data of a B color is 3.

[0080] As mentioned above, in a liquid crystal display apparatus of the present invention allowing a display mode in which a different image can be displayed with respect to each of a plurality of display directions to be realized by bonding a liquid crystal panel and a parallax barrier, the liquid crystal panel being provided with a display pixel including a switching element and a pixel electrode which display pixel corresponds to each intersection of a plurality of gate lines and a plurality of source lines: the parallax barrier separates display images viewed in different directions, respectively, by treating, as one unit, three pixels including R, G, and B pixels provided in a direction in which a gate line is extended; in a case where, among the three pixels constituting the one unit, a pixel that is present at one end in the direction in which the gate line is extended is a first display pixel and a pixel that is adjacent to the first display pixel and belongs to a display image that is separated into a display direction different from

that of the first display pixel is a second display pixel, a source line connected to the second display pixel is adjacent to the first display pixel, the first display pixel is a display pixel of a B (blue) color, and an applied voltage to be supplied to the display pixel of the B (blue) color and an input gradation are set to have a relationship along a γ curve that makes luminance variation difficult to occur in a low luminance area, compared with applied voltages supplied to display pixels of R (red) and G (green) colors, respectively.

[0081] According to the arrangement, in a pixel other than the first display pixel, influence of crosstalk from other source line (other than a source line that supplies data to the pixel other than the first display pixel) is hard to appear because the pixel other than the first display pixel and a pixel that is connected to the other source line relate to the same image and are highly correlated to each other. On the other hand, influence of crosstalk that is caused by other source line (other than a source line that supplies data to the first display pixel) easily appears in the first display pixel because the first display pixel and a pixel that is connected to the other source line relates to images different from each other and are not correlated.

[0082] In other words, influence of the crosstalk is concentrated in the first display pixel, by treating three pixels including R, G, and B pixels as one unit in separation of display images with the use of a parallax barrier at the time when a dual view display is performed. By arranging the first display pixel to be a B pixel that has a low correlation with luminance information, a change in a luminance due to the crosstalk can be suppressed. Accordingly, influence of the crosstalk to a display screen can be reduced.

[0083] Further, the applied voltage to be supplied to the display pixel of the B (blue) color and the input gradation are set to have a relationship along a γ curve that makes luminance variation difficult to occur in a low luminance area (dark area), compared with applied voltages supplied to display pixels of R (red) and G (green) colors, respectively (γ of B is set large in the dark area).

[0084] This is for two reasons. A ratio of luminance variation due to crosstalk becomes larger in a darker area. However, in an area that is darker than a certain level, even if the luminance changes at a large ratio, the luminance variation becomes insensible. In a bright area, because the ratio of the luminance variation is small, it is clearly not necessary to care about color crosstalk. That is, influence of the color crosstalk is the largest in a half-tone area that is relatively dark. Luminance variation in the relatively dark area can be avoided by setting γ so that γ on a low gradation side is relatively large. γ may be separately set such that, for example, γ of a gradation not more than a certain level is 2.5 and γ of a gradation not less than the certain level is 2.2. In particular, it is preferable to set γ so that γ of a gradation not less than 128th gradation is 2.2 and γ of a gradation not more than 128th gradation varies sequentially from 2.2 towards 2.5 that is γ at 0th gradation. This is for the purpose of realizing a gradation display characteristic that is relatively sequential and smooth even in a case where the crosstalk tends to influence the gradation display. The γ of 2.5 that is set here is one example. The γ may be set as appropriate according to an application, as long as a video image does not deviate largely from the image in a case where γ is set to 2.2 as in a general case.

[0085] Moreover, the liquid crystal display apparatus may include: a reference voltage generating circuit that generates an applied voltage to be supplied to the display pixel of the B

(blue) color, the reference voltage generating circuit performing an input gradation signal/applied voltage conversion along the γ curve that makes the luminance variation difficult to occur in the low luminance area.

[0086] According to the arrangement, an input gradation signal-application voltage relationship can be arranged along an optimum γ curve.

[0087] Further, the liquid crystal display apparatus may include: a data converting section converting input gradation data of the B (blue) color so as to generate an output gradation signal to be outputted to a data driving section, the data converting section carrying out data conversion so that an output gradation signal-applied voltage relationship is along the γ curve that makes the luminance variation difficult to occur in the low luminance area

[0088] According to the arrangement, it is not necessary to separately have a reference voltage generating circuit for a B (blue) color and a reference voltage generation circuit for R (red) and G (green) colors. Therefore, the gradation signal-applied voltage relationship can be arranged along an appropriate γ curve in a simple arrangement.

1. A display apparatus comprising:
 - a display panel including display pixels of at least a red (R) color, green (G) color, and blue (B) color, and which a set of at least one R color display pixel, at least one G color display pixel, and at least one B color display pixel serves as a unit of the display pixels; and
 - a parallax barrier disposed on the display panel, configured to separate images that are displayed on the display panel and viewed in different directions,
 - an applied voltage to be supplied to the display pixel of the B color and an input gradation being set to have a relationship along a γ curve, the applied voltage to be applied to the display pixel of the B color producing a luminance having a lesser variation than luminances produced by applied voltages supplied to display pixels of R and G colors, respectively.
2. The display apparatus as set forth in claim 1, further comprising:
 - a reference voltage generating circuit that generates an applied voltage to be supplied to the display pixel of the B color, the reference voltage generating circuit performing an input gradation signal/applied voltage conversion along the γ curve.
3. The display apparatus as set forth in claim 1, further comprising:
 - a data converting section converting input gradation data of the B color so as to generate an output gradation signal to be outputted to a data driving section,
 - the data converting section carrying out data conversion so that an output gradation signal-applied voltage relationship is along the γ curve.

4. A display apparatus comprising:

- a plurality of gate lines;
- a plurality of source lines intersecting the plurality of gate lines, the intersections corresponding to locations of at least a first plurality of pixels and a second plurality of pixels;
- at least a first picture element having the first plurality of pixels, the first picture element being configured to display a first image; and
- at least a second picture element having the second plurality of pixels, the second picture element configured to display a second image, the second image being in a different display direction than the first image; and
- a reference voltage generating circuit configured to generate an applied voltage for the first pixel of the first plurality of pixels based on an input gradation signal and a first γ curve, the reference voltage generating circuit configured to generate an applied voltage for the first pixel of the second plurality of pixels based on an input gradation signal and a second γ curve, the second γ curve being different than the first γ curve.

5. The display apparatus of claim 4, wherein the first plurality of pixels includes red, green and blue pixels, and the second plurality of pixels includes red, green and blue pixels.

6. The display apparatus of claim 5, wherein the first pixel of the first plurality of pixels is a blue pixel and the first pixel of the second plurality of pixels is a red pixel.

7. A display apparatus comprising:

- a display panel including display pixels of at least a red (R) color, green (G) color, and blue (B) color, and which a set of at least one R color display pixel, at least one G color display pixel, and at least one B color display pixel serves as a unit of a plurality of display pixels;
- a parallax barrier disposed on the display panel, configured to separate images that are displayed on the display panel and viewed in different directions;
- at least a first picture element having a first plurality of display pixels, the first picture element being configured to display a first image;
- at least a second picture element having a second plurality of display pixels, the second picture element configured to display a second image, the second image being in a different display direction than the first image; and
- a reference voltage generating circuit configured to generate an applied voltage for the first pixel of the first plurality of pixels based on an input gradation signal and a first γ curve, the reference voltage generating circuit configured to generate an applied voltage for the first pixel of the second plurality of pixels based on an input gradation signal and a second γ curve, the second γ curve being different than the first γ curve.

* * * * *

专利名称(译)	具有输入灰度的液晶显示装置被设定为具有沿伽马曲线的关系		
公开(公告)号	US20110194039A1	公开(公告)日	2011-08-11
申请号	US13/064791	申请日	2011-04-15
[标]申请(专利权)人(译)	潮见MAKOTO		
申请(专利权)人(译)	潮见MAKOTO		
当前申请(专利权)人(译)	夏普株式会社		
[标]发明人	SHIOMI MAKOTO		
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摘要(译)

在通过粘合液晶面板和视差屏障实现双视图显示的液晶显示装置中，视差屏障通过将包括R，G和B像素的三个像素视为一个单元（一个像素）来分离显示图像。此时，由串扰引起的亮度变化集中在构成一个像素的三个像素中的右端像素上（在每个像素从紧邻像素左侧的源极线接收数据的情况下）。因此，右端像素被布置为与亮度信息具有低相关性并且难以观察到串扰的影响的B像素。此外，将要提供给B（蓝色）显示像素的施加电压和输入灰度设置为具有沿 γ 曲线的关系，这使得在低亮度区域中难以发生亮度变化。

