



US 20120281172A1

(19) **United States**(12) **Patent Application Publication**
PARK et al.(10) **Pub. No.: US 2012/0281172 A1**(43) **Pub. Date: Nov. 8, 2012**(54) **LIQUID CRYSTAL DISPLAY**(30) **Foreign Application Priority Data**

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May 19, 2011 (KR) 10-2011-0047435

Publication Classification

(51) **Int. Cl.**
G02F 1/1337 (2006.01)
G02F 1/1343 (2006.01)

(52) **U.S. Cl.** **349/123; 349/139**

(57) **ABSTRACT**

A liquid crystal display according to an exemplary embodiment of the present invention includes: a first substrate; a pixel electrode disposed on the first substrate; a first alignment layer disposed on the first substrate and the pixel electrode; a second substrate facing the first substrate; a common electrode disposed on the second substrate; a second alignment layer disposed on the second substrate and the common electrode; and a liquid crystal layer disposed between the first substrate and the second substrate, wherein the common electrode has a plurality of cutouts having a cross shape, and when viewing the first substrate and the second substrate upward, the edge of the cutout is protruded from the edge of the pixel electrode.

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(21) Appl. No.: **13/460,667**(22) Filed: **Apr. 30, 2012****Related U.S. Application Data**

(60) Provisional application No. 61/481,546, filed on May 2, 2011.

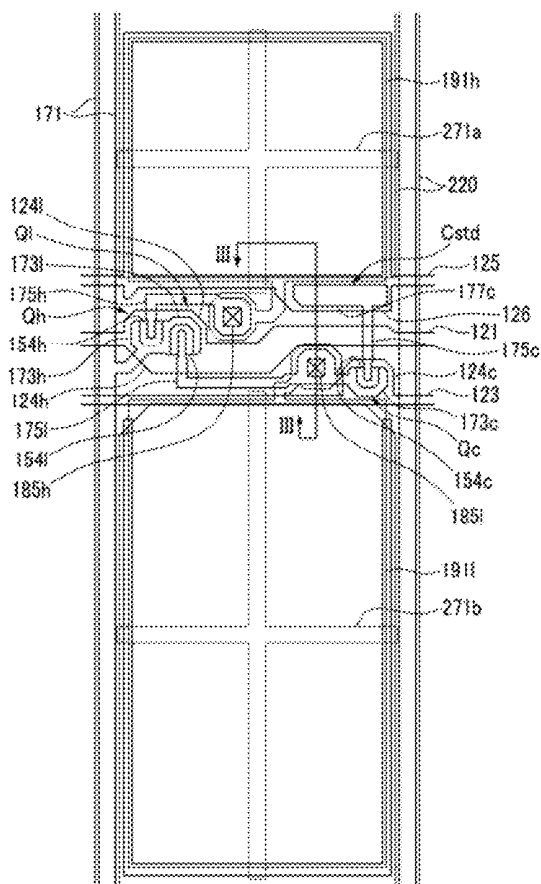


FIG.1

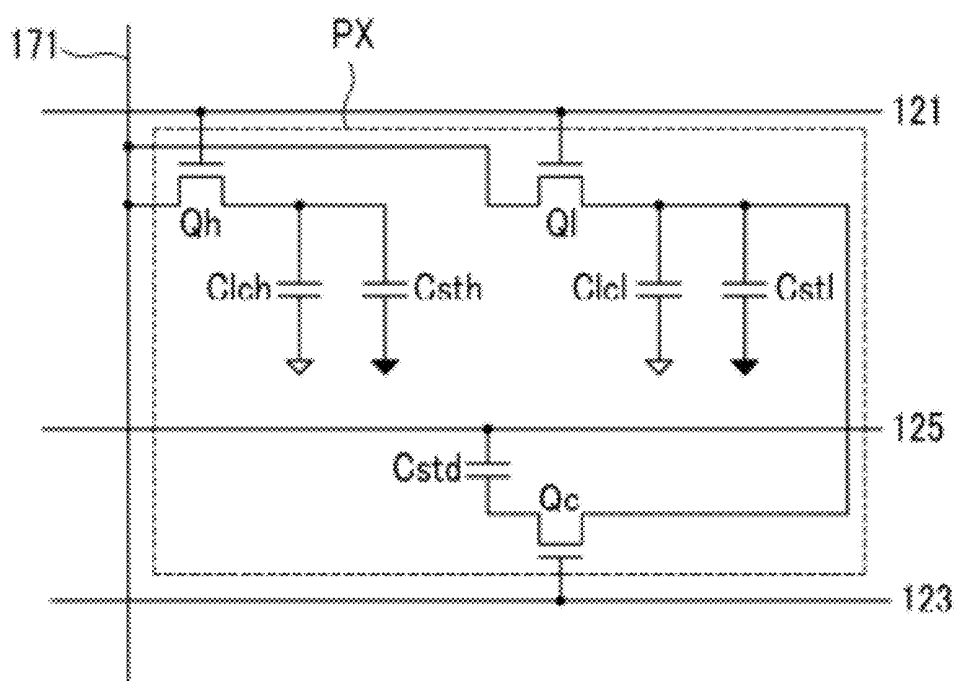
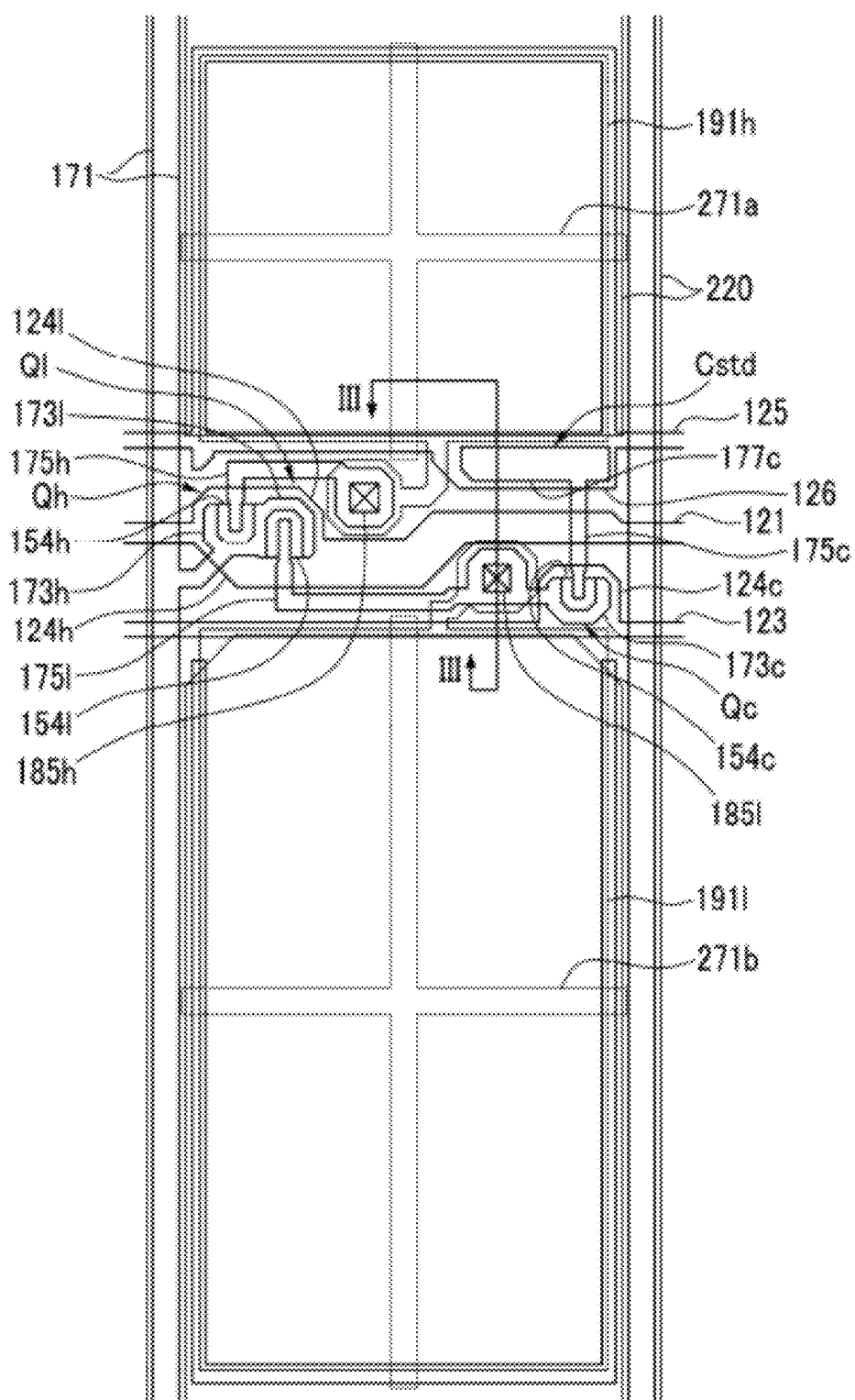


FIG.2



361

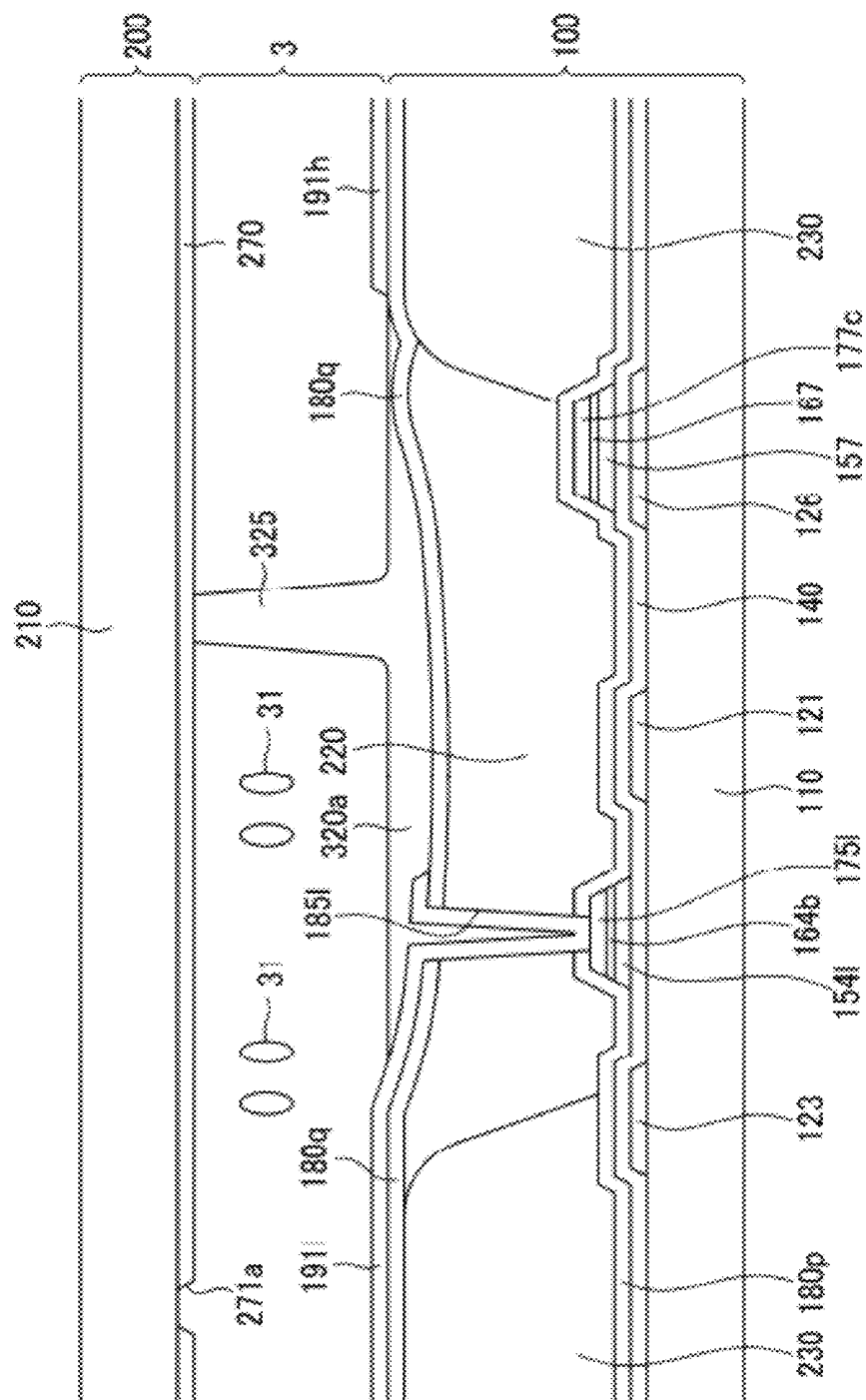


FIG.4

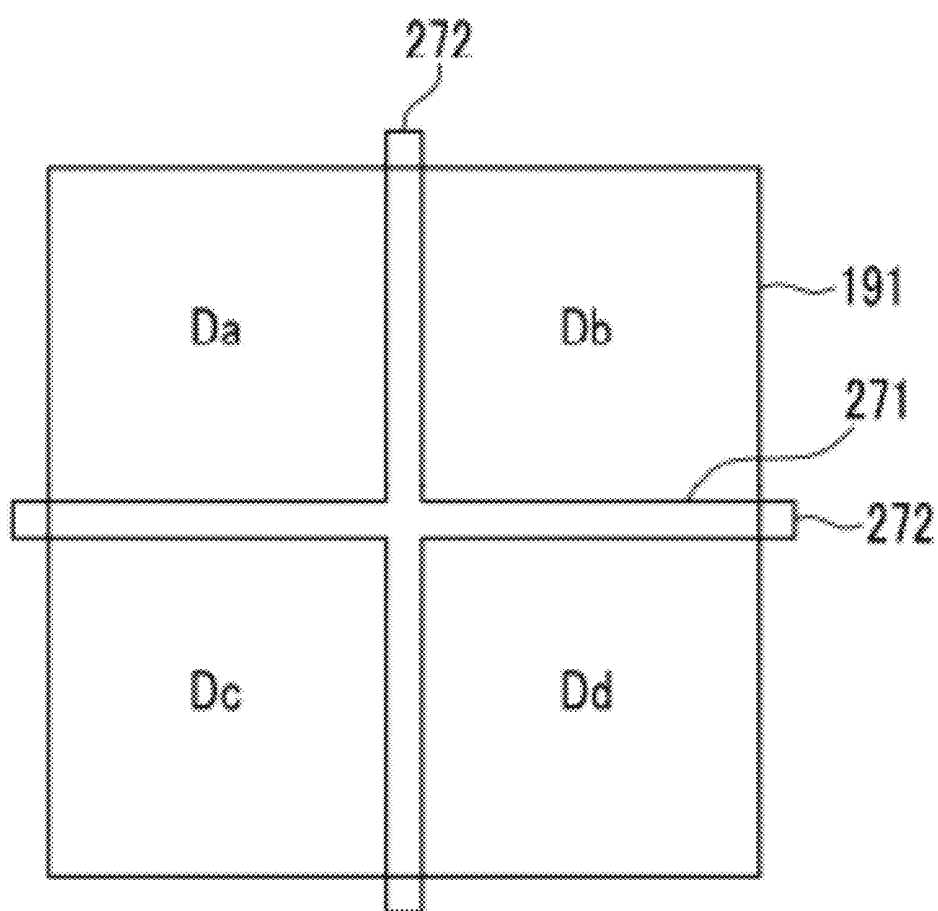


FIG. 5

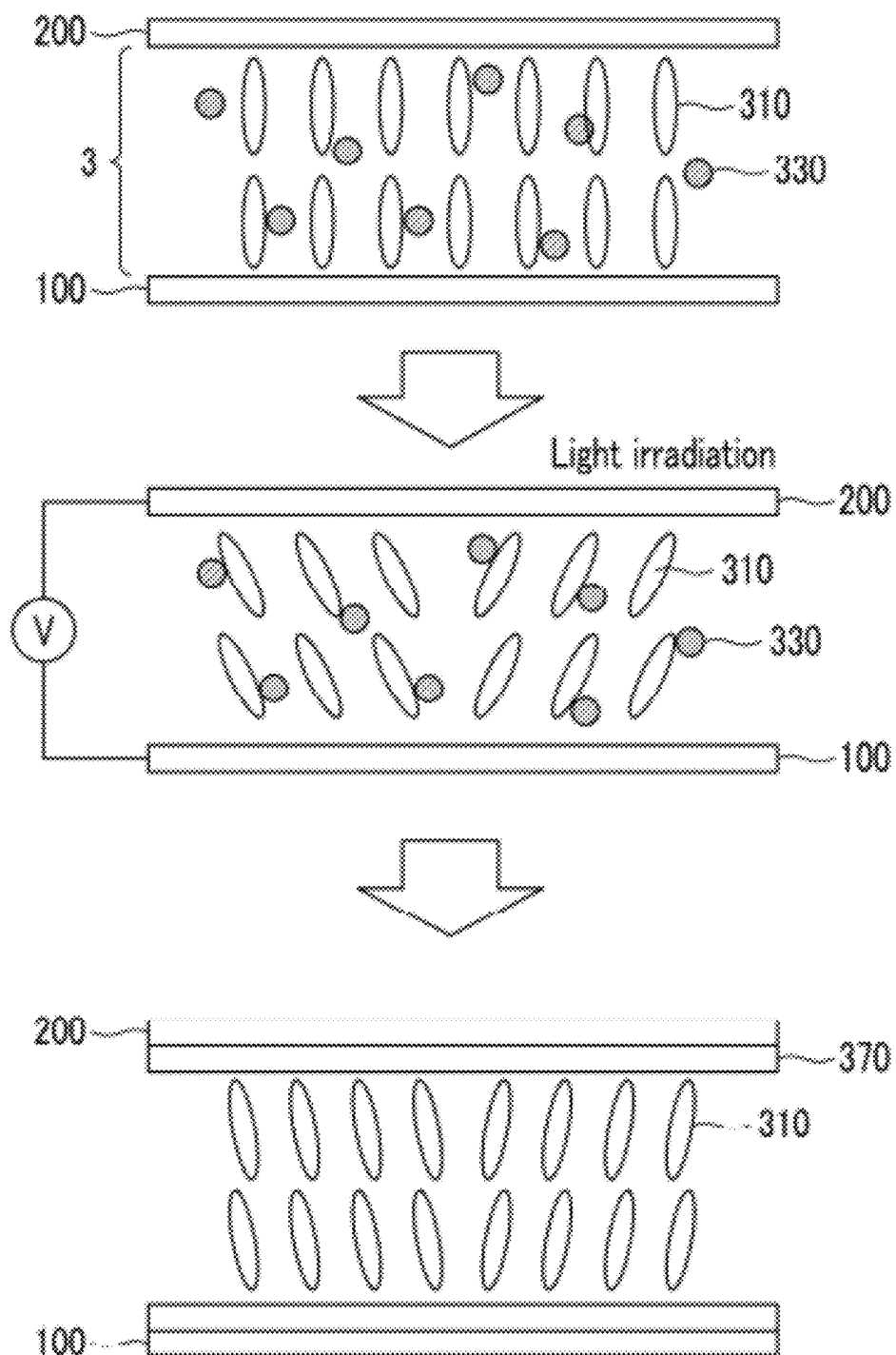


FIG. 6

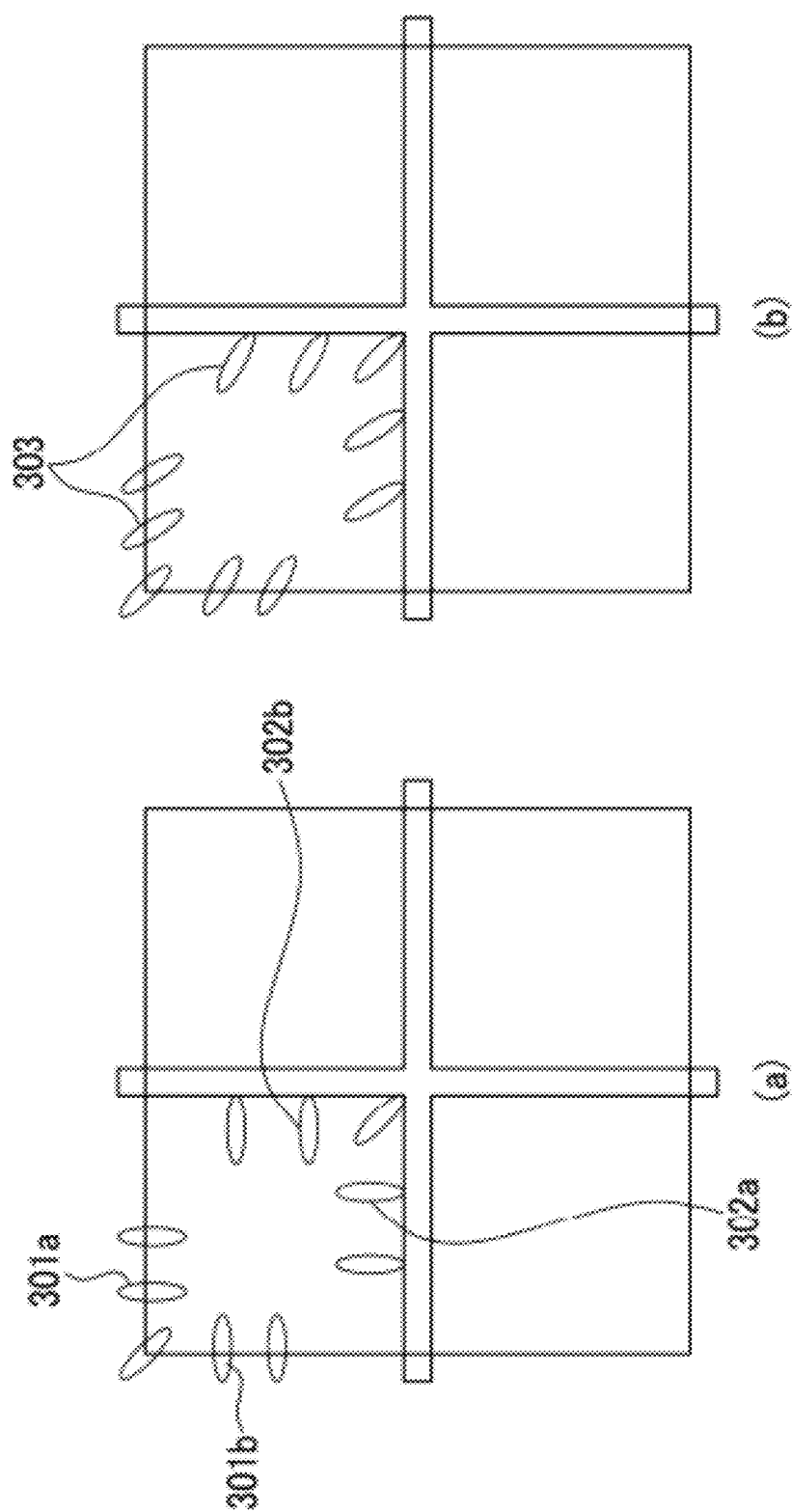
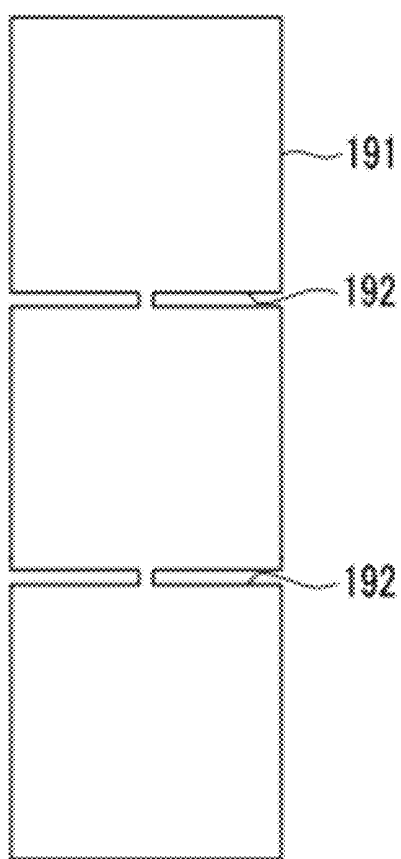
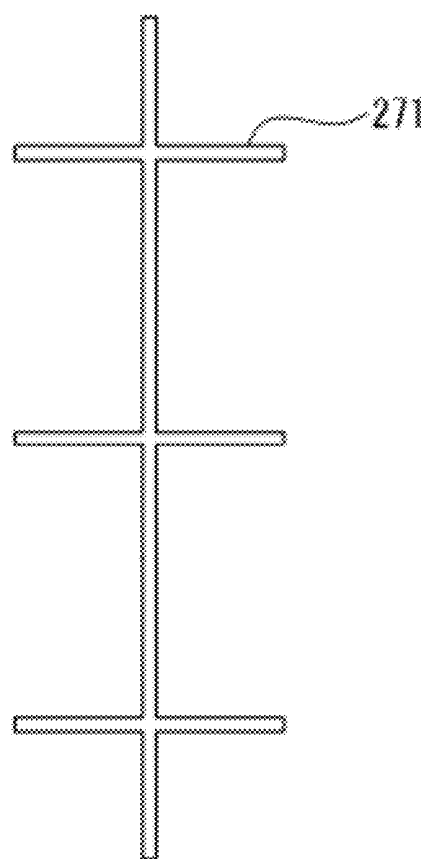


FIG. 7

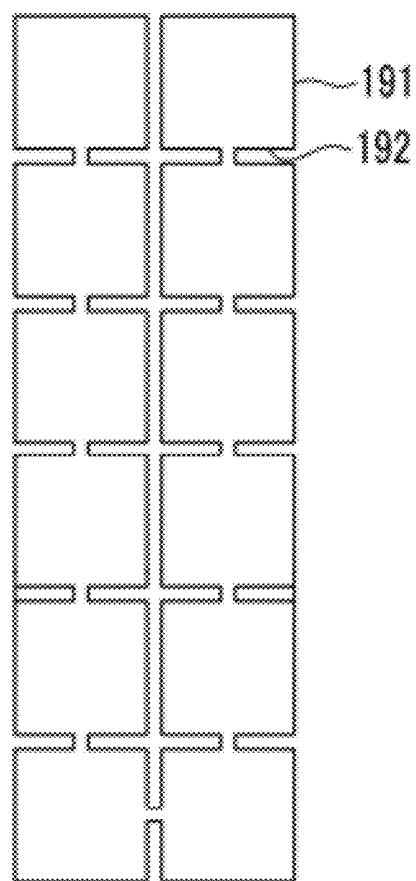


(a)

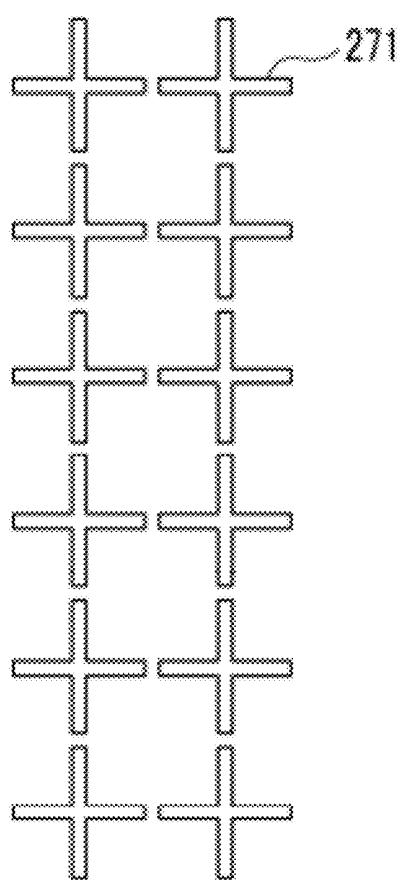


(b)

FIG. 8



(a)



(b)

FIG. 9

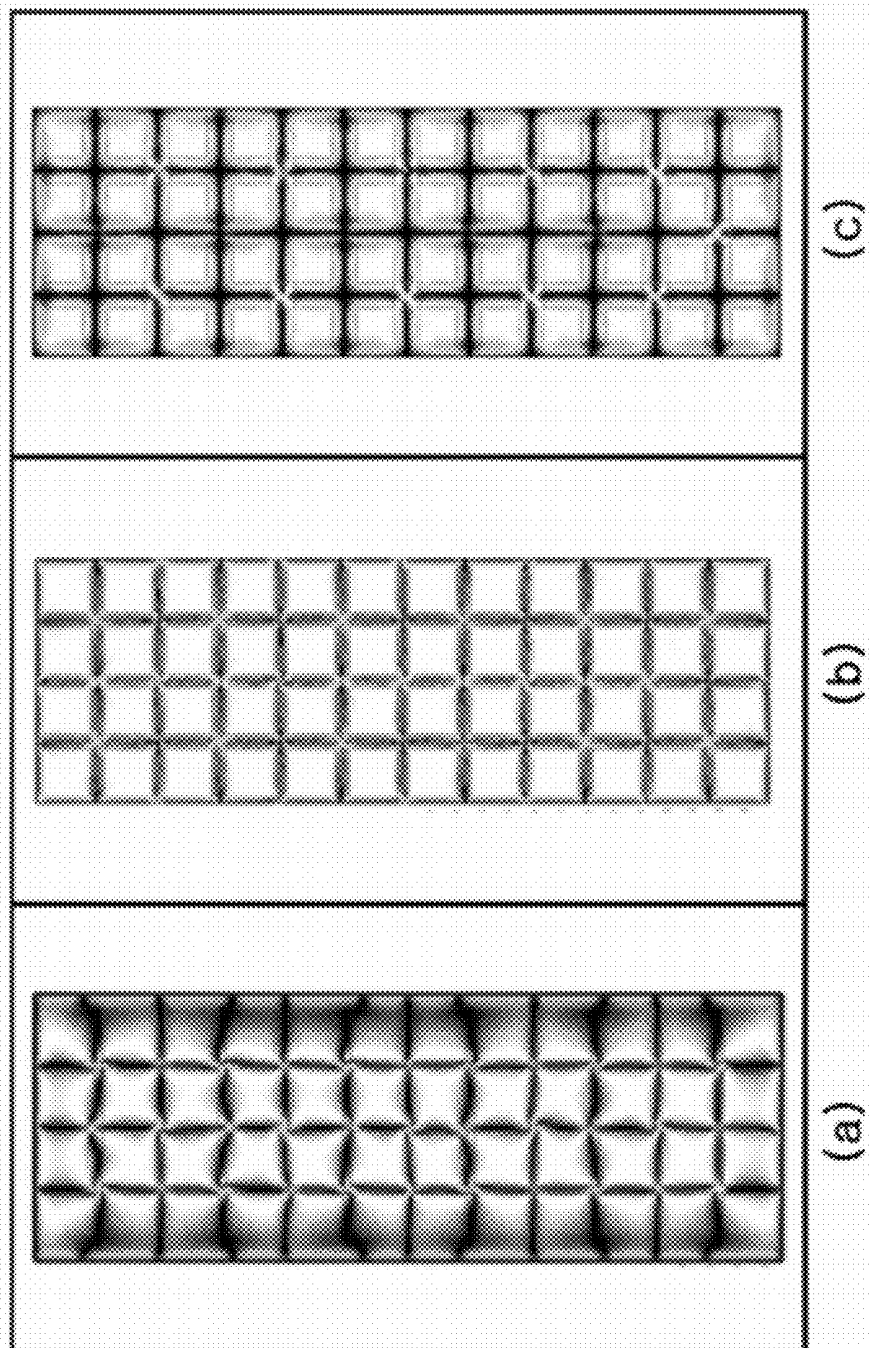


FIG.10

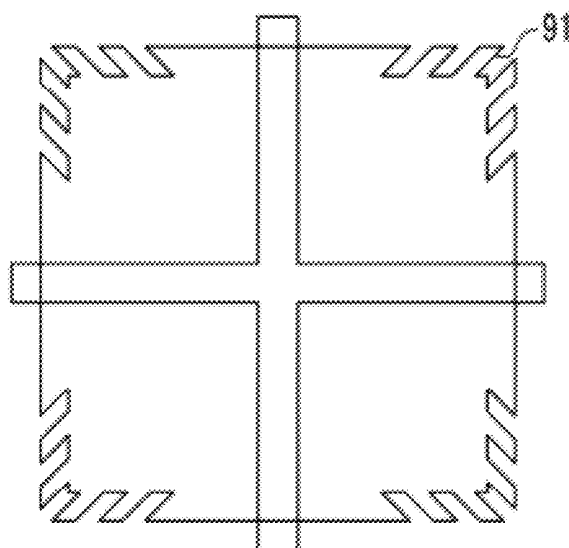


FIG.11

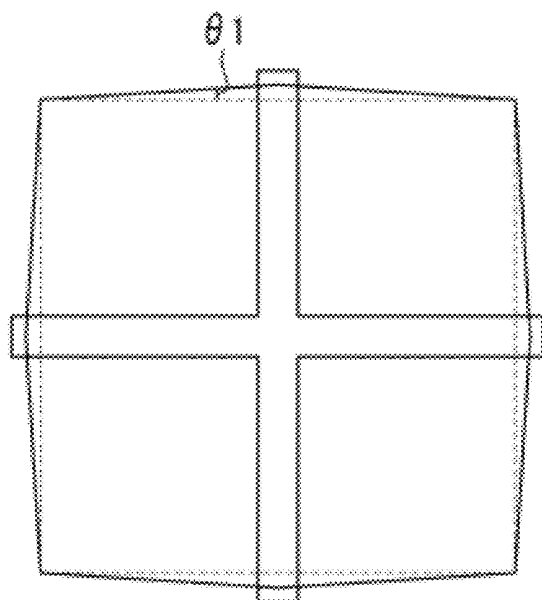


FIG.12

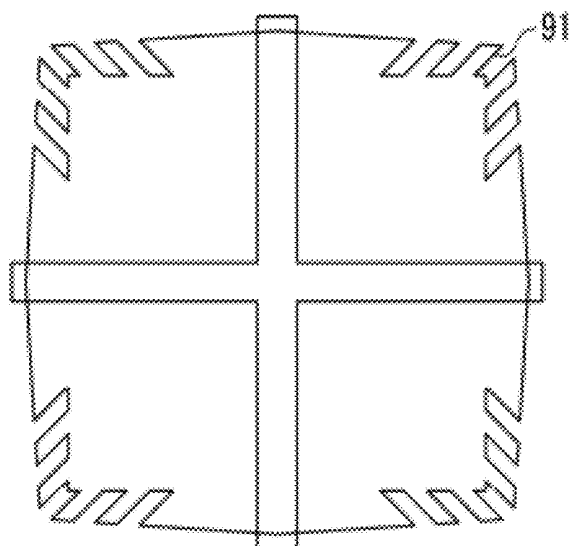


FIG.13

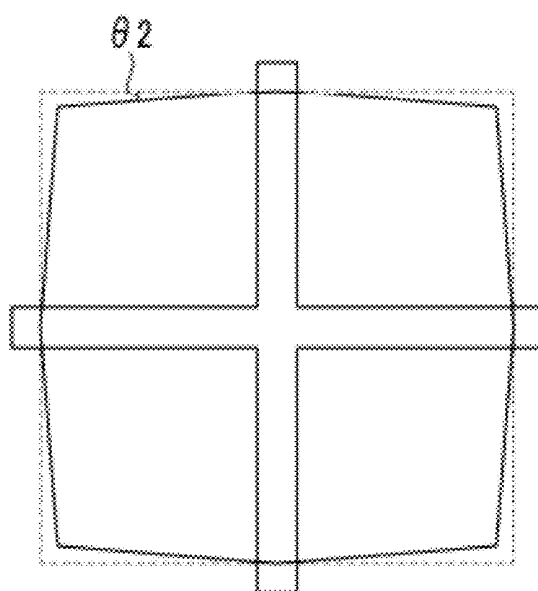


FIG.14

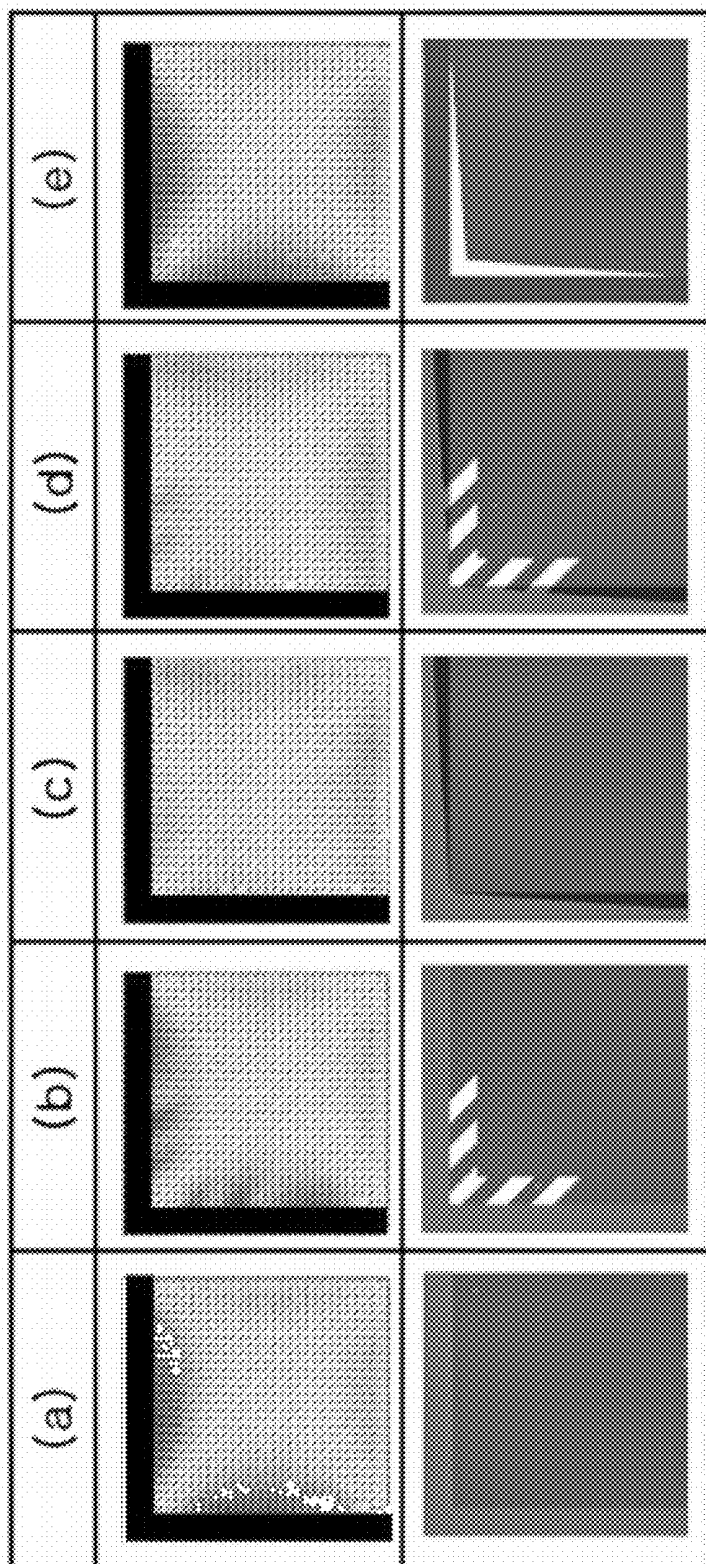


FIG.15

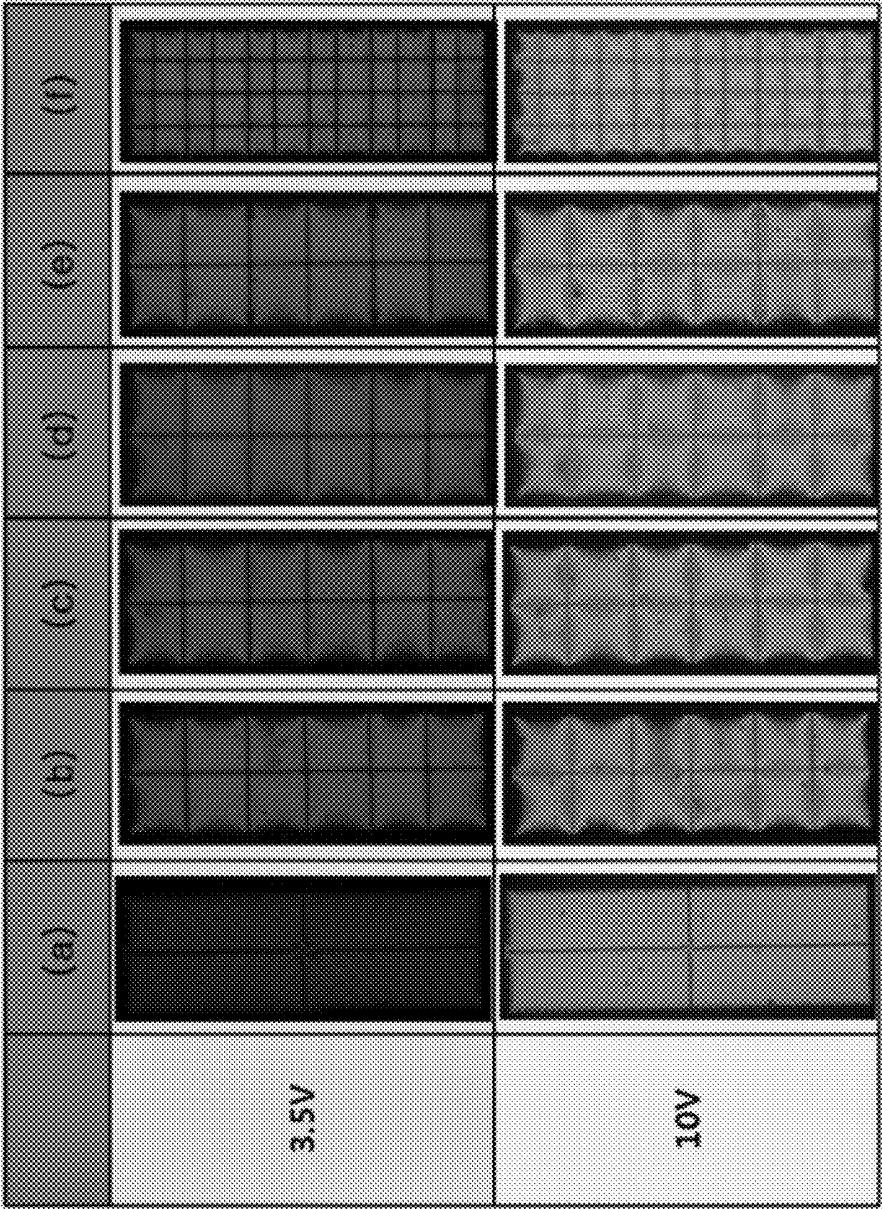


FIG. 16

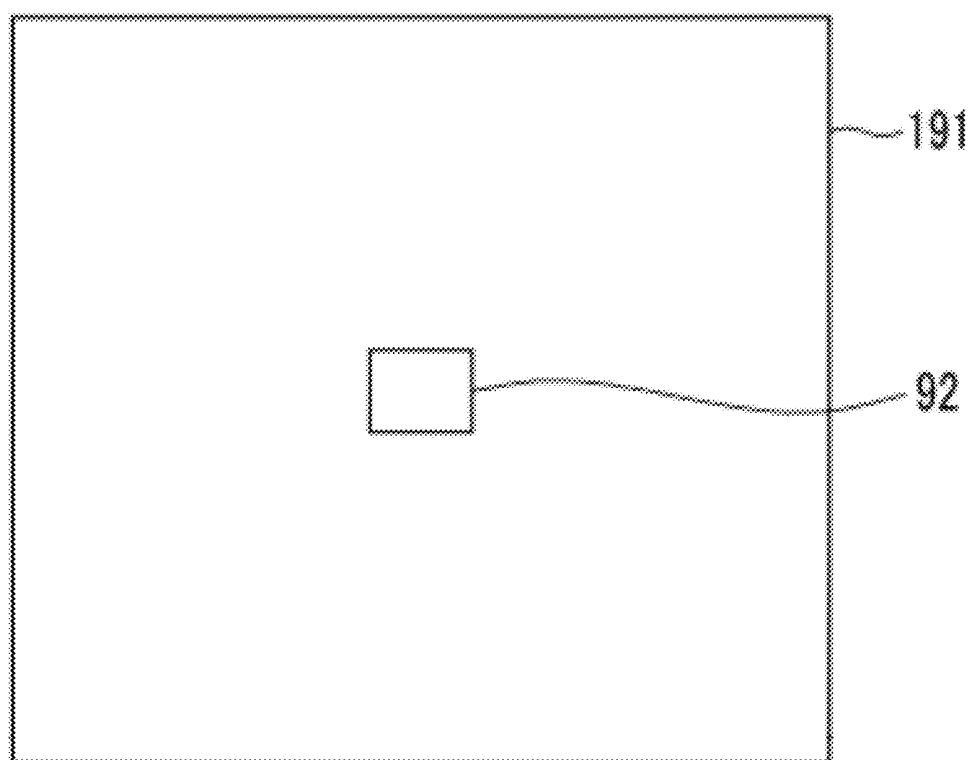
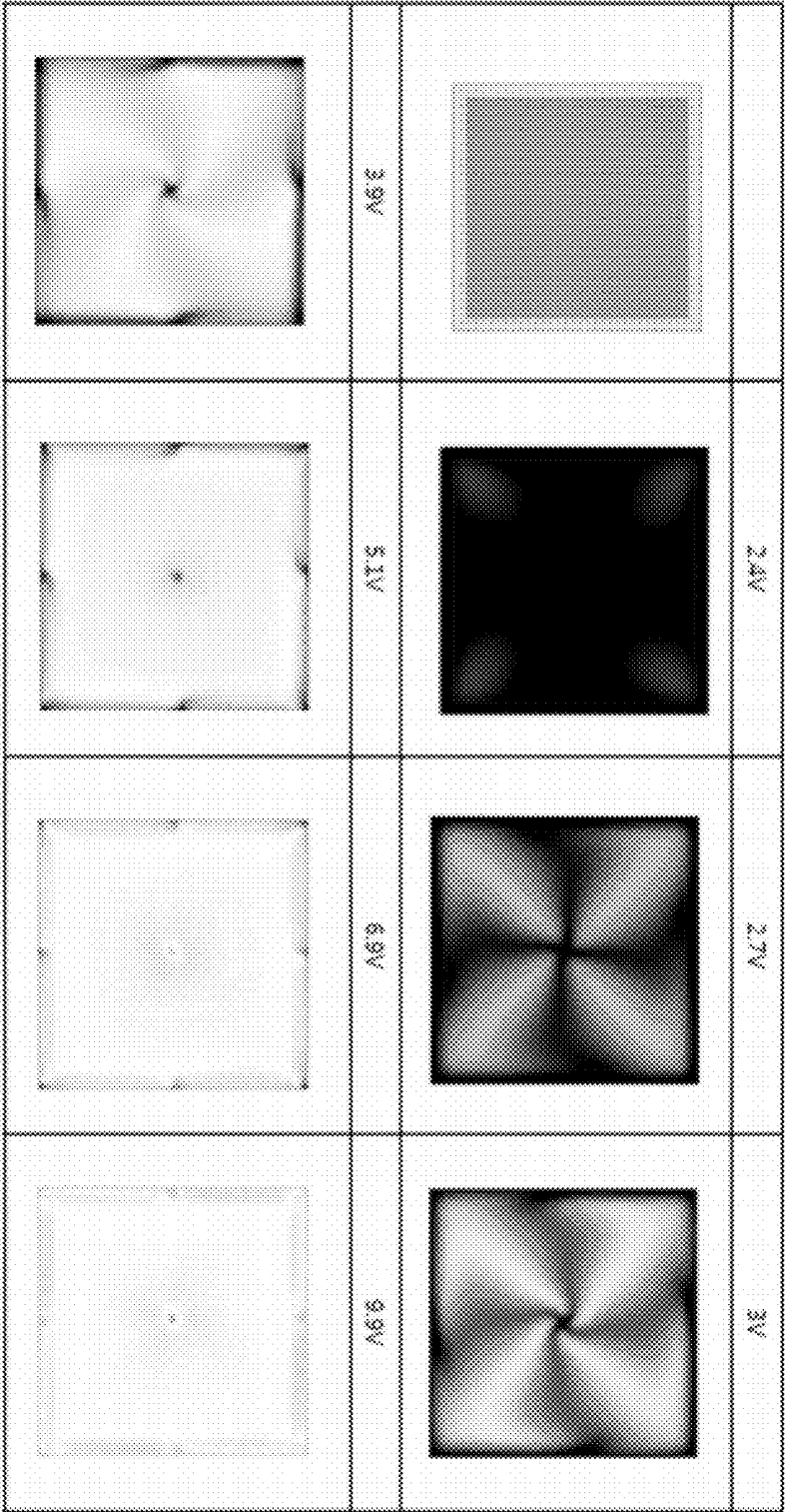


FIG.17



LIQUID CRYSTAL DISPLAY

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] This application claims priority to and the benefit of Korean Patent Application No. 10-2011-0047435 filed in the Korean Intellectual Property Office on May 19, 2011, and the benefit of U.S. Provisional Patent Application No. 61/481,546, filed on May 2, 2011, which are both hereby incorporated by reference for all purposes as if fully set forth herein.

BACKGROUND

[0002] 1. Field

[0003] Exemplary embodiments of the present invention relate to a liquid crystal display.

[0004] 2. Discussion of the Background

[0005] Liquid crystal displays (LCDs) are one of the most widely used flat panel displays. An LCD includes a pair of panels provided with field-generating electrodes, such as pixel electrodes and a common electrode, and a liquid crystal (LC) layer interposed between the two panels. The LCD displays images by applying voltages to the field-generating electrodes to generate an electric field in the LC layer that determines the orientations of LC molecules therein to adjust polarization of incident light.

[0006] Among the LCDs, a vertical alignment (VA) mode LCD, which aligns LC molecules such that their long axes are perpendicular to the panels in the absence of an electric field, has been developed.

[0007] In the VA mode LCD, a wide viewing angle can be realized by cutouts such as minute slits in the field-generating electrodes. Since the cutouts and protrusions can determine the tilt directions of the LC molecules, the tilt directions can be distributed severally by using the cutouts and protrusions such that the reference viewing angle is widened.

[0008] In the case providing a plurality of branch electrodes by forming the minute slits in the pixel electrode, an aperture ratio of the liquid crystal display may be decreased.

[0009] The above information disclosed in this Background section is only for enhancement of understanding of the background of the invention and therefore it may contain information that does not form any part of the prior art nor what the prior art may suggest to a person of ordinary skill in the art.

SUMMARY

[0010] Exemplary embodiments of the present invention provide a liquid crystal display reducing a reduction of an aperture ratio while having a wide viewing angle and a fast response speed.

[0011] Additional features of the invention will be set forth in the description which follows, and in part will be apparent from the description, or may be learned by practice of the invention.

[0012] An exemplary embodiment of the present invention discloses a liquid crystal display including a first substrate, a pixel electrode disposed on the first substrate, a second substrate facing the first substrate, a common electrode disposed on the second substrate, and a liquid crystal layer disposed between the first substrate and the second substrate. The common electrode has a plurality of openings having a cross shape, and when viewing the first substrate and the second substrate upward, the edges of the opening protrude beyond

edges of the pixel electrode. At least one of the liquid crystal layer, the first alignment layer, and the second alignment layer includes a photoreactive material.

[0013] Liquid crystal molecules of the liquid crystal layer may be arranged substantially vertical to the surface of the first substrate and the second substrate in the absence of an electric field to the liquid crystal layer.

[0014] The liquid crystal molecules of the liquid crystal layer may be arranged in a direction parallel to the direction from a position where the edges of the pixel electrode meet toward a center portion of the cutout of the common electrode to have a pretilt.

[0015] The pixel electrode may be divided into a plurality of sub-regions by the edges of the pixel electrode and the cutout of the common electrode, in the sub-region; and the liquid crystal molecules of the liquid crystal layer are arranged to have the pretilt of a different direction.

[0016] When viewing the first substrate and the second substrate upward, the sub-regions may be symmetrical to each other.

[0017] The pixel electrode has at least one cutout formed at a position where two edges of the pixel electrode meet, and the cutout extends from the position where two edges of the pixel electrode meet toward the center portion of the cutout of the common electrode.

[0018] The width of the pixel electrode may be gradually widened from the edges of the pixel electrode facing each other to the center portion of the pixel electrode.

[0019] According to an exemplary embodiment of the present invention, while increasing the viewing angle of the liquid crystal display, and the response speed and the visibility, the aperture ratio and the transmittance may be increased.

[0020] It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

BRIEF DESCRIPTION OF THE DRAWINGS

[0021] The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in and constitute a part of this specification, illustrate embodiments of the invention, and together with the description serve to explain the principles of the invention.

[0022] FIG. 1 is an equivalent circuit diagram of one pixel of a liquid crystal display according to an exemplary embodiment of the present invention.

[0023] FIG. 2 is a layout view of a liquid crystal display according to an exemplary embodiment of the present invention.

[0024] FIG. 3 is a cross-sectional view of the liquid crystal display of FIG. 2 taken along line III-III.

[0025] FIG. 4 is a top plan view of a base region of a field generating electrode of a liquid crystal display according to an exemplary embodiment of the present invention.

[0026] FIG. 5 is a view showing a process of providing a pretilt angle to liquid crystal molecules by using prepolymers that are polarized by light such as ultraviolet rays.

[0027] FIG. 6 is a view conceptually showing a liquid crystal direction in a base region of a field generating electrode of a liquid crystal display according to an exemplary embodiment of the present invention.

[0028] FIG. 7 is a top plan view of one pixel area of a liquid crystal display according to an exemplary embodiment of the present invention.

[0029] FIG. 8 is a top plan view of one pixel area of a liquid crystal display according to another exemplary embodiment of the present invention.

[0030] FIG. 9 is a top plan view showing a transmittance result of a liquid crystal display according to one experimental example of the present invention.

[0031] FIG. 10 is a top plan view of a base region of a field generating electrode of a liquid crystal display according to another exemplary embodiment of the present invention.

[0032] FIG. 11 is a top plan view of a base region of a field generating electrode of a liquid crystal display according to another exemplary embodiment of the present invention.

[0033] FIG. 12 is a top plan view of a base region of a field generating electrode of a liquid crystal display according to another exemplary embodiment of the present invention.

[0034] FIG. 13 is a top plan view of a base region of a field generating electrode of a liquid crystal display according to another exemplary embodiment of the present invention.

[0035] FIG. 14 is a top plan view of an arrangement direction of liquid crystal directors of a liquid crystal display according to another experimental example of the present invention.

[0036] FIG. 15 is a top plan view showing a transmittance result of a liquid crystal display according to another experimental example of the present invention.

[0037] FIG. 16 is a top plan view of a base region of a field generating electrode of a liquid crystal display according to another exemplary embodiment of the present invention.

[0038] FIG. 17 is a top plan view showing a transmittance result of a liquid crystal display according to another experimental example of the present invention.

DETAILED DESCRIPTION OF THE ILLUSTRATED EMBODIMENTS

[0039] The present invention will be described more fully hereinafter with reference to the accompanying drawings, in which exemplary embodiments of the invention are shown. As those skilled in the art would realize, the described embodiments may be modified in various different ways, all without departing from the spirit or scope of the present invention.

[0040] In the drawings, the thickness of layers, films, panels, regions, etc., are exaggerated for clarity. Like reference numerals designate like elements throughout the specification. It will be understood that when an element such as a layer, film, region, or substrate is referred to as being "on" another element, it can be directly on the other element or intervening elements may also be present. In contrast, when an element is referred to as being "directly on" another element, there are no intervening elements present. It will be understood that for the purposes of this disclosure, "at least one of X, Y, and Z" can be construed as X only, Y only, Z only, or any combination of two or more items X, Y, and Z (e.g., XYZ, XYY, YZ, ZZ).

[0041] A liquid crystal display according to an exemplary embodiment of the present invention will be described with reference to FIG. 1. FIG. 1 is an equivalent circuit diagram of one pixel of a liquid crystal display according to an exemplary embodiment of the present invention.

[0042] Referring to FIG. 1, a liquid crystal display according to an exemplary embodiment of the present invention includes signal lines including a gate line 121, a storage electrode line 125, a step-down gate line 123, and a data line

171, and a pixel PX connected to the signal lines. The step-down gate line 123 may be a next gate line or a separate gate line.

[0043] The pixel PX includes a first switching element Qh, a second switching element Ql, and a third switching element Qc, first and second liquid crystal capacitors Clch and Clcl, first and second storage capacitors Csth and Cstl, and a step-down capacitor Cstd. Here, the first switching element Qh and the first thin film transistor Qh are denoted by the same reference numeral, the second switching element Ql and the second thin film transistor Ql are denoted by the same reference numeral, and the third switching element Qc and the third thin film transistor Qc are denoted by the same reference numeral.

[0044] The first and second thin film transistors Qh and Ql are both connected to the gate line 121 and the data line 171, and the third thin film transistor Qc is connected to the step-down gate line 123.

[0045] The first thin film transistor Qh as a three terminal element provided in a lower panel 100 has a control terminal connected to the gate line 121, an input terminal connected to the data line 171, and an output terminal connected to the first liquid crystal capacitor Clch and the first storage capacitor Csth. The second thin film transistor Ql as a three terminal element provided in the lower panel 100 has a control terminal connected to the gate line 121, an input terminal connected to the data line 171, and an output terminal connected to the second liquid crystal capacitor Clcl and the second storage capacitor Cstl.

[0046] The third thin film transistor Qc as a three terminal element provided in the lower panel 100 has a control terminal connected to the step-down gate line 123, an input terminal connected to the second liquid crystal capacitor Clcl, and an output terminal connected to the step-down capacitor Cstd.

[0047] The first and second liquid crystal capacitors Clch and Clcl are formed by overlapping first and second subpixel electrodes 191h and 191l connected to the first and second switching elements Qh and Ql and a common electrode 270 of an upper panel 200 (see FIG. 2). The first and second storage capacitors Csth and Cstl are formed by the storage electrode line 125 as well as a storage electrode overlapping first and second subpixel electrodes 191h and 191l.

[0048] The step-down capacitor Cstd is connected to the output terminal of the third thin film transistor Qc and the storage electrode line 125. The storage electrode line 125 is provided in the lower panel 100 (see FIG. 2) and the output terminal of the third thin film transistor Qc overlaps the storage electrode line 125 via an insulator.

[0049] A driving method of the liquid crystal display will be described with reference to FIG. 1.

[0050] If the gate line 121 is applied with the gate-on signal, the first switching element Qh and the second switching element Ql connected thereto are turned on. Accordingly, the data voltage applied to the data line 171 is applied to the first sub-pixel electrode and the second sub-pixel electrode through the turned on first switching element Qh and second switching element Ql. At this time, the data voltages applied to the first sub-pixel electrode 191h and the second sub-pixel electrode 191l have the same magnitude. Accordingly, the voltages charged to the first and second liquid crystal capacitors Clch and Clcl are the same. Next, if the gate line 121 is applied with the gate-off signal and the step-down gate line 123 is applied with the gate-on signal, the first switching element Qh and the second switching element Ql are turned

off and the third switching element Qc is turned on. Thus, the charge is moved from the second sub-pixel electrode 191/ to the step-down capacitor Cstd through the third switching element Qc. Thus, the charge voltage of the second liquid crystal capacitor Clcl is decreased and the step-down capacitor Cstd is charged. The charge voltage of the second liquid crystal capacitor Clcl is decreased by the capacitance of the step-down capacitor Cstd such that the charge voltage of the second liquid crystal capacitor Clcl is lower than the charge voltage of the first liquid crystal capacitor Clch.

[0051] In this state, charge voltages of two liquid crystal capacitors Clch and Clcl represent the different gamma curves, and the gamma curve of one pixel voltage is a combination curved line of the different gamma curves. The front combination gamma curve coincides with the reference gamma curve at the optimally-determined front side, and the lateral gamma curve approximates the front reference gamma curve. In this way, the image data are converted so that the lateral visibility is improved.

[0052] The liquid crystal display shown in FIG. 1 will be described with reference to FIG. 2, FIG. 3, and FIG. 4. FIG. 2 is a layout view of a liquid crystal display according to an exemplary embodiment of the present invention, FIG. 3 is a cross-sectional view of the liquid crystal display of FIG. 2 taken along line III-III, and FIG. 4 is a top plan view of a base region of a field generating electrode of a liquid crystal display according to an exemplary embodiment of the present invention.

[0053] Referring to FIG. 2 and FIG. 3, the liquid crystal display according to the present exemplary embodiment includes the lower panel 100 and the upper panel 200 facing each other, a liquid crystal layer 3 interposed between the two display panels 100 and 200, and a pair of polarizers (not shown) attached at the outer surfaces of the display panels 100 and 200.

[0054] Now, the lower panel 100 will be described.

[0055] A plurality of gate conductors including a plurality of gate lines 121, a plurality of step-down gate lines 123, and a plurality of storage electrode lines 125 are formed on an insulation substrate 110.

[0056] The gate lines 121 and the step-down gate lines 123 transfer gate signals and mainly extend in a transverse direction. Each gate line 121 includes a first gate electrode 124h and a second gate electrode 124l protruding upward and downward, and each step-down gate line 123 includes a third gate electrode 124c protruding upward. The first gate electrode 124h and the second gate electrode 124l are connected to each other, thereby forming one protrusion.

[0057] The storage electrode lines 125 are mainly extended in the transverse direction, and transfer a predetermined voltage such as a common voltage Vcom. Each storage electrode line 125 includes a storage electrode (not shown) protruding upward and downward, a pair of longitudinal portions (not shown) extending almost perpendicularly to the gate line 121 downward, and a transverse portion (not shown) connecting the ends of a pair of longitudinal portions to each other. The transverse portion includes a storage expansion 126 extended downward.

[0058] A gate insulating layer 140 is formed on the gate conductors 121, 123, and 125.

[0059] A plurality of semiconductor stripes (not shown) made of hydrogenated amorphous silicon (a-Si), polysilicon, or so on are formed on the gate insulating layer 140. The semiconductors are mainly extended in the vertical direction,

and each semiconductor includes first and second semiconductors 154h and 154l extending toward the first and second gate electrodes 124h and 124l and connected to each other, and a third semiconductor 154c connected to the second semiconductor 154l. The third semiconductor 154c is extended, thereby forming a fourth semiconductor 157.

[0060] A plurality of ohmic contact stripes (not shown) are formed on the semiconductor stripes, wherein first ohmic contacts (not shown) are formed on the first semiconductor 154h, and a second ohmic contact 164b and a third ohmic contact (not shown) are also formed on the second semiconductor 154l and the third semiconductor 154c. The ohmic contact stripes include a first protrusion (not shown) forming a pair along with a first ohmic contact island and disposed on a first protrusion of the semiconductor, a second protrusion (not shown) forming a pair along with a second ohmic contact island and disposed on a second protrusion of the semiconductor, and a third protrusion (not shown) forming a pair along with a third ohmic contact island and disposed on a third protrusion of the semiconductor. The third ohmic contact is extended, thereby forming a fourth ohmic contact 167.

[0061] A data conductor is formed on the ohmic contacts. The data conductor includes a plurality of data lines 171, a plurality of first drain electrodes 175h, a plurality of second drain electrodes 175l, and a plurality of third drain electrodes 175c.

[0062] The data lines 171 transmit data signals and extend in the longitudinal direction thereby crossing the gate lines 121 and the step-down gate lines 123. Each data line 171 includes a first source electrode 173h and a second source electrode 173l forming a "W" shape together and extending toward a first gate electrode 124h and a second gate electrode 124l.

[0063] The first drain electrode 175h, the second drain electrode 175l, and the third drain electrode 175c have one end portion having a wide area and the other end portion of a linear shape. The bar end portions of the first drain electrode 175h and the second drain electrode 175l are partially enclosed by the first source electrode 173h and the second source electrode 173l. The wide end portion of the second drain electrode 175l is again extended thereby forming a third source electrode 173c of a "U" shape. An expansion 177c of the third drain electrode 175c overlaps the storage expansion 126 thereby forming a step-down capacitor Cstd, and the bar end portion of the third drain electrode 175c is partially enclosed by the third source electrode 173c.

[0064] The first gate electrode 124h, the first source electrode 173h, and the first drain electrode 175h form a first thin film transistor (TFT) Qh along with the first semiconductor island 154h, and a channel of the first thin film transistor Qh is formed in the first semiconductor 154h between the first source electrode 173h and the first drain electrode 175h.

[0065] The second gate electrode 124l, the second source electrode 173l, and the second drain electrode 175l form a second thin film transistor (TFT) Ql along with the second semiconductor island 154l, and a channel of the second thin film transistor Ql is formed in the second semiconductor 154l between the second source electrode 173l and the second drain electrode 175l.

[0066] The third gate electrode 124c, the third source electrode 173c, and the third drain electrode 175c form a third thin film transistor (TFT) Qc along with the third semiconductor island 154c, and a channel of the third thin film transistor Qc

is formed in the third semiconductor **154c** between the third source electrode **173c** and the third drain electrode **175c**.

[0067] Also, the semiconductor stripes including the semiconductors **154h**, **154l**, and **154c** except for the channel region between the source electrodes **173h**, **173l**, and **173c**, and the drain electrodes **175h**, **175l**, and **175c** have substantially the same shape as the data conductors **171**, **175h**, **175l**, and **175c** and the ohmic contacts **164b** and **167**. That is, the semiconductor stripes including the semiconductors **154h**, **154l**, and **154c** have a portion that is exposed without being covered by the data conductors **171**, **175h**, **175l**, and **175c**, between the source electrodes **173h**, **173l**, and **173c** and the drain electrodes **175h**, **175l**, and **175c**.

[0068] A lower passivation layer **180p** made of an inorganic insulator such as silicon nitride or silicon oxide is formed on the data conductors **171**, **175h**, **175l**, **175c** and the exposed semiconductors **154h**, **154l**, and **154c**.

[0069] A color filter **230** is formed on the lower passivation layer **180p**. The color filter **230** is formed in most of the region except for where the first thin film transistor Qh, the second thin film transistor Ql, and the third thin film transistor Qc are positioned. Also, each color filter **230** may be formed long in a space in the longitudinal direction between the neighboring data lines **171**. Each color filter **230** may display one of primary colors such as three primary colors of red, green, and blue.

[0070] A light blocking member **220** is formed on a region that the color filter **230** does not occupy and on a portion of the color filter **230**. The light blocking member **220** is referred to as a black matrix, and prevents light leakage. The light blocking member **220** is extended upward and downward along the gate line **121** and the step-down gate line **123**, includes a first light blocking member (not shown) covering a region where the first thin film transistor Qh, the second thin film transistor Ql, and the third thin film transistor Qc are disposed, and a second light blocking member (not shown) extending along the data line **171**. The height of the portion of the light blocking member **220** may be lower than the height of the color filter **230**.

[0071] On the color filter **230** and light blocking member **220**, an upper passivation layer **180q** is formed. The upper passivation layer **180q** prevents peeling of the color filter **230** and the light blocking member **220**, and suppresses contamination of the liquid crystal layer **3** by an organic material of the solvent that inflows from the color filter **230**, so that it prevents defects such as afterimages that may occur when an image is driven.

[0072] On the lower passivation layer **180p**, the light blocking member **220**, and the upper passivation layer **180q**, a plurality of first contact holes **185h** and a plurality of second contact holes **185l** that expose the wide end portions of the first drain electrodes **175h** and the wide end portions of the second drain electrodes **175l**, respectively, are formed.

[0073] On the upper passivation layer **180q**, a plurality of pixel electrodes **191** are formed.

[0074] Referring to FIG. 2, each pixel electrode **191** includes a first sub-pixel electrode **191h** and a second sub-pixel electrode **191l** that are separated from each other with two gate lines **121** and **123** therebetween, and disposed above and below the area of the pixel with the center of the gate lines **121** and **123** to be adjacent in a column direction.

[0075] The first sub-pixel electrode **191h** and the second sub-pixel electrode **191l** receive a data voltage through the first contact hole **185h** and the second contact hole **185l** from

the first drain electrode **175h** and the second drain electrode **175l**, respectively. The first sub-pixel electrode **191h** and the second sub-pixel electrode **191l** to which the data voltage is applied generate an electric field in conjunction with the common electrode **270** of the common electrode panel **200** to determine a direction of the liquid crystal molecules of the liquid crystal layer **3** between the two electrodes **191** and **270**. As described above, according to the determined direction of the liquid crystal molecules, the luminance of light that passes through the liquid crystal layer **3** is changed.

[0076] The first sub-pixel electrode **191h** and the common electrode **270** form the first liquid crystal capacitor Clch in conjunction with the liquid crystal layer **3** therebetween, and the second sub-pixel electrode **191l** and common electrode **270** form the second liquid crystal capacitor Clcl in conjunction with the liquid crystal layer **3** therebetween, so that the applied voltage is maintained even though the first and second thin film transistors Qh and Ql are turned off.

[0077] The first and second sub-pixel electrodes **191h** and **191l** overlap the storage electrode and the storage electrode line **125** to form the first and second storage capacitors Csth and Cstl, and the first and second storage capacitors Csth and Cstl strengthen the voltage maintaining ability of the first and second liquid crystal capacitors Clch and Clcl.

[0078] The storage expansion **126** and the expansion **177c** of the third drain electrode **175c** overlap the gate insulating layer **140** and semiconductor layers **157** and **167** therebetween to form a voltage drop capacitor Cstd. In another exemplary embodiment of the present invention, the semiconductor layers **157** and **167** that are disposed between the storage expansion **126** and the expansion **177c** of the third drain electrode **175c** that constitute the voltage drop capacitor Cstd may be removed.

[0079] A colored member is formed on the upper passivation layer **180q**. The colored member is disposed on the light blocking member **220**. The colored member includes a first colored member **320a** disposed along the first light blocking member extending upward and downward along the gate line **121** and the step-down gate line **123** and covering the region where the first thin film transistor Qh, the second thin film transistor Ql, and the third thin film transistor Qc are positioned and a second colored member (not shown) disposed along the second light blocking member extending along the data line **171**.

[0080] The first colored member **320a** and the second colored member compensate the height difference of the light blocking member **220** and the color filter **230**, and thereby the cell interval of the liquid crystal layer disposed on the color filter **230** and the liquid crystal layer disposed on the light blocking member **220** is uniformly controlled and the function of preventing the light leakage of the light blocking member **220** is reinforced. Since the first colored member **320a** and the second colored member compensate the height difference of the light blocking member **220** and the color filter **230**, the light leakage of the edge portion of the pixel electrode due to inaccurate control of the liquid crystal molecules disposed between the light blocking member **220** and the color filter **230** by the step between the light blocking member **220** and the color filter **230** may be prevented. Also, since the cell gap on the light blocking member **220** is reduced, the average cell gap is reduced, and thereby the entire liquid crystal amount of the liquid crystal display may be reduced.

[0081] A lower alignment layer (not shown) is formed on the pixel electrode **191**, the exposed upper passivation layer **180g**, and the first colored member **320a** and the second colored member. The lower alignment layer may be a vertical alignment layer, and may be an alignment layer that is light-aligned by using a light-polymerized material.

[0082] Now, the common electrode panel **200** will be described.

[0083] A common electrode **270** is formed on an insulation substrate **210**. The common electrode **270** includes a plurality of first cutouts (openings) **271a** and a plurality of second cutouts **271b**.

[0084] Each first cutout **271a** of the common electrode **270** is disposed at the position corresponding to each first sub-pixel electrode **191h**, and each second cutout **271b** is disposed at the position corresponding to each second sub-pixel electrode **191l**.

[0085] In a plan view, the first cutout **271a** and the second cutout **271b** may each have a cross shape, and the edge thereof is protruded from the corresponding edge of the first sub-pixel electrode **191h** and the second sub-pixel electrode **191l**. As described above, by protruding the edge of the cutout of the common electrode **270** from the edge of the pixel electrode, the influence of the fringe field stably affects to the edge of the pixel area, and thereby the arrangement of the liquid crystal molecules may be controlled in the desired direction in the edge of the pixel area.

[0086] By the first cutout **271a** and the second cutout **271b**, and the edges of the first sub-pixel electrode **191h** and the second sub-pixel electrode **191l**, the first sub-pixel electrode **191h** and the second sub-pixel electrode **191l** may be divided into a plurality of sub-regions.

[0087] An upper alignment layer (not shown) is formed on the common electrode **270**. The upper alignment layer may include the vertical alignment layer, and may be an alignment layer that is light-aligned by using the light-polymerized material.

[0088] A polarizer (not shown) may be provided on the outer surface of the two display panels **100** and **200**, and the transmissive axes of the two polarizers may be orthogonal to each other and any one transmissive axis parallel to the gate line **121**. However, the polarizer may only be disposed at one outer surface of the two display panels **100** and **200**.

[0089] The liquid crystal layer **3** has negative dielectric anisotropy, and the liquid crystal molecules of the liquid crystal layer **3** may be aligned so that long axes thereof are vertical with respect to the surface of the two display panels **100** and **200** in a state in which there is no electric field. Therefore, the incident light does not pass through the crossed polarizers but is blocked in a state in which there is no electric field. At least one of the liquid crystal layer, the first alignment layer, and the second alignment layer includes a photoreactive material.

[0090] As described above, since the first sub-pixel electrode **191h** and the second sub-pixel electrode **191l** to which the data voltage is applied generate an electric field in conjunction with the common electrode **270** of the common electrode panel **200**, the liquid crystal molecules of the liquid crystal layer **3**, which are aligned vertically with respect to the surfaces of the two electrodes **191** and **270** in a state in which there is no electric field, lie in a horizontal direction with respect to the surfaces of the two electrodes **191** and **270**, and

the luminance of light that passes through the liquid crystal layer **3** is changed according to the degree of tilt of the liquid crystal molecules.

[0091] According to another exemplary embodiment of the present invention, the liquid crystal display further includes a spacer **325** to maintain the cell gap between the two display panels **100** and **200**, and the spacer **325** may be simultaneously formed with the same layer as the first colored member **320a** and the second colored member.

[0092] The liquid crystal layer **3** interposed between the lower panel **100** and the upper panel **200** includes liquid crystal molecules **31** having negative dielectric anisotropy and a polymer. The liquid crystal molecules **31** are aligned such that the long axes thereof are arranged by the cutouts **271a** and **271b** of the common electrode and the edges of sub-pixel electrodes **191h** and **191l** to be parallel to the direction from four portions where the edges extending into the different directions of each sub-pixel electrodes **191h** and **191l** meet each other (corners) toward the center portion of the openings **271a** and **271b** of the common electrode **270** of the cross shape with the pretilt by the polymer, and to be vertical with respect to the surface of the two display panels **100** and **200**. Accordingly, the first and second subpixels respectively have four sub-regions having different pretilt directions of the liquid crystal molecules **31**.

[0093] Next, a basic region of the field generating electrode of the liquid crystal display according to an exemplary embodiment of the present invention will be described with reference to FIG. 4.

[0094] As shown in FIG. 4, the base region of the field generating electrode is made of an opening **271** of the common electrode **270** and a pixel electrode **191** enclosing the opening **271**. When viewing the liquid crystal display from above, the base region defined by the opening **271** of the common electrode **270** and the edges of the pixel electrode **191** may be divided into a plurality of sub-regions Da, Db, Dc, and Dd, and the plurality of sub-regions may be symmetrical to each other with respect to the opening **271** of the common electrode **270**.

[0095] As described above, the cutout (opening) **271** of the common electrode **270** may have the cross shape in a plan view, and edges **272** of the cutout **271** protrude from the corresponding edges of the pixel electrode **191**. The width of the cutout **271** of the common electrode **270** may be about 2 μm to about 10 μm .

[0096] Next, an initial alignment method providing the pretilt to the liquid crystal molecules of the liquid crystal layer will be described with reference to FIG. 5 and FIG. 6. FIG. 5 is a view showing a process of providing a pretilt angle to liquid crystal molecules by using prepolymers that are polarized by light such as ultraviolet rays, and FIG. 6 is a view conceptually showing a liquid crystal direction in a base region of a field generating electrode of a liquid crystal display according to an exemplary embodiment of the present invention.

[0097] Prepolymers **330** such as a monomer that is polymerized by light such as ultraviolet rays are injected along with a liquid crystal material between the two display panels **100** and **200**. The prepolymers **330** may be injected into the alignment layers. The prepolymer **330** may be a reactive mesogen that is polymerized by light such as ultraviolet rays.

[0098] Next, the data voltage is applied to the first and second subpixel electrodes **191h**, **191l** and a common voltage is applied to the common electrode **270** of the upper panel

200, thereby forming the electric field in the liquid crystal layer **3** between the two display panels **100** and **200**. In response to the applied electric field, the liquid crystal molecules **31** of the liquid crystal layer **3** are tilted in the direction approximately parallel to the direction from four portions where the edges extending in the different directions of pixel electrode **191** meet each other toward the center portion of the opening **271** of the common electrode **270** of the cross shape by the fringe field due to the cutout **271** of the common electrode **270** and the edges of the pixel electrode **191**, and thereby the total inclination directions of the liquid crystal molecules **31** is four directions in one base region of the field generating electrode.

[0099] This will be described with reference to FIG. 6. Referring to FIG. 6 (a), directors **301a** and **301b** of the liquid crystal molecules in the portion near the edges of the pixel electrode **191** forming the base region of the field generating electrode are respectively vertical to the edges of the pixel electrode **191**. Also, directors **302a** and **302b** of the liquid crystal molecules in the portion near the cutout **271** of the common electrode forming the base region of the field generating electrode are respectively vertical to the edges of the cutout **271** of the common electrode **270**. Like this, the liquid crystal directors according to the fringe field generated by the edges of the pixel electrode **191** forming the base region of the field generating electrode and the cutout **271** of the common electrode are firstly determined and are secondarily arranged in the direction that the liquid crystal molecules meet each other and the deformation is minimized, and the secondary arrangement direction is a vector sum direction of the direction of each director. Accordingly, as shown in FIG. 6 (b), the liquid crystal directors **303** are finally parallel to the direction from four portions where the edges extending in the different directions of the pixel electrode **191** meet each other toward the center portion of the opening **271** of the common electrode **270** of the cross shape. In each sub-region Da, Db, Dc, and Dd, the directors of the liquid crystal molecules **31** according to the fringe field are similarly arranged, and the total directions that the liquid crystal molecules are inclined in each base region of the field generating electrode is four directions. In detail, in the first sub-region of each base region, the directors of the liquid crystal molecules **31** are obliquely arranged in the right lower direction from the edge of the pixel toward the center of the opening **271**, in the second sub-region, the directors of the liquid crystal molecules **31** are obliquely arranged in the left lower direction from the edge of the pixel toward the center of the opening **271**, in the third sub-region, the directors of the liquid crystal molecules **31** are obliquely arranged in the right upper direction from the edge of the pixel toward the center of the opening **271**, and in the fourth sub-region, the directors of the liquid crystal molecules **31** are obliquely arranged in the left upper direction from the edge of the pixel toward the center of the opening **271**.

[0100] After the electric field is applied to the liquid crystal layer **3**, the light such as ultraviolet rays is irradiated, and the prepolymers **330** are polymerized to form polymers **370**, as shown in FIG. 5. The polymers **370** are formed while contacting the display panels **100** and **200**. By the polymers **370**, the alignment direction of the liquid crystal molecules **31** is determined to have the pretilt of the directions as described above. Accordingly, even when the voltage is not applied to the electrodes **191** and **270**, the liquid crystal molecules **310** are arranged with the pretilt of the four different directions.

[0101] In the liquid crystal display according to the above-described exemplary embodiment, the common electrode **270** has the cutout **271** of one cross shape disposed at the position corresponding to each sub-pixel electrode **191h** and **191l**, and the base region formed by the sub-pixel electrodes **191h** and **191l** and the common electrode **270** has four sub-regions, however according to another exemplary embodiment of the present invention, a plurality of openings **271** of a quadrangle ring shape may be formed in one unit pixel and a plurality of base regions of the field generating electrode described with reference to FIG. 4 may be formed in one pixel area.

[0102] This will be described with reference to FIG. 7 and FIG. 8. FIG. 7 is a top plan view of one pixel area of a liquid crystal display according to an exemplary embodiment of the present invention, and FIG. 8 is a top plan view of one pixel area of a liquid crystal display according to another exemplary embodiment of the present invention.

[0103] Referring to FIG. 7, the cutout **271** of the common electrode **270** corresponding to the pixel electrode **191** disposed in one pixel area has a shape in which three basic cross shapes are combined. Also, the pixel electrode **191** has an edge **192** enclosing the basic cross shape of the cutout **271** of the common electrode **270**. Accordingly, in one pixel area, three base regions of the field generating electrode described with reference to FIG. 4 are formed.

[0104] Referring to FIG. 8, the cutout **271** of the common electrode **270** corresponding to the pixel electrode **191** disposed in one pixel area has a twelve basic cross shape. Also, the pixel electrode **191** has the edge **192** enclosing the basic cross shape of the cutout **271** of each common electrode **270**. Accordingly, one pixel area includes twelve base regions of the field generating electrode described with reference to FIG. 4.

[0105] As described above, a number of base regions of the field generating electrode formed in each pixel area may be changed according to a size of the pixel or other conditions.

[0106] Next, referring to FIG. 9, a transmittance result of a liquid crystal display according to one experimental example of the present invention will be described. FIG. 9 is a top plan view showing a transmittance result of a liquid crystal display according to one experimental example of the present invention.

[0107] In the present experimental example, as shown in FIG. 8, one pixel area has twelve base regions of the field generating electrode, the width of the cutouts **271** formed in the common electrode and the interval between the edges **192** of the pixel electrode **191** are controlled, and after the voltage of the same magnitude is applied to the pixel electrode **191** and the common electrode **270**, the transmittance is simulated. In the present experimental example, referring to FIG. 9, a result when the width of the cutouts **271** formed in the common electrode and the interval between the edges **192** of the pixel electrode **191** are formed at about 3 μm is shown in (a), a result when the width of the cutouts **271** formed in the common electrode and the interval between the edges **192** of the pixel electrode **191** are formed at about 5 μm is shown in (b), and a result when the width of the cutouts **271** formed in the common electrode and the interval between the edges **192** of the pixel electrode **191** are formed at about 10 μm is shown in (c).

[0108] Referring to FIG. 9, when the width of the cutout **271** formed in the common electrode and the interval between the edges **192** of the pixel electrode **191** is relatively small, the

transmittance at the edge of the base region is decreased, and when the width of the cutout **271** formed in the common electrode and the interval between the edges **192** of the pixel electrode **191** is relatively large, the transmittance of the edge of the pixel area is increased, however the loss of transmittance in the edge portion of the base region is large. Accordingly, when considering the size of the pixel area and other conditions, by controlling the width of the cutout **271** and the interval of the edges **192** between the pixel electrode **191**, optimized transmittance may be ensured.

[0109] Also, in the case of the liquid crystal display according to an exemplary embodiment of the present invention, compared with a conventional liquid crystal display including the pixel electrode having a plurality of minute branch electrodes formed by a plurality of cutouts in the pixel electrode, the entire cross-section of the pixel electrode is large such that the total transmittance of the liquid crystal display is increased.

[0110] Next, referring to FIG. **10**, FIG. **11**, FIG. **12**, and FIG. **13**, the base region of the field generating electrode of the liquid crystal display according to further exemplary embodiments of the present invention will be described. FIG. **10** is a top plan view of a base region of a field generating electrode of a liquid crystal display according to an exemplary embodiment of the present invention, FIG. **11** is a top plan view of a base region of a field generating electrode of a liquid crystal display according to another exemplary embodiment of the present invention, FIG. **12** is a top plan view of a base region of a field generating electrode of a liquid crystal display according to still another exemplary embodiment of the present invention, and FIG. **13** is a top plan view of a base region of a field generating electrode of a liquid crystal display according to still another exemplary embodiment of the present invention.

[0111] Referring to FIG. **10**, in the case of the base region of the field generating electrode of the liquid crystal display according to the present exemplary embodiment, in the direction parallel to the direction from each portion that the edges of the pixel electrode extending in the different directions meet each other toward the center of the base region of the field generating electrode, a plurality of cutouts (openings) **91** are formed in the pixel electrode **191**. The plurality of cutouts **91** of the pixel electrode **191** strongly guide the arrangement direction of the directors of the liquid crystal molecule. Accordingly, the arrangement of the liquid crystal directors in the edges of the base region of the field generating electrode may be induced in the desired direction. That is, the arrangement of the liquid crystal directors may be induced in the direction from each portion where the edges of the pixel electrode extending in the different directions meet each other toward the center of the base region of the field generating electrode.

[0112] Referring to FIG. **11**, in the case of the base region of the field generating electrode of the liquid crystal display of the present exemplary embodiment, the edges of the pixel electrode are gradually widened closer to the center of the pixel electrode from the portion where the edges extending in the different directions meet each other, and in detail, the width of the pixel electrode is formed to be expanded with a first angle $\theta 1$ from both edges facing each other of the pixel electrode to the center portion of the pixel electrode. Accordingly, the arrangement of the liquid crystal directors in the edges of the pixel area may be induced in the desired direction.

[0113] In this way, by inclining the edges of the pixel electrode **191** with the predetermined angle $\theta 1$ in each sub-region, with reference to a plane surface, the arrangement direction of the liquid crystal directors in the edges of the pixel electrode **191** may also be induced to be toward the direction that the liquid crystal directors are finally inclined. Accordingly, the arrangement of the liquid crystal directors in the edges of the pixel area may be induced in the desired direction. That is, the arrangement of the liquid crystal directors may be induced in the direction from the portion where the edges of the pixel electrode extending in the different directions meet each other toward the center portion of the base region of the field generating electrode.

[0114] Next, referring to FIG. **12**, in the case of the base region of the field generating electrode of the liquid crystal display of the present exemplary embodiment, like the exemplary embodiment of FIG. **10**, in the direction parallel to the direction from each portion where the edges of the pixel electrode extending in the different directions meet each other toward the center portion of the base region of the field generating electrode, a plurality of cutouts **91** are formed in the pixel electrode **191**, and simultaneously the edges of the pixel electrode are gradually widened closer to the center of the pixel electrode from the portion where the edges extending in the different directions meet each other, and in detail, the width of the pixel electrode is formed to be expanded with the first angle $\theta 1$ from both edges facing each other of the pixel electrode to the center portion of the pixel electrode. Accordingly, the arrangement of the liquid crystal directors in the edges of the pixel area may be induced in the desired direction.

[0115] Referring to FIG. **13**, in the case of the base region of the field generating electrode of the liquid crystal display of the present exemplary embodiment, the edges of the pixel electrode are gradually narrowed closer to the portion where the edges extending in the different directions meet each other from the center of the pixel electrode, and in detail, the width of the pixel electrode is formed to be decreased with a second angle $\theta 2$ from the center of the pixel to the end of the pixel. In this way, by inclining the edges of the pixel electrode **191** with the predetermined angle $\theta 2$ in each sub-region, with reference to a plane surface, the arrangement direction of the liquid crystal directors in the edges of the pixel electrode **191** may also be induced to be toward the direction that the liquid crystal directors are finally inclined. Accordingly, the arrangement of the liquid crystal directors in the edges of the pixel area may be induced in the desired direction. That is, the arrangement of the liquid crystal directors may be induced in the direction from the portion where the edges of the pixel electrode extending in the different directions meet each other toward the center portion of the base region of the field generating electrode.

[0116] Next, referring to FIG. **14**, the arrangement direction of the liquid crystal directors of one sub-region of the base region of the liquid crystal display according to another experimental example of the present invention will be described. FIG. **14** is a top plan view of an arrangement direction of liquid crystal directors of a liquid crystal display according to the present experimental example of the present invention. In the present experimental example, a result of one sub-region among the base region of the field generating electrode shown in FIG. **4** is shown in (a), a result of one sub-region among the base region of the field generating electrode shown in FIG. **10** is shown in (b), a result of one

sub-region among the base region of the field generating electrode shown in FIG. 11 is shown in (c), a result of one sub-region among the base region of the field generating electrode shown in FIG. 12 is shown in (d), and a result of one sub-region among the base region of the field generating electrode shown in FIG. 13 is shown in (e). In the present experimental example, except for the shape of the base region of the field generating electrode, the remaining conditions are the same.

[0117] Referring to FIG. 14, like the liquid crystal display according to the exemplary embodiment shown in FIG. 11 and FIG. 12, when controlling the shape of the base region of the field generating electrode, particularly, the arrangement direction of the liquid crystal directors of the pixel edges accord with the average liquid crystal arrangement direction of the sub-region, and accordingly, the liquid crystal may be easily arranged in the desired direction to have the pretilt.

[0118] Next, referring to FIG. 15, a transmittance result of a liquid crystal display according to one experimental example of the present invention will be described. FIG. 15 is a top plan view showing a transmittance result of a liquid crystal display according to another experimental example of the present invention. In the present experimental example, the shape of the base region of each pixel area is different, and pixels induced with the voltage differences 3.5 V and 10 V between two field generating electrodes 191 and 270 are provided. The conditions except for the shape of the base region of each pixel area are the same. In FIG. 15, like the conventional liquid crystal display, (a) shows a case that the pixel electrode has the shape of a plurality of minute branches. Like the liquid crystal display according to an exemplary embodiment of the present invention, (b) in FIG. 15 shows a case that one pixel area includes three base regions and the widths of the cutout 271 of the common electrode 270 and the interval between the edges 192 of the pixel electrode 191 are about 5 μm , like the liquid crystal display according to an exemplary embodiment of the present invention, (c) shows a case that one pixel area includes three base regions, the widths of the cutout 271 of the common electrode 270 and the interval between the edges 192 of the pixel electrode 191 are about 5 μm , and the edge of the pixel electrode overlaps an opaque member, like the liquid crystal display according to an exemplary embodiment of the present invention, (d) shows a case that one pixel area includes three base regions and the width of the cutout 271 of the common electrode 270 is about 5 μm , the interval between the edges 192 of the pixel electrode 191 is about 5 μm , and the base region shown in FIG. 11 is formed, and like the liquid crystal display according to an exemplary embodiment of the present invention, (e) shows a case that one pixel area includes three base regions, the widths of the cutout 271 of the common electrode 270 and the interval between the edges 192 of the pixel electrode 191 are about 5 μm , and the base region shown in FIG. 10 is formed.

[0119] Referring to FIG. 15, comparing the case (a) that the pixel electrode is formed of a plurality of minute branch shapes like the conventional liquid crystal display with the cases (b, c, d, and e) of the liquid crystal displays according to the exemplary embodiments of the present invention, in the case of the liquid crystal display according to the exemplary embodiments of the present invention, the liquid crystal is well aligned according to four sub-regions, and the transmittance is high.

[0120] According to exemplary embodiments of the present invention, while increasing the viewing angle of the

liquid crystal display, and the response speed and the visibility, the aperture ratio and the transmittance may be increased.

[0121] In the case of the liquid crystal display according to the above-described exemplary embodiments, by the edge of the pixel electrode 191 and the cutout 271 of the common electrode 270, the base region of the field generating electrode is divided into a plurality of sub-regions, however in the case of the liquid crystal display according to another exemplary embodiment of the present invention, the common electrode 270 does not include the cutout 271, a chiral dopant is added to the liquid crystal layer 3 of the liquid crystal display, and then a pitch of the chiral dopant is controlled, and light alignment is performed by using a light-polymerized material or polarized ultraviolet rays to divide into the different sub-regions. In the case of the liquid crystal display according to the present exemplary embodiment, disclination of the liquid crystal molecules in the boundary portion of each sub-region may be prevented. The pitch of the liquid crystal according to the chiral dopant may be induced in Gooch and Tarry equation, and in detail it is satisfied with

$$u = \frac{\Gamma}{2\phi} = \frac{\pi\Delta nd}{\lambda\phi}, u = \sqrt{3}.$$

[0122] Here, ϕ as a twisting angle is $(P/d)2\pi$, P is the pitch, and d is the cell gap.

[0123] In this way, the cutout is not formed in the field generating electrode, the chiral dopant is added to the liquid crystal layer, and the light alignment is performed, and thereby the multiple domains may be formed without a reduction of the aperture ratio.

[0124] Next, the liquid crystal display according to another exemplary embodiment of the present invention will be described with reference to FIG. 16. FIG. 16 is a top plan view of a base region of a field generating electrode of a liquid crystal display according to another exemplary embodiment of the present invention.

[0125] Referring to FIG. 16, the liquid crystal display according to the present exemplary embodiment includes the pixel electrode 191 having a cutout (opening) 92 formed at the center. The liquid crystal layer of the liquid crystal display according to the present exemplary embodiment includes a chiral dopant, and the liquid crystal according to the chiral dopant may be induced according to the Gooch the Tarry equation, and in detail,

$$u = \frac{\Gamma}{2\phi} = \frac{\pi\Delta nd}{\lambda\phi}, u = \sqrt{3}.$$

[0126] Here, ϕ as a twisting angle is $(P/d)2\pi$, P is the pitch, and d is the cell gap.

[0127] Differently from the above-described exemplary embodiment, the pixel electrode 191 has the cutout 92, thereby strongly inducing the plurality of domain divisions.

[0128] Next, a transmittance result of a liquid crystal display according to another experimental example of the present invention will be described with reference to FIG. 17. FIG. 17 is a top plan view showing a transmittance result of a liquid crystal display according to another experimental example of the present invention.

[0129] In the present experimental example, the cell gap of the liquid crystal is about 6.04 μm , the pitch of the liquid crystal according to the chiral dopant is about 20.67 μm , and Δn is about 0.0921.

[0130] Referring to FIG. 17, in the case of the liquid crystal display according to the exemplary embodiment shown in FIG. 16, the high transmittance may be obtained without disclination of the liquid crystal in the edge of the base region of the pixel.

[0131] While this invention has been described in connection with what is presently considered to be practical exemplary embodiments, it is to be understood that the invention is not limited to the disclosed embodiments, but, on the contrary, is intended to cover various modifications and equivalent arrangements included within the spirit and scope of the appended claims.

What is claimed is:

1. A liquid crystal display, comprising:

a first substrate;

a pixel electrode disposed on the first substrate;

a second substrate facing the first substrate;

a common electrode disposed on the second substrate; and
a liquid crystal layer disposed between the first substrate and the second substrate,

wherein the common electrode comprises a plurality of openings having a cross shape, and
in plan view, the edges of each opening protrude beyond edges of the pixel electrode.

2. The liquid crystal display of claim 1, further comprising:
a first alignment layer disposed on the first substrate and the pixel electrode; and
a second alignment layer disposed on the second substrate and the common electrode,

wherein
at least one of the liquid crystal layer, the first alignment layer, and the second alignment layer comprises a polymerized photoreactive material.

3. The liquid crystal display of claim 2, wherein
liquid crystal molecules of the liquid crystal layer are arranged substantially vertical to the surfaces of the first substrate and the second substrate in the absence of an electric field applied to the liquid crystal layer.

4. The liquid crystal display of claim 3, wherein
the liquid crystal molecules of the liquid crystal layer are arranged in a direction parallel to the direction from a position where two edges of the pixel electrode meet toward a center portion of the opening of the common electrode to have a pretilt.

5. The liquid crystal display of claim 4, wherein
the pixel electrode is divided into a plurality of sub-regions by the edges of the pixel electrode and the cutout of the common electrode, and in each sub-region the liquid crystal molecules of the liquid crystal layer are arranged to have the pretilt of a different direction from the other sub-regions.

6. The liquid crystal display of claim 5, wherein
the pixel electrode comprises at least one opening formed at a position where two edges of the pixel electrode meet, and the opening extends from the position where two edges of the pixel electrode meet toward the center portion of the opening of the common electrode.

7. The liquid crystal display of claim 6, wherein
the width of the pixel electrode gradually widens from the edges of the pixel electrode facing each other to the center portion of the pixel electrode.

8. The liquid crystal display of claim 1, wherein
the liquid crystal molecules of the liquid crystal layer are arranged substantially vertical to the surface of the first substrate and the second substrate in the absence of an electric field applied to the liquid crystal layer.

9. The liquid crystal display of claim 8, wherein
the liquid crystal molecules of the liquid crystal layer are arranged in a direction parallel to a direction from a position where two edges of the pixel electrode meet toward a center portion of the opening of the common electrode to have a pretilt.

10. The liquid crystal display of claim 9, wherein
the pixel electrode is divided into a plurality of sub-regions by the edges of the pixel electrode and the opening of the common electrode, and in each sub-region the liquid crystal molecules of the liquid crystal layer are arranged to have a pretilt of a different direction from the other sub-regions.

11. The liquid crystal display of claim 10, wherein
the pixel electrode comprises at least one opening at a position where two edges of the pixel electrode meet, and the opening extends from the position where two edges of the pixel electrode meet toward the center portion of the opening of the common electrode.

12. The liquid crystal display of claim 11, wherein
the width of the pixel electrode gradually widens from the edges of the pixel electrode facing each other to the center portion of the pixel electrode.

13. The liquid crystal display of claim 1, wherein
the liquid crystal molecules of the liquid crystal layer are arranged in the direction parallel to the direction from a position where two edges of the pixel electrode meet toward a center portion of the opening of the common electrode to have a pretilt.

14. The liquid crystal display of claim 13, wherein
the pixel electrode is divided into a plurality of sub-regions by the edges of the pixel electrode and the opening of the common electrode, and in each sub-region the liquid crystal molecules of the liquid crystal layer are arranged to have a pretilt of a different direction from the other sub-regions.

15. The liquid crystal display of claim 14, wherein
the pixel electrode comprises at least one opening at a position where two edges of the pixel electrode meet, and the opening extends from the position where two edges of the pixel electrode meet toward the center portion of the opening of the common electrode.

16. The liquid crystal display of claim 15, wherein
the width of the pixel electrode gradually widens from the edges of the pixel electrode facing each other to the center portion of the pixel electrode.

17. The liquid crystal display of claim 1, wherein
the pixel electrode is divided into a plurality of sub-regions by the edges of the pixel electrode and the opening of the common electrode, and in each sub-region the liquid crystal molecules of the liquid crystal layer are arranged to have a pretilt of a different direction from the other sub-regions.

- 18.** The liquid crystal display of claim **17**, wherein the pixel electrode comprises at least one opening at a position where two edges of the pixel electrode meet, and the opening extends from the position where two edges of the pixel electrode meet toward the center portion of the opening of the common electrode.
- 19.** The liquid crystal display of claim **18**, wherein the width of the pixel electrode gradually widens from the edges of the pixel electrode facing each other to the center portion of the pixel electrode.

- 20.** The liquid crystal display of claim **1**, wherein the pixel electrode comprises at least one opening formed at a position where two edges of the pixel electrode meet, and the opening extends from the position where two edges of the pixel electrode meet toward the center portion of the opening of the common electrode.
- 21.** The liquid crystal display of claim **20**, wherein the width of the pixel electrode gradually widens from the edges of the pixel electrode facing each other to the center portion of the pixel electrode.

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专利名称(译)	液晶显示器		
公开(公告)号	US20120281172A1	公开(公告)日	2012-11-08
申请号	US13/460667	申请日	2012-04-30
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IPC分类号	G02F1/1337 G02F1/1343		
CPC分类号	G02F1/133707 G02F1/133753 G02F1/133788 G02F1/13624 G02F2001/133742 G02F2001/134318 G02F2001/133757 G02F2201/122 G02F2201/123 G02F2001/134345 G02F2001/133746		
优先权	61/481546 2011-05-02 US 1020110047435 2011-05-19 KR		
其他公开文献	US8810761		
外部链接	Espacenet USPTO		

摘要(译)

根据本发明示例性实施例的液晶显示器包括：第一基板;像素电极设置在第一基板上;第一取向层，设置在第一基板和像素电极上;面向第一基板的第二基板;公共电极设置在第二基板上;第二取向层，设置在第二基板和公共电极上;液晶层设置在第一基板和第二基板之间，其中公共电极具有多个具有十字形状的切口，并且当向上观察第一基板和第二基板时，切口的边缘从突出部分突出。像素电极的边缘。

