



US 20120026214A1

(19) **United States**(12) **Patent Application Publication**
Oke et al.(10) **Pub. No.: US 2012/0026214 A1**(43) **Pub. Date: Feb. 2, 2012**(54) **LIQUID CRYSTAL DISPLAY DEVICE
HAVING A PLURALITY OF FIRST AND
SECOND SCANNING LINES AND A
PLURALITY OF FIRST AND SECOND VIDEO
LINES****Publication Classification**(51) **Int. Cl.**
G09G 3/36 (2006.01)
G09G 5/10 (2006.01)
(52) **U.S. Cl.** **345/690; 345/102**(76) **Inventors:** **Ryutaro Oke**, Chiba (JP); **Kikuo Ono**, Mobara (JP); **Ikuko Mori**, Mobara (JP); **Masashi Baba**, Chiba (JP); **Yuya Sato**, Mobara (JP)(21) **Appl. No.: 13/270,680**(22) **Filed: Oct. 11, 2011****Related U.S. Application Data**

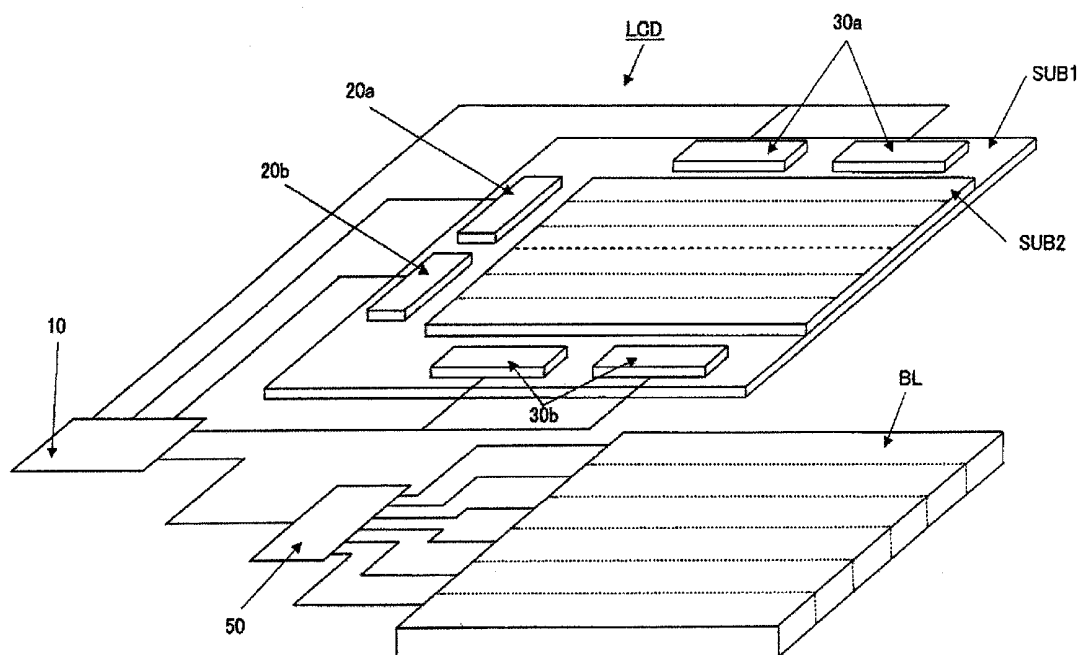
(62) Division of application No. 12/203,953, filed on Sep. 4, 2008.

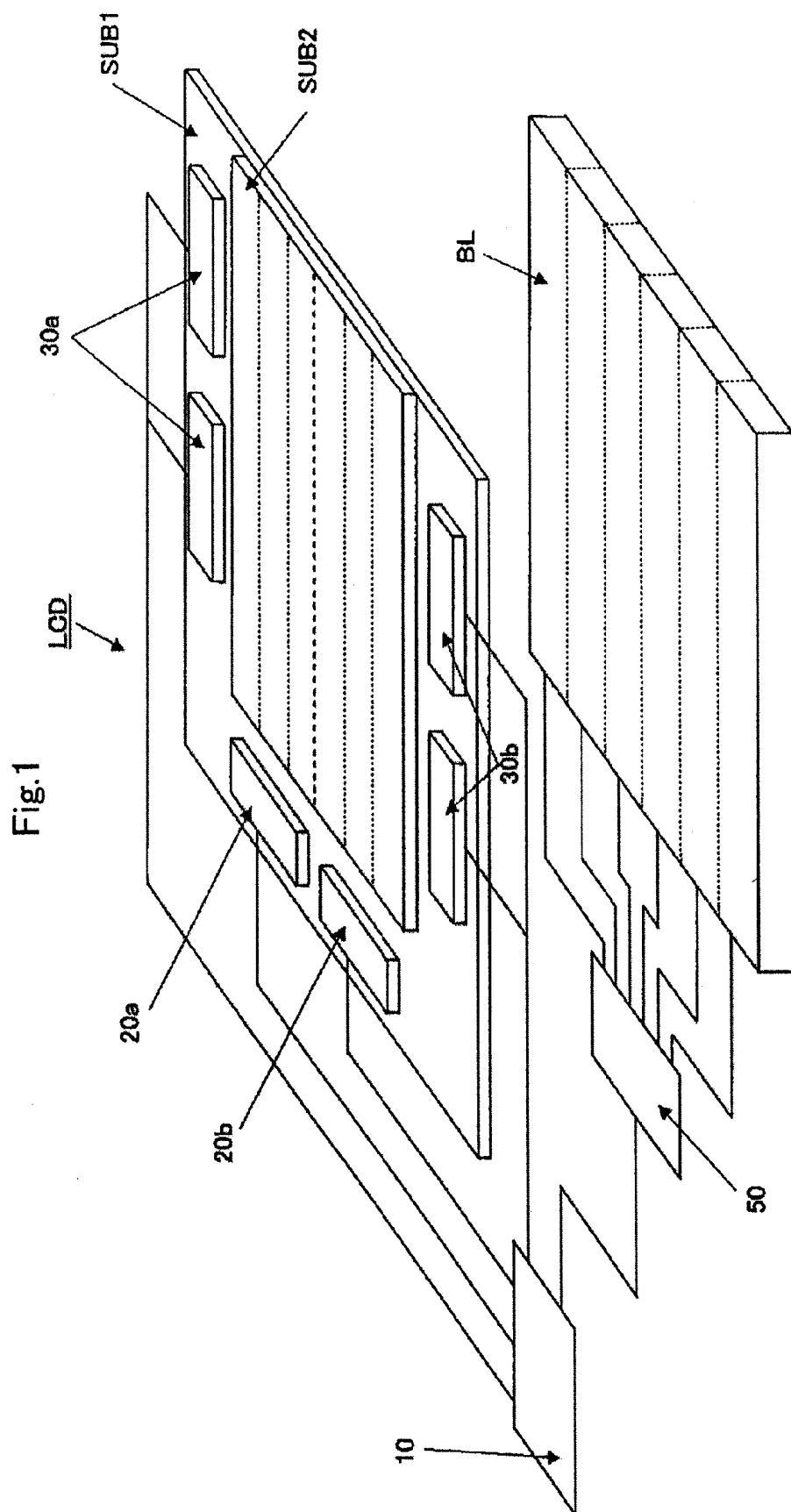
(30) **Foreign Application Priority Data**

Sep. 5, 2007 (JP) 2007-230465

(57) **ABSTRACT**

A liquid crystal display device includes a liquid crystal display panel having first and second scanning lines, first and second video lines, a first scanning line driving circuit, a second scanning line driving circuit, at least one first video line driving circuit, at least one second video line driving circuit, a backlight having a number of light sources, and a light controlling circuit for controlling the turning on and off of the backlight. The first scanning lines are divided into M groups where M is an integer of 2 or more ($M \geq 2$), the second scanning lines are divided into N groups where N is an integer of 2 or more ($N \geq 2$), the region of the backlight which corresponds to the first scanning lines is divided into M regions, and the region which corresponds to the second scanning lines is divided into N regions.





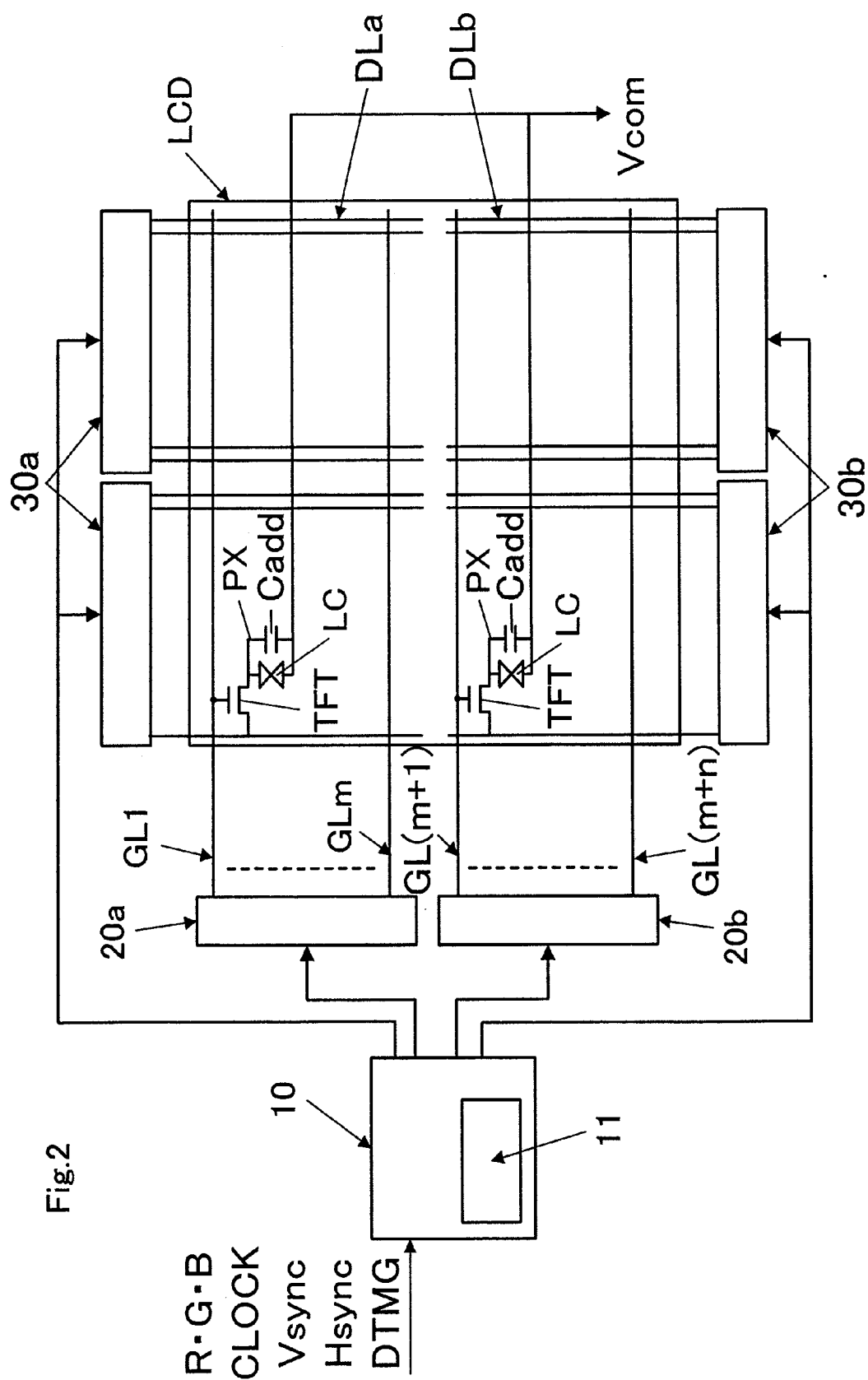
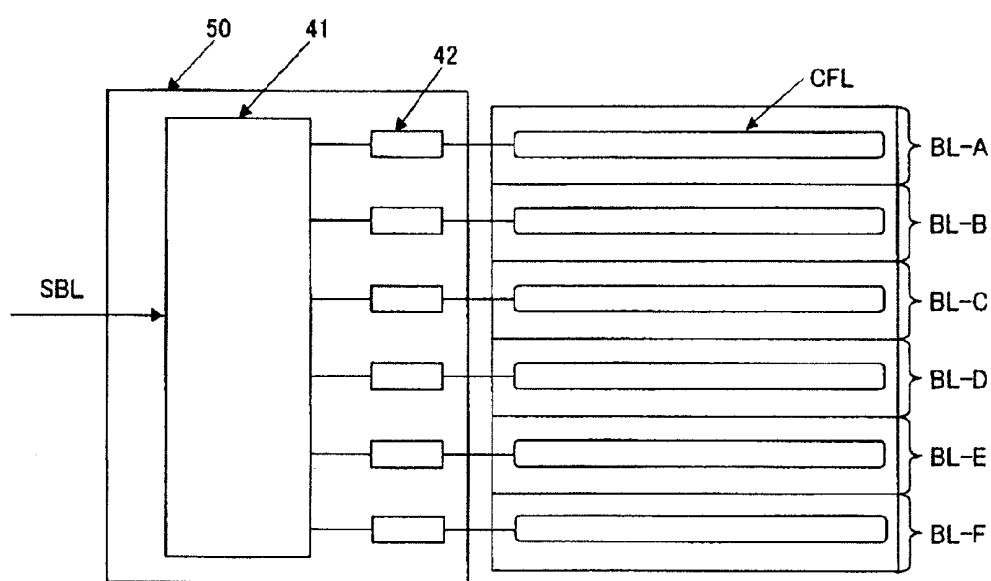
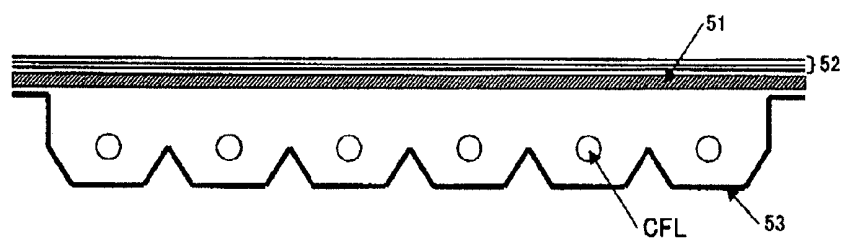


Fig.3



(a)



(b)

Fig.4(a)

A	$GL1 \sim GL(m/3)$
B	$GL(m/3+1) \sim GL(2m/3)$
C	$GL(2m/3+1) \sim GLm$
D	$GL(m+1) \sim GL(m+n/3)$
E	$GL(m+n/3+1) \sim GL(m+2n/3)$
F	$GL(m+2n/3+1) \sim GL(m+n)$

Fig.4(b)

BL-A
BL-B
BL-C
BL-D
BL-E
BL-F

Fig.5(a)

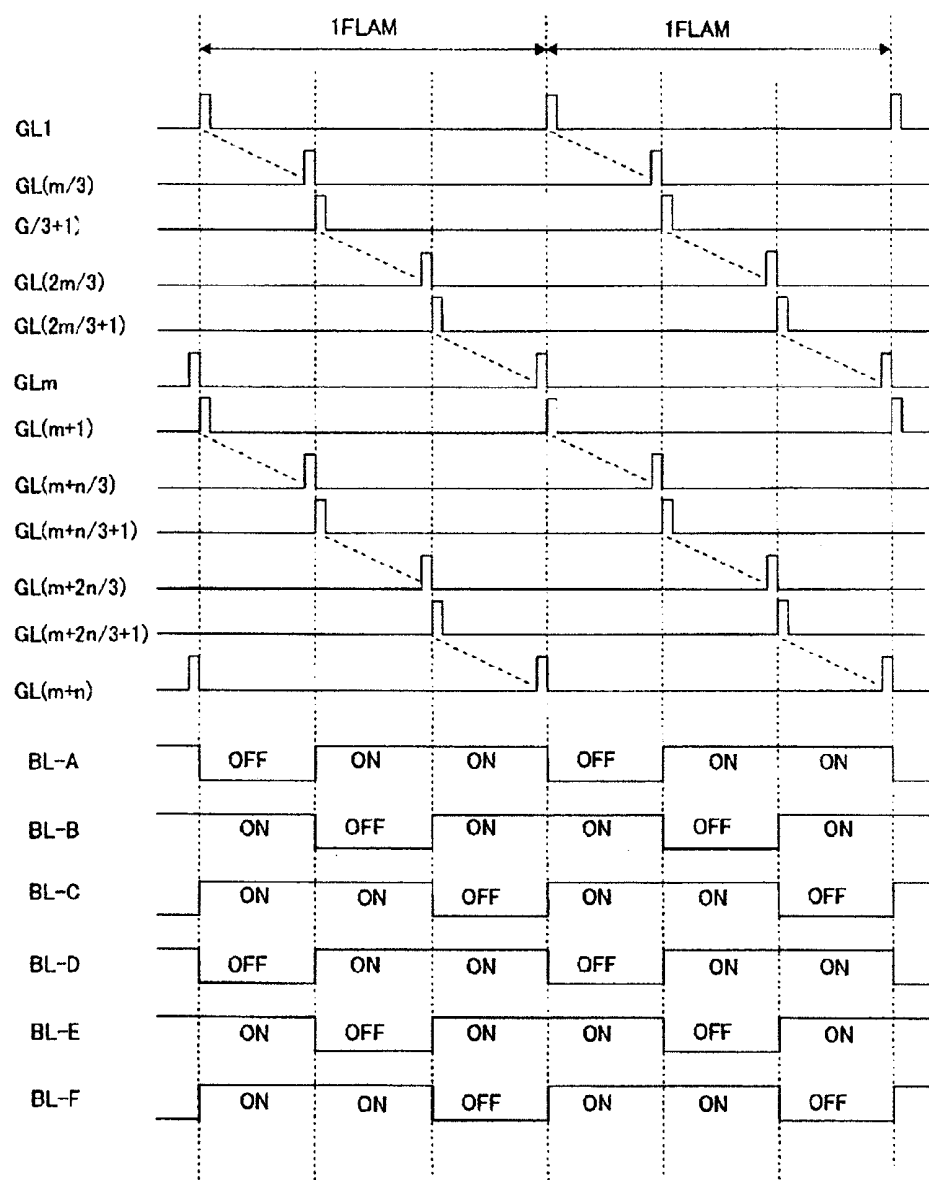


Fig.5(b)

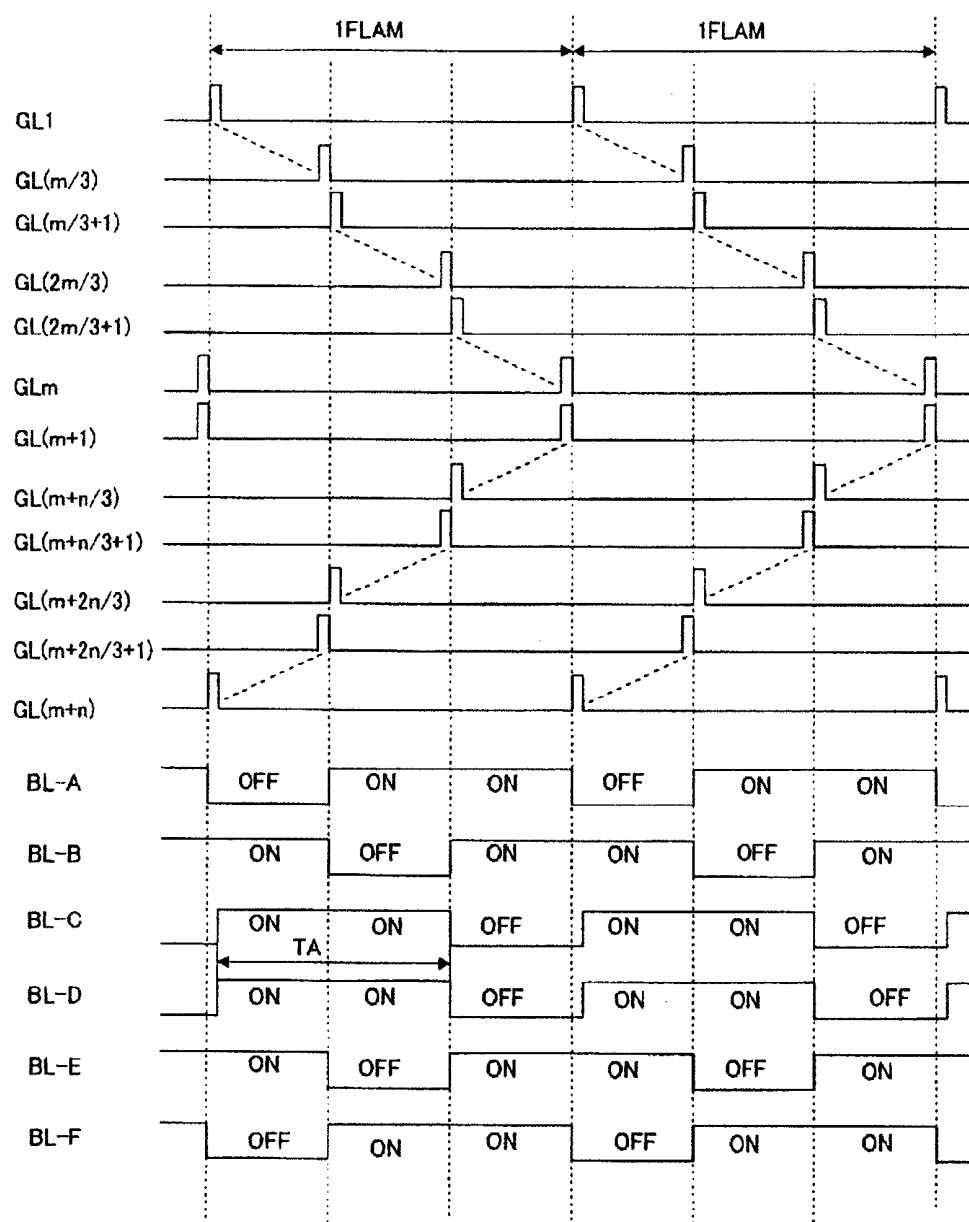


Fig.5(c)

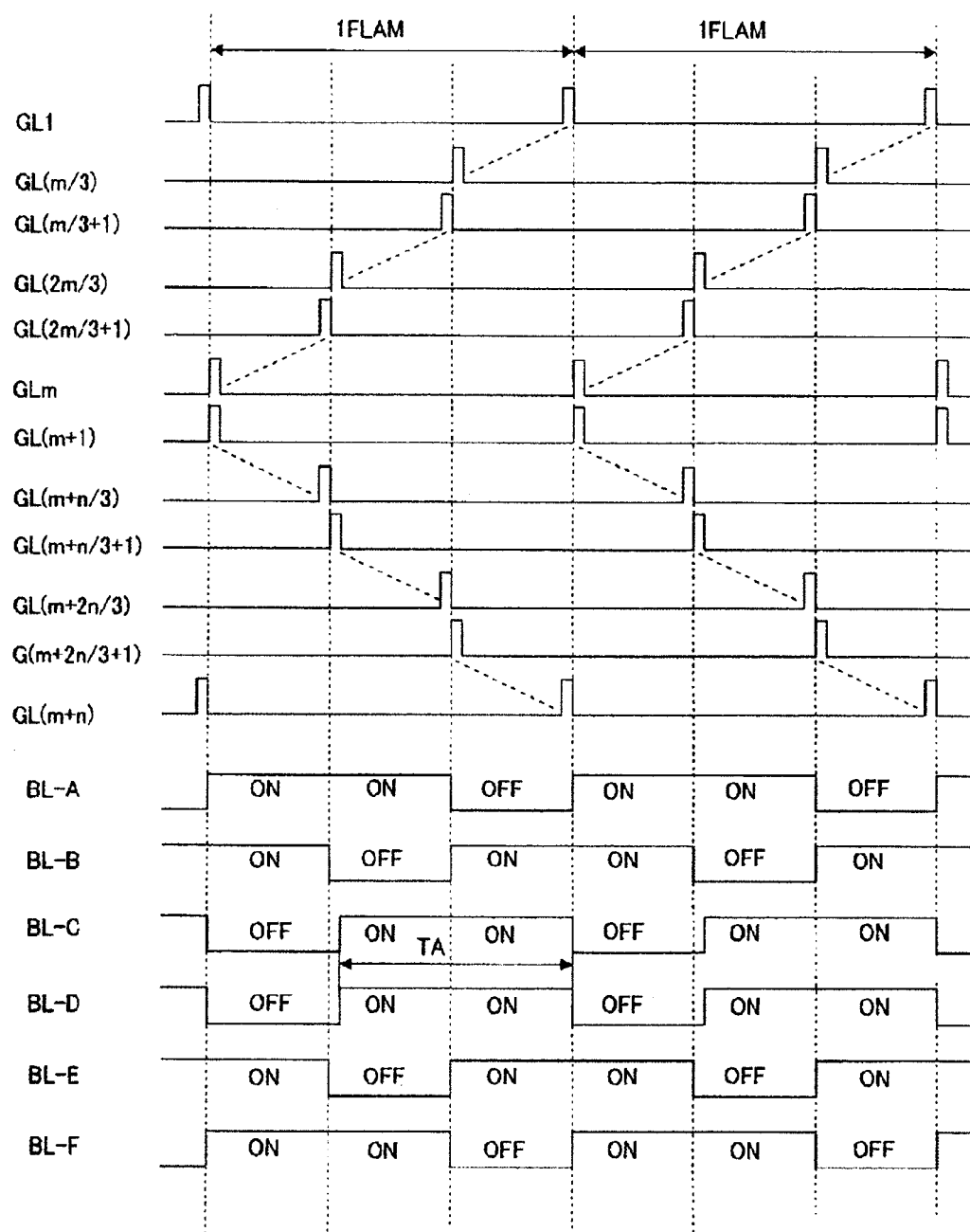
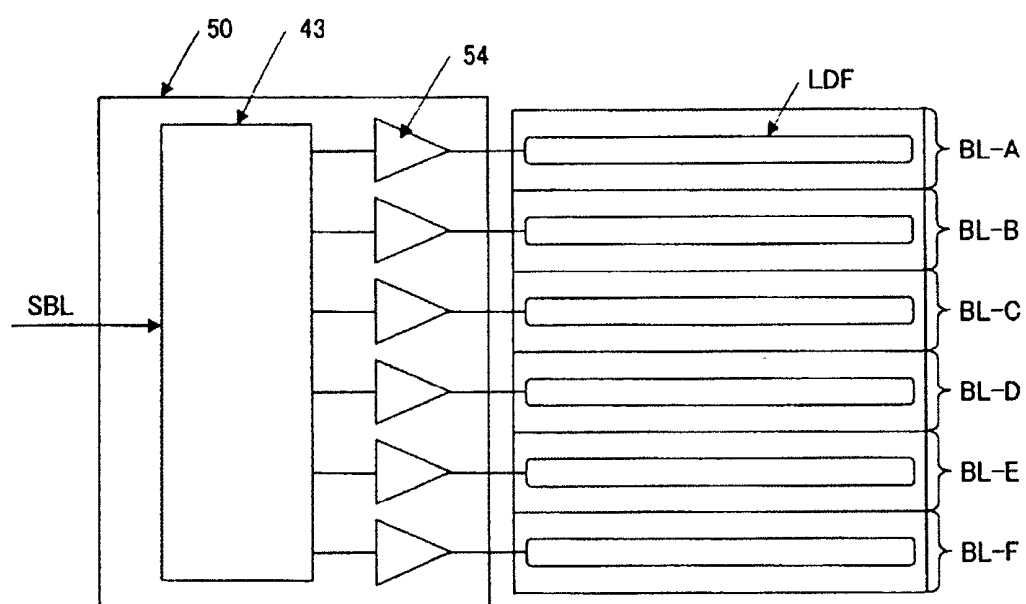
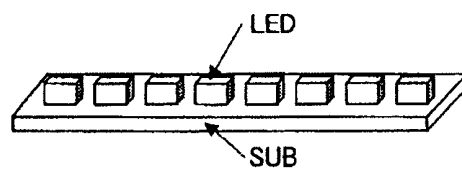


Fig.6



(a)



(b)

**LIQUID CRYSTAL DISPLAY DEVICE
HAVING A PLURALITY OF FIRST AND
SECOND SCANNING LINES AND A
PLURALITY OF FIRST AND SECOND VIDEO
LINES**

CROSS REFERENCE TO RELATED
APPLICATION

[0001] This application is a divisional application of U.S. application Ser. No. 12/203,953, filed Sep. 4, 2008, the contents of which are incorporated herein by reference.

[0002] The present application claims priority from Japanese application JP2007-230465 filed on Sep. 5, 2007, the content of which is hereby incorporated by reference into this application.

BACKGROUND OF THE INVENTION

[0003] (1) Field of the Invention

[0004] The present invention relates to a liquid crystal display device, and in particular, a display device which is appropriate for displaying videos.

[0005] (2) Related Art Statement

[0006] Display devices, particularly those for videos, can be roughly categorized into impulse response type display devices and hold response type display devices. Impulse response type display devices are display devices where the response of brightness lowers immediately after scanning, for example afterglow remains in cathode ray tubes, and cold response type display devices are such display devices as liquid crystal display devices which hold the brightness on the basis of display data until the next scan.

[0007] Hold response type display devices are characterized in that excellent display quality without flickering can be gained in the case of a still image but the outline of moving objects is blurry to the eye in the case of videos. That is to say, there is so-called video blurring, and thus, a problem arises, such that the display quality significantly lowers.

[0008] The cause of this video blurring is a so-called after-image which remains on the retina, where the viewer interpolates the displayed image before and after the movement of the display image of which the brightness is held when moving the line of sight together with the moving object, and therefore, video blurring does not completely disappear, no matter how much the response rate of the display device is increased.

[0009] Methods for intermittently turning on the backlight in order to solve this problem are known. As one method for intermittently turning on the backlight, a method for driving scan-back light according to which the backlight is divided into a number of regions so that the divided regions can be turned on in sequence is known (see Patent Document 2 below).

[0010] Meanwhile, a method for simultaneously driving two screens according to which the liquid crystal display panel is divided in two and the respective liquid crystal display panels on the top and bottom are simultaneously driven in order to compensate for the lack of write-in information due to the high resolution of the liquid crystal display panel, or in order to drive the liquid crystal display panel as fast as 120 Hz (see Patent Document 1 below).

[0011] Here, the following prior art documents relate to the present invention.

[0012] (Patent Document 1) Japanese Unexamined Patent Publication 2002-372956 (Corresponding U.S. Application US2003/0043097 A1)

[0013] (Patent Document 2) Japanese Unexamined Patent Publication 2007-123233 (Corresponding U.S. Application US2007/0097288 A1)

SUMMARY OF THE INVENTION

Problem to Be Solved by the Invention

[0014] As described above, a method for driving scan-back light and a method for simultaneously driving two screens are described in Patent Documents 1 and 2, respectively, as a method for driving a liquid crystal display device.

[0015] The respective patent documents described above, however, do not mention anything about the method for simultaneously driving two screens and the method for driving scan-back light being used simultaneously as one method for driving a liquid crystal display device. If a conventional method for driving scan-back light is applied to a method for simultaneously driving two screens, the liquid crystal panel and the backlight are not driven in sync, because simultaneously driving two screen is not taken into consideration in conventional methods for driving scan-back light, and therefore, the video properties cannot be improved, and in addition, there is a risk that the display of still images may deteriorate.

[0016] The present invention is provided in order to solve the above described problems with the prior art, and an object of the present invention is to provide a liquid crystal display device which simultaneously uses a method for simultaneously driving two screens and a method for driving scan-back light.

[0017] The above described and other objects of the present invention, as well as novel features, will become clearer from the description of the present specification and the accompanying drawings.

Means for Solving Problem

[0018] The gist of typical inventions from among the inventions disclosed in the present application is briefly described below.

(1) A liquid crystal display device is provided with: a liquid crystal display panel having a number of first scanning lines and a number of second scanning lines; a first scanning line driving circuit for supplying a scanning voltage to the above described number of first scanning lines; a second scanning line driving circuit for supplying a scanning voltage to the above described number of second scanning lines; a backlight having a number of light sources; and a light controlling circuit for controlling the turning on and off of the above described backlight, and the above described number of first scanning lines are divided into M groups when M is an integer of 2 or more ($M \geq 2$), the above described number of second scanning lines are divided into N groups when N is an integer of 2 or more ($N \geq 2$), the region of the above described backlight which corresponds to the above described number of first scanning lines is divided into M regions and the region which corresponds to the above described number of second scanning lines is divided into N regions, and the above described light controlling circuit turns off the jth region of the above described backlight ($1 \leq j \leq M$) when the above described first scanning line driving circuit is supplying a selective scanning voltage to the first scanning lines within

the j th group of the above described number of first scanning lines, turns on the j th region of the above described backlight when no selective scanning voltage is supplied to the first scanning lines within the j th group, turns off the k th region of the above described backlight ($1 \leq k \leq N$) when at least one of the above described second scanning line driving circuits supplies a selective scanning voltage to the second scanning lines within the k th group of the above described number of second scanning lines, and turns on the k th region of the above described backlight when no selective scanning voltage is supplied to the second scanning lines within the k th group.

(2) In the same structure as in (1), the above described liquid crystal display panel has: a number of first video lines which cross the above described number of first scanning lines; and a number of second video lines which cross the above described number of second scanning lines, and each of the above described number of first video lines and the above described number of second video lines is provided in such a manner as to be cut out between the above described number of first scanning lines and the above described number of second scanning lines.

(3) In the same structure as in (1) or (2), at least one first video line driving circuit for supplying a video voltage to the above described number of first video lines; and at least one second video line driving circuit for supplying a video voltage to the above described number of second video lines are provided, and the above described first video line driving circuit and the above described second video line driving circuit are provided along two sides of the above described liquid crystal display panel which face each other, and when the direction from the above described first video line driving circuit toward the above described second video line driving circuit is a first scanning direction and the direction from the above described second video line driving circuit toward the above described first video line driving circuit is a second scanning direction, the direction in which a selective scanning voltage is supplied from the above described first scanning line driving circuit to each scanning line of the above described number of first scanning lines in sequence, and the direction in which a selective scanning voltage is supplied from the above described second scanning line driving circuit to each scanning line of the above described number of second scanning lines in sequence are either the above described first scanning direction or the above described second scanning direction.

(4) In the same structure as in (1) or (2), at least one first video line driving circuit for supplying a video voltage to the above described number of first video lines; and at least one second video line driving circuit for supplying a video voltage to the above described number of second video lines are provided, and the above described first video line driving circuit and the above described second video line driving circuit are provided along two sides of the above described liquid crystal display panel which face each other, and when the direction from the above described first video line driving circuit toward the above described second video line driving circuit is a first scanning direction and the direction from the above described second video line driving circuit toward the above described first video line driving circuit is a second scanning direction, the direction in which a selective scanning voltage is supplied from the above described first scanning line driving circuit to each scanning line of the above described number of first scanning lines in sequence is the above described first scanning direction, and the direction in which a selective

scanning voltage is supplied from the above described second scanning line driving circuit to each scanning line of the above described number of second scanning lines in sequence is the above described second scanning direction.

(5) In the same structure as in (1) or (2), at least one first video line driving circuit for supplying a video voltage to the above described number of first video lines; and at least one second video line driving circuit for supplying a video voltage to the above described number of second video lines are provided, and the above described first video line driving circuit and the above described second video line driving circuit are provided along two sides of the above described liquid crystal display panel which face each other, and when the direction from the above described first video line driving circuit toward the above described second video line driving circuit is a first scanning direction and the direction from the above described second video line driving circuit toward the above described first video line driving circuit is a second scanning direction, the direction in which a selective scanning voltage is supplied from the above described first scanning line driving circuit to each scanning line of the above described number of first scanning lines in sequence is the above described second scanning direction, and the direction in which a selective scanning voltage is supplied from the above described second scanning line driving circuit to each scanning line of the above described number of second scanning lines in sequence is the above described first scanning direction.

(6) In the same structure as in (4) or (5), the above described light controlling circuit makes the period during which the M th region from among the above described M regions of the above described backlight and the first region from among the above described N regions which are adjacent to the above described M th region are turned on shorter than the time during which the remaining regions are turned on.

(7) In the same structure as in any of (1) to (6), the j th region and the k th region of the above described backlight have at least one cold cathode fluorescent tube, and the above described light controlling circuit controls the turning on and off of the above described cold cathode fluorescent tube in the j th region and the k th region of the above described backlight.

(8) In the same structure as in any of (1) to (6), the j th region and the k th region of the above described backlight have at least one light emitting diode, and the above described light controlling circuit controls the turning on and off of the above described light emitting diode in the j th region and the k th region of the above described backlight.

Effects of the Invention

[0019] Typical effects of the inventions disclosed in the present specification are briefly described below.

[0020] According to the present invention, it becomes possible to provide a liquid crystal display device which simultaneously uses a method for simultaneously driving two screens and a method for driving scan-back light.

BRIEF DESCRIPTION OF THE DRAWINGS

[0021] FIG. 1 is a block diagram schematically showing the configuration of the liquid crystal display device according to an embodiment of the present invention;

[0022] FIG. 2 is a block diagram showing the configuration of the liquid crystal display panel shown in FIG. 1;

[0023] FIG. 3 is a block diagram showing the configuration of the backlight shown in FIG. 1;

[0024] FIG. 4 is a diagram illustrating the liquid crystal display panel and the manner in which the backlight is grouped according to an embodiment of the present invention;

[0025] FIG. 5(a) is a diagram showing an example of the method for driving a liquid crystal display device according to an embodiment of the present invention;

[0026] FIG. 5(b) is a diagram showing another example of the method for driving a liquid crystal display device according to an embodiment of the present invention;

[0027] FIG. 5(c) is a diagram showing still another example of the method for driving a liquid crystal display device according to an embodiment of the present invention; and

[0028] FIG. 6 is a diagram showing an example of a directly-behind-the-display type backlight having light emitting diodes.

EXPLANATION OF SYMBOLS

[0029]	10 display controlling circuit (timing controller)
[0030]	11 frame memory
[0031]	20a first scanning line driving circuit
[0032]	20b second scanning line driving circuit
[0033]	30a first video line driving circuit
[0034]	30b second video line driving circuit
[0035]	41 inverter controlling circuit
[0036]	42 inverter circuit
[0037]	43 LED driver controlling circuit
[0038]	44 LED driver
[0039]	50 light controlling circuit
[0040]	51 light guiding plate
[0041]	52 optical sheets
[0042]	53 frame
[0043]	LCD liquid crystal display panel
[0044]	SUB1 first substrate
[0045]	SUB2 second substrate
[0046]	BL directly-behind-the-display type backlight
[0047]	DLa, DLb video lines
[0048]	GL scanning line
[0049]	PX pixel electrode
[0050]	TFT thin film transistor
[0051]	LC liquid crystal capacitor
[0052]	Cadd holding capacitor
[0053]	CFL cold cathode fluorescent tube
[0054]	LED white light emitting diode
[0055]	LEF column of light emitting diodes

DETAILED DESCRIPTION OF THE INVENTION

Best Mode for Carrying Out the Invention

[0056] In the following, the embodiments of the present invention are described in detail in reference to the drawings.

[0057] Here, the same symbols are attached to components having the same function in all of the drawings showing the embodiments, and the descriptions are not repeated.

[0058] FIG. 1 is a block diagram schematically showing the configuration of the liquid crystal display device according to an embodiment of the present invention. The liquid crystal display device in the present embodiment has a liquid crystal display panel (LCD) and a directly-behind-the-display type backlight (BL).

[0059] The liquid crystal display panel (LCD) has a first substrate (SUB1) and a second substrate (SUB2), thin film transistors, pixel electrodes and the like are formed on the first substrate (SUB1), and light blocking film, a color filter and

the like are formed on the second substrate (SUB2). Here, the facing electrodes are formed on the first substrate (SUB1) in the case of a lateral electrical field type liquid crystal display panel (LCD), for example of an IPS type, and on the second substrate (SUB2) in the case of a longitudinal electrical field type liquid crystal display panel (LCD), for example of a VA type.

[0060] The liquid crystal display panel (LCD) is formed by pasting the first substrate (SUB1) and the second substrate (SUB2) together using a sealing material and injecting and sealing in liquid crystal between the first substrate (SUB1) and the second substrate (SUB2). In addition, polarizing plates are provided on the outside of the first substrate (SUB1) and the second substrate (SUB2).

[0061] Here, the present invention does not directly relate to the structure of the liquid crystal display panel (LCD), and therefore, the structure of the liquid crystal display panel (LCD) is not shown.

[0062] A first video line driving circuit (30a) and a second video line driving circuit (30b) are provided in the periphery of the first substrate (SUB1) on the two long sides which face each other. The first video line driving circuit (30a) and the second video line driving circuit (30b) are controlled and driven by a display controlling circuit (timing controller) 10.

[0063] In addition, a first scanning line driving circuit (20a) and a second scanning line driving circuit (20b) are provided in the periphery of the first substrate (SUB1) on one short side. The first scanning line driving circuit (20a) and the second scanning line driving circuit (20b) are controlled and driven by the display controlling circuit (timing controller) 10.

[0064] Here, though a case where the first video line driving circuit (30a) and the second video line driving circuit (30b) are formed of two video line driving circuits (semiconductor chips) is shown in FIG. 1, the first video line driving circuit (30a) and the second video line driving circuit (30b) may be formed of one video line driving circuit or three or more video line driving circuits. In addition, the video line driving circuits may be provided on a flexible substrate connected to the first substrate (SUB1) instead of on the first substrate (SUB1).

[0065] In addition, though a case where the first scanning line driving circuit (20a) and the second scanning line driving circuit (20b) are formed of one scanning line driving circuit (semiconductor chip) is shown in FIG. 1, the first scanning line driving circuit (20a) and the second scanning line driving circuit (20b) may be formed of a number of scanning line driving circuits. In addition, the scanning line driving circuit may be provided on a flexible substrate connected to the first substrate (SUB1) instead of on the first substrate (SUB1).

[0066] Here, as described below, scanning lines are divided into six groups. In addition, the backlight (BL) is divided into six regions which correspond to the groups of scanning lines, and the six regions are individually controlled and driven by the light controlling circuit 50.

[0067] FIG. 2 is a block diagram showing the configuration of the liquid crystal display panel (LCD) shown in FIG. 1.

[0068] The liquid crystal display panel (LCD) is divided in two, top and bottom, and first scanning lines (GL1 to GLm) are provided in the upper half and second scanning lines (GL(m+1) to GL(m+n)) are provided in the lower half. Here, m and n are integers of 2 or more.

[0069] Here, as described below, the first scanning lines (GL1 to GLm) and the second scanning lines (GL(m+1) to

GL(m+n)) are each divided into three groups. That is to say, the scanning lines are divided into six groups.

[0070] In addition, first video lines (DLa) are provided in the upper half of the liquid crystal display panel (LCD) so as to cross the first scanning lines (GL1 to GLm), and furthermore, second video lines (DLb) are provided in the lower half of the liquid crystal display panel (LCD) so as to cross the second scanning lines (GL(m+1) to GL(m+n)).

[0071] The first video lines (DLa) are connected to the first video line driving circuit 30a and the second video lines (DLb) are connected to the second video line driving circuit 30b. The first scanning lines (GL1 to GLm) are connected to the first scanning line driving circuit 20a, and the second scanning line (GL(m+1) to GL(m+n)) are connected to the second scanning line driving circuit 20b.

[0072] In the liquid crystal display panel (LCD) shown in FIG. 2, the drain electrodes (or source electrodes) of the thin film transistors (TFT) in the respective pixels aligned in the direction of the columns are connected to the video lines (DLa, DLb), and the respective video lines (DLa, DLb) supply a gradation voltage corresponding to the display data to the pixel electrodes (PX) in the pixels aligned in the direction of the columns.

[0073] In addition, the gate electrodes of the thin film transistors (TFT) in the respective pixels aligned in the direction of the rows are respectively connected to the scanning lines (GL1 to GL(m+n)), and the respective scanning lines (GL1 to GL(m+n)) supply a selective scanning voltage to the gate electrodes of the thin film transistors (TFT) during one horizontal scan, and supply a non-selective scanning voltage at other times.

[0074] Here, in FIG. 2, LC is a liquid crystal capacitor which is equivalent to an element including the liquid crystal layer, and Cadd is a holding capacitor formed between a pixel electrode (PX) and a facing electrode to which a driving voltage Vcom is supplied.

[0075] The display controlling circuit 10 has a frame memory 11, and controls and drives the first video line driving circuit 30a, the second video line driving circuit 30b, the first scanning line driving circuit 20a and the second scanning line driving circuit 20b on the basis of the respective display controlling signals, including the dot clock (CLOCK), the vertical sync signal (Vsync), the horizontal sync signal (Hsync) and the display timing signal (DTMG), as well as data for display (R-G-B) sent from the main body side.

[0076] The first scanning line driving circuit 20a selects a first scanning line (GL1 to GLm), for example from the top to the bottom (in the order GL1→GL2 . . .), and at the same time, the second scanning line driving circuit 20b selects a second scanning line (GL(m+1) to GL(m+n)), for example from the top to the bottom (in the order GL(m+1)→GL(m+2)). Meanwhile, during the period when a certain scanning line is selected, the first video line driving circuit 30a supplies a gradation voltage corresponding to the display data to the first video line (DLa) and the second video line driving circuit 30b supplies a gradation voltage corresponding to the display data to the second video line (DLb), so that the voltage is applied to the pixel electrode (PX).

[0077] That is to say, the first scanning line driving circuit 20a and the second scanning line driving circuit 20b apply a selective scanning voltage of a high level (hereinafter referred to as H level) to the gate electrodes of the thin film transistors (TFT) during one horizontal scanning period (1 H), so that all of the thin film transistors (TFT) connected to one scanning

line are converted to an ON state, that is to say, a selected state, and thus, the gradation voltage outputted from the first video line driving circuit 30a and the second video line driving circuit 30b is inputted into the pixel electrodes (PX).

[0078] Conversely, in the case of a non-selective scanning voltage of a low level (hereinafter referred to as L level), all of the thin film transistors (TFT) connected to one scanning line are converted to an OFF state, that is to say, an unselected state.

[0079] As a result, the holding capacitor (Cstg) and the liquid crystal capacitor (LC) are charged, so as to control the liquid crystal molecules, and an image is displayed.

[0080] FIG. 3 is a block diagram showing the configuration of the backlight (BL) shown in FIG. 1.

[0081] As shown in FIG. 3(a), the light controlling circuit 50 is formed of an inverter controlling circuit 41 and an inverter circuit 42.

[0082] In addition, as shown in FIG. 3(b), the backlight (BL) are formed of six cold cathode fluorescent tubes (CFL), a frame 53 which also works as a reflective plate, a light guiding plate 51 provided on the liquid crystal display panel (LCD) side of the cold cathode fluorescent tubes (CFL), and optical sheets 52, including a lens sheet, a diffusion sheet and the like, placed on the light guiding plate 51.

[0083] As described above, the backlight (BL) is divided into six regions (BL-A to BL-F) and corresponds to the groups of the scanning lines on the liquid crystal display panel (LCD), and one cold cathode fluorescent tube (CFL) is provided in each of the six regions (DL-A to DL-F).

[0084] The inverter controlling circuit 41 individually controls and drives the cold cathode fluorescent tubes (CFL) in the six regions (BL-A to BL-F) on the basis of a light controlling signal (SBL) from the display controlling circuit 10.

[0085] Here, two or more cold cathode fluorescent tubes (CFL) may be provided in each of the six regions (BL-A to BL-F).

[0086] FIG. 4 is a diagram showing the liquid crystal display panel (LCD) and the manner in which the backlight (BL) is grouped according to the embodiment of the present invention.

[0087] As shown in FIG. 4, the liquid crystal display panel (LCD) is divided in two, top and bottom, and the first scanning lines (GL1 to GLm) in the top half are divided into three groups A, B and C, and the first scanning lines in the bottom half are divided into three groups D, E and F.

[0088] In FIG. 4, the group A is formed of the first to m/3th first scanning lines (GL1 to GL(m/3)), the group B is formed of the (m/3+1)th to (2m/3)th first scanning lines (GL(m/3+1) to GL(2m/3)), and the group C is formed of the (2m/3+1)th to mth first scanning lines (GL(2m/3+1) to GLm).

[0089] Furthermore, the group D is formed of the first to m/3th second scanning lines (GL(m+1) to GL(m+n/3)), the group E is formed of the (m+n/3+1)th to (m+2n/3)th second scanning lines (GL(m+n/3+1) to GL(m+2n/3)), and the group F is formed of the (m+2n/3+1)th to mth second scanning lines (GL(m+2n/3+1) to GLn).

[0090] In addition, the backlight (BL) is divided into six regions (BL-A to BL-F) and corresponds to the groups of scanning lines on the liquid crystal display panel (LCD), and as shown in FIG. 4, the region BL-A of the backlight (BL) corresponds to the group A, and likewise, the region BL-B of the backlight (BL) corresponds to the group B, the region BL-C of the backlight (BL) corresponds to the group C, the region BL-D of the backlight (BL) corresponds to the group

D, the region BL-E of the backlight (BL) corresponds to the group E, and the region BL-F of the backlight (BL) corresponds to the group F.

[0091] Here, though FIG. 4 shows a case where the top half and the bottom half of the liquid crystal display panel (LCD) are each divided into three groups, the present invention is not limited to this, and any number of groups is possible, as long as there are two or more, and the number of groups may be different between the top half and the bottom half of the liquid crystal display panel (LCD), and furthermore, the number of scanning lines may be the same of different in each group.

[0092] FIG. 5(a) is a diagram showing an example of the method for driving a liquid crystal display device according to an embodiment of the present invention.

[0093] The liquid crystal display device in the present embodiment adopts a method for simultaneously driving two screens. In accordance with the driving method in FIG. 5(a), the first scanning line driving circuit 20a supplies a selection scanning voltage to the number of first scanning lines (GL1 to GLm) one by one in the order of group A→B→C within one frame (FLAM), and at the same time, the second scanning line driving circuit 20b supplies a selection scanning voltage to the number of second scanning lines (GLm to GL(m+n)) one by one in the order of group D→E→F.

[0094] In addition, the inverter controlling circuit 41 turns off the cold cathode fluorescent tube (CFL) in the region corresponding to each group A to F when a selection scanning voltage is supplied to a scanning line within the groups A to F, and turns on the cold cathode fluorescent tube (CFL) in the region corresponding to each group A to F when no scanning line within the groups A to F is supplied with a selection scanning voltage.

[0095] When a selection scanning voltage is supplied to the first scanning lines (GL1 to GL(m/3)) in group A from the first scanning line driving circuit 20a, for example, the region (BL-A) in the backlight (BL) becomes of an OFF state, and when no first scanning line (GL1 to GL(m/3)) in group A is supplied with a selection scanning voltage from the first scanning line driving circuit 20a, the region (BL-A) in the backlight (BL) becomes of an ON state.

[0096] Likewise, when a selection scanning voltage is supplied to the first scanning lines (GLm to GL(m+n/3)) in group D from the second scanning line driving circuit 20b, for example, the region (BL-D) in the backlight (BL) becomes of an OFF state, and when no first scanning line (GLm to GL(m+n/3)) in group D is supplied with a selection scanning voltage from the second scanning line driving circuit 20b, the region (BL-D) in the backlight (BL) becomes of an ON state.

[0097] FIG. 5(b) is a diagram illustrating another example of the method for driving a liquid crystal display device according to an embodiment of the present invention.

[0098] The driving method in FIG. 5(b) is different from the driving method in FIG. 5(a) in that the first scanning line driving circuit 20a supplies a selection scanning voltage to the number of first scanning lines (GL1 to GLm) one by one in the order of group A→B→C within one frame (FLAM), and at the same time, the second scanning line driving circuit 20b supplies a selection scanning voltage to the number of second scanning lines (GLm to GL(m+n)) one by one in the order of group F→E→D.

[0099] FIG. 5(c) is a diagram illustrating another example of the method for driving a liquid crystal display device according to an embodiment of the present invention.

[0100] The driving method in FIG. 5(c) is different from the driving method in FIG. 5(a) in that the first scanning line driving circuit 20a supplies a selection scanning voltage to the number of first scanning lines (GL1 to GLm) one by one in the order of group C→B→A within one frame (FLAM), and at the same time, the second scanning line driving circuit 20b supplies a selection scanning voltage to the number of second scanning lines (GLm to GL(m+n)) one by one in the order of group D→E→F.

[0101] As shown in FIGS. 5(b) and 5(c), in accordance with the driving method shown in FIGS. 5(b) and 5(c), the region (BL-C) and the region (BL-D) in the backlight (BL) become of an OFF state and an ON state at the same time.

[0102] Therefore, the viewer feels like the region (BL-C) and the region (BL-D) in the backlight (BL) are brighter than other regions.

[0103] In order to prevent this, the period during which the region (BL-C) and the region (BL-D) in the backlight (BL) are turned on (period TA in FIGS. 5(b) and 5(c)) is made shorter than that in other regions (BL-A, BL-B, BL-E, BL-F).

[0104] Here, in the present embodiment, a directly-behind-the-display type backlight (BL) having light emitting diodes (LED) may be used instead of a directly-behind-the-display type backlight (BL) having a cold cathode fluorescent tube (CFL).

[0105] FIG. 6 shows an example of a directly-behind-the-display type backlight (BL) having light emitting diodes (LED).

[0106] In FIG. 6(a), the light controlling circuit 50 shown in FIG. 1 is formed of an LED driver controlling circuit 43 and an LED driver 44.

[0107] In addition, in FIG. 6(a), LDF is a light emitting diode column, and this light emitting diode column (LDF) is formed of a substrate (SUB) where a wire layer is formed and a number of white light emitting diodes (LED) provided on the substrate (SUB), as shown in FIG. 6(b).

[0108] The LED driver controlling circuit 43 individually controls and drives the respective light emitting columns (LDF) in the six regions (BL-A to BL-F) on the basis of a light controlling signal (SBL) from the display controlling circuit 10.

[0109] Thus, in the present embodiment, it becomes possible to provide a liquid crystal display device where a method for simultaneously driving two screens and a method for driving scan-back light are adopted at the same time.

[0110] Though the invention made by the present inventor is concretely described on the basis of the above described embodiments, the present invention is not limited to the above described embodiments, and various modifications are, of course, possible, in such a scope as not to deviate from the gist.

1. (canceled)
2. (canceled)
3. (canceled)
4. (canceled)
5. (canceled)
6. (canceled)
7. (canceled)
8. (canceled)
9. (canceled)

10. A liquid crystal display device, comprising:

a liquid crystal display panel having a number of first scanning lines, a number of second scanning lines, a number of first video lines which cross said number of

first scanning lines, and a number of second video lines which cross said number of second scanning lines;
 a first scanning line driving circuit for supplying a scanning voltage to said number of first scanning lines;
 a second scanning line driving circuit for supplying a scanning voltage to said number of second scanning lines;
 at least one first video line driving circuit for supplying a video voltage to said number of first video lines; and at least one second video line driving circuit for supplying a video voltage to said number of second video lines, said first video line driving circuit and said second video line driving circuit are provided along two sides of said liquid crystal display panel which face each other;
 a backlight having a number of light sources; and
 a light controlling circuit for controlling the turning on and off of said backlight;
 wherein said number of first scanning lines are divided into M groups where M is an integer of 2 or more ($M \geq 2$), said number of second scanning lines are divided into N groups where N is an integer of 2 or more ($N \geq 2$), the region of said backlight which corresponds to said number of first scanning lines is divided into M regions and the region which corresponds to said number of second scanning lines is divided into N regions, and said light controlling circuit turns off a jth region of said backlight ($1 \leq j \leq M$) when said first scanning line driving circuit is supplying a selective scanning voltage to the first scanning lines within a jth group of said number of first scanning lines, turns on the jth region of said backlight when no selective scanning voltage is supplied to the first scanning lines within the jth group, turns off a kth region of said backlight ($1 \leq k \leq N$) when at least one of said second scanning line driving circuits supplies a selective scanning voltage to the second scanning lines within a kth group of said number of second scanning lines, and turns on the kth region of said backlight when no selective scanning voltage is supplied to the second scanning lines within the kth group.

11. The liquid crystal display device according to claim **10**, wherein said liquid crystal display panel has each of said number of first video lines and said number of second video lines is provided in such a manner as to be cut out between said number of first scanning lines and said number of second scanning lines.

12. The liquid crystal display device according to claim **10**, wherein when the direction from said first video line driving circuit toward said second video line driving circuit is a first scanning direction and the direction from said second video line driving circuit toward said first video line driving circuit is a second scanning direction, the direction in which a selective scanning voltage is supplied from said first scanning line driving circuit to each scanning line of said number of first scanning lines in sequence is said first scanning direction, and the direction in which a selective scanning voltage is supplied from said second scanning line driving circuit to each scanning line of said number of second scanning lines in sequence is said second scanning direction.

13. The liquid crystal display device according to claim **12**, wherein said light controlling circuit makes the period during which the Mth region from among the M regions of said backlight and the first region from among the N regions which are adjacent to the Mth region are turned on shorter than the time during which the remaining regions are turned on.

14. The liquid crystal display device according to claim **10**, wherein the jth region and the kth region of said backlight have at least one cold cathode fluorescent tube, and said light controlling circuit controls the turning on and off of said cold cathode fluorescent tube in the jth region and the kth region of said backlight.

15. The liquid crystal display device according to claim **10**, wherein the jth region and the kth region of said backlight have at least one light emitting diode, and said light controlling circuit controls the turning on and off of said light emitting diode in the jth region and the kth region of said backlight.

* * * * *

专利名称(译)	具有多条第一和第二扫描线以及多条第一和第二视频线的液晶显示装置		
公开(公告)号	US20120026214A1	公开(公告)日	2012-02-02
申请号	US13/270680	申请日	2011-10-11
[标]申请(专利权)人(译)	OKE龙太郎 ONO KIKUO 森郁子 BABA政志 佐藤佑		
申请(专利权)人(译)	OKE龙太郎 ONO KIKUO 森郁子 BABA政志 佐藤佑		
当前申请(专利权)人(译)	日立显示器有限公司. 松下液晶显示CO., LTD.		
[标]发明人	OKE RYUTARO ONO KIKUO MORI IKUKO BABA MASASHI SATO YUYA		
发明人	OKE, RYUTARO ONO, KIKUO MORI, IKUKO BABA, MASASHI SATO, YUYA		
IPC分类号	G09G3/36 G09G5/10		
CPC分类号	G09G3/342 G09G3/3666 G09G2320/064 G09G2320/0233 G09G2310/024		
优先权	2007230465 2007-09-05 JP		
其他公开文献	US8482513		
外部链接	Espacenet USPTO		

摘要(译)

一种液晶显示装置，包括具有第一和第二扫描线的液晶显示面板，第一和第二视频线，第一扫描线驱动电路，第二扫描线驱动电路，至少一个第一视频线驱动电路，至少一个第二视频线驱动电路，具有多个光源的背光，以及用于控制背光的开启和关闭的光控制电路。第一扫描线被分成M组，其中M是2或更大的整数（ $M \geq 2$ ），第二扫描线被分成N组，其中N是2或更大的整数（ $N \geq 2$ ），该区域对应于第一扫描线的背光的一部分被分成M个区域，并且对应于第二扫描线的区域被分成N个区域。

