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LI(10) **Pub. No.: US 2019/0204689 A1**(43) **Pub. Date: Jul. 4, 2019**(54) **LIQUID CRYSTAL DISPLAY DEVICE****Publication Classification**(71) Applicant: **Shenzhen China Star Optoelectronics Semiconductor Display Technology Co., Ltd., Shenzhen (CN)**(51) **Int. Cl.**
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H01L 27/12 (2006.01)(72) Inventor: **Wenying LI, Shenzhen (CN)**(52) **U.S. Cl.**
CPC .. **G02F 1/134309** (2013.01); **G02F 2201/123** (2013.01); **H01L 27/124** (2013.01); **G02F 1/1368** (2013.01)(21) Appl. No.: **15/746,429**(22) PCT Filed: **Jan. 12, 2018**(86) PCT No.: **PCT/CN2018/072466**

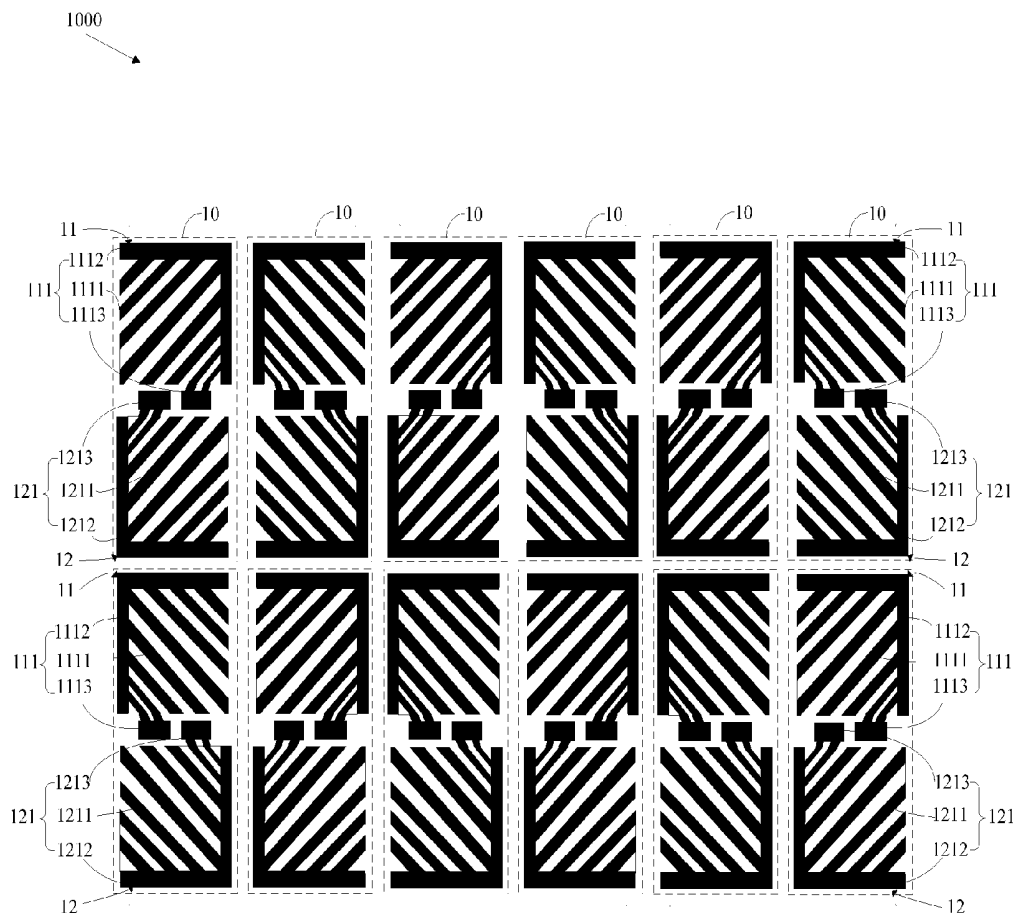
§ 371 (c)(1),

(2) Date: **Jan. 22, 2018**(30) **Foreign Application Priority Data**

Dec. 29, 2017 (CN) 201711476040.3

(57) **ABSTRACT**

The present disclosure provides a liquid crystal display device including a plurality of pixels arranged in an array, each of the pixels comprising: a first pixel region and a second pixel region aligned vertically with the first pixel region. Any two adjacent pixels in the same row have structures being mirror-symmetric with respect to each other. And, for any two adjacent rows of pixels, a structure of the m^{th} pixel in the $(N+1)^{th}$ row and a structure of the $(m+1)^{th}$ pixel in the N^{th} row are the same, where N and M are natural numbers.



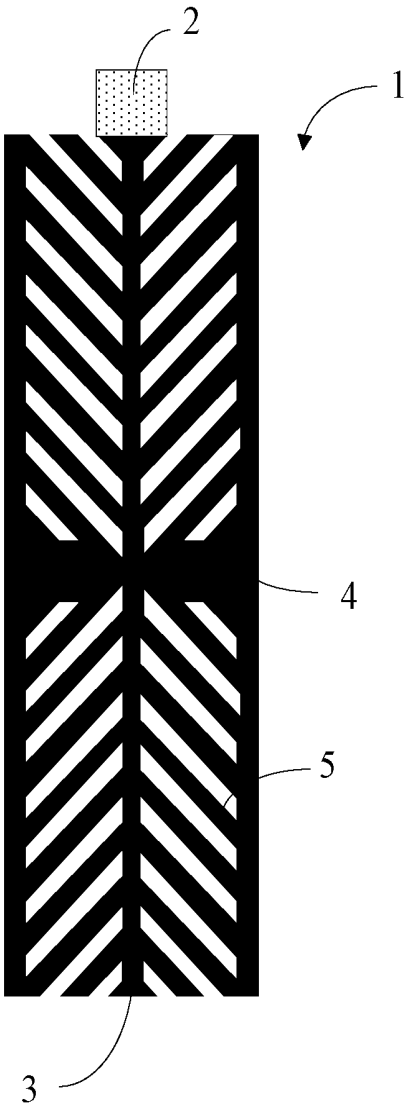


FIG. 1
(PRIOR ART)

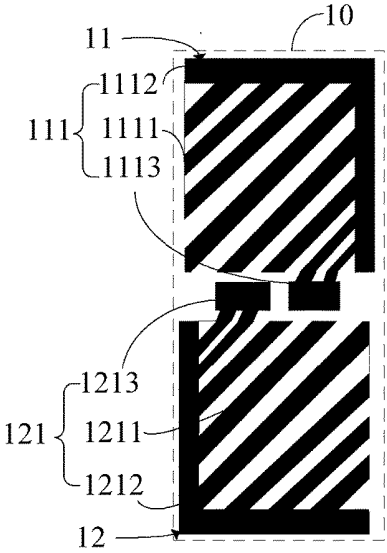


FIG. 2

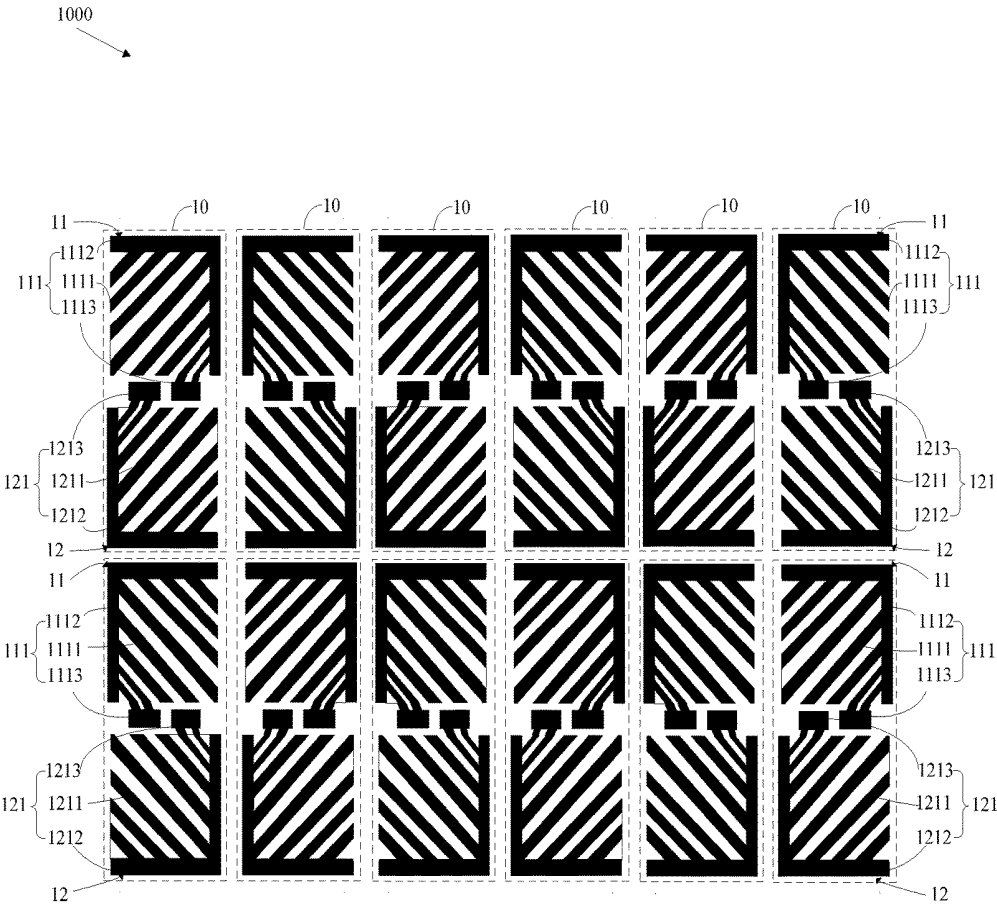


FIG. 3

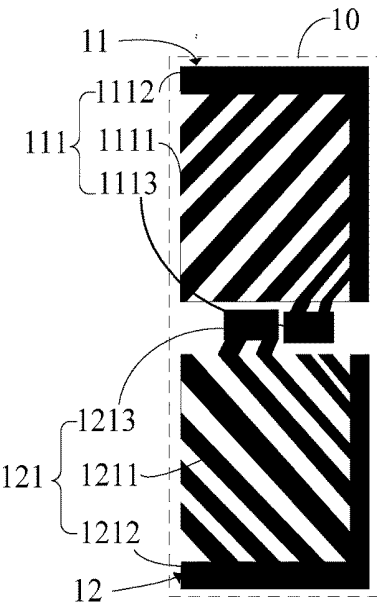


FIG. 4

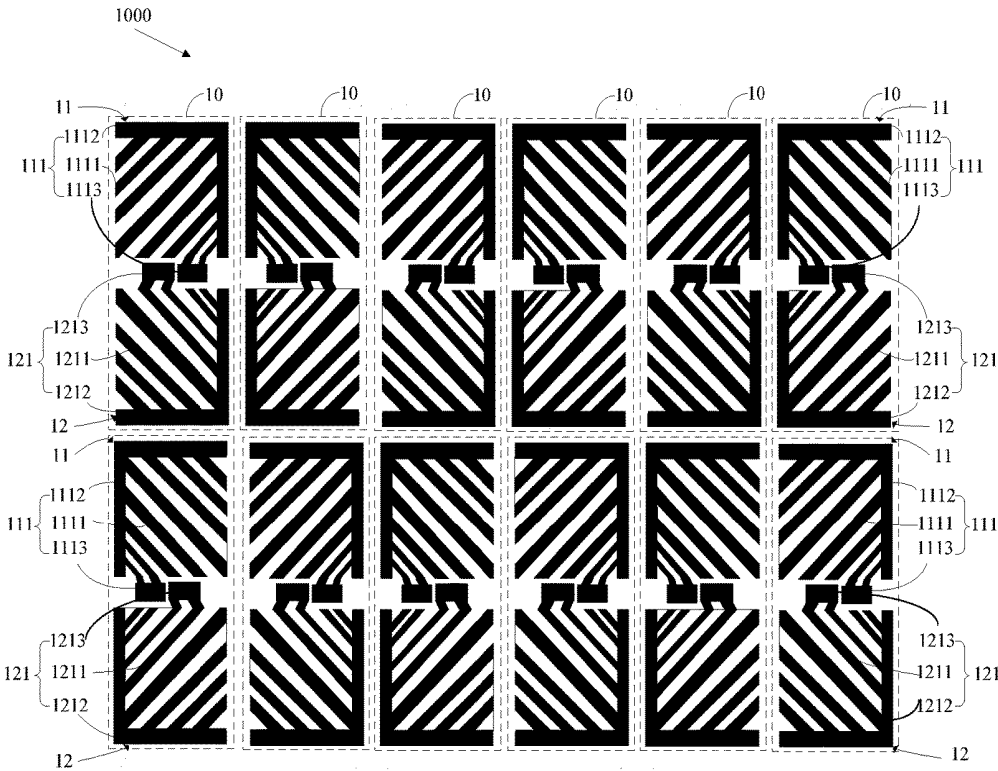


FIG. 5

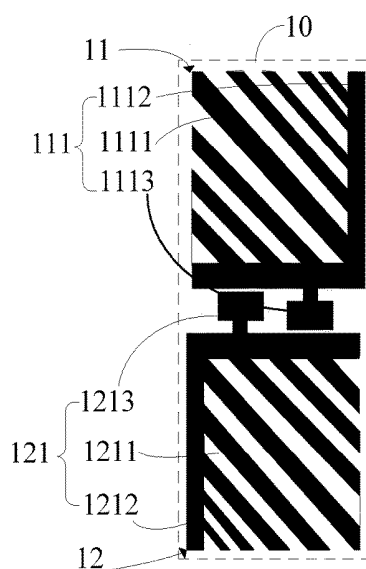


FIG. 6

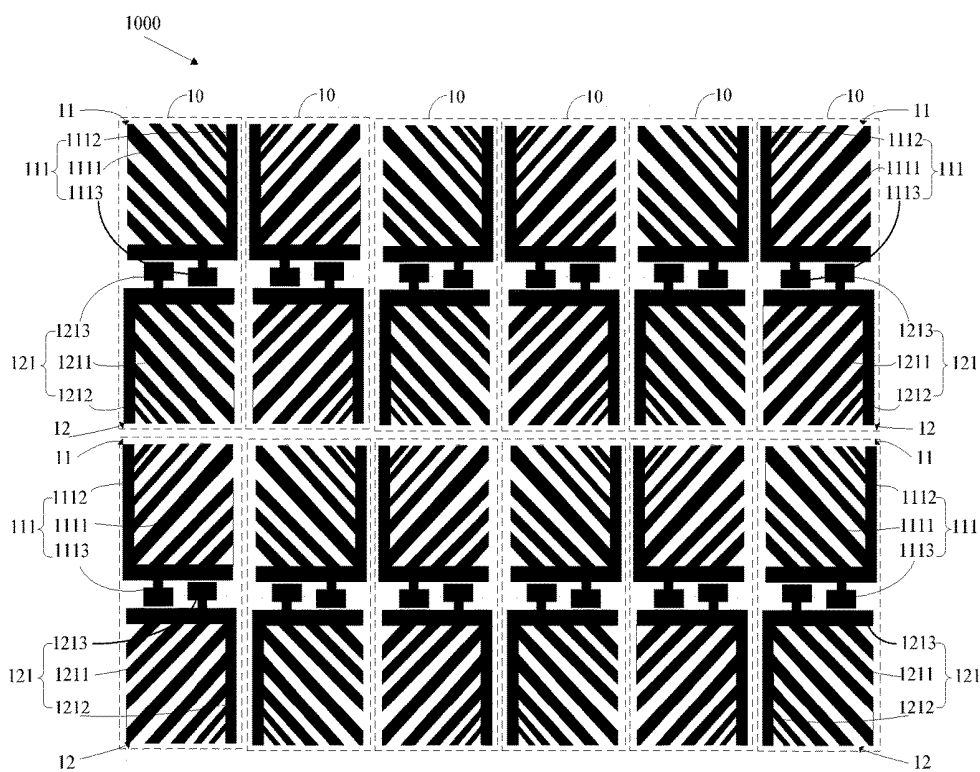


FIG. 7

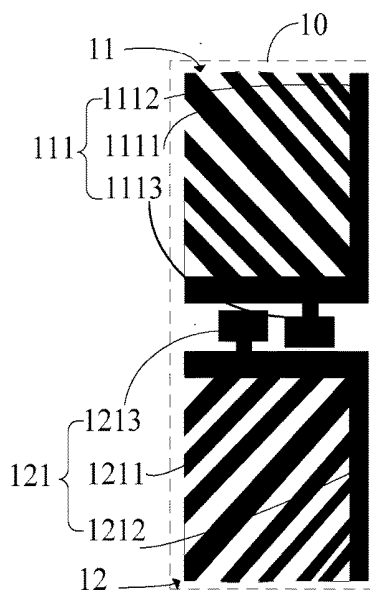


FIG. 8

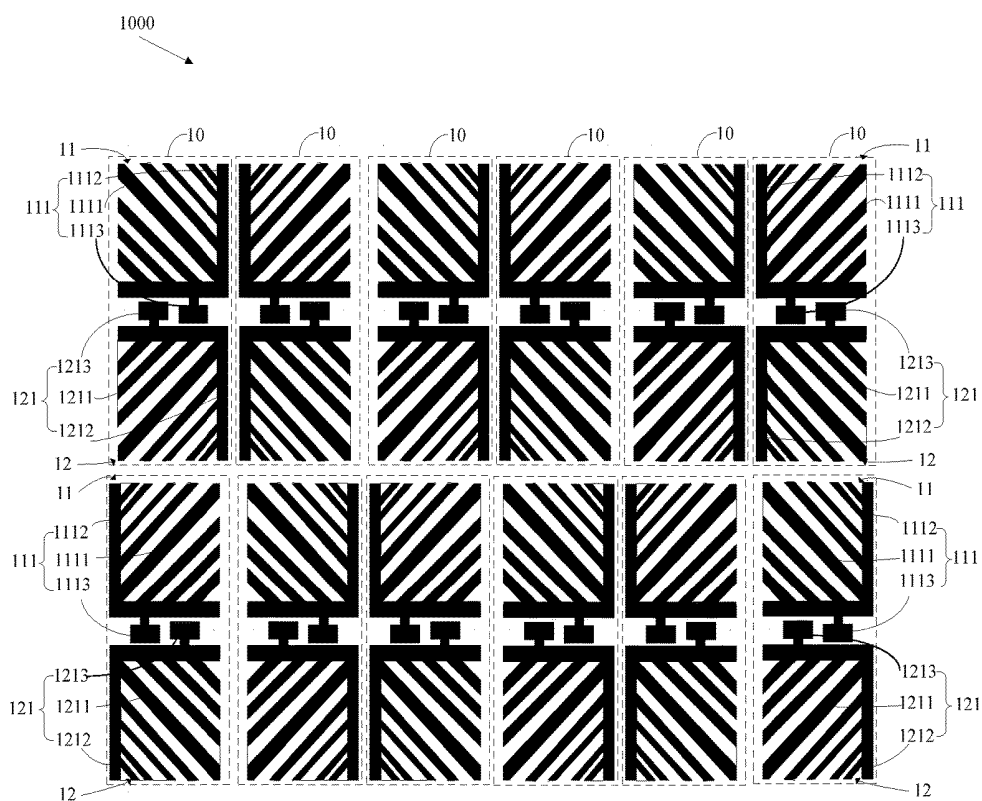


FIG. 9

LIQUID CRYSTAL DISPLAY DEVICE

FIELD OF INVENTION

[0001] The present disclosure relates to the field of display technology, and more particularly to a liquid crystal display device.

BACKGROUND

[0002] Liquid crystal displays (LCDs) are light weight and have a thin volume among various types of flat panel displays, therefore LCDs have gradually become a mainstream flat panel display. Nevertheless, compared to cathode ray tube (CRT) displays, LCDs have narrower viewing angles, making LCDs limited in application of fields requiring wide viewing angles, such as aviation and medical fields.

[0003] For this reason, multi-domain vertical alignment (MVA) technique and in plane switching (IPS) technique are employed to realize wide viewing angles. As shown in FIG. 1, for an LCD where MVA technique is employed, each pixel 1 includes a connection end 2 used for connecting to a drain electrode of thin-film transistor (TFT). Each pixel 1 further includes a striped vertical bar 3, a striped horizontal bar 4, and a plurality of striped branches 5 angled with respect to the horizontal bar 4. The vertical bar 3 and the horizontal bar 4 intersect with each other at midpoints thereof, such that the vertical bar 3 is perpendicular to the horizontal bar 4, and the pixel is divided into four regions. Each region includes plural striped branches 5 angled with respect to the horizontal bar 4. In the LCD as shown in FIG. 1, however, there will be swirling liquid crystal vortex areas generated at edges of the vertical bar 3 and the horizontal bar 4, resulting in poor display quality of LCD.

SUMMARY OF DISCLOSURE

[0004] The present disclosure is aimed to provide a liquid crystal display device, where viewing angle is widened.

[0005] In one aspect, the present disclosure provides a liquid crystal display device comprising a plurality of pixels arranged in an array, each of the pixels comprising:

[0006] a first pixel region, wherein the first pixel region includes a first pixel electrode, the first pixel electrode includes a plurality of first branch electrodes and a first connection portion connected to one end of each of the first branch electrodes, and the other end of each of the first branch electrodes extends outwardly in a first predetermined angle along a first predetermined direction; and

[0007] a second pixel region, wherein the second pixel region is aligned vertically with the first pixel region, the second pixel region includes a second pixel electrode, the second pixel electrode includes a plurality of second branch electrodes and a second connection portion connected to one end of each of the second branch electrodes, and the other end of each of the second branch electrodes extends outwardly in a second predetermined angle along a second predetermined direction;

[0008] wherein any two adjacent pixels in the same row have structures being mirror-symmetric with respect to each other; and for any two adjacent rows of pixels, a structure of the m^{th} pixel in the $(N+1)^{th}$ row and a structure of the $(m+1)^{th}$ pixel in the N^{th} row are the same, where N and M are natural numbers;

[0009] wherein the first connection portion is L-shaped, and the second connection portion is L-shaped; and

[0010] wherein the first pixel region is disposed above the second pixel region, or the first pixel region is disposed below the second pixel region.

[0011] In some embodiments, the first connection portion is disposed at an upper right corner of the first pixel region, and the second connection portion is disposed at a lower left corner of the second pixel region.

[0012] In some embodiments, the first connection portion is disposed at an upper right corner of the first pixel region, and the second connection portion is disposed at a lower right corner of the second pixel region.

[0013] In some embodiments, the first connection portion is disposed at a lower right corner of the first pixel region, and the second connection portion is disposed at an upper left corner of the second pixel region.

[0014] In some embodiments, the first connection portion is disposed at a lower right corner of the first pixel region, and the second connection portion is disposed at an upper right corner of the second pixel region.

[0015] In some embodiments, the first pixel electrode further includes a first connection end, and the second pixel electrode further includes a second connection end; the first pixel region further includes a first thin-film transistor, and the second pixel region further includes a second thin-film transistor; and the first pixel electrode is connected to a drain electrode of the first thin-film transistor via the first connection end, and the second pixel electrode is connected to a drain electrode of the second thin-film transistor via the second connection end.

[0016] In some embodiments, the first connection end is connected to the first connection portion, or the first connection end is connected to the first branch electrodes; and the second connection end is connected to the second connection portion, or the second connection end is connected to the second branch electrodes.

[0017] In some embodiments, an area ratio of the first pixel region to the second pixel region is ranged from 0.2 to 5.

[0018] In another aspect, the present disclosure provides a liquid crystal display device comprising a plurality of pixels arranged in an array, each of the pixels comprising:

[0019] a first pixel region, wherein the first pixel region includes a first pixel electrode, the first pixel electrode includes a plurality of first branch electrodes and a first connection portion connected to one end of each of the first branch electrodes, and the other end of each of the first branch electrodes extends outwardly in a first predetermined angle along a first predetermined direction; and

[0020] a second pixel region, wherein the second pixel region is aligned vertically with the first pixel region, the second pixel region includes a second pixel electrode, the second pixel electrode includes a plurality of second branch electrodes and a second connection portion connected to one end of each of the second branch electrodes, and the other end of each of the second branch electrodes extends outwardly in a second predetermined angle along a second predetermined direction;

[0021] wherein any two adjacent pixels in the same row have structures being mirror-symmetric with respect to each other; and for any two adjacent rows of pixels, a structure of the m^{th} pixel in the $(N+1)^{th}$ row and a structure of the $(m+1)^{th}$ pixel in the N^{th} row are the same, where N and M are natural numbers.

[0022] In some embodiments, the first connection portion is L-shaped, and the second connection portion is L-shaped.

[0023] In some embodiments, the first connection portion is disposed at an upper right corner of the first pixel region, and the second connection portion is disposed at a lower left corner of the second pixel region.

[0024] In some embodiments, the first connection portion is disposed at an upper right corner of the first pixel region, and the second connection portion is disposed at a lower right corner of the second pixel region.

[0025] In some embodiments, the first connection portion is disposed at a lower right corner of the first pixel region, and the second connection portion is disposed at an upper left corner of the second pixel region.

[0026] In some embodiments, the first connection portion is disposed at a lower right corner of the first pixel region, and the second connection portion is disposed at an upper right corner of the second pixel region.

[0027] In some embodiments, the first pixel electrode further includes a first connection end, and the second pixel electrode further includes a second connection end; the first pixel region further includes a first thin-film transistor, and the second pixel region further includes a second thin-film transistor; and the first pixel electrode is connected to a drain electrode of the first thin-film transistor via the first connection end, and the second pixel electrode is connected to a drain electrode of the second thin-film transistor via the second connection end.

[0028] In some embodiments, the first connection end is connected to the first connection portion, or the first connection end is connected to the first branch electrodes; and the second connection end is connected to the second connection portion, or the second connection end is connected to the second branch electrodes.

[0029] In some embodiments, an area ratio of the first pixel region to the second pixel region is ranged from 0.2 to 5.

[0030] In some embodiments, the first pixel region is disposed above the second pixel region, or the first pixel region is disposed below the second pixel region.

[0031] Compared to the conventional liquid crystal display device, the liquid crystal display device of the present disclosure is characterized by dividing each pixel to include a first pixel region and a second pixel region, in which viewing angle of display device is widened by coordination of the two pixel regions. Since any two adjacent pixels in the same row have structures being mirror-symmetric with respect to each other, and for any two adjacent rows of pixels, a structure of the m^{th} pixel in the $(N+1)^{\text{th}}$ row and a structure of the $(m+1)^{\text{th}}$ pixel in the N^{th} row are the same, where N and M are natural numbers, viewing angle of display device is widened.

[0032] To explain in detail the above summarized description of the subject invention, preferred embodiments are provided in conjunction with the drawings below.

BRIEF DESCRIPTION OF DRAWINGS

[0033] FIG. 1 is a schematic diagram showing a structure of a pixel according to the prior art.

[0034] FIG. 2 is a schematic diagram showing a first structure of a pixel according to one embodiment of the present disclosure.

[0035] FIG. 3 is a schematic diagram showing a first structure of a liquid crystal display device according to one embodiment of the present disclosure.

[0036] FIG. 4 is a schematic diagram showing a second structure of a pixel according to one embodiment of the present disclosure.

[0037] FIG. 5 is a schematic diagram showing a second structure of a liquid crystal display device according to one embodiment of the present disclosure.

[0038] FIG. 6 is a schematic diagram showing a third structure of a pixel according to one embodiment of the present disclosure.

[0039] FIG. 7 is a schematic diagram showing a third structure of a liquid crystal display device according to one embodiment of the present disclosure.

[0040] FIG. 8 is a schematic diagram showing a fourth structure of a pixel according to one embodiment of the present disclosure.

[0041] FIG. 9 is a schematic diagram showing a fourth structure of a liquid crystal display device according to one embodiment of the present disclosure.

DETAILED DESCRIPTION

[0042] The following embodiments refer to the accompanying drawings for exemplifying specific implementable embodiments of the present disclosure. Moreover, directional terms described by the present disclosure, such as upper, lower, front, back, left, right, inner, outer, side, etc., are only directions by referring to the accompanying drawings, and thus the used directional terms are used to describe and understand the present disclosure, but the present disclosure is not limited thereto.

[0043] In the drawings, the same reference symbol represents the same or similar components.

[0044] Reference in the specification to “one embodiment” or “an embodiment” means that a particular feature, structure, or characteristic described in conjunction with the embodiment can be included in at least one embodiment of the invention. The appearances of the phrase “in one embodiment” in various places in the specification do not necessarily all refer to the same embodiment, nor an independent or unique embodiment that is incompatible with other embodiments. It should be appreciated by any person having ordinary skill in the art that any embodiment referred in the specification can combine with other embodiment in the specification.

[0045] The present disclosure provides a liquid crystal display device. Please refer to FIG. 3, which is a schematic diagram showing a structure of a liquid crystal display device according to one embodiment of the present disclosure. The liquid crystal display device 1000 includes a plurality of pixels 10 arranged in an array. The pixels 10 can be red pixels, blue pixels, or green pixels that are arranged sequentially and repeatedly.

[0046] Each of the pixels 10 includes a first pixel region 11 and a second pixel region 12, wherein the second pixel region is aligned vertically with the first pixel region. The first pixel region 11 could function as a primary pixel region, and the second pixel region 12 could function as a secondary pixel region which assists the first pixel region 11 in improving viewing angles and color shift of LCD device. An area ratio of the first pixel region 11 to the second pixel region 12 is ranged from 0.2 to 5.

[0047] In some embodiments, as shown in FIG. 2, the first pixel region 11 is disposed above the second pixel region 12. In other embodiments, the first pixel region 11 is disposed below the second pixel region 12.

[0048] The first pixel region 11 includes a first pixel electrode 111 and a first thin-film transistor (TFT). Specifically, the first pixel electrode 111 includes a plurality of first branch electrodes 1111 and a first connection portion 1112. Likewise, the second pixel region 12 includes a second pixel electrode 121 and a second thin-film transistor (TFT). Specifically, the second pixel electrode 121 includes a plurality of second branch electrodes 1211 and a second connection portion 1212.

[0049] Each of the first branch electrodes 1111 includes two ends, where one end is connected to the first connection portion 1112, and the other end extends outwardly in a first predetermined angle along a first predetermined direction. As shown in FIG. 2, the upper right end of each of the first branch electrodes 1111 is connected to the first connection portion 1112, and the lower left end of each of the first branch electrodes 1111 extends outwardly in a first predetermined angle along a first predetermined direction. Likewise, each of the second branch electrodes 1211 also includes two ends, where one end is connected to the second connection portion 1212, and the other end extends outwardly in a second predetermined angle along a second predetermined direction. As shown in FIG. 2, the lower left end of each of the second branch electrodes 1211 is connected to the second connection portion 1212, and the upper right end of each of the second branch electrodes 1211 extends outwardly in a second predetermined angle along a second predetermined direction. The first predetermined direction and the second predetermined direction could be a direction that is horizontal or vertical. The first predetermined angle and the second predetermined angle could be 30, 40, or 45 degrees, but are not limited thereto. Preferably, the second predetermined angle could be the same as or different from the first predetermined angle. The second predetermined direction could be identical to the first predetermined direction.

[0050] The first pixel electrode further includes a first connection end, and the second pixel electrode further includes a second connection end. The first pixel electrode is connected to a drain electrode of the first thin-film transistor via the first connection end, and the second pixel electrode is connected to a drain electrode of the second thin-film transistor via the second connection end. Specifically, as shown in FIGS. 2-9, the first connection end 1113 and the second connection end 1213 are disposed between the first pixel region 11 and the second pixel region 12.

[0051] As shown in FIGS. 2-9, the first connection portion 1112 is L-shaped and is connected to the first branch electrodes 1111, and the second connection portion 1212 is L-shaped and is connected to the second branch electrodes 1211.

[0052] In some embodiments, as shown in FIG. 2, the first connection portion 1112 is disposed at the upper right corner of the first pixel region 11, i.e. being disposed at upper edge and right edge of the first pixel region 11, and is connected to the upper right end of each of the first branch electrode 1111. The second connection portion 1212 is disposed at the lower left corner of the second pixel region 12, i.e. being disposed at lower edge and left edge of the second pixel region 12, and is connected to the lower left end of each of

the second branch electrode 1211. Additionally, as shown in FIG. 2, the first connection end 1113 is directly connected to the first branch electrodes 1111, and the second connection end 1213 is directly connected to the second branch electrodes 1211.

[0053] A plurality of pixels having the structure as shown in FIG. 2 can be arranged to form the arrangement shown in FIG. 3 in the LCD device. As shown in FIG. 3, in the LCD device 1000, a plurality of pixels 10 are arranged in an array, such that any two adjacent pixels 10 in the same row have structures being mirror-symmetric with respect to each other. Moreover, for any two adjacent rows of pixels, a structure of the m^{th} pixel in the $(N+1)^{th}$ row and a structure of the $(m+1)^{th}$ pixel in the N^{th} row are the same, where N and M are natural numbers. With such an arrangement, not only compensation of viewing angle is accomplished but viewing angle of display device is widened.

[0054] In some embodiments, as shown in FIG. 4, the first connection portion 1112 is disposed at the upper right corner of the first pixel region 11, i.e. being disposed at upper edge and right edge of the first pixel region 11, and is connected to the upper right end of each of the first branch electrode 1111. The second connection portion 1212 is disposed at the lower right corner of the second pixel region 12, i.e. being disposed at lower edge and right edge of the second pixel region 12, and is connected to the lower right end of each of the second branch electrode 1211. Additionally, as shown in FIG. 4, the first connection end 1113 is directly connected to the first branch electrodes 1111, and the second connection end 1213 is directly connected to the second branch electrodes 1211.

[0055] A plurality of pixels having the structure as shown in FIG. 4 can be arranged to form the arrangement shown in FIG. 5 in the LCD device 1000. As shown in FIG. 5, in the LCD device 1000, a plurality of pixels 10 are arranged in an array, such that any two adjacent pixels 10 in the same row have structures being mirror-symmetric with respect to each other. Moreover, for any two adjacent rows of pixels, a structure of the m^{th} pixel in the $(N+1)^{th}$ row and a structure of the $(m+1)^{th}$ pixel in the N^{th} row are the same, where N and M are natural numbers. With such an arrangement, not only compensation of viewing angle is accomplished but viewing angle of display device is widened.

[0056] In some embodiments, as shown in FIG. 6, the first connection portion 1112 is disposed at the lower right corner of the first pixel region 11, i.e. being disposed at lower edge and right edge of the first pixel region 11, and is connected to the lower right end of each of the first branch electrode 1111. The second connection portion 1212 is disposed at the upper left corner of the second pixel region 12, i.e. being disposed at upper edge and left edge of the second pixel region 12, and is connected to the upper left end of each of the second branch electrode 1211. Additionally, as shown in FIG. 6, the first connection end 1113 is connected to the first connection portion 1112, and the second connection end 1213 is connected to the second connection portion 1212.

[0057] A plurality of pixels having the structure as shown in FIG. 6 can be arranged to form the arrangement shown in FIG. 7 in the LCD device 1000. As shown in FIG. 7, in the LCD device 1000, a plurality of pixels 10 are arranged in an array, such that any two adjacent pixels 10 in the same row have structures being mirror-symmetric with respect to each other. Moreover, for any two adjacent rows of pixels, a structure of the m^{th} pixel in the $(N+1)^{th}$ row and a structure

of the $(m+1)^{th}$ pixel in the N^{th} row are the same, where N and M are natural numbers. With such an arrangement, not only compensation of viewing angle is accomplished but viewing angle of display device is widened.

[0058] In some embodiments, as shown in FIG. 8, the first connection portion 1112 is disposed at the lower right corner of the first pixel region 11, i.e. being disposed at lower edge and right edge of the first pixel region 11, and is connected to the lower right end of each of the first branch electrode 1111. The second connection portion 1212 is disposed at the upper right corner of the second pixel region 12, i.e. being disposed at upper edge and right edge of the second pixel region 12, and is connected to the upper right end of each of the second branch electrode 1211. Additionally, as shown in FIG. 8, the first connection end 1113 is connected to the first connection portion 1112, and the second connection end 1213 is connected to the second connection portion 1212.

[0059] A plurality of pixels having the structure as shown in FIG. 8 can be arranged to form the arrangement shown in FIG. 9 in the LCD device 1000. As shown in FIG. 9, in the LCD device 1000, a plurality of pixels 10 are arranged in an array, such that any two adjacent pixels 10 in the same row have structures being mirror-symmetric with respect to each other. Moreover, for any two adjacent rows of pixels, a structure of the m^{th} pixel in the $(N+1)^{th}$ row and a structure of the $(m+1)^{th}$ pixel in the N^{th} row are the same, where N and M are natural numbers. With such an arrangement, not only compensation of viewing angle is accomplished but viewing angle of display device is widened.

[0060] According to the liquid crystal display device of the present disclosure, each pixel is divided to include a first pixel region and a second pixel region, in which viewing angle of display device is widened by coordination of the two pixel regions. Since any two adjacent pixels in the same row have structures being mirror-symmetric with respect to each other, and for any two adjacent rows of pixels, a structure of the m^{th} pixel in the $(N+1)^{th}$ row and a structure of the $(m+1)^{th}$ pixel in the N^{th} row are the same, where N and M are natural numbers, viewing angle of display device is widened.

[0061] While the present disclosure has been described with the aforementioned preferred embodiments, it is preferable that the above embodiments should not be construed as limiting of the present disclosure. Anyone having ordinary skill in the art can make a variety of modifications and variations without departing from the spirit and scope of the present disclosure as defined by the following claims.

1. A liquid crystal display device comprising a plurality of pixels arranged in an array, each of the pixels comprising:

a first pixel region, wherein the first pixel region includes a first pixel electrode, the first pixel electrode includes a plurality of first branch electrodes and a first connection portion connected to one end of each of the first branch electrodes, and the other end of each of the first branch electrodes extends outwardly in a first predetermined angle along a first predetermined direction; and

a second pixel region, wherein the second pixel region is aligned vertically with the first pixel region, the second pixel region includes a second pixel electrode, the second pixel electrode includes a plurality of second branch electrodes and a second connection portion connected to one end of each of the second branch electrodes, and the other end of each of the second

branch electrodes extends outwardly in a second predetermined angle along a second predetermined direction;

wherein any two adjacent pixels in the same row have structures being minor-symmetric with respect to each other; and for any two adjacent rows of pixels, a structure of the m^{th} pixel in the $(N+1)^{th}$ row and a structure of the $(m+1)^{th}$ pixel in the N^{th} row are the same, where N and M are natural numbers;

wherein the first connection portion is L-shaped, and the second connection portion is L-shaped;

wherein the first pixel region is disposed above the second pixel region, or the first pixel region is disposed below the second pixel region; and

wherein the first pixel electrode and the second pixel electrode are separated from each other.

2. The liquid crystal display device according to claim 1, wherein the first connection portion is disposed at an upper right corner of the first pixel region, and the second connection portion is disposed at a lower left corner of the second pixel region.

3. The liquid crystal display device according to claim 1, wherein the first connection portion is disposed at an upper right corner of the first pixel region, and the second connection portion is disposed at a lower right corner of the second pixel region.

4. The liquid crystal display device according to claim 1, wherein the first connection portion is disposed at a lower right corner of the first pixel region, and the second connection portion is disposed at an upper left corner of the second pixel region.

5. The liquid crystal display device according to claim 1, wherein the first connection portion is disposed at a lower right corner of the first pixel region, and the second connection portion is disposed at an upper right corner of the second pixel region.

6. The liquid crystal display device according to claim 1, wherein

the first pixel electrode further includes a first connection end, and the second pixel electrode further includes a second connection end;

the first pixel region further includes a first thin-film transistor, and the second pixel region further includes a second thin-film transistor; and

the first pixel electrode is connected to a drain electrode of the first thin-film transistor via the first connection end, and the second pixel electrode is connected to a drain electrode of the second thin-film transistor via the second connection end.

7. The liquid crystal display device according to claim 6, wherein

the first connection end is connected to the first branch electrodes; and

the second connection end is connected to the second branch electrodes.

8. The liquid crystal display device according to claim 1, wherein an area ratio of the first pixel region to the second pixel region is ranged from 0.2 to 5.

9. A liquid crystal display device comprising a plurality of pixels arranged in an array, each of the pixels comprising:

a first pixel region, wherein the first pixel region includes a first pixel electrode, the first pixel electrode includes a plurality of first branch electrodes and a first connection portion connected to one end of each of the first

branch electrodes, and the other end of each of the first branch electrodes extends outwardly in a first predetermined angle along a first predetermined direction; and

a second pixel region, wherein the second pixel region is aligned vertically with the first pixel region, the second pixel region includes a second pixel electrode, the second pixel electrode includes a plurality of second branch electrodes and a second connection portion connected to one end of each of the second branch electrodes, and the other end of each of the second branch electrodes extends outwardly in a second predetermined angle along a second predetermined direction;

wherein any two adjacent pixels in the same row have structures being minor-symmetric with respect to each other; and for any two adjacent rows of pixels, a structure of the m^{th} pixel in the $(N+1)^{th}$ row and a structure of the $(m+1)^{th}$ pixel in the N^{th} row are the same, where N and M are natural numbers; and

wherein the first pixel electrode and the second pixel electrode are separated from each other.

10. The liquid crystal display device according to claim **9**, wherein the first connection portion is L-shaped, and the second connection portion is L-shaped.

11. The liquid crystal display device according to claim **10**, wherein the first connection portion is disposed at an upper right corner of the first pixel region, and the second connection portion is disposed at a lower left corner of the second pixel region.

12. The liquid crystal display device according to claim **10**, wherein the first connection portion is disposed at an upper right corner of the first pixel region, and the second connection portion is disposed at a lower right corner of the second pixel region.

13. The liquid crystal display device according to claim **10**, wherein the first connection portion is disposed at a

lower right corner of the first pixel region, and the second connection portion is disposed at an upper left corner of the second pixel region.

14. The liquid crystal display device according to claim **10**, wherein the first connection portion is disposed at a lower right corner of the first pixel region, and the second connection portion is disposed at an upper right corner of the second pixel region.

15. The liquid crystal display device according to claim **9**, wherein

the first pixel electrode further includes a first connection end, and the second pixel electrode further includes a second connection end;

the first pixel region further includes a first thin-film transistor, and the second pixel region further includes a second thin-film transistor; and

the first pixel electrode is connected to a drain electrode of the first thin-film transistor via the first connection end, and the second pixel electrode is connected to a drain electrode of the second thin-film transistor via the second connection end.

16. The liquid crystal display device according to claim **15**, wherein

the first connection end is connected to the first branch electrodes; and

the second connection end is connected to the second branch electrodes.

17. The liquid crystal display device according to claim **9**, wherein an area ratio of the first pixel region to the second pixel region is ranged from 0.2 to 5.

18. The liquid crystal display device according to claim **9**, wherein the first pixel region is disposed above the second pixel region, or the first pixel region is disposed below the second pixel region.

* * * * *

专利名称(译)	液晶显示装置		
公开(公告)号	US20190204689A1	公开(公告)日	2019-07-04
申请号	US15/746429	申请日	2018-01-12
[标]申请(专利权)人(译)	深圳市华星光电技术有限公司		
[标]发明人	LI WENYING		
发明人	LI, WENYING		
IPC分类号	G02F1/1343 G02F1/1368 H01L27/12		
CPC分类号	G02F1/134309 G02F1/1368 H01L27/124 G02F2201/123 G02F2001/134345		
优先权	201711476040.3 2017-12-29 CN		
其他公开文献	US10571754		
外部链接	Espacenet USPTO		

摘要(译)

本公开提供了一种液晶显示装置，包括以阵列布置的多个像素，每个像素包括：第一像素区域和与第一像素区域垂直对齐的第二像素区域。同一行中的任何两个相邻像素具有相对于彼此镜像对称的结构。并且，对于任何两个相邻的像素行， $(N+1)$ 行中的 m 个像素的结构和 $(m+1)$ 的结构 N 行中的 th 像素是相同的，其中 N 和 M 是自然数。

