

FIG. 1

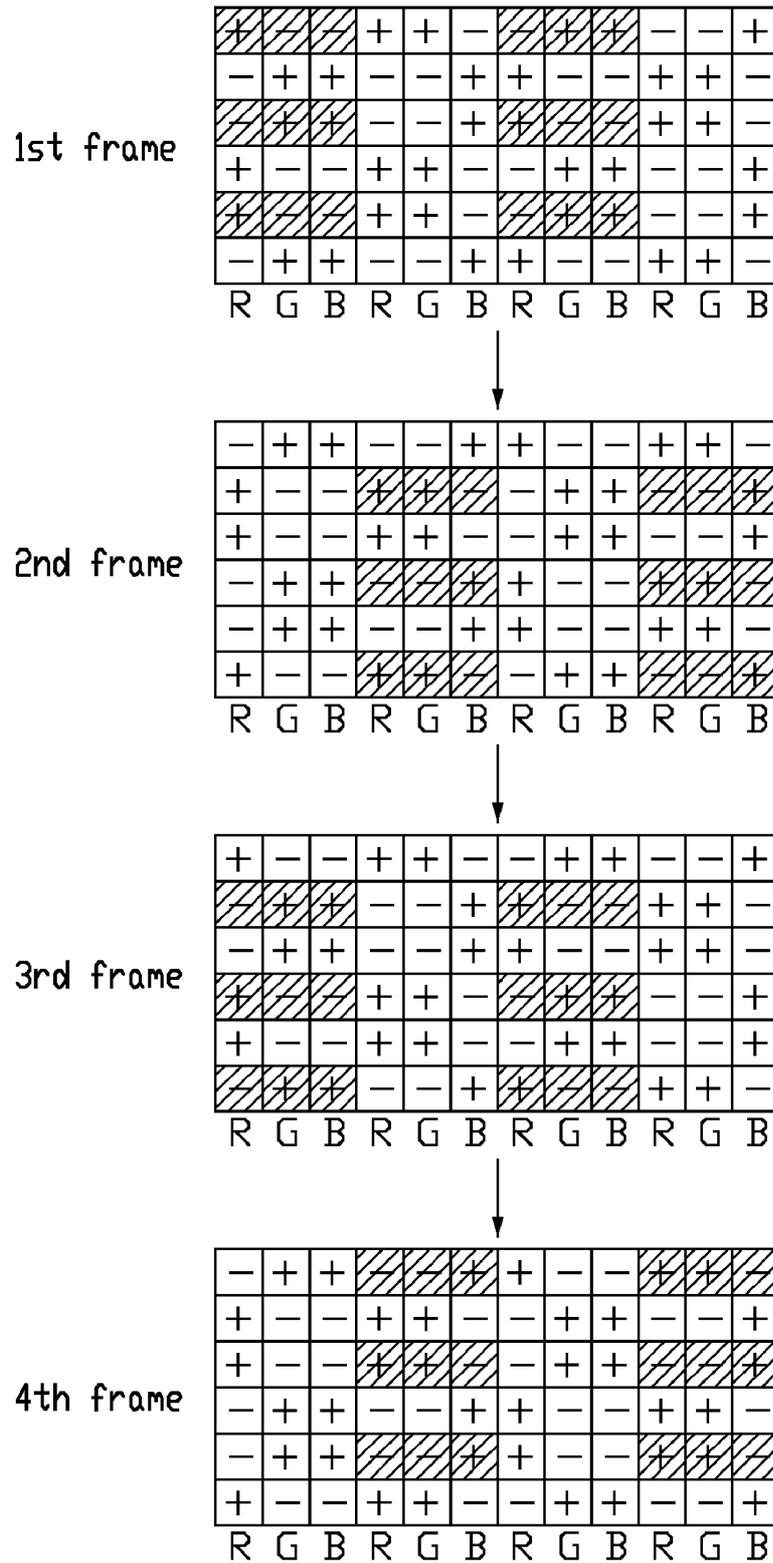


FIG. 2

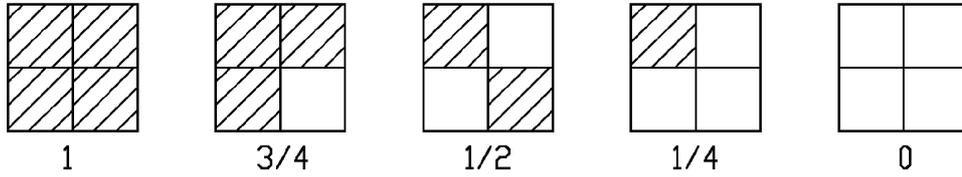


FIG. 3  
(RELATED ART)

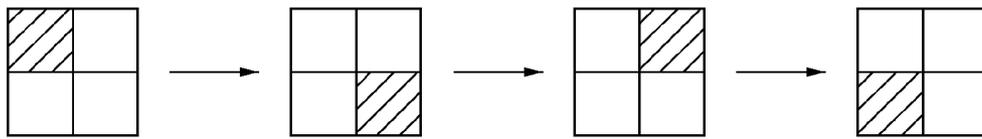


FIG. 4  
(RELATED ART)

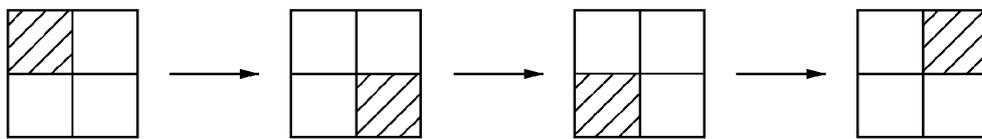


FIG. 5  
(RELATED ART)

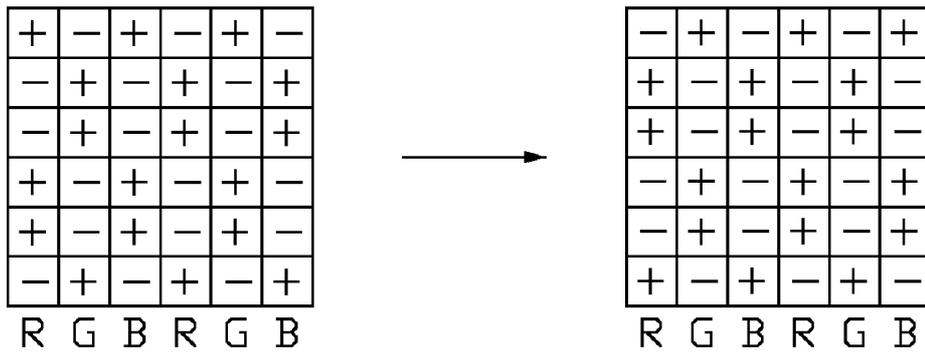


FIG. 6  
(RELATED ART)

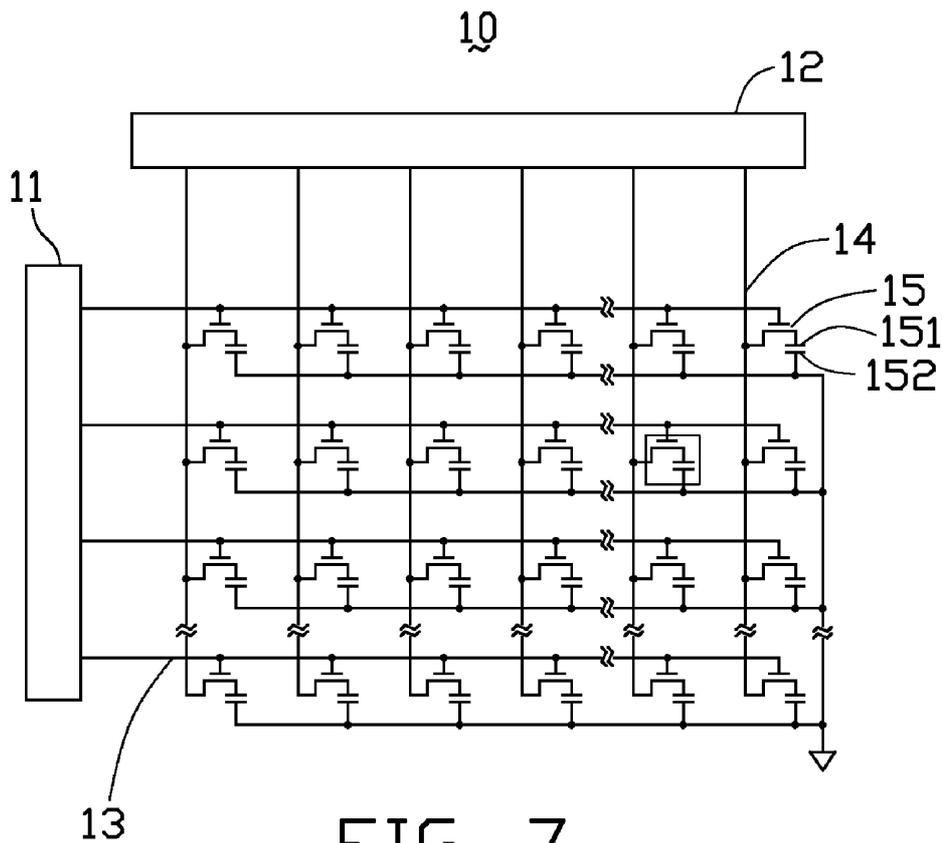


FIG. 7  
(RELATED ART)

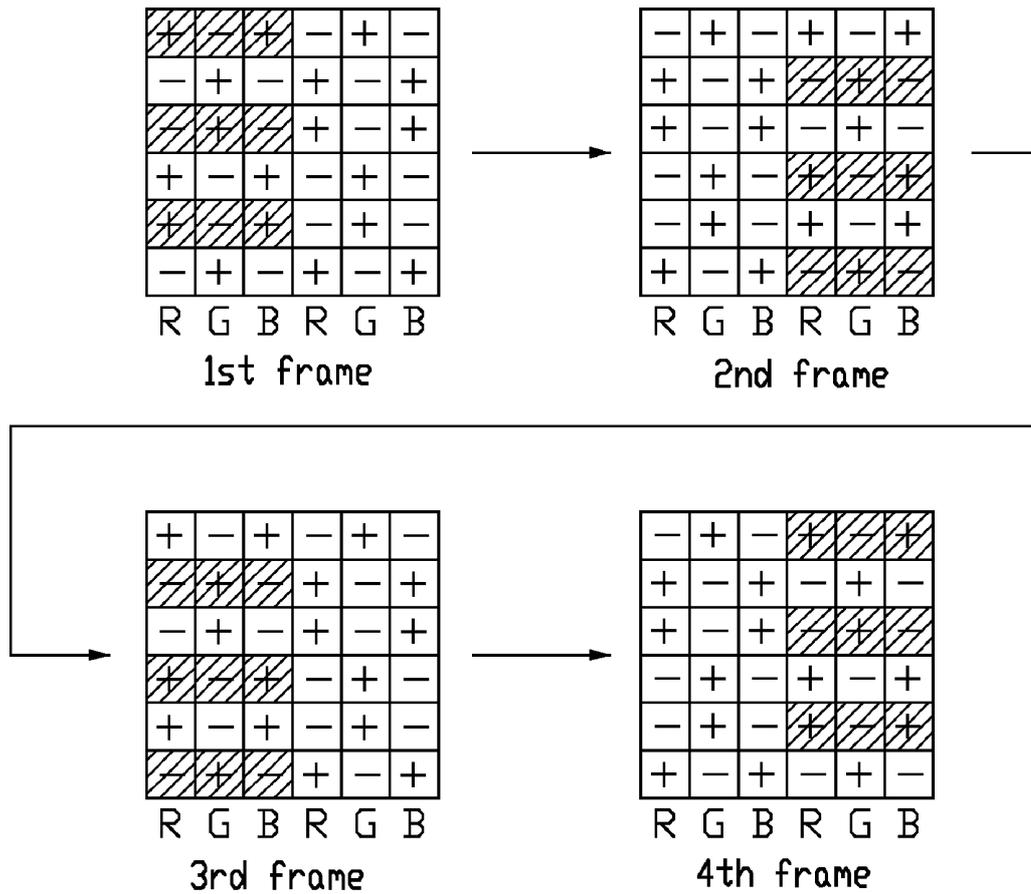


FIG. 8  
(RELATED ART)

**METHOD FOR DRIVING LIQUID CRYSTAL PANEL WITH CANCELING OUT OF OPPOSITE POLARITIES OF COLOR SUB-PIXEL UNITS**

CROSS-REFERENCE TO RELATED APPLICATION

[0001] This application is a continuation application of U.S. patent application Ser. No. 12/011,666, filed Jan. 28, 2008 and entitled "METHOD FOR DRIVING LIQUID CRYSTAL PANEL WITH CANCELING OUT OF OPPOSITE POLARITIES OF COLOR SUB-PIXEL UNITS." The disclosure of such parent application is incorporated herein by reference.

BACKGROUND

[0002] 1. Technical Field

[0003] The present invention relates to a method for driving a liquid crystal panel, wherein both an inversion driving method and a dynamic dithering method are employed simultaneously.

[0004] 2. Description of Related Art

[0005] A liquid crystal display (LCD) has the advantages of portability, low power consumption, and low radiation, and has been widely used in various portable information products such as notebooks, personal digital assistants (PDAs), video cameras and the like. Furthermore, the LCD is considered by many to have the potential to completely replace CRT (cathode ray tube) monitors and televisions. An LCD usually includes a liquid crystal panel configured for color image display. With the ongoing growth of the LCD industry, consumers' requirements for good display performance of color images are becoming more demanding.

[0006] Generally, the display performance of a color image is measured and determined by an amount of bits ("bit amount") in every single color channel of a liquid crystal panel. For example, if a liquid crystal panel has 6 bits for a single color channel, the liquid crystal panel can display a 2<sup>6</sup> (64) gray scale color image in the single color channel. Such liquid crystal panel is called a 6-bit panel. A typical liquid crystal panel includes a red channel, a green channel, and a blue channel, therefore a total amount of 262,144 (=64×64×64) different colors can be displayed by the liquid crystal panel. An 8-bit panel can display 256 gray scales in each single color channel, and therefore can display 16,777,216 (about 16.7 million) different colors. That is, theoretically, the total amount of different colors that a 6-bit panel can display is less than 2% of the total amount of different colors that an 8-bit panel can display. Thus the 8-bit panel should have better display performance of color images than a 6-bit panel, because the 8-bit panel can display more colors. In particular, the 8-bit panel can provide brighter and more vivid images for viewers.

[0007] The bit amount of a liquid crystal panel is related to driving channels of a driving circuit employed in the liquid crystal panel. A driving circuit of a 6-bit panel has only 64 driving channels. On the one hand, the 6-bit panel may not display very high quality color images. On the other hand, the 6-bit panel can be easy to configure and cost-effective. From a structural point of view of liquid crystal panels, the 6-bit panel has 64 different gray scales between pure black and pure white. Therefore the display of the 6-bit panel can be

more easily controlled. In particular, the 6-bit panel has a relatively fast response time, for example between 6 milliseconds and 12 milliseconds.

[0008] Typically, color enhancement technology is employed in a 6-bit panel in order that the 6-bit panel can display more colors. The color enhancement technology usually involves a pixel dithering method or a frame rate control method. By adopting such color enhancement technology, the 6-bit panel can display as many as 16.2 million colors.

[0009] Referring to FIG. 3, this illustrates a simple principle of the pixel dithering method. Each small square represents a pixel of a typical 6-bit panel. Four pixels in the form of 2×2 matrix constitute a dithering unit. In order to simplify the following explanation of the dithering method, it is assumed that each of the four pixels can only display a white color or a black color. In FIG. 3, a white color is shown as a blank area, and a black color is shown as a hatched area. A gray scale of the white color is defined as "0", a gray scale of the black color is defined as "1", and a pattern corresponding to the four pixels' gray scales is defined as a dithering pattern. As shown in FIG. 3, the dithering units from left to right sequentially have a gray scale of "1", "3/4", "1/2", "1/4", and "0". Therefore four pixels cooperatively can display 5 different gray scales, with each pixel displaying only white or black. In other examples, the dithering unit can have more than four pixels. The more pixels the dithering unit has, the more dithering patterns the dithering unit has, such that more gray scales can be displayed.

[0010] However, a liquid crystal panel using the above-described dithering method is prone to display images with low uniformity. To mitigate this problem, a dynamic pixel dithering method can be employed. Referring to FIG. 4, this illustrates a simple principle of the dynamic pixel dithering method. In one dithering unit, three pixels are white and one pixel is black. Four different dithering patterns are provided, each corresponding to a gray scale of 1/4. The four dithering patterns are switched from one to the other swiftly. Thus the four dithering patterns are essentially equivalent to one image with a gray scale of 1/4 as seen by a viewer, because of a so-called visual staying phenomenon.

[0011] The 8-bit panel can provide 256 different gray scales of 0-255, and the 6-bit panel can only provide 64 different gray scales of 0-63. Therefore, a simulating method is adopted to help increase the amount of gray scales of the 6-bit panel. Two bits of "0" are added to the end of the 6 bit binary number of each gray scale to generate an 8 bit binary number. Thereby, the 6-bit panel can provide gray scales of 0, 4, 8, 12, . . . 248, 252, which are defined as valid gray scales.

6 bit	8 bit
63 = 111111	11111100 = 252
62 = 111110	11111000 = 248
...	...
1 = 000001	00000100 = 4
0 = 000000	00000000 = 0

[0012] The gray scales among the valid gray scales can be achieved by adopting the dynamic dithering method. Referring to FIG. 5, each small square represents a pixel, and four pixels in the form of a 2×2 matrix constitute a dithering unit. Each dithering unit shows a particular dithering pattern. The four dithering patterns are switched from one to the other

rapidly in order to be perceived by a viewer as an image of a desired gray scale. For example, if a gray scale of 249 is desired, three pixels of the dithering unit each display a gray scale of 248 (shown as blank areas in FIG. 5), and the other one pixel of the dithering unit displays a gray scale of 252 (shown with hatching in FIG. 5). Therefore, the four dithering patterns cooperatively display a gray scale of 249. Other gray scales can be displayed according to the same principles. Thus, the 6-bit panel can provide 253 different gray scales from 0 to 252.

**[0013]** In general, liquid crystal molecules are utilized to control light transmission at each of pixel regions of the liquid crystal panel. A typical pixel region includes a red sub-pixel region, a green sub-pixel region, and a blue sub-pixel region. The liquid crystal molecules are driven according to external video signals received by the liquid crystal panel. A conventional liquid crystal panel generally employs a selected one of a frame inversion system, a line inversion system, or a dot inversion system to drive the liquid crystal molecules. Each of these driving systems can protect the liquid crystal molecules from decay or damage.

**[0014]** In the following description, unless the context indicates otherwise, a reference to a "sub-pixel" is a reference to a sub-pixel region.

**[0015]** A typical method employed in the dot inversion system is known as a 2-line inversion driving method. That is, a polarity of each sub-pixel in a first row of the sub-pixels is the same as that of an adjacent sub-pixel in a second row of the sub-pixels. A polarity of each sub-pixel in a third row of the sub-pixels is the same as that of an adjacent sub-pixel in a fourth row of the sub-pixels, and is opposite to that of an adjacent sub-pixel in the second row. Polarities of the sub-pixels in each column of the sub-pixels are opposite to the polarities of the sub-pixels in both adjacent columns. Moreover, the polarity of each sub-pixel is reversed once in every frame period.

**[0016]** By adopting the 2-line inversion driving method, the polarity of each sub-pixel in a current frame is opposite to that in the previous frame and opposite to that in the next frame. Thereby, liquid crystal molecules of the liquid crystal panel avoid decay or damage.

**[0017]** Referring to FIG. 6, another typical method employed in the dot inversion system is known as a 1-2 line inversion driving method. The 1-2 line inversion driving method is similar to the 2-line inversion method, except that a single row of sub-pixels is arranged as the first row. The sub-pixels of the first row have opposite polarities to the sub-pixels of the second row. From the second row down, the sub-pixels are driven according to the 2-line inversion driving method.

**[0018]** In general, a typical liquid crystal panel has a refresh rate of 60 frames per second. That is, 60 frames are displayed during a single second. Usually, the naked human eye cannot distinguish between two different frames if the refresh rate is over 30 frames per second.

**[0019]** Referring to FIG. 7, a typical liquid crystal panel **10** includes a first substrate (not shown), a second substrate (not shown) parallel to the first substrate, a liquid crystal layer (not shown) sandwiched between the first and second substrates, a scanning driving circuit **11**, and a data driving circuit **12**.

**[0020]** The first substrate includes a plurality of scanning lines **13** that are parallel to each other, a plurality of data lines **14** that are parallel to each other and perpendicularly intersecting with but insulated from the scanning lines **13**, a plu-

rality of pixel electrodes **151**, and a plurality of thin film transistors **15** each arranged in the vicinity of a respective point of intersection of the scanning lines **13** and the data lines **14**. The scanning driving circuit **11** is configured for providing a plurality of scanning signals to the scanning lines **13**. The data driving circuit **12** is configured for providing a plurality of gray scale voltages to the data lines **14**. A minimum area cooperatively defined by two adjacent scanning lines **13** and two adjacent data lines **14** is defined as a sub-pixel (not labeled). The second substrate includes a plurality of common electrodes **152** corresponding to the pixel electrodes **151** respectively. Each thin film transistor **15** includes a gate electrode (not labeled) connected to the corresponding scanning line **13**, a source electrode (not labeled) connected to the corresponding data line **14**, and a drain electrode (not labeled) connected to a corresponding pixel electrode **151**.

**[0021]** When a scanning signal is applied to the gate electrode of each thin film transistor **15** via the corresponding scanning line **13**, the thin film transistor **15** is activated. A gray scale voltage is applied to the corresponding pixel electrode **151** via the corresponding source electrode and drain electrode. The corresponding common electrode **152** has a common voltage applied thereto. Therefore, an electrical field is generated between the pixel electrode **151** and the common electrode **152**. The liquid crystal molecules in the electrical field are twisted such that light rays are allowed to pass through. When the gray scale voltage is greater than the common voltage, a directionality of the electrical field is from the pixel electrode **151** to the common electrode **152**, and the sub-pixel is defined as having a positive polarity. Conversely, when the gray scale voltage is less than the common voltage of the common electrode **152**, the directionality of the electrical field is from the common electrode **152** to the pixel electrode **151**, and the sub-pixel is defined as having a negative polarity. Moreover, when absolute values of the gray scale voltages applied to the pixel electrodes **151** of two sub-pixels are the same, with the gray scale voltages only differing in polarity, the gray scales of the two sub-pixels are assumed to be the same.

**[0022]** Because the common electrodes **152** have different inherent resistances, the actual common voltages applied to the common electrodes **152** are slightly different from each other, and are either more or less than an ideal common voltage. Due to the differences between the actual common voltages and the ideal common voltage, strip lines may occur in an image displayed by the liquid crystal panel **10**.

**[0023]** Referring to FIG. 8, each small square represents a sub-pixel of the liquid crystal panel **10**. The sub-pixels in each same row of the sub-pixels are arranged in a sequence of repeating red, green, and blue ("RGB") sub-pixel groups. Thus the sub-pixels of each column of the sub-pixels are capable of displaying a same color. In each row, each group of a red sub-pixel, an adjacent green sub-pixel, and an adjacent blue sub-pixel cooperatively constitutes a pixel region. In the following description, unless the context indicates otherwise, a reference to a "pixel" is a reference to a pixel region. Each group of four pixels adjacent to each other in the form of a 2×2 matrix constitutes a dithering unit. The liquid crystal panel **10** is a normal white mode panel. Therefore, the greater the gray scale voltage applied, the less the amount of light rays that can pass through the corresponding sub-pixel.

**[0024]** The liquid crystal panel **10** employs an inversion driving method and a dynamic dithering method simultaneously. The inversion driving method employed by the li-

uid crystal panel **10** is a 1-2 line inversion driving method. The dynamic dithering method employed by the liquid crystal panel **10** is detailed as follows:

**[0025]** In the first frame, the upper-left pixel of each dithering unit displays a first gray scale of M (shown with hatching in FIG. 8), and the upper-right pixel, the lower-left pixel, and the lower-right pixel of each dithering unit display a second gray scale of N (shown with a blank background in FIG. 8).

**[0026]** In the second frame, the lower-right pixel of each dithering unit displays the first gray scale of M, and the other pixels of each dithering unit display the second gray scale of N.

**[0027]** In the third frame, the lower-left pixel of each dithering unit displays the first gray scale of M, and the other pixels of each dithering unit display the second gray scale of N.

**[0028]** In the fourth frame, the upper-right pixel of each dithering unit displays the first gray scale of M, and the other pixels of each dithering unit display the second gray scale of N.

**[0029]** The liquid crystal panel **10** repeats displaying from the first frame through the fourth frame again and again. The actual image as viewed by the naked human eye has a dithering gray scale of  $(M+3N)/4$ . For example, if M is equal to 252 and N is equal to 248, then a gray scale of 249 is dithered.

**[0030]** The liquid crystal panel **10** is also driven by the 1-2 line inversion driving method. The following text shows summations of the polarities of the pixels that display the first gray scale of M, from the first frame through the fourth frame, for each row of the pixels:

the first row:  $(R^+ + G^- + B^+) + (R^+ + G^- + B^+) = 2R^+ + 2G^- + 2B^+$ ;  
 the second row:  $(R^- + G^+ + B^-) + (R^- + G^+ + B^-) = 2R^- + 2G^+ + 2B^-$ ;  
 the third row:  $(R^- + G^+ + B^-) + (R^+ + G^- + B^+) = 2R^- + 2G^+ + 2B^-$ ;  
 the fourth row:  $(R^+ + G^- + B^+) + (R^+ + G^- + B^+) = 2R^+ + 2G^- + 2B^+$ ;  
 the fifth row:  $(R^+ + G^- + B^+) + (R^- + G^+ + B^-) = 2R^+ + 2G^- + 2B^+$ ;  
 and  
 the sixth row:  $(R^- + G^+ + B^-) + (R^- + G^+ + B^-) = 2R^- + 2G^+ + 2B^-$ .

**[0031]** The sums of the polarities of the sub-pixels in the second row and in the third row are the same, and the sums of the polarities of the pixels in the fourth row and in the fifth row are the same. When the actual common voltage is lower than the ideal common voltage, the red and blue sub-pixels of the pixels of the second row each have a gray scale voltage less than a desired gray scale voltage, and the green sub-pixels of the pixels of the second row each have a gray scale voltage greater than a desired gray scale voltage. Therefore the second row exhibits purple color shift. The polarities of the sub-pixels of the first row are opposite to the polarities of the sub-pixels of the second row. Therefore the first row exhibits green color shift. However, to the naked human eye, the color shifts of the first and second rows counteract each other, and the displayed image appears normal. The polarities of the sub-pixels of the third row are the same as the polarities of the sub-pixels of the second row. Therefore the third row exhibits purple color shift, just like the second row. As a result, a purple strip line between the second row and the third row is visible to the naked human eye.

**[0032]** In summary, because the actual common voltages are slightly different from the ideal common voltage, images displayed by the liquid crystal panel **10** are liable to have visible strip lines. That is, the color image display performance of the liquid crystal panel **10** is impaired.

**[0033]** What is needed, therefore, is a method for driving a liquid crystal panel which can overcome the above-described deficiencies.

#### SUMMARY

**[0034]** In one preferred embodiment, a method for driving a display panel which includes plural dithering units formed by plural pixels arranged in a matrix form, includes: driving the sub-pixel units according to a sequence of frames to enable each of the sub-pixel units selectively having a first polarity or a second polarity reversed to the first polarity, the polarity of each sub-pixel unit being reversed at each frame; and providing a series of predetermined dithering patterns for the plurality of dithering units, the dithering patterns provided to the pixels of each dithering unit being cyclically changed. More particularly, in each dithering unit, the pixels have a first pattern having a first gray scale and a second pattern having a second gray scale smaller than the first gray scale, an average value of the gray scales of the pixels in each dithering unit defines the gray scale of the dithering unit, and in each cycle of dithering patterns, the first pattern are only provided once to each pixel. According to the portion pixels provided with the first pattern in each row of each cycle of the dithering patterns, the pixels satisfy one of the following conditions. One is the number of a same color of the sub-pixel units having the first polarity is equal to the number of the same color of the sub-pixel units having the second polarity. The other is summations of the polarities of a same color of the sub-pixel units cancel each other out.

**[0035]** Other aspects, novel features and advantages will become more apparent from the following detailed description when taken in conjunction with the accompanying drawings.

#### BRIEF DESCRIPTION OF THE DRAWINGS

**[0036]** The components in the drawings are not necessarily drawn to scale, the emphasis instead being placed upon clearly illustrating the principles of at least one embodiment of the present invention. In the drawings, like reference numerals designate corresponding parts throughout various views, and all the views are schematic.

**[0037]** FIG. 1 is essentially an abbreviated circuit diagram of a liquid crystal panel according to an exemplary embodiment of the present invention, the liquid crystal panel including a plurality of dithering units.

**[0038]** FIG. 2 illustrates a series of dithering patterns of six dithering units of the liquid crystal panel of FIG. 1, each of the dithering units comprised of four RGB pixels arranged in a 2x2 matrix, the six dithering units arranged in a matrix of 3 rows and 2 columns.

**[0039]** FIG. 3 is a schematic view of dithering units of a conventional liquid crystal panel, illustrating a principle of a pixel dithering method.

**[0040]** FIG. 4 is a schematic view of dithering units of a conventional liquid crystal panel, illustrating a principle of a dynamic pixel dithering method.

**[0041]** FIG. 5 is a schematic view of dithering units of a conventional liquid crystal panel, illustrating a principle of displaying a predetermined gray scale in a dithering unit.

**[0042]** FIG. 6 is a schematic view of an array of sub-pixels of a conventional liquid crystal panel, illustrating a principle of a 1-2 line inversion driving method.

[0043] FIG. 7 is essentially an abbreviated circuit diagram of a conventional liquid crystal panel.

[0044] FIG. 8 illustrates a series of dithering patterns of three dithering units of the liquid crystal panel of FIG. 7, each of the dithering units comprised of four RGB pixels arranged in a 2x2 matrix, the three dithering units arranged in a matrix of 3 rows and 1 column.

#### DETAILED DESCRIPTION

[0045] Reference will now be made to the drawings to describe preferred and exemplary embodiments of the present invention in detail.

[0046] Referring to FIG. 1, a liquid crystal panel 20 according to an exemplary embodiment of the present invention is shown. The liquid crystal panel 20 includes a first substrate (not shown), a second substrate (not shown) parallel to the first substrate, a liquid crystal layer (not shown) sandwiched between the first substrate and the second substrate, a scanning driving circuit 21, and a data driving circuit 22.

[0047] The first substrate includes a plurality of scanning lines 23 that are parallel to each other, a plurality of data lines 24 that are parallel to each other and perpendicularly intersecting with but insulated from the scanning lines 23, a plurality of pixel electrodes 251, and a plurality of thin film transistor 25 each arranged in the vicinity of a respective point of intersection of the scanning lines 23 and the data lines 24. The scanning driving circuit 21 is configured for providing a plurality of scanning signals to the scanning lines 23. The data driving circuit 22 is configured for providing a plurality of gray scale voltages to the data lines 24. A minimum area cooperatively defined by two adjacent scanning lines 23 and two adjacent data lines 24 is defined as a sub-pixel (not labeled). The second substrate includes a plurality of common electrodes 252 corresponding to the pixel electrodes 251 respectively. Each thin film transistor 25 includes a gate electrode (not labeled) connected to the corresponding scanning line 23, a source electrode (not labeled) connected to the corresponding data line 24, and a drain electrode (not labeled) connected to a corresponding pixel electrode 251.

[0048] When a scanning signal is applied to the gate electrode of each thin film transistor 25 via the corresponding scanning line 23, the thin film transistor 25 is activated. A gray scale voltage is applied to the corresponding pixel electrode 251 via the corresponding source electrode and drain electrode. The corresponding common electrode 252 has a common voltage applied thereto. Therefore, an electrical field is generated between the pixel electrode 251 and the common electrode 252. The liquid crystal molecules in the electrical field are twisted such that light rays are allowed to pass through.

[0049] A directionality of the electrical field needs to be varied periodically, in order that decay of or damage to the liquid crystal molecules can be avoided. The present embodiment provides a method for driving the liquid crystal panel 20 to accomplish such requirement, as follows. To simplify the explanation, the following definitions are used. When the gray scale voltage is greater than the common voltage of the common electrode 252, a directionality of the electrical field is from the pixel electrode 251 to the common electrode 252, and the sub-pixel is defined as having a positive polarity. Conversely, when the gray scale voltage is less than the common voltage of the common electrode 252, the directionality of the electrical field is from the common electrode 252 to the pixel electrode 251, and the sub-pixel is defined as having a

negative polarity. Moreover, when absolute values of the gray scale voltages applied to the pixel electrodes 251 of two sub-pixels are the same, with the gray scale voltages only differing in polarity, the gray scales of the two sub-pixels are assumed to be the same.

[0050] The sub-pixels in each same row of the sub-pixels are arranged in a sequence of repeating RGB sub-pixel groups. Thus the sub-pixels in each column of the sub-pixels are capable of displaying the same color. In each row, each group of a red sub-pixel, an adjacent green sub-pixel, and an adjacent blue sub-pixel cooperatively constitutes a pixel 26. All the pixels 26 of the liquid crystal panel 20 are arranged as a plurality of dithering units (not labeled). Each of the dithering units includes four pixels 26 adjacent to each other in the form of a 2x2 matrix.

[0051] Referring also to FIG. 2, the liquid crystal panel 20 employs an inversion driving method and a dynamic dithering method simultaneously. Each small square in FIG. 2 represents a sub-pixel.

[0052] In this embodiment, the liquid crystal panel 20 employs a 1-2 line inversion driving method along both the row direction and the column direction. That is, a polarity of each sub-pixel in a second row of the sub-pixels is the same as that of an adjacent sub-pixel in a third row of the sub-pixels, and is opposite to that of an adjacent sub-pixel in a first row of the sub-pixels. A polarity of each sub-pixel in a fourth row of the sub-pixels is the same as that of an adjacent sub-pixel in a fifth row of the sub-pixels, and is opposite to that of an adjacent sub-pixel in the third row of the sub-pixels. A polarity of each sub-pixel in a second column of the sub-pixels is the same as that of an adjacent sub-pixel in a third column of the sub-pixels, and is opposite to that of an adjacent sub-pixel in a first column of the sub-pixels. A polarity of each sub-pixel in a fourth column of the sub-pixels is the same as that of an adjacent sub-pixel in a fifth column of the sub-pixels, and is opposite to that of an adjacent sub-pixel in the third column of the sub-pixels. Moreover, the polarity of each sub-pixel is reversed once in each frame period.

[0053] The dynamic dithering method employed by the liquid crystal display is described in detail as follows:

[0054] In the first frame, the upper-left pixel of each dithering unit displays a first gray scale of M (shown with hatching in FIG. 2), and the upper-right pixel, the lower-left pixel, and the lower-right pixel of each dithering unit display a second gray scale of N (shown with a blank background in FIG. 2).

[0055] In the second frame, the lower-right pixel of each dithering unit displays the first gray scale of M, and the other pixels of each dithering unit display the second gray scale of N.

[0056] In the third frame, the lower-left pixel of each dithering unit displays the first gray scale of M, and the other pixels of each dithering unit display the second gray scale of N.

[0057] In the fourth frame, the upper-right pixel of each dithering unit displays the first gray scale of M, and the other pixels of each dithering unit display the second gray scale of N.

[0058] The liquid crystal panel 20 repeats displaying from the first frame through the fourth frame again and again. Each frame from the first frame to the fourth frame corresponds to a different dithering pattern. The actual image as viewed by the naked human eye has a dithering gray scale of  $(M+3N)/4$ .

For example, if M is equal to 252, and N is equal to 248, then a gray scale of 249 is dithered.

**[0059]** The following text shows summations of the polarities of the pixels that display the first gray scale of M, from the first frame through the fourth frame, for each row of pixels: the first row:  $(R^+ + G^- + B^- + R^- + G^+ + B^+) + (R^- + G^- + B^+ + R^+ + G^+ + B^-) = 2R^+ + 2R^- + 2B^- + 2B^+ + 2G^+ + 2G^-$ ; the second row:  $(R^+ + G^+ + B^- + R^- + G^- + B^+) + (R^- + G^+ + B^+ + R^+ + G^- + B^-) = 2R^+ + 2R^- + 2B^- + 2B^+ + 2G^+ + 2G^-$ ; the third row:  $(R^- + G^+ + B^+ + R^+ + G^- + B^-) + (R^+ + G^+ + B^- + R^- + G^- + B^+) = 2R^+ + 2R^- + 2B^- + 2B^+ + 2G^+ + 2G^-$ ; the fourth row:  $(R^- + G^- + B^- + R^+ + G^+ + B^-) + (R^+ + G^- + B^- + R^- + G^+ + B^+) = 2R^+ + 2R^- + 2B^- + 2B^+ + 2G^+ + 2G^-$ ; the fifth row:  $(R^+ + G^- + B^- + R^- + G^+ + B^+) + (R^- + G^- + B^+ + R^+ + G^+ + B^-) = 2R^+ + 2R^- + 2B^- + 2B^+ + 2G^+ + 2G^-$ ; and the sixth row:  $(R^+ + G^+ + B^- + R^- + G^- + B^+) + (R^- + G^+ + B^+ + R^+ + G^- + B^-) = 2R^+ + 2R^- + 2B^- + 2B^+ + 2G^+ + 2G^-$ .

**[0060]** As seen, for each row of the sub-pixels, the polarities of the red sub-pixels displaying the first gray scale of M cancel each other out, the polarities of the green sub-pixels displaying the first gray scale of M cancel each other out, and the polarities of the blue sub-pixels displaying the first gray scale of M cancel each other out. Thus for each row of the pixels 26, the polarities of the pixels 26 cancel each other out. Accordingly, even if the actual common voltage at any of the sub-pixels is greater or less than the ideal common voltage, an image displayed by the liquid crystal panel 20 has little or no color shift and few or no color strip lines.

**[0061]** In an alternative embodiment, the liquid crystal panel 20 can employ a 2-line inversion driving method along both the row direction and the column direction of the pixels.

**[0062]** It is to be further understood that even though numerous characteristics and advantages of preferred and exemplary embodiments have been set out in the foregoing description, together with details of the structures and functions of the embodiments, the disclosure is illustrative only; and that changes may be made in detail within the principles of the present invention to the full extent indicated by the broad general meaning of the terms in which the appended claims are expressed.

What is claimed is:

1. A method for driving a display panel, the display panel comprising a plurality of dithering units arranged in a matrix form, each dithering unit comprising a plurality of pixels arranged in a matrix form, each of the plurality of pixels comprising a plurality of sub-pixel units having different colors arranged in a row, the method comprising:

driving the sub-pixel units according to a sequence of frames to enable each of the sub-pixel units selectively having a first polarity or a second polarity reversed to the first polarity, the polarity of each sub-pixel unit being reversed at each frame; and

providing a series of predetermined dithering patterns for the plurality of dithering units, the dithering patterns provided to the pixels of each dithering unit being cyclically changed;

wherein in each dithering unit, the pixels have a first pattern having a first gray scale and a second pattern having a second gray scale smaller than the first gray scale, an average value of the gray scales of the pixels in each dithering unit defines the gray scale of the dithering unit, and in each cycle of dithering patterns, the first pattern are only provided once to each pixel;

wherein, in each frame, only portion pixels of each dithering unit are provided with the first pattern, and according to the portion pixels provided with the first pattern in each row of each cycle of the dithering patterns, the number of a same color of the sub-pixel units having the first polarity is equal to the number of the same color of the sub-pixel units having the second polarity.

2. The method of claim 1, wherein in each frame, and both the polarities of the sub-pixels at rows and the polarities of sub-pixels at columns are changed according to a 1-2 line driving method.

3. The method of claim 2, wherein each pixel comprising three sub-pixel units, each dithering unit comprising four pixels arranged in the form of a 2.times.2 matrix, the cycle of dithering patterns corresponds to four frames, and in each frame, one of the four pixels are provided with the first pattern.

4. The method of claim 3, wherein in a first one of the four frames, an upper-left pixel of each dithering unit displays the first pattern, and an upper-right pixel, a lower-left pixel, and a lower-right pixel of the dithering unit display the second pattern; in a second one of the four frames, the lower-right pixel of the dithering unit displays the first pattern, and the other three pixels display the second pattern; in a third one of the four frames, the lower-left pixel of the dithering unit displays the first pattern, and the other three pixels display the second pattern; and in a fourth one of the four frames, the upper-right pixel of the dithering unit displays the first pattern, and the other three pixels display the second pattern.

5. The method of claim 1, wherein the sub-pixel units comprise red sub-pixel units, green sub-pixel units, and blue sub-pixel units, and each pixel comprises a red sub-pixel unit, a green sub-pixel unit, and a blue sub-pixel unit.

6. The method of claim 1, wherein the sub-pixel units in a same column have a same color.

7. The method of claim 1, wherein the liquid crystal panel further comprises a plurality of scanning lines parallel to each other and a plurality of data lines parallel to each other and intersecting with the scanning lines, the scanning lines and the data lines cooperatively defining the sub-pixel units.

8. The method of claim 1, wherein in each frame, and both the polarities of the sub-pixels at rows and the polarities of sub-pixels at columns are changed according to a 2 line driving method.

9. A method for driving a display panel, the display panel comprising a plurality of dithering units arranged in a matrix form, each dithering unit comprising a plurality of pixels arranged in a matrix form, each of the plurality of pixels comprising a plurality of sub-pixel units having different colors arranged in a row, the method comprising:

driving the sub-pixel units according to a sequence of frames to enable each of the sub-pixel units selectively having a first polarity or a second polarity reversed to the first polarity, the polarity of each sub-pixel unit being reversed at each frame; and

providing a series of predetermined dithering patterns for the plurality of dithering units, the dithering patterns provided to the pixels of each dithering unit being cyclically changed;

wherein in each dithering unit, the pixels have a first pattern having a first gray scale and a second pattern having a second gray scale smaller than the first gray scale, an average value of the gray scales of the pixels in each dithering unit defines the gray scale of the dithering unit,

and in each cycle of dithering patterns, the first pattern are only provided once to each pixel;

wherein, in each frame, only portion pixels of each dithering unit are provided with the first pattern, and according to the portion pixels provided with the first pattern in each row of each cycle of the dithering patterns, summations of the polarities of a same color of the sub-pixel units cancel each other out.

**10.** The method of claim **9**, wherein in each frame, and both the polarities of the sub-pixels at rows and the polarities of sub-pixels at columns are changed according to a 1-2 line driving method.

**11.** The method of claim **10**, wherein each pixel comprising three sub-pixel units, each dithering unit comprising four pixels arranged in the form of a 2.times.2 matrix, the cycle of dithering patterns corresponds to four frames, and in each frame, one of the four pixels are provided with the first pattern.

**12.** The method of claim **11**, wherein in a first one of the four frames, an upper-left pixel of each dithering unit displays the first pattern, and an upper-right pixel, a lower-left pixel, and a lower-right pixel of the dithering unit display the second pattern; in a second one of the four frames, the lower-right

pixel of the dithering unit displays the first pattern, and the other three pixels display the second pattern; in a third one of the four frames, the lower-left pixel of the dithering unit displays the first pattern, and the other three pixels display the second pattern; and in a fourth one of the four frames, the upper-right pixel of the dithering unit displays the first pattern, and the other three pixels display the second pattern.

**13.** The method of claim **9**, wherein the sub-pixel units comprise red sub-pixel units, green sub-pixel units, and blue sub-pixel units, and each pixel comprises a red sub-pixel unit, a green sub-pixel unit, and a blue sub-pixel unit.

**14.** The method of claim **9**, wherein the sub-pixel units in a same column have a same color.

**15.** The method of claim **9**, wherein the liquid crystal panel further comprises a plurality of scanning lines parallel to each other and a plurality of data lines parallel to each other and intersecting with the scanning lines, the scanning lines and the data lines cooperatively defining the sub-pixel units.

**16.** The method of claim **9**, wherein in each frame, both the polarities of the sub-pixels at rows and the polarities of sub-pixels at columns are changed according to a 2 line driving method.

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