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HUH et al.(10) **Pub. No.: US 2014/0211133 A1**(43) **Pub. Date: Jul. 31, 2014**(54) **LIQUID CRYSTAL DISPLAY WITH
REDUCED COLOR MIXING****Publication Classification**(71) Applicant: **SAMSUNG DISPLAY CO., LTD.,**
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Hong, Seoul (KR)(52) **U.S. Cl.**
CPC **G02F 1/133512** (2013.01)
USPC **349/104; 349/139**(73) Assignee: **SAMSUNG DISPLAY CO., LTD.,**
YONGIN-CITY (KR)(57) **ABSTRACT**(21) Appl. No.: **14/155,128**(22) Filed: **Jan. 14, 2014**(30) **Foreign Application Priority Data**

Jan. 25, 2013 (KR) 10-2013-0008635

A liquid crystal display includes a first pixel, a second pixel, and a third pixel each displaying a different color. A first data line is positioned between the first pixel and the second pixel, and a second data line is positioned between the second pixel and the third pixel. A first pixel electrode, a second pixel electrode, and a third pixel electrode are respectively positioned in the first, second, and third pixels. An interval between the first pixel electrode and the first data line is larger than an interval between the second pixel electrode and the first data line. An interval between the second pixel electrode and the second data line is smaller than an interval between the third pixel electrode and the second data line.

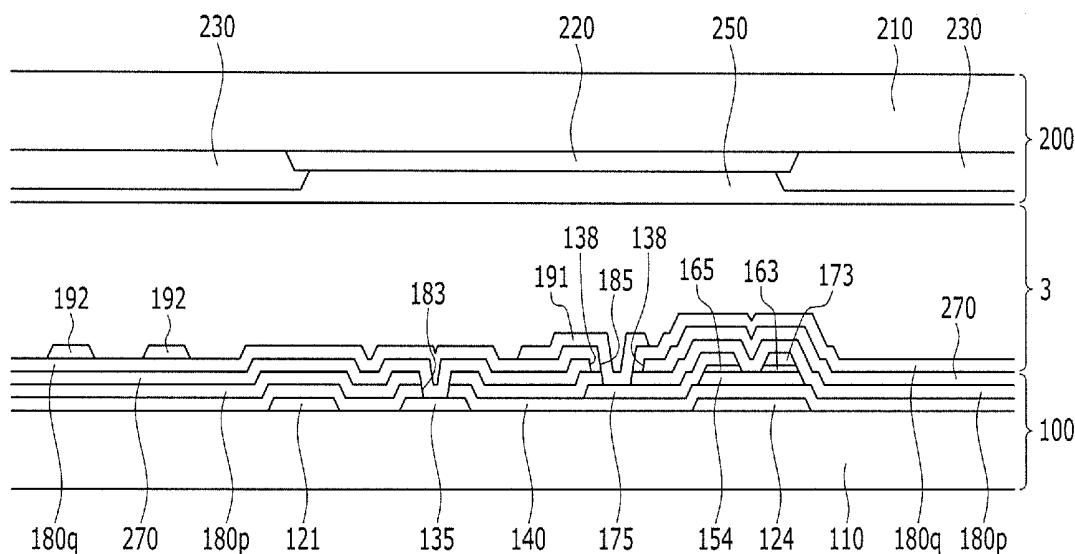


FIG. 1

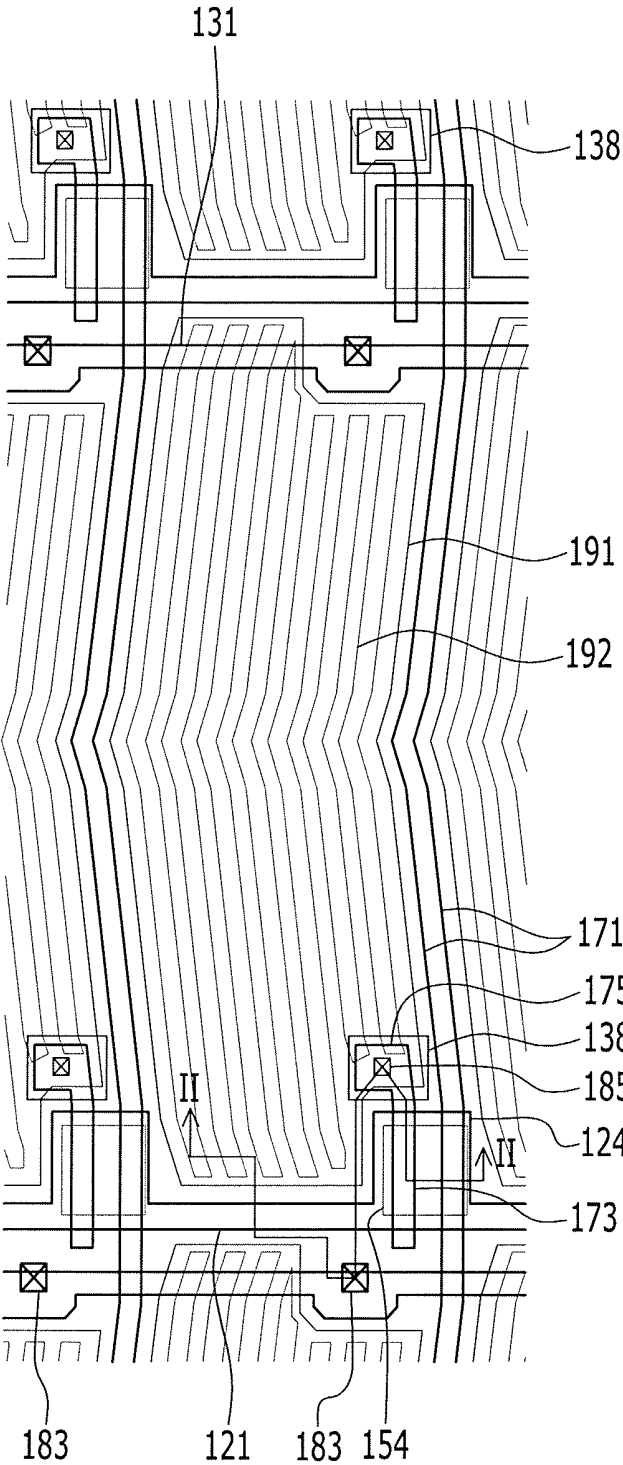


FIG. 2

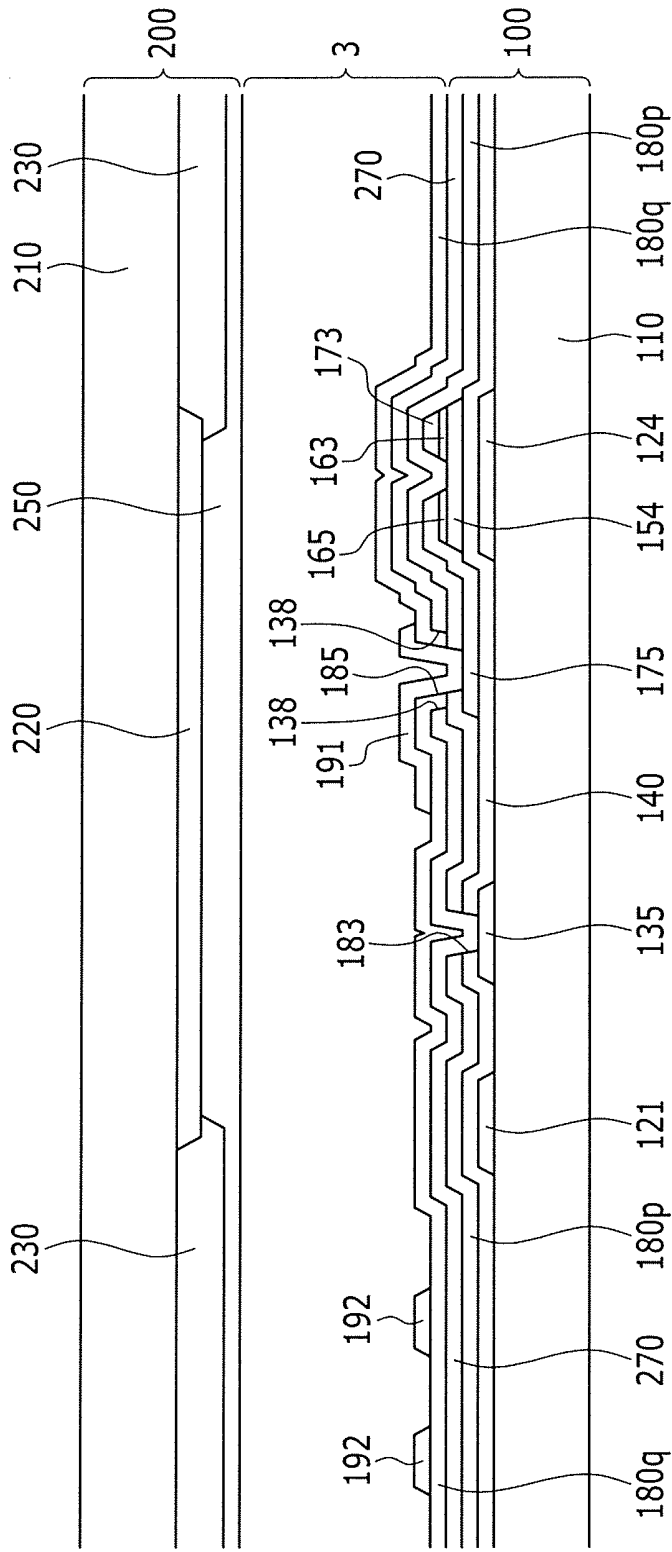


FIG. 3

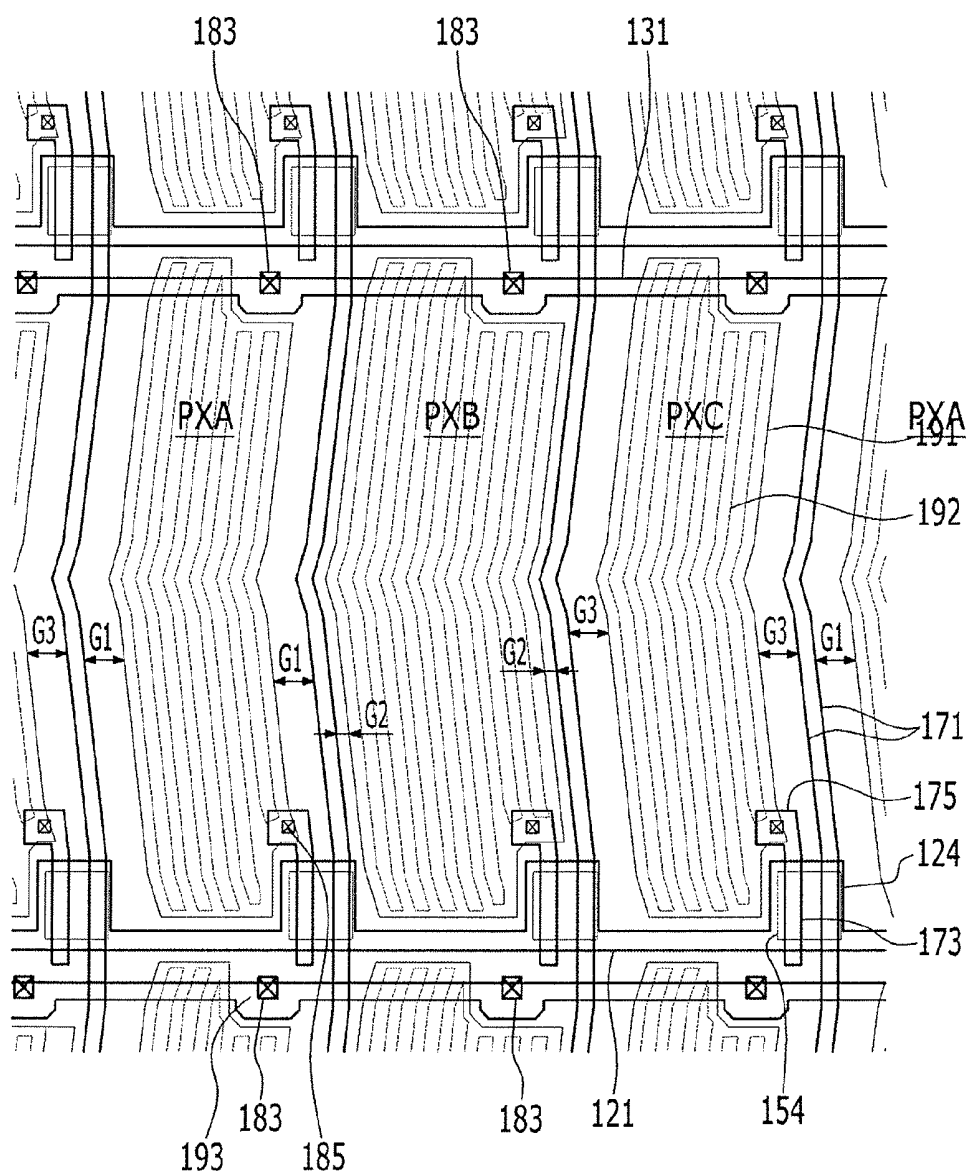


FIG. 4

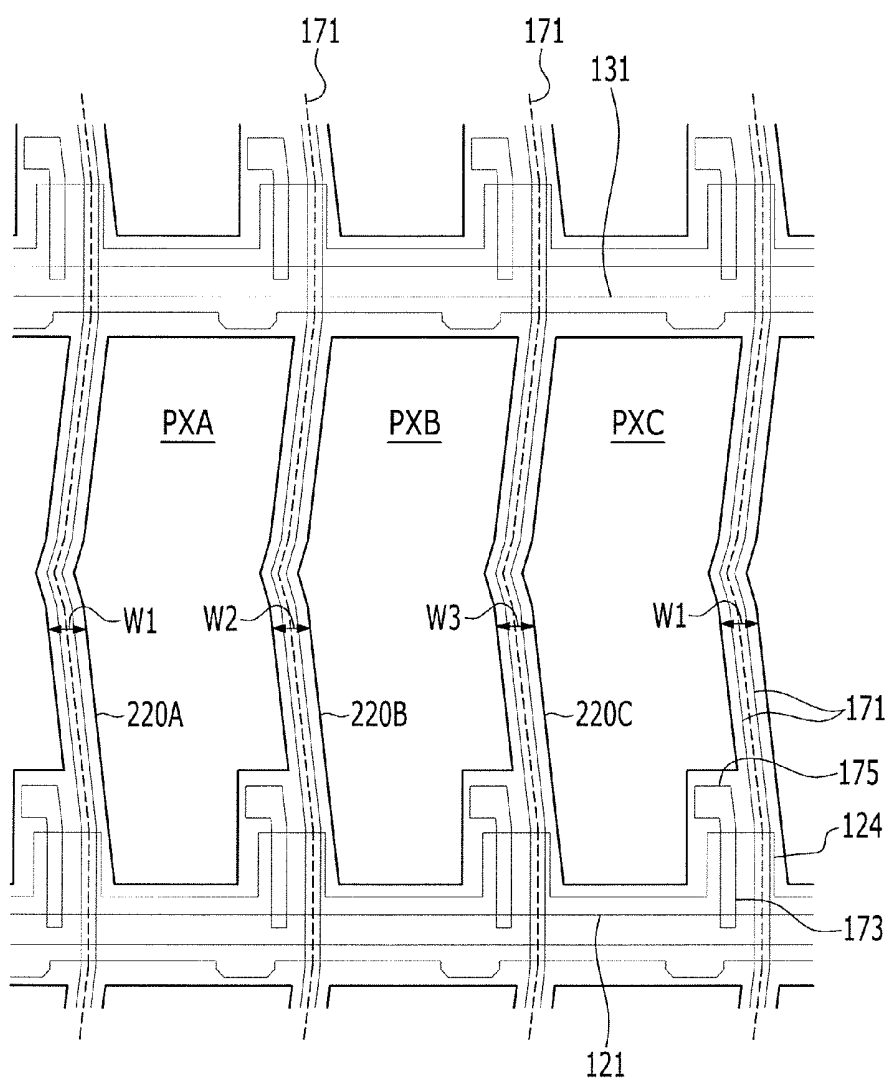


FIG. 5

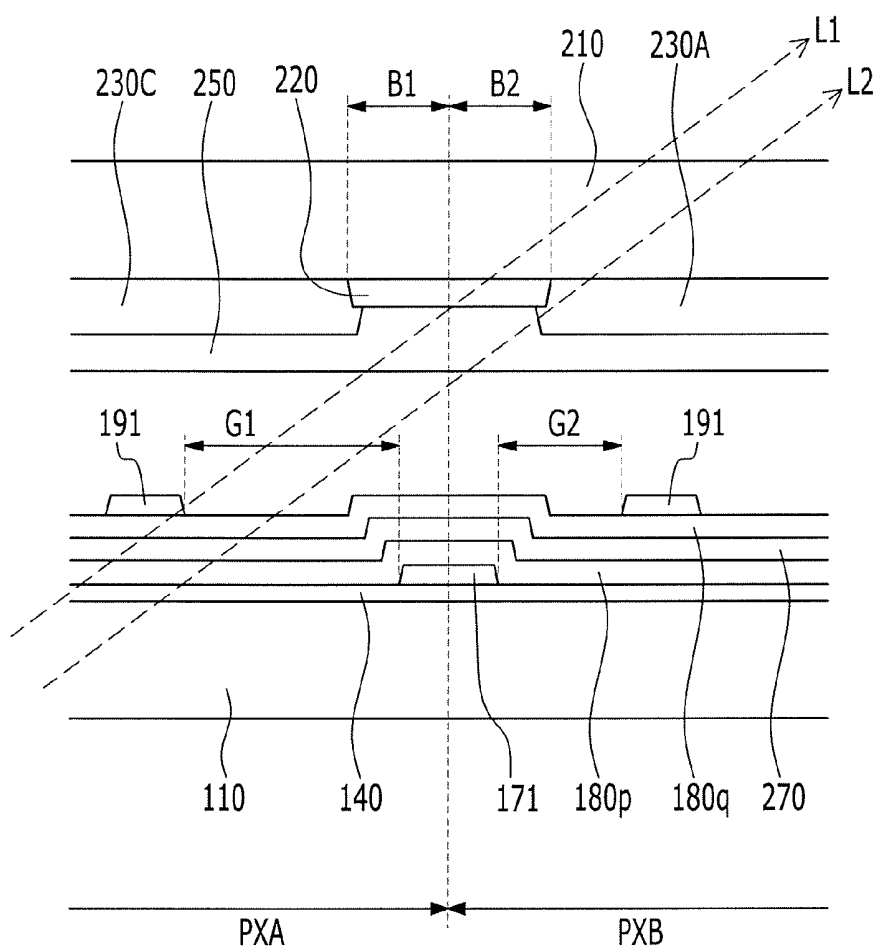


FIG. 6

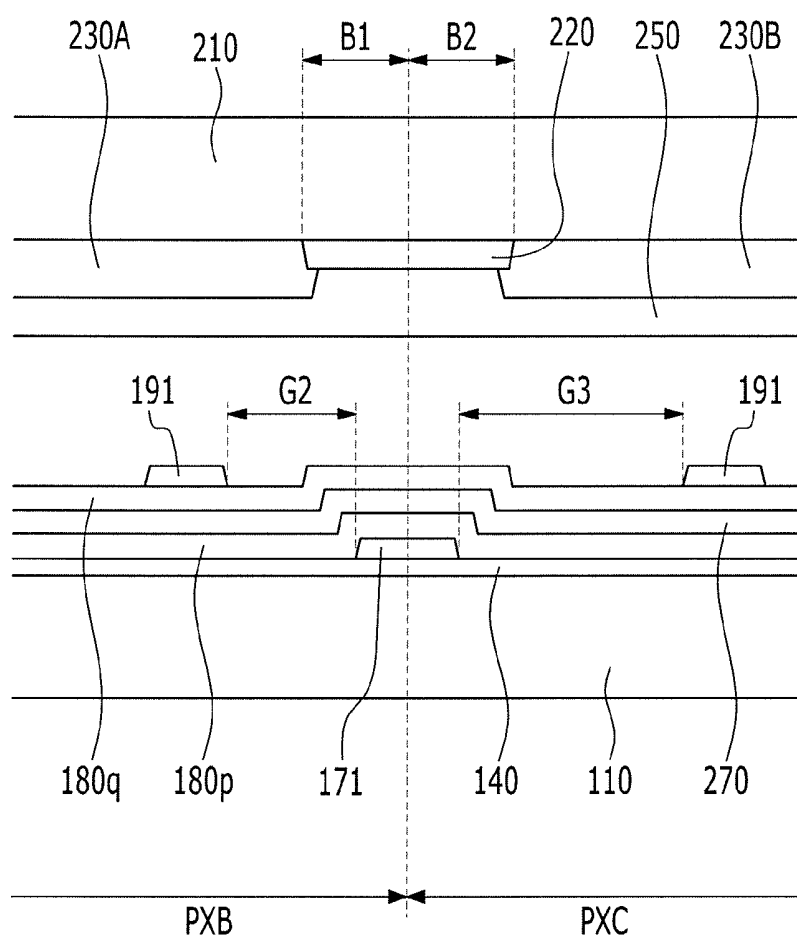


FIG. 7

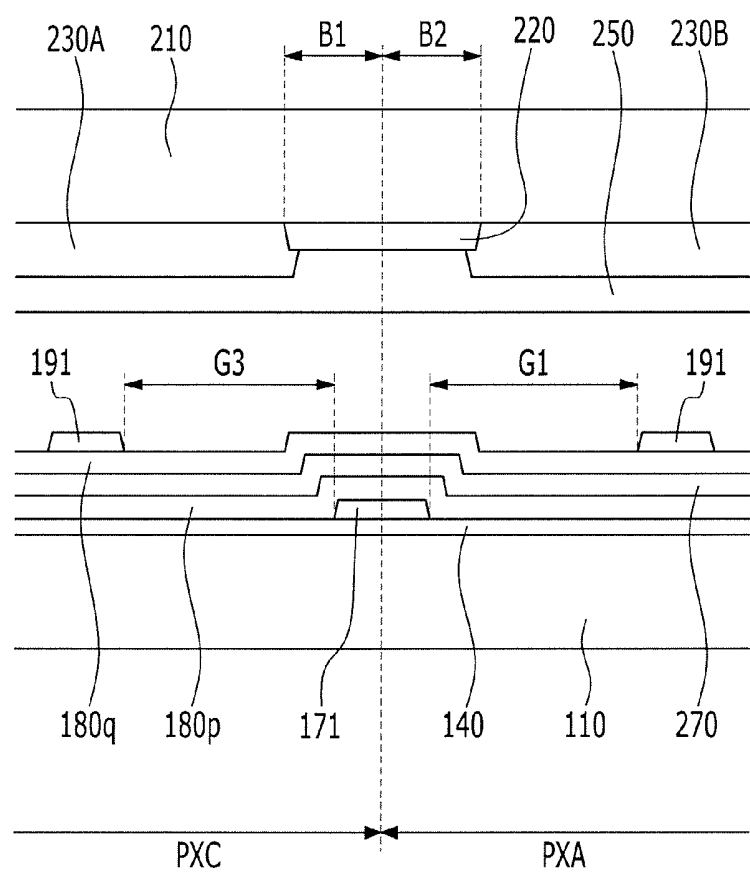


FIG. 8

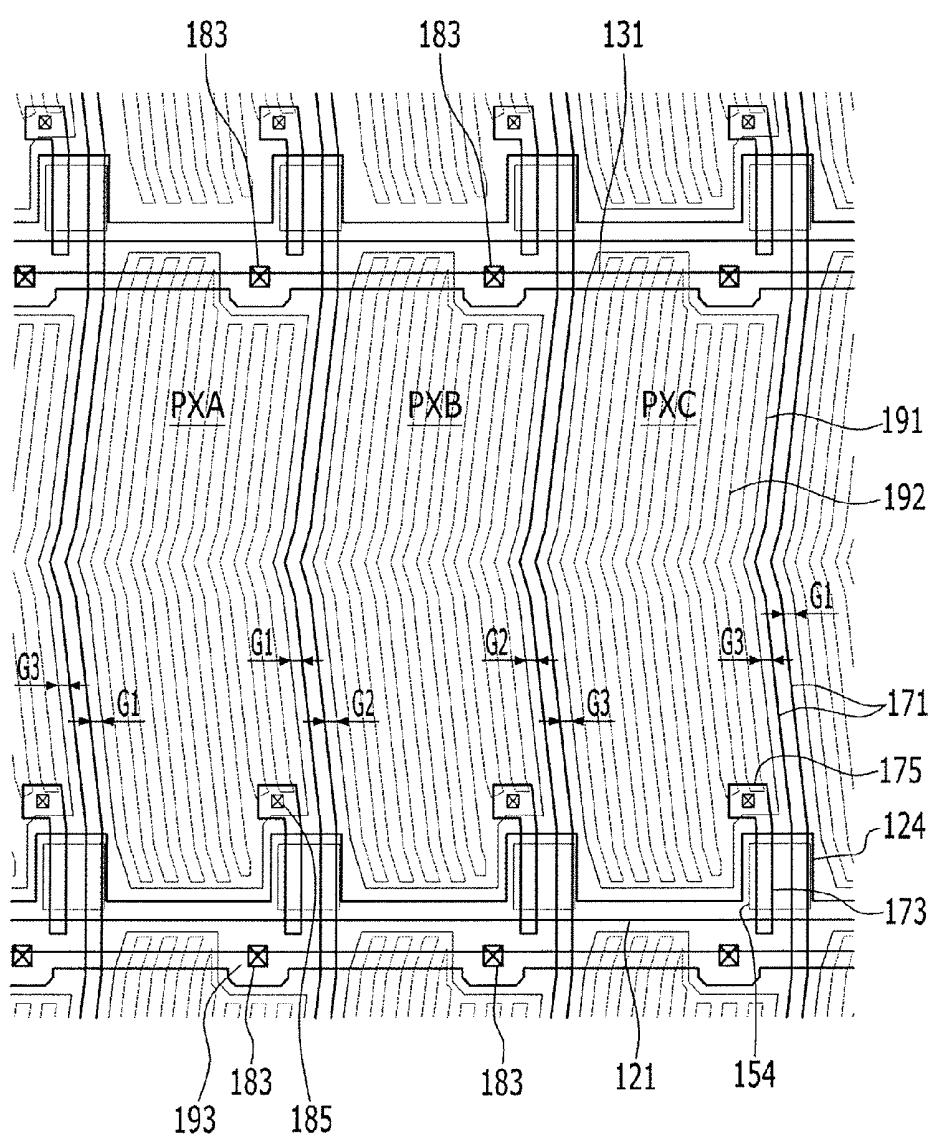


FIG. 9

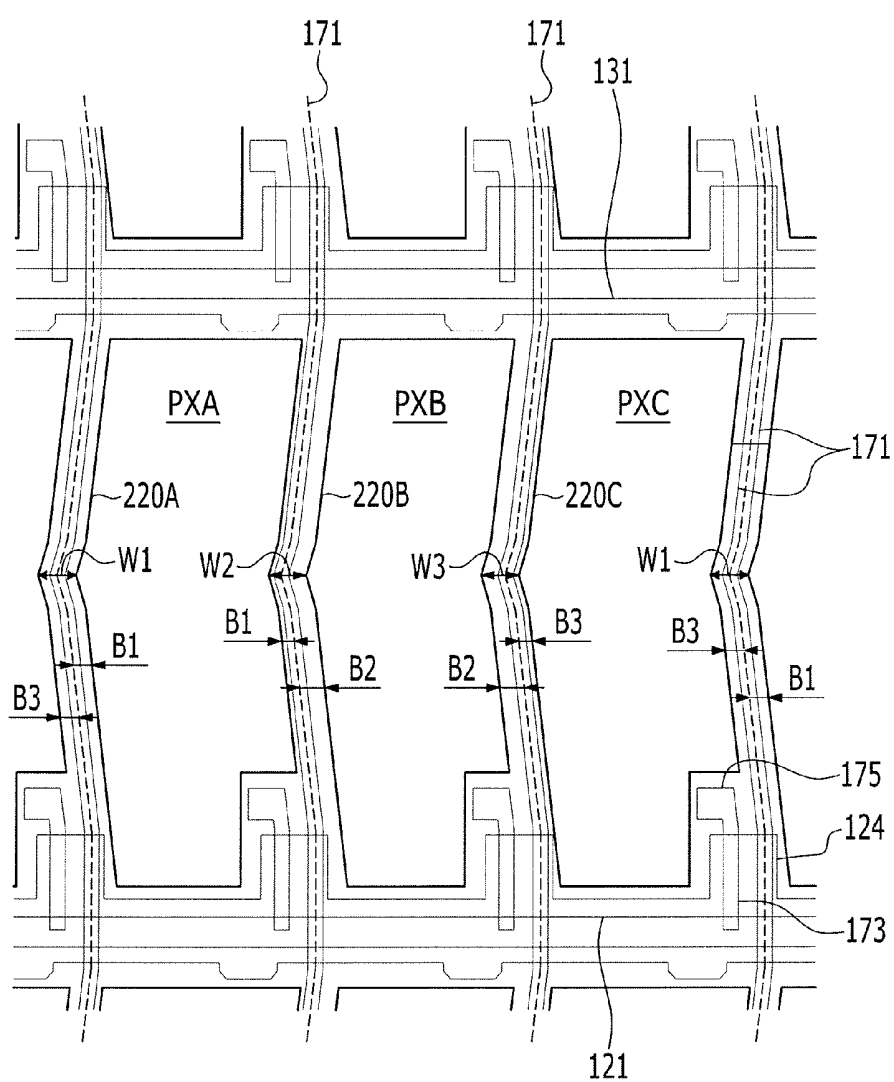


FIG. 10

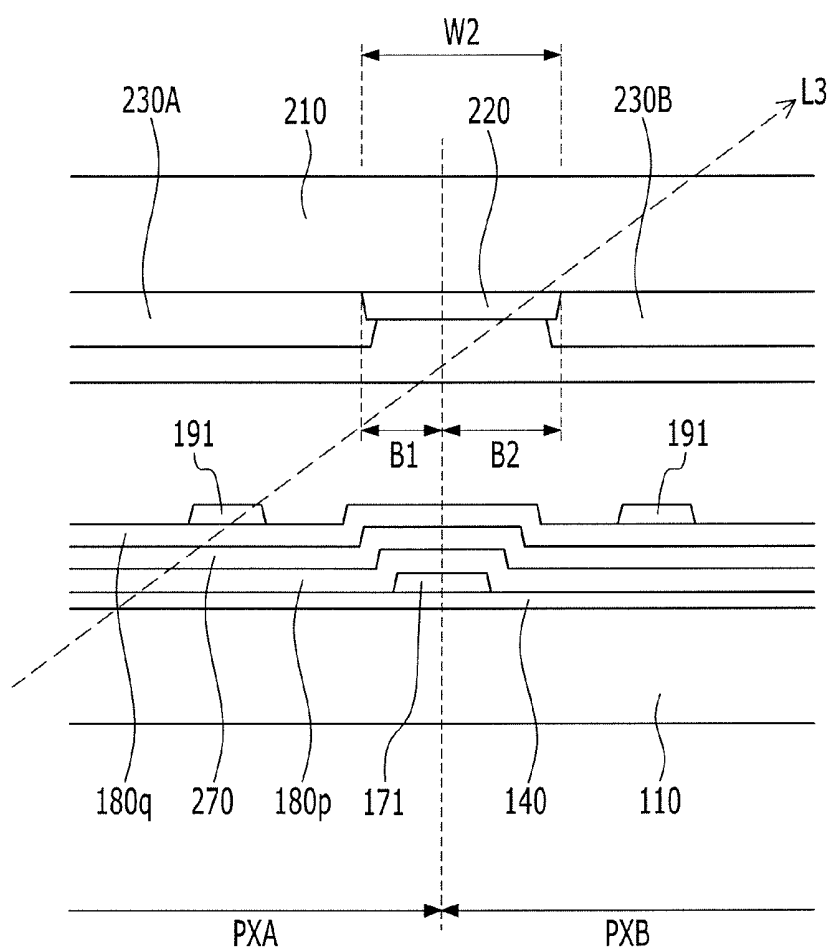


FIG. 11

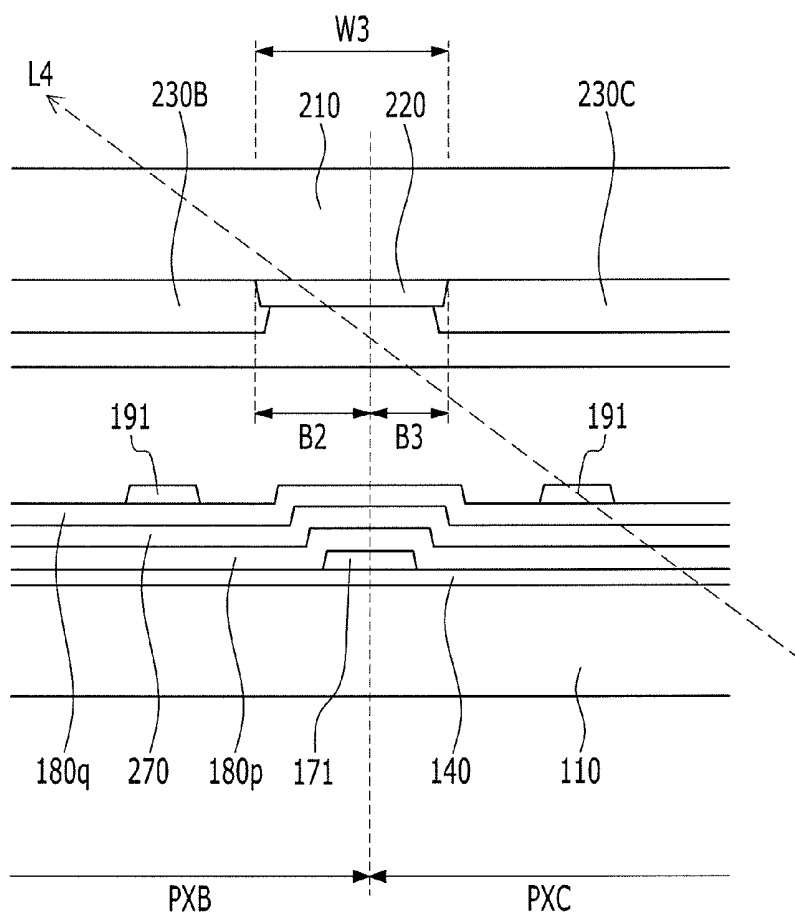


FIG. 12

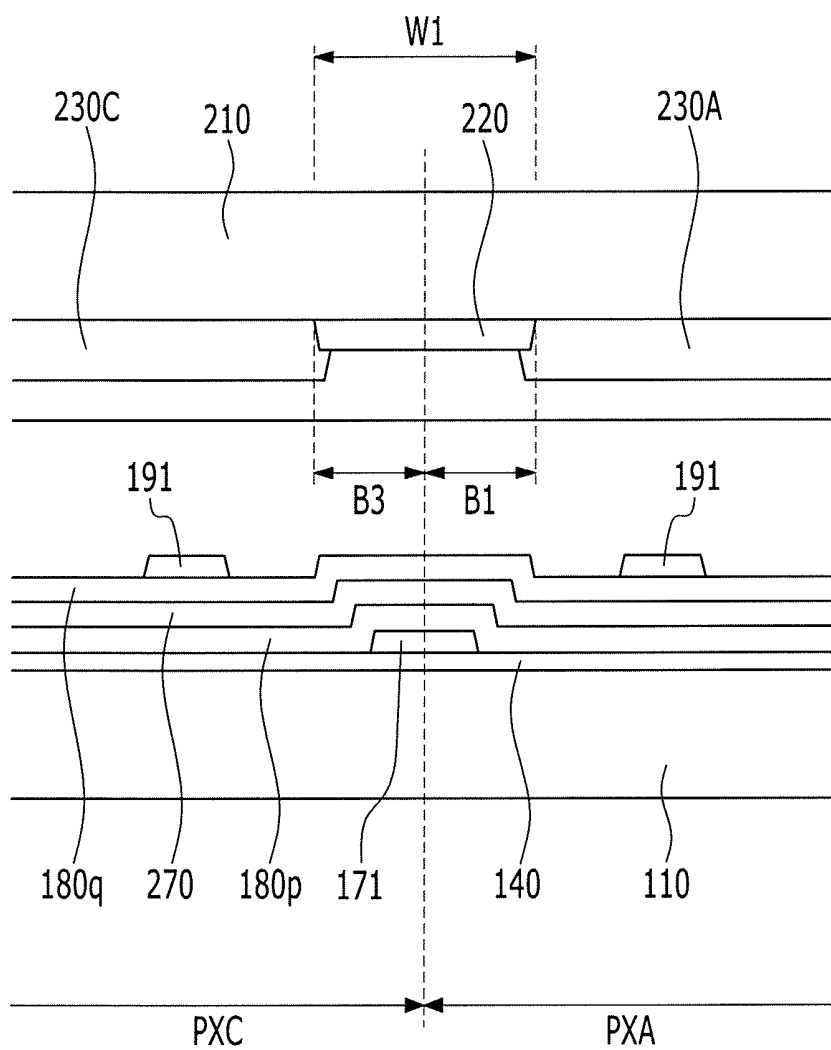


FIG. 13

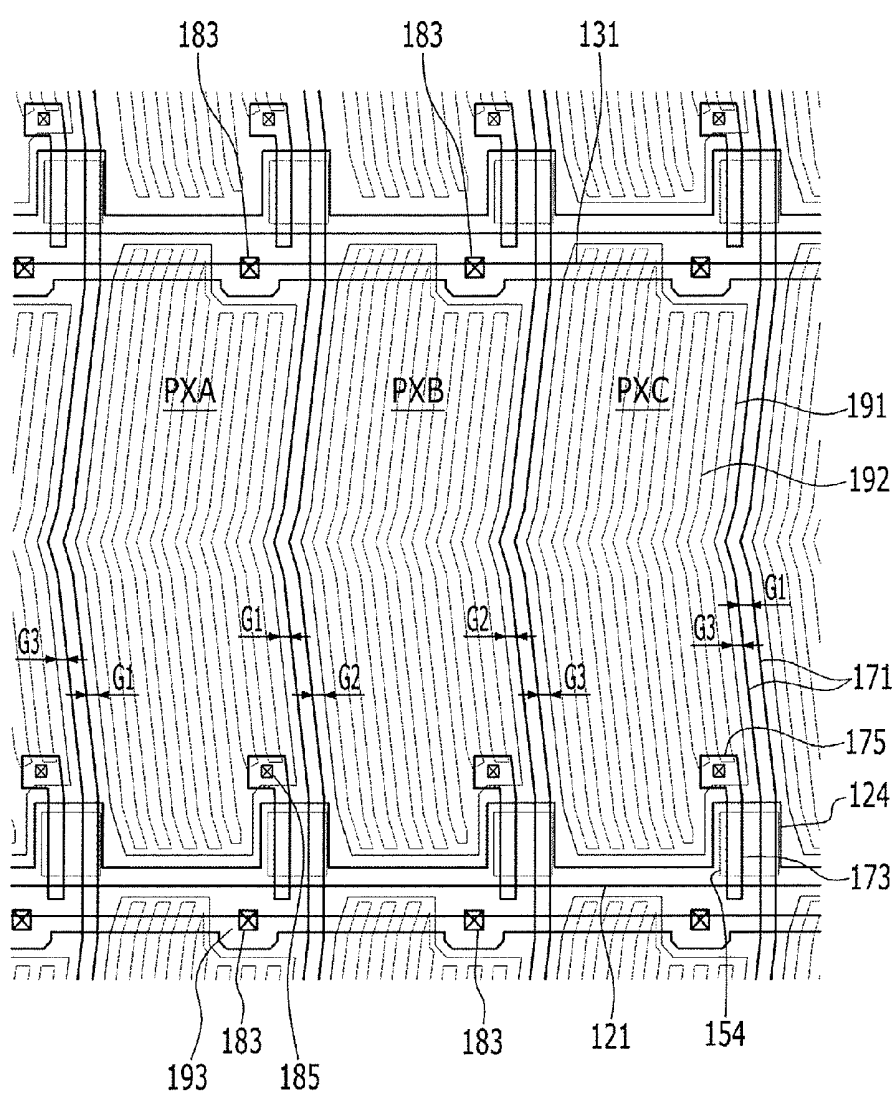


FIG. 14

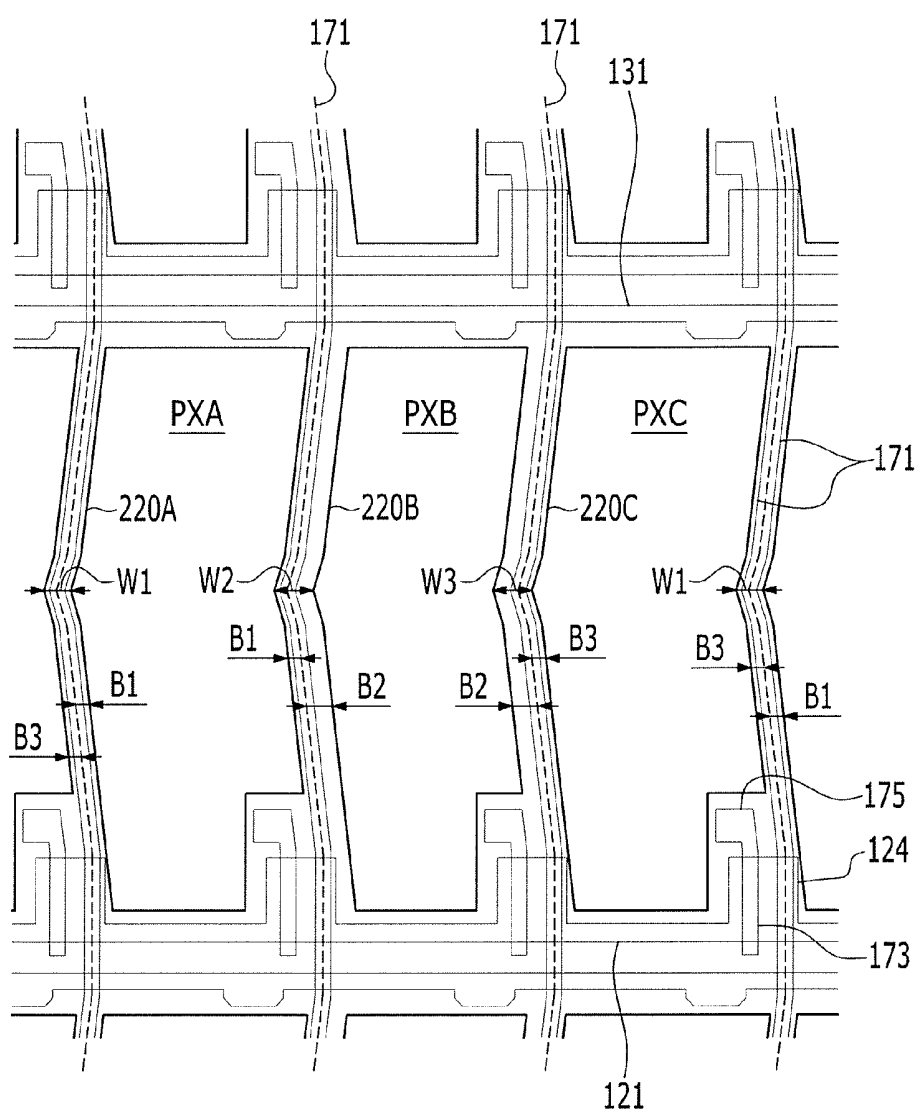


FIG. 15

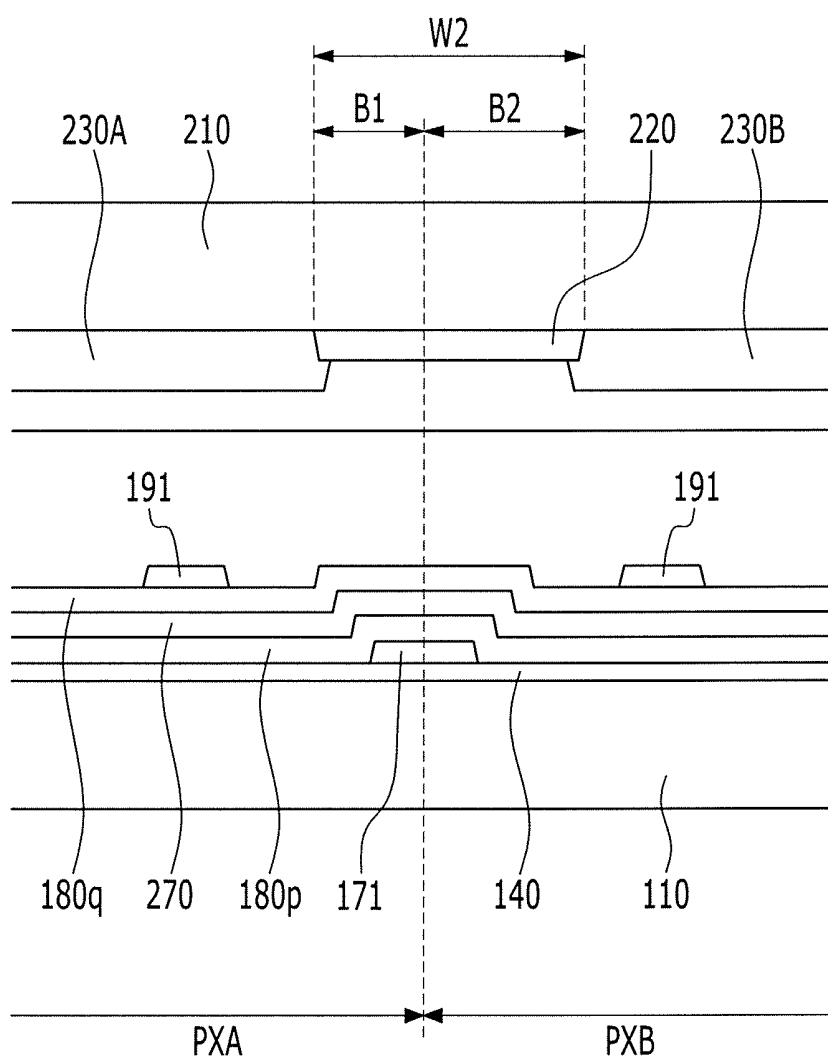


FIG. 16

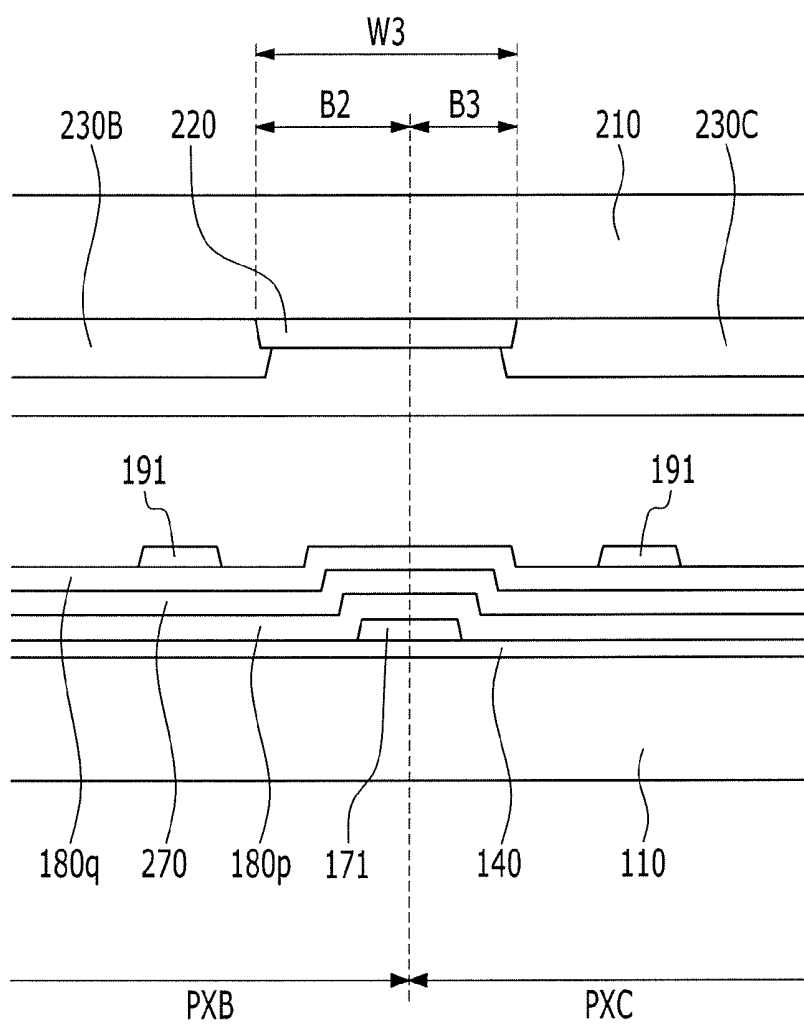


FIG. 17

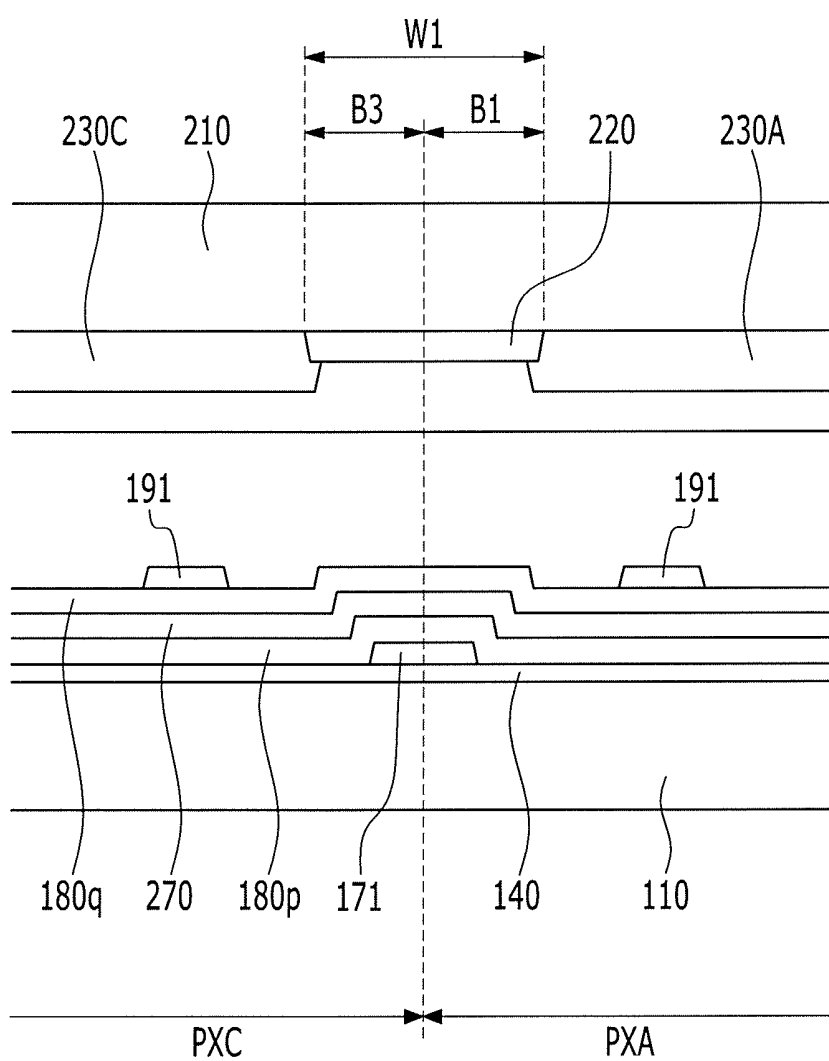


FIG. 18

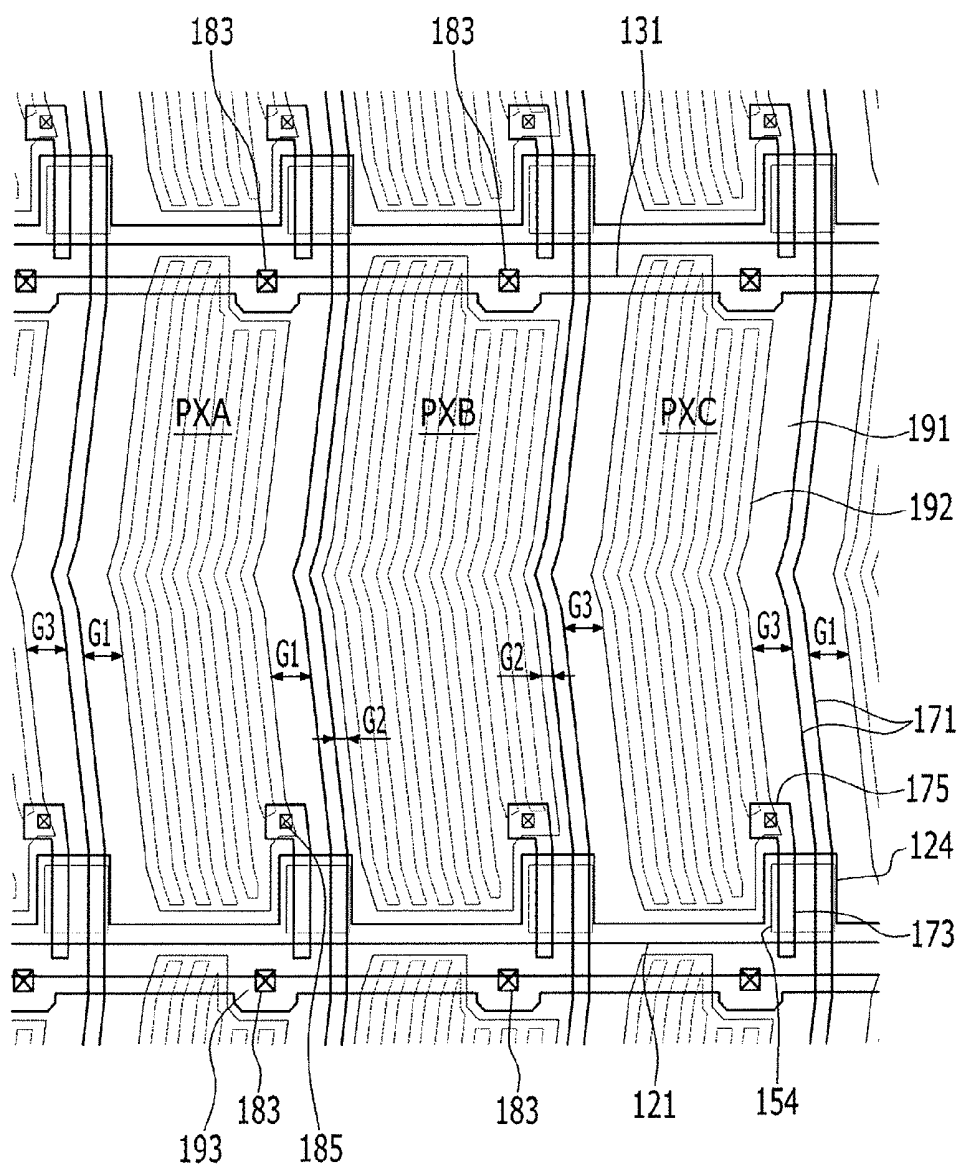


FIG. 19

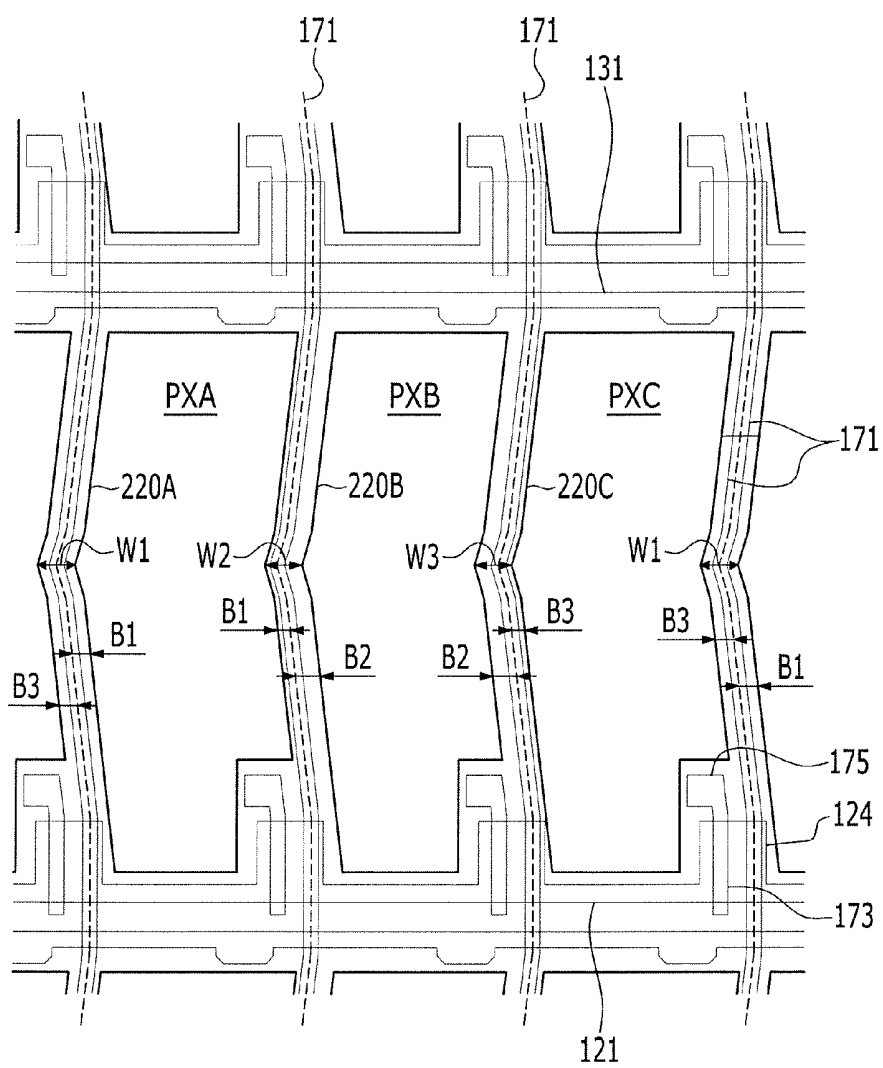


FIG. 20

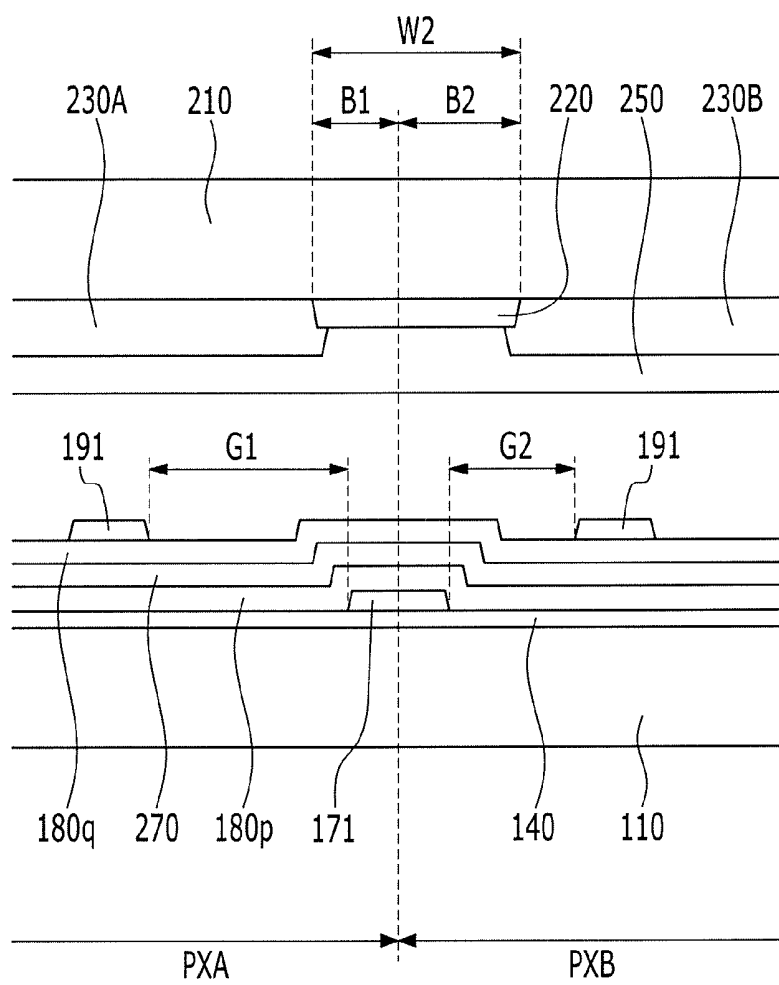


FIG. 21

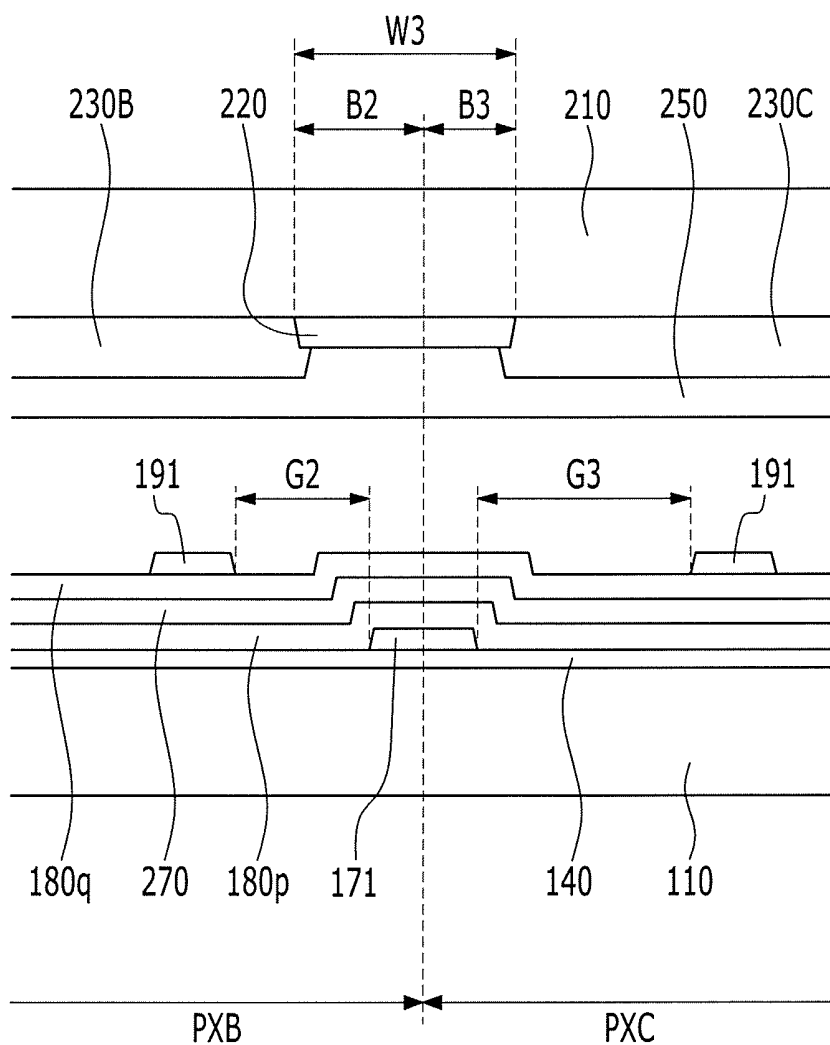
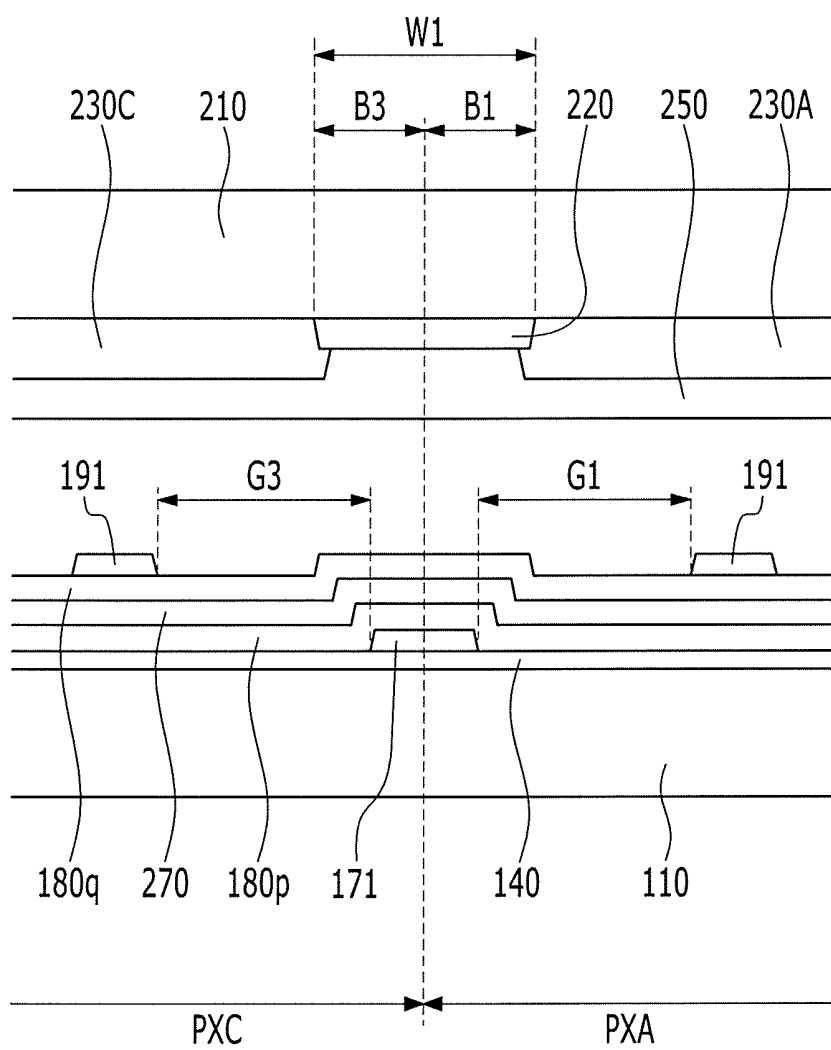


FIG. 22



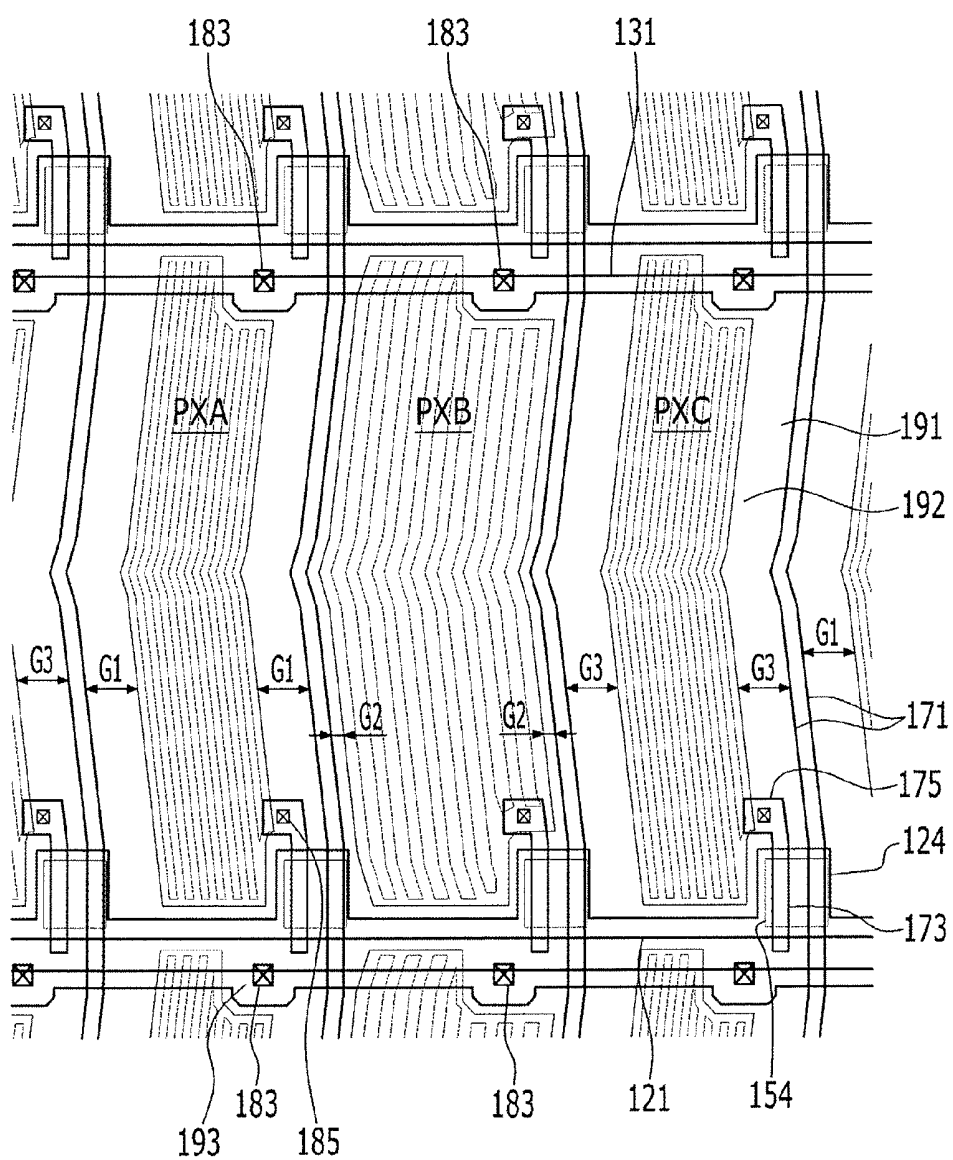


FIG. 24

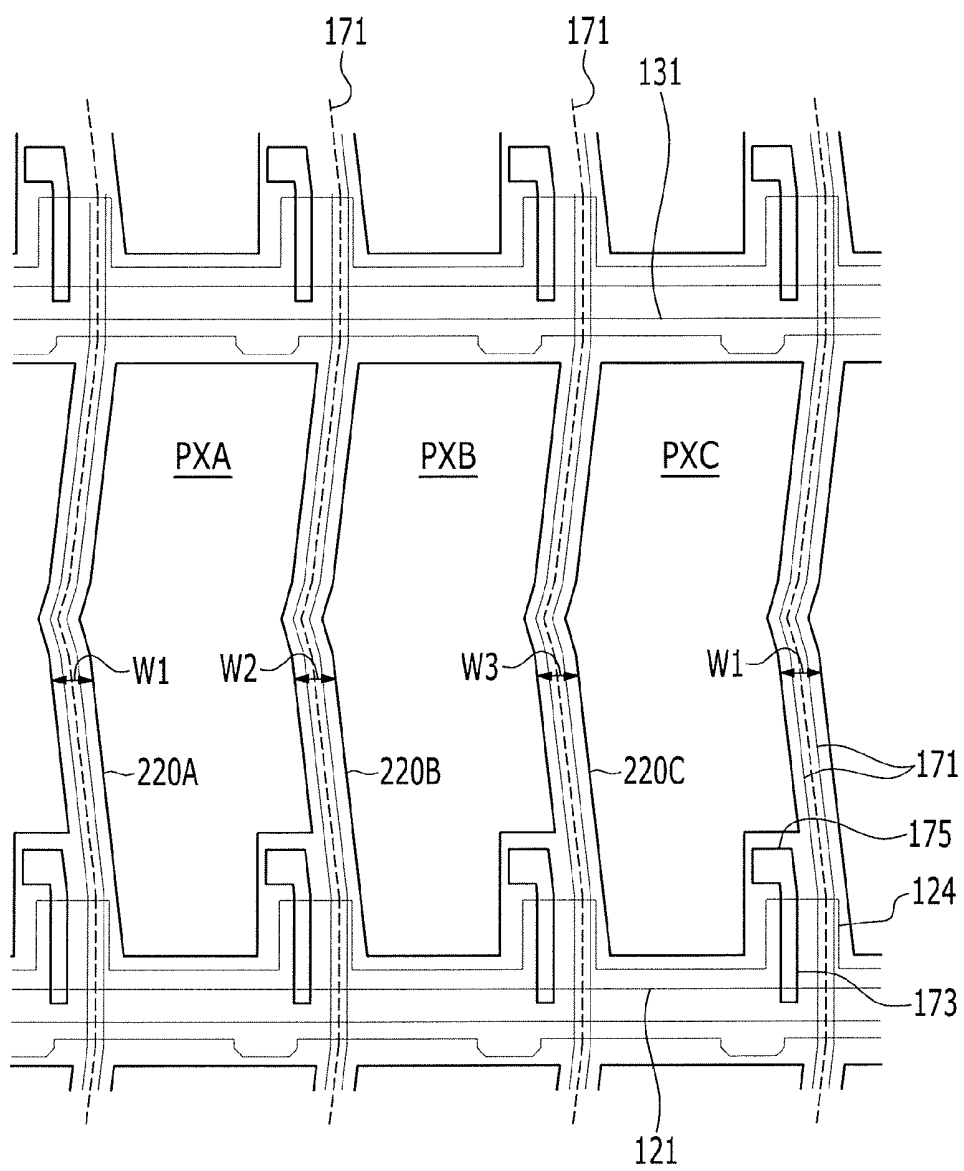
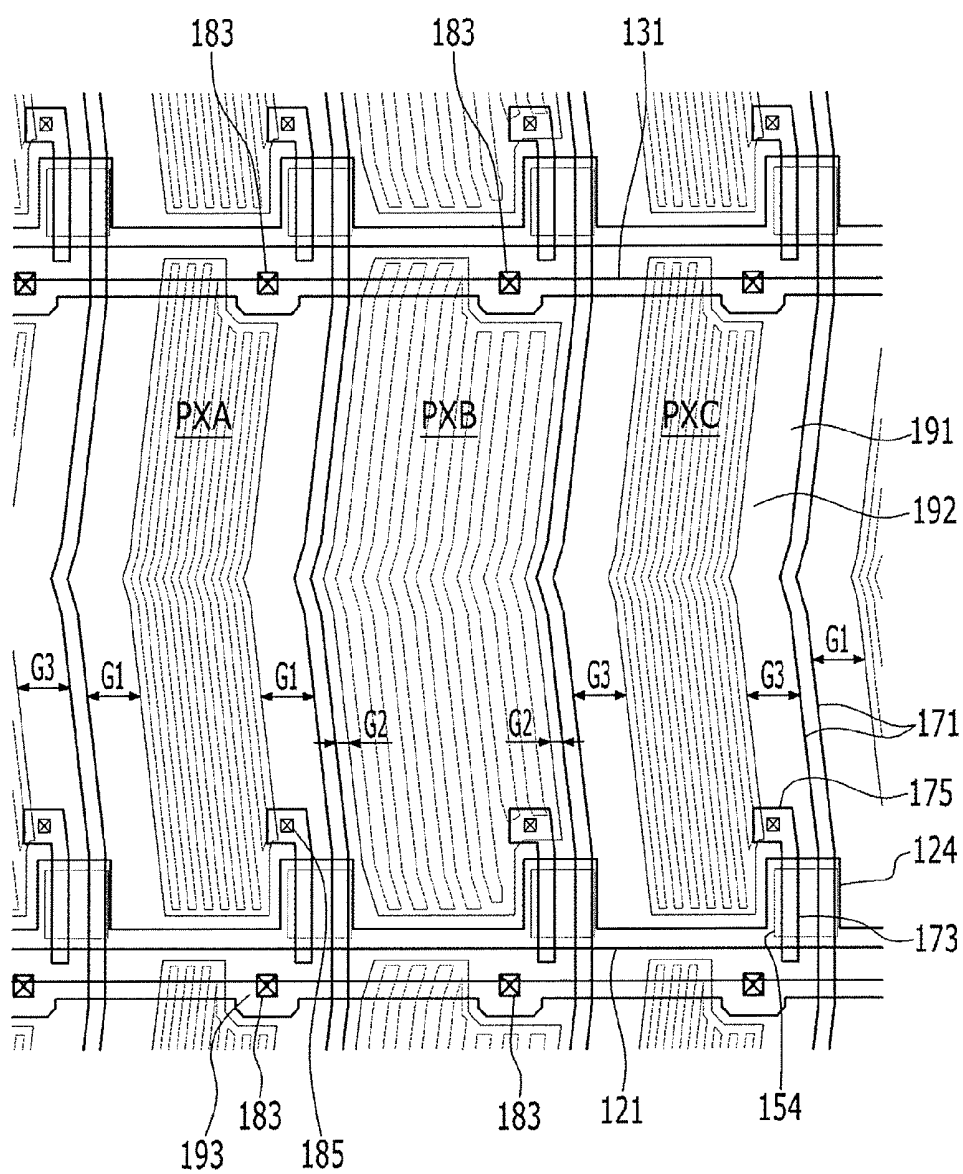
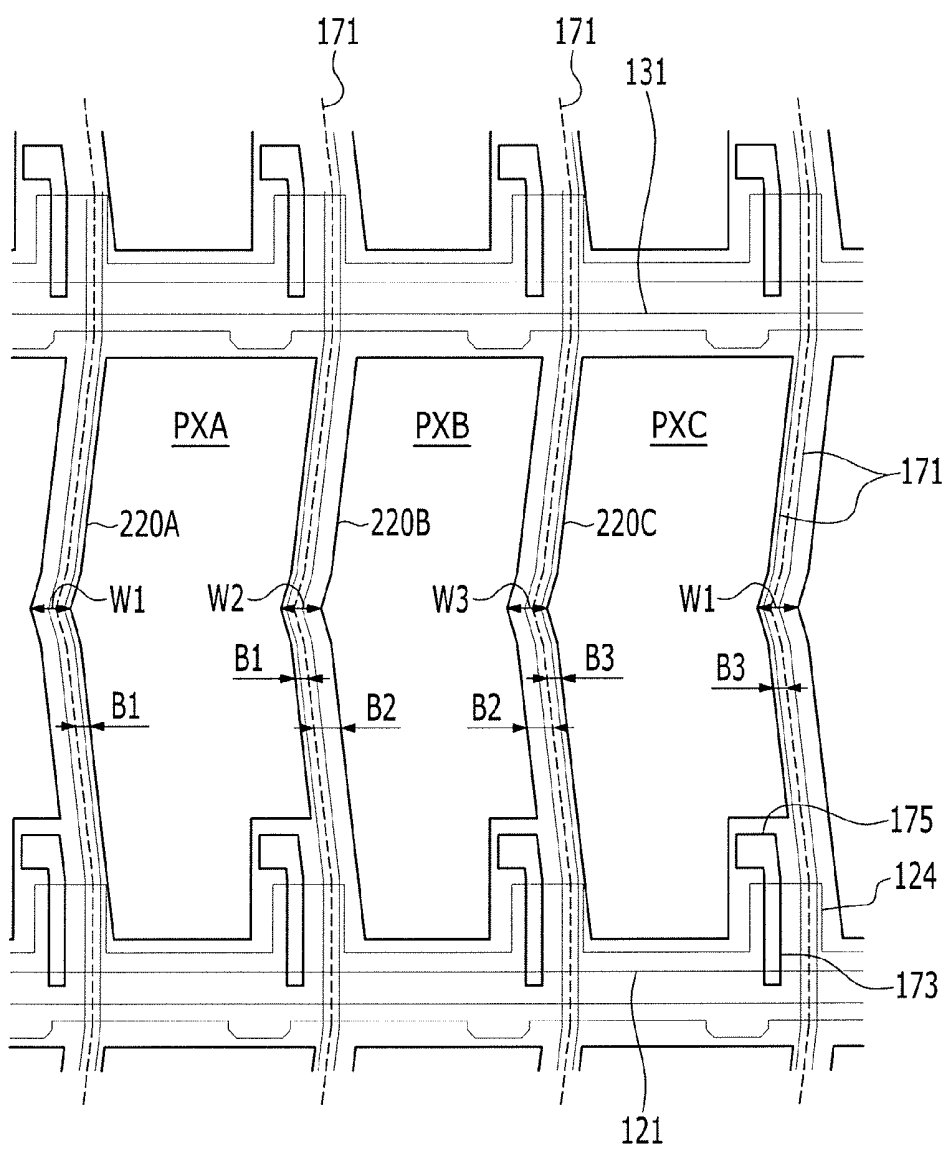


FIG. 25





LIQUID CRYSTAL DISPLAY WITH REDUCED COLOR MIXING

CROSS-REFERENCE TO RELATED APPLICATION

[0001] This U.S. non-provisional application claims priority under 35 U.S.C. §119 to Korean Patent Application No. 10-2013-0008635 filed in the Korean Intellectual Property Office on Jan. 25, 2013, the disclosure of which is incorporated by reference herein in its entirety.

TECHNICAL FIELD

[0002] Exemplary embodiments of the present invention relate to a liquid crystal display, and more particularly, to a liquid crystal display having reduced color mixing.

DISCUSSION OF THE RELATED ART

[0003] A liquid crystal display has two display panels and a liquid crystal layer interposed between the display panels. In the liquid crystal display, voltages are applied to a pixel electrode and a common electrode to form an electric field in the liquid crystal layer, and liquid crystal molecules in the liquid crystal layer are aligned by the electric field, thereby displaying images. Pixel electrodes and the common electrode may be provided on one display panel where switching elements are formed. In such a structure, the liquid crystal molecules may be shifted in a horizontal direction, thus resulting in a color mixture between pixels displaying different colors from each other.

SUMMARY

[0004] A liquid crystal display according to an exemplary embodiment of the present invention includes a first pixel, a second pixel, and a third pixel. The first, second, and third pixels each display a different color. A first data line is positioned between the first pixel and the second pixel, and a second data line is positioned between the second pixel and the third pixel. A first pixel electrode, a second pixel electrode, and a third pixel electrode are respectively positioned in the first, second, and third pixels. An interval between the first pixel electrode and the first data line is larger than an interval between the second pixel electrode and the first data line. An interval between the second pixel electrode and the second data line is smaller than an interval between the third pixel electrode and the second data line.

[0005] Each of the first, second, and third pixel electrodes may include a plurality of branch electrodes. A width of the plurality of branch electrodes in the first pixel electrode may be substantially equal to a width of the plurality of branch electrodes in the second pixel electrode. An interval between the plurality of branch electrodes in the first pixel electrode may be substantially equal to an interval between the plurality of branch electrodes in the second pixel electrode. A width of the plurality of branch electrodes in the second pixel electrode may be substantially equal to a width of the plurality of branch electrodes in the third pixel electrode. An interval between the plurality of branch electrodes in the second pixel electrode may be substantially equal to an interval between the plurality of branch electrodes in the third pixel electrode.

[0006] Each of the first, second, and third pixel electrodes may include a plurality of branch electrodes. A width of the plurality of branch electrodes in the first pixel electrode may be smaller than a width of the plurality of branch electrodes in

the second pixel electrode. An interval between the plurality of branch electrodes in the first pixel electrode may be smaller than an interval between the plurality of branch electrodes in the second pixel electrode. A width of the plurality of branch electrodes in the second pixel electrode may be larger than a width of the plurality of branch electrodes in the third pixel electrode. An interval between the plurality of branch electrodes in the second pixel electrode may be larger than an interval between the plurality of branch electrodes in the third pixel electrode.

[0007] The liquid crystal display may further include a first light blocking member overlapping the first data line and disposed close to the first pixel with respect to a longitudinal center line of the first data line. A second light blocking member overlaps the first data line and the second data line and is disposed close to the second pixel with respect to the longitudinal center line of the first data line and a longitudinal center line of the second data line. A third light blocking member overlaps the second data line and is disposed close to the third pixel with respect to the longitudinal center line of the second data line. With respect to the longitudinal center line of the first data line, a width of the first light blocking member overlapping the first data line may be smaller than a width of the second light blocking member overlapping the first data line. With respect to the longitudinal center line of the second data line, the width of the second light blocking member overlapping the second data line may be larger than a width of the third light blocking member overlapping the second data line.

[0008] The liquid crystal display may further include a third data line positioned between the third pixel and a fourth pixel close to the third pixel. A third light blocking member overlaps the third data line and is disposed close to the third pixel with respect to a longitudinal center line of the third data line. A fourth light blocking member overlaps the third data line and is disposed close to the fourth pixel with respect to the longitudinal center line of the third data line. The width of the third light blocking member overlapping the third data line is substantially equal to a width of the fourth light blocking member overlapping the third data line with respect to the longitudinal center line of the third data line.

[0009] The width of the first light blocking member and the second light blocking member overlapping the first data line may be substantially equal to the width of the second light blocking member and the third light blocking member overlapping the second data line. The width of the first light blocking member and the second light blocking member overlapping the first data line may be substantially equal to the width of the third light blocking member and the fourth light blocking member overlapping the third data line.

[0010] The width of the first light blocking member and the second light blocking member overlapping the first data line may be substantially equal to the width of the second light blocking member and the third light blocking member overlapping the second data line. The width of the first light blocking member and the second light blocking member overlapping the first data line may be larger than the width of the third light blocking member and the fourth light blocking member overlapping the third data line.

[0011] A liquid crystal display according to an exemplary embodiment of the present invention includes a first pixel, a second pixel, and a third pixel. The first, second, and third pixels display different colors from each other. A first data line is positioned between the first pixel and the second pixel,

and a second data line is positioned between the second pixel and the third pixel. A first electrode, a second electrode, and a third pixel electrode are respectively positioned in the first, second, and third pixels. A first light blocking member overlaps the first data line and is disposed close to the first pixel with respect to a longitudinal center line of the first data line. A second light blocking member overlaps the first data line and the second data line and is disposed close to the second pixel with respect to the longitudinal center line of the first data line and a longitudinal center line of the second data line. A third light blocking member overlaps the second data line and is disposed close to the third pixel with respect to the longitudinal center line of the second data line. With respect to the longitudinal center line of the first data line, a width of the first light blocking member overlapping the first data line is smaller than a width of the second light blocking member overlapping the first data line. With respect to the longitudinal center line of the second data line, the width of the second light blocking member overlapping the second data line is larger than a width of the third light blocking member overlapping the second data line.

[0012] According to an exemplary embodiment of the present invention, a liquid crystal display includes a first pixel, a second pixel, and a third pixel arranged in a direction of the liquid crystal display. The second pixel is positioned between the first and third pixels. A first data line is positioned between the first pixel and the second pixel, and a second data line is positioned between the second pixel and the third pixel. A first pixel electrode, a second pixel electrode, and a third pixel electrode are respectively positioned in the first, second, and third pixels. A first light blocking member overlaps the first data line and is disposed close to the first pixel with respect to a longitudinal center line of the first data line. A second light blocking member overlaps the first data line and the second data line and is disposed close to the second pixel with respect to the longitudinal center line of the first data line and a longitudinal center line of the second data line. A third light blocking member overlaps the second data line and is disposed close to the third pixel with respect to the longitudinal center line of the second data line. An interval between the first pixel electrode and the first data line is different from an interval between the second pixel electrode and the first data line or a width of the first light blocking member overlapping the first data line is different from a width of the second light blocking member overlapping the first data line.

BRIEF DESCRIPTION OF THE DRAWINGS

[0013] A more complete appreciation of the present disclosure and many of the attendant aspects thereof will be readily obtained as the same becomes better understood by reference to the following detailed description when considered in connection with the accompanying drawings, wherein:

[0014] FIG. 1 is a layout view of one pixel of a liquid crystal display according to an exemplary embodiment of the present invention;

[0015] FIG. 2 is a cross-sectional view of the liquid crystal display of FIG. 1 taken along the line II-II;

[0016] FIG. 3 is a layout view of three pixels of a liquid crystal display according to an exemplary embodiment of the present invention;

[0017] FIG. 4 is a layout view of a portion of the liquid crystal display of FIG. 3;

[0018] FIG. 5 to FIG. 7 are cross-sectional views of a portion of the liquid crystal display of FIG. 3;

[0019] FIG. 8 is a layout view of three pixels of a liquid crystal display according to an exemplary embodiment of the present invention;

[0020] FIG. 9 is a layout view of a portion of the liquid crystal display of FIG. 8;

[0021] FIG. 10 to FIG. 12 are cross-sectional views of a portion of the liquid crystal display of FIG. 8;

[0022] FIG. 13 is a layout view of three pixels of a liquid crystal display according to an exemplary embodiment of the present invention;

[0023] FIG. 14 is a layout view of a portion of the liquid crystal display of FIG. 13;

[0024] FIG. 15 to FIG. 17 are cross-sectional views of a portion of the liquid crystal display of FIG. 13;

[0025] FIG. 18 is a layout view of three pixels of a liquid crystal display according to an exemplary embodiment of the present invention;

[0026] FIG. 19 is a layout view of a portion of the liquid crystal display of FIG. 18;

[0027] FIG. 20 to FIG. 22 are cross-sectional views of a portion of the liquid crystal display of FIG. 18;

[0028] FIG. 23 is a layout view of three pixels of a liquid crystal display according to an exemplary embodiment of the present invention;

[0029] FIG. 24 is a layout view of a portion of the liquid crystal display of FIG. 23;

[0030] FIG. 25 is a layout view of three pixels of a liquid crystal display according to an exemplary embodiment of the present invention; and

[0031] FIG. 26 is a layout view of a portion of the liquid crystal display of FIG. 25.

DETAILED DESCRIPTION OF EXEMPLARY EMBODIMENTS

[0032] Exemplary embodiments of the present invention will be described in more detail hereinafter with reference to the accompanying drawings. As those skilled in the art would realize, the described embodiments may be modified in various different ways.

[0033] In the drawings, the thickness of layers, films, panels, regions, etc., may be exaggerated for clarity. Like reference numerals may designate like or similar elements throughout the specification and the drawings. It will be understood that when an element is referred to as being “on,” “connected to,” or “coupled to” another element, it can be directly on, connected to or coupled to the other element or intervening elements may also be present.

[0034] As used herein, the singular forms “a,” “an,” and “the” are intended to include the plural forms as well, unless the context clearly indicates otherwise.

[0035] FIG. 1 is a layout view illustrating one pixel of a liquid crystal display according to an exemplary embodiment of the present invention, and FIG. 2 is a cross-sectional view taken along line II-II of FIG. 1.

[0036] A liquid crystal display according to an exemplary embodiment of the present invention includes a lower panel 100 and an upper panel 200, and a liquid crystal layer 3 interposed between the two display panels 100 and 200.

[0037] A plurality of gate lines 121 and reference voltage lines 131 is formed on a first insulation substrate 110.

[0038] The gate lines 121 carry gate signals and extend in a transverse direction of the liquid crystal display. Each gate line 121 includes a plurality of gate electrodes 124.

[0039] The reference voltage lines 131 extend parallel to the gate lines 121 and apply a voltage of a predetermined magnitude to a common electrode. Each reference voltage line 131 includes a plurality of expansions 135.

[0040] A gate insulating layer 140 is formed on the gate line 121 and the reference voltage line 131. The gate insulating layer 140 may be formed of an inorganic insulator such as silicon nitride (SiN_x) or silicon oxide (SiO_x).

[0041] A plurality of semiconductor layers 154 are formed on the gate insulating layer 140. The semiconductor layers 154 may include an oxide semiconductor.

[0042] A plurality of ohmic contacts 163 and 165 is formed on the semiconductor layers 154. The ohmic contacts 163 and 165 face each other with respect to the gate electrode 124 and are disposed in a pair on the semiconductor layer 154. The ohmic contacts 163 and 165 may be formed of a material such as n⁺ hydrogenated amorphous silicon doped with a high-concentration n-type impurity such as phosphorus, or the ohmic contacts 163 and 165 may be formed of silicide. According to an exemplary embodiment of the present invention, the semiconductor layer 154 may include an oxide semiconductor, and in this case, the ohmic contacts 163 and 165 may be omitted.

[0043] A data conductor including a plurality of data lines 171 and a plurality of drain electrodes 175 is formed on the ohmic contacts 163 and 165.

[0044] The data lines 171 carry data signals and extend substantially in a longitudinal direction of the liquid crystal display. The data lines 171 intersect the gate lines 121. Each data line 171 includes a plurality of source electrodes 173 that are extended toward the gate electrode 124. The data line 171 includes one or more bends, and the data line 171 forms an oblique angle with respect to the extending direction of the gate line 121. The oblique angle between the data line 171 and the extending direction of the gate line 121 may be about 45 degrees or more. Alternatively, the data line 171 may be extended in a straight line without any bends.

[0045] The drain electrode 175 includes a first end shaped as a bar that faces the source electrode 173 with respect to the gate electrode 124 and a second end having a larger area than the first end.

[0046] The gate electrode 124, the source electrode 173, and the drain electrode 175, together with the semiconductor layer 154, may form a thin film transistor (TFT) that may function as a switching element. The semiconductor layer 154 may have substantially the same plane shape as the data line 171, the drain electrode 175, and the underlying ohmic contacts 163 and 165 except for the semiconductor protrusion 154 of the thin film transistor.

[0047] A first passivation layer 180p is positioned on the data conductors 171 and 175 and the exposed semiconductor layer 154, and the first passivation layer 180p may be formed of an organic insulating material or an inorganic insulating material.

[0048] According to an exemplary embodiment of the present invention, a plurality of color filters may be positioned on the first passivation layer 180p.

[0049] A reference electrode 270 is formed on the first passivation layer 180p. The reference electrode 270 may be formed of a transparent conductive material such as ITO (Indium Tin Oxide) or IZO (Indium Zinc Oxide). The reference electrode 270 with a planar shape may be formed on an

entire surface of the substrate 110 and may have an opening 138 disposed at a region corresponding to a periphery of the drain electrode 175.

[0050] Reference electrodes 270 respectively positioned in adjacent pixels may be connected to each other.

[0051] The reference electrode 270 may receive a reference voltage from the reference voltage line 131 through a first contact hole 183 formed in the gate insulating layer 140 and the first passivation layer 180p.

[0052] Alternatively, the reference electrode 270 may receive the reference voltage from a reference voltage application unit formed in a peripheral area outside the display area including a plurality of pixels.

[0053] A second passivation layer 180q is formed on the reference electrode 270. The second passivation layer 180q may be formed of an organic insulating material or an inorganic insulating material.

[0054] A pixel electrode 191 is formed on the second passivation layer 180q. The pixel electrode 191 includes a plurality of branch electrodes 192 substantially parallel to each other and separated from each other, and lower and upper transverse portions connecting upper and lower ends of the branch electrodes 192. The branch electrodes 192 of the pixel electrode 191 may be bent along the data lines 171. Alternatively, the data line 171 and the branch electrode 192 of the pixel electrode 191 may be extended in a straight line. The pixel electrode 191 may be formed of a transparent conductive material such as ITO or IZO.

[0055] The first passivation layer 180p and the second passivation layer 180q have a plurality of contact holes 185 exposing the drain electrode 175, and the pixel electrode 191 is electrically connected to the drain electrode 175 through the contact hole 185, thereby receiving a data voltage. The contact hole 185 is formed at a position corresponding to the opening 138 of the reference electrode 270.

[0056] The pixel electrode 191 applied with a data voltage and the reference electrode 270 applied with a reference voltage, such as a common voltage, generate an electric field in the liquid crystal layer 3.

[0057] The branch electrode 192 of the pixel electrode 191 overlaps the reference electrode 131 having the plane shape.

[0058] Alternatively, the reference electrode 270 covers each of a plurality of data lines 171 and overlaps the data lines 171. Accordingly, crosstalk may be reduced between the data line 171 and the pixel electrode 191, and light leakage caused by parasitic capacitance between the data line 171 and the neighboring pixel electrode 191 may be reduced.

[0059] A first alignment layer is coated on an inner surface of the lower panel 100.

[0060] A light blocking member 220 is formed on the second insulation substrate 210. The light blocking member 220 is also referred to as a black matrix and prevents light leakage.

[0061] A plurality of color filters 230 are formed on a second insulation substrate 210. The color filters 230 are mainly formed in a region enclosed by the light blocking member 220 and may extend in a predetermined direction, e.g., along the extending direction of the data line 171.

[0062] Each of the color filters 230 may display a primary color, for example, red, green, and blue, or each color filter 230 may display yellow, cyan, or magenta. The color filters may further include a color filter displaying a mixture of the primary colors or white. The color filters 230 are formed of an organic material. Each color filter 230 may be extended along

the data line 171, and two neighboring color filters 230 may overlap each other on the boundary of the data line 171.

[0063] According to an exemplary embodiment of the present invention, the color filters 230 may be formed on the first passivation layer 180p of the first insulation substrate 110, and in this case, the light blocking member 220 may also be formed on the first insulation substrate 110.

[0064] A second alignment layer is coated on an inner surface of the upper panel 200.

[0065] The first alignment layer and the second alignment layer may be horizontal alignment layers.

[0066] The liquid crystal layer 3 interposed between the lower panel 100 and the upper panel 200 includes liquid crystal molecules. The liquid crystal molecules may be aligned so that a major axis thereof is substantially parallel with the surfaces of the two display panels 100 and 200 when no electric field is applied to the liquid crystal layer 3.

[0067] The liquid crystal layer 3 may have positive dielectric anisotropy or negative dielectric anisotropy. The liquid crystal molecules of the liquid crystal layer 3 may be aligned to have a pretilt in a predetermined direction. The pretilt direction of the liquid crystal molecules may be changed according to the dielectric anisotropy of the liquid crystal layer 3.

[0068] A backlight unit generating light and providing the light to the two display panels 100 and 200 may be provided outside the substrate 110 of the lower panel 100.

[0069] The pixel electrode 191 is applied with a data voltage, and the common electrode 131 is applied with a common voltage. The pixel electrode 191 and the common electrode 131 generate an electric field in the liquid crystal layer 3, thereby determining the direction of the liquid crystal molecules in the liquid crystal layer 3 and displaying a corresponding image.

[0070] In the exemplary embodiment described above in connection with FIG. 1 and FIG. 2, the reference electrode 270 and the pixel electrode 191, which are two field generating electrodes, respectively, overlap each other with the insulating layer disposed between the reference electrode 270 and the pixel electrode 191. The reference electrode 270 is positioned under the insulating layer, and the pixel electrode 191 is positioned on the insulating layer. However, exemplary embodiments of the present invention are not limited thereto. Alternatively, the pixel electrode 191 may be positioned under the insulating layer, and the reference electrode 270 may be positioned on the insulating layer. In the exemplary embodiment described above in connection with FIG. 1 and FIG. 2, the pixel electrode 191 has a plurality of branch electrodes 192. However, exemplary embodiments of the present invention are not limited thereto. Alternatively, the reference electrode 270 may have a plurality of branch electrodes.

[0071] In the exemplary embodiment described above in connection with FIG. 1 and FIG. 2, two field generating electrodes overlap each other. For example, the reference electrode 270 and the pixel electrode 191 overlap each other with the insulating layer disposed between the reference electrode 270 and the pixel electrode 191. One of the two field generating electrodes is a plate-type electrode, and the other has the branches. However, exemplary embodiments of the present invention are not limited thereto, and other types of field generating electrodes may be used.

[0072] FIG. 3 is a layout view of three pixels of a liquid crystal display according to an exemplary embodiment of the

present invention, FIG. 4 is a layout view of a portion of the liquid crystal display of FIG. 3, and FIG. 5 to FIG. 7 are cross-sectional views of a portion of the liquid crystal display of FIG. 3.

[0073] The structure of each pixel illustrated in FIGS. 3 to 7 is substantially the same as the pixel of the liquid crystal display described above with reference to FIG. 1 and FIG. 2.

[0074] Referring to FIG. 3, the liquid crystal display according to an exemplary embodiment of the present invention includes a first pixel PXA, a second pixel PXB, and a third pixel PXC that are disposed adjacent to each other and that display different colors from each other. The first pixel PXA, the second pixel PXB, and the third pixel PXC are sequentially arranged in a repeating pattern. For example, the third pixel PXC and the second pixel PXB may be positioned at both sides of the first pixel PXA, the first pixel PXA and the third pixel PXC may be positioned at both sides of the second pixel PXB, or the second pixel PXB and the first pixel PXA may be positioned at both sides of the third pixel PXC.

[0075] The first pixel PXA, the second pixel PXB, and the third pixel PXC each include a pixel electrode 191 including a plurality of branch electrodes 192.

[0076] In the first pixel PXA, an interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 is a first interval G1. In the second pixel PXB, an interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 is a second interval G2. Similarly, in the third pixel PXC, an interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 is a third interval G3. Here, the first interval G1 and the third interval G3 are substantially equal to each other, and the second interval G2 is smaller than the first interval G1 and the third interval G3.

[0077] For example, among the first pixel PXA, the second pixel PXB, and the third pixel PXC that display different colors from each other, at least one pixel, for example, the second pixel PXB may have the interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 further reduced as compared with the other pixels (e.g., pixels PXA and PXC).

[0078] Referring to FIG. 4, the first pixel PXA, the second pixel PXB and the third pixel PXC are respectively enclosed by a first light blocking member 220A, a second light blocking member 220B, and a third light blocking member 220C.

[0079] The first light blocking member 220A, the second light blocking member 220B, and the third light blocking member 220C are divided based on longitudinal center lines of the data lines 171 as indicated by dotted lines in FIG. 4. For example, the first light blocking member 220A is positioned close to the first pixel PXA between the longitudinal center lines of two data lines 171 positioned at both sides of the first pixel PXA, the second light blocking member 220B is positioned close to the second pixel PXB between the longitudinal center lines of two data lines 171 positioned at both sides of the second pixel PXB, and the third light blocking member 220C is positioned close to the third pixel PXC between the longitudinal center lines of two data lines 171 positioned at both sides of the third pixel PXC.

[0080] Referring to FIG. 4, a width between the first light blocking member 220A and the third light blocking member 220C positioned between the first pixel PXA and the third

pixel PXC adjacent to each other is referred to as a first width W1, a width between the first light blocking member 220A and the second light blocking member 220B positioned between the first pixel PXA and the second pixel PXB adjacent to each other is referred to as a second width W2, and a width between the second light blocking member 220B and the third light blocking member 220C positioned between the second pixel PXB and the third pixel PXC adjacent to each other is referred to as a third width W3. Here, the first width W1, the second width W2, and the third width W3 may be substantially equal to each other.

[0081] Referring to FIG. 5 to FIG. 7, the first color filter 230A displaying a first color is positioned in the first pixel PXA, the second color filter 230B displaying a second color is positioned in the second pixel PXB, and the third color filter 230C displaying a third color is positioned in the third pixel PXC. When the first color and the third color are mixed with the second color, a mixed color may be recognized. For example, the first color may be red, the second color may be green, and the third color may be blue. When blue is mixed with green, when red is mixed with green, or when blue and red are mixed with each other, the mixed color may be recognized. However, the first color, the second color, and the third color are not limited thereto, and may be changed according to the color characteristics of the backlight of the liquid crystal display.

[0082] Referring to FIG. 5, with respect to the data line 171 positioned between the first pixel PXA and the second pixel PXB, the first interval G1 between the pixel electrode 191 of the first pixel PXA and the data line 171 is larger than the second interval G2 between the pixel electrode 191 of the second pixel PXB and the data line 171. Accordingly, when observing the second pixel PXB, first light L1 displaying a first color passing through the first color filter 230A and the pixel electrode 191 of the first pixel PXA is covered by the light blocking member 220 and is not recognized. Therefore, light passing through the pixel electrode 191 of the second pixel PXB and the first light L1 are not mixed.

[0083] When the interval between the data line 171 positioned between the first pixel PXA and the second pixel PXB and the pixel electrode 191 of the first pixel PXA is substantially the same as the interval between the data line 171 and the pixel electrode 191 of the second pixel PXB, when observing at a side of the second pixel PXB, second light L2 passing through the pixel electrode 191 of the first pixel PXA at an angle of less than 90 degrees with respect to the second insulation substrate 210 is not covered by the light blocking member 220 and is recognized in the second pixel PXB, and thereby the second light L2 is mixed with light passing through the second color filter 230B of the second pixel PXB and may be recognized.

[0084] According to an exemplary embodiment of the present invention, in the pixel where the color mixture may be recognized between the colors displayed by the pixels adjacent to each other, the interval between the data line 171 and the pixel electrode 191 is increased and thus the color mixture may be prevented from occurring between the adjacent pixels of the liquid crystal display, thus preventing a deterioration of the display quality due to the color mixture.

[0085] Similarly, as shown in FIG. 6, with respect to the data line 171 positioned between the second pixel PXB and the third pixel PXC, the third interval G3 between the pixel electrode 191 of the third pixel PXC and the data line 171 is

larger than the second interval G2 between the pixel electrode 191 of the second pixel PXB and the data line 171.

[0086] Accordingly, when observing the second pixel PXB, light displaying a third color passing through the third color filter 230C and the pixel electrode 191 of the third pixel PXC is covered by the light blocking member 220 and is not recognized. Therefore, light passing through the pixel electrode 191 of the second pixel PXB and light displaying the third color through the third color filter 230C are not mixed.

[0087] Referring to FIG. 7, with respect to the data line 171 positioned between the third pixel PXC and the first pixel PXA, the third interval G3 between the pixel electrode 191 of the third pixel PXC and the data line 171 is substantially equal to the first interval G1 between the pixel electrode 191 of the first pixel PXA and the data line 171, and the third interval G2 and the first interval G1 are larger than the second interval G2. Accordingly, the third color displayed by the third pixel PXC and the first color displayed by the first pixel PXA are not mixed. Therefore, a deterioration of the display quality due to the color mixture between the first pixel PXA displaying the first color and the third pixel PXC displaying the third color adjacent to each other may be prevented.

[0088] Referring back to FIG. 4 to FIG. 7, the first light blocking member 220A and the third light blocking member 220C positioned between the first pixel PXA and the third pixel PXC adjacent to each other have approximately the same width with respect to the longitudinal center line of the data line 171. For example, the first light blocking member 220A and the third light blocking member 220C forming the first width W1 have substantially the same width. Similarly, the first light blocking member 220A and the second light blocking member 220B positioned between the first pixel PXA and the second pixel PXB adjacent to each other have substantially the same width with respect to the longitudinal center line of the data line 171. For example, the first light blocking member 220A and the second light blocking member 220B forming the second width W2 have substantially the same width. The second light blocking member 220B and the third light blocking member 220C positioned between the second pixel PXB and the third pixel PXC adjacent to each other have substantially the same width with respect to the longitudinal center line of the data line 171. For example, the second light blocking member 220B and the third light blocking member 220C forming the third width W3 have substantially the same width. The first width W1, the second width W2, and the third width W3 may be substantially equal to each other.

[0089] The width of the plurality of branch electrodes 192 of the pixel electrode 191 in the first pixel PXA may be substantially the same as the width of the plurality of branch electrodes 192 of the pixel electrode 191 in the second pixel PXB or third pixel PXC, and the interval between the plurality of branch electrodes 192 of the pixel electrode 191 in the first pixel PXA may be substantially equal to the interval between the plurality of branch electrodes 192 of the pixel electrode 191 in the second pixel PXB or third pixel PXC. The number of the plurality of branch electrodes 192 of the pixel electrode 191 in the second pixel PXB may be larger than the number of the plurality of branch electrodes 192 of the pixel electrode 191 in the first pixel PXA or the third pixel PXC. As such, by reducing the number of the plurality of branch electrodes 192 in the first pixel PXA or the third pixel PXC where mixed colors are

recognized, luminance may be relatively decreased, thereby preventing the colors from being mixed in the second pixel PXB.

[0090] FIG. 8 is a layout view of three pixels of a liquid crystal display according to an exemplary embodiment of the present invention, FIG. 9 is a layout view of a portion of the liquid crystal display of FIG. 8, and FIG. 10 to FIG. 12 are cross-sectional views of a portion of the liquid crystal display of FIG. 8.

[0091] The structure of each pixel according to an exemplary embodiment is substantially the same as the pixel of the liquid crystal display described above with reference to FIG. 1 and FIG. 2.

[0092] Referring to FIG. 8, the liquid crystal display according to an exemplary embodiment includes the first pixel PXA, the second pixel PXB, and the third pixel PXC adjacent to each other and displaying different colors from each other. The first pixel PXA, the second pixel PXB, and the third pixel PXC are sequentially arranged in a repeating pattern. For example, the third pixel PXC and the second pixel PXB may be positioned at both sides of the first pixel PXA, the first pixel PXA and the third pixel PXC may be positioned at both sides of the second pixel PXB, and the second pixel PXB and the first pixel PXA may be positioned at both sides of the third pixel PXC.

[0093] The first pixel PXA, the second pixel PXB, and the third pixel PXC each include the pixel electrode 191 including a plurality of branch electrodes 192.

[0094] In the first pixel PXA, an interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 is the first interval G1. In the second pixel PXB, an interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 is the second interval G2. Similarly, in the third pixel PXC, an interval between an edge of the pixel electrode 191, which is substantially parallel to the data line 171, and the adjacent data line 171 is the third interval G3.

[0095] The first interval G1, the second interval G2, and the third interval G3 are substantially equal to each other.

[0096] Referring to FIG. 9, the first pixel PXA, the second pixel PXB, and the third pixel PXC are respectively enclosed by the first light blocking member 220A, the second light blocking member 220B, and the third light blocking member 220C.

[0097] The first light blocking member 220A, the second light blocking member 220B, and the third light blocking member 220C are divided based on longitudinal center lines of the data lines 171 as indicated by dotted lines in FIG. 9. For example, the first light blocking member 220A is positioned close to the first pixel PXA between the longitudinal center lines of two data lines 171 positioned at both sides of the first pixel PXA, the second light blocking member 220B is positioned close to the second pixel PXB between the longitudinal center lines of two data lines 171 positioned at both sides of the second pixel PXB, and the third light blocking member 220C is positioned close to the third pixel PXC between the longitudinal center lines of two data lines 171 positioned at both sides on the basis of the third pixel PXC.

[0098] A width of the first light blocking member 220A and the third light blocking member 220C positioned between the first pixel PXA and the third pixel PXC adjacent to each other is referred to as the first width W1, a width of the first light blocking member 220A and the second light blocking mem-

ber 220B positioned between the first pixel PXA and the second pixel PXB adjacent to each other is referred to as the second width W2, and a width of the second light blocking member 220B and the third light blocking member 220C positioned between the second pixel PXB and the third pixel PXC adjacent to each other is referred to as the third width W3. The first width W1, the second width W2, and the third width W3 may be substantially equal to each other.

[0099] Referring to FIG. 10 to FIG. 12, the first color filter 230A displaying the first color is positioned in the first pixel PXA, the second color filter 230B displaying the second color is positioned in the second pixel PXB, and the third color filter 230C displaying the third color is positioned in the third pixel PXC. When the first color and the third color are mixed with the second color, a mixed color may be recognized. For example, the first color may be red, the second color may be green, and the third color may be blue. When blue is mixed with green, when red is mixed with green, or when blue and red are mixed with each other, the mixed color may be recognized. However, the first color, the second color, and the third color are not limited thereto, and may be changed according to color characteristics of the backlight of the liquid crystal display.

[0100] With the data line 171 positioned between the first pixel PXA and the second pixel PXB, the first interval G1 between the pixel electrode 191 of the first pixel PXA and the data line 171 is substantially equal to the second interval G2 between the pixel electrode 191 of the second pixel PXB and the data line 171. With respect to the data line 171 positioned between the second pixel PXB and the third pixel PXC, the third interval G3 between the pixel electrode 191 of the third pixel PXC and the data line 171 is substantially equal to the second interval G2 between the pixel electrode 191 of the second pixel PXB and the data line 171. Similarly, with respect to the data line 171 positioned between the third pixel PXC and the first pixel PXA, the third interval G3 between the pixel electrode 191 of the third pixel PXC and the data line 171 is substantially equal to the first interval G1 between the pixel electrode 191 of the first pixel PXA and the data line 171.

[0101] The width of the second light blocking member 220B is larger than the width of the first light blocking member 220A and the width of the third light blocking member 220C.

[0102] Referring to FIG. 9 and FIG. 10, the first light blocking member 220A and the second light blocking member 220B positioned between the first pixel PXA and the second pixel PXB adjacent to each other have different widths from each other with respect to the longitudinal center line of the data line 171. Of the first light blocking member 220A and the second light blocking member 220B that form the second width W2, the first blocking member 220A has the width B1 which is smaller than the width B2 of the second light blocking member 220B. Accordingly, when observing the second pixel PXB, the third light L3 displaying the first color through the pixel electrode 191 of the first pixel PXA is covered by the second light blocking member 220B having the relatively large width B2, and accordingly, the third light L3 is not mixed with light displaying the second color through the pixel electrode 191 of the second pixel PXB.

[0103] Referring to FIG. 9 and FIG. 11, the second light blocking member 220B and the third light blocking member 220C positioned between the second pixel PXB and the third pixel PXC adjacent to each other have different widths from

each other with respect to the longitudinal center line of the data line 171. Of the second light blocking member 220B and the third light blocking member 220C that form the third width W3, the second blocking member 220B has the width B2 which is larger than the width B3 of the third light blocking member 220C. Accordingly, when observing the second pixel PXB, the fourth light L4 displaying the third color through the pixel electrode 191 of the third pixel PXC is covered by the second light blocking member 220B having the relatively large width B2, and accordingly, the fourth light L4 is not mixed with light displaying the second color through the pixel electrode 191 of the second pixel PXB.

[0104] Referring to FIG. 9 and FIG. 12, the third light blocking member 220C and the first light blocking member 220A positioned between the third pixel PXC and the first pixel PXA adjacent to each other have substantially the same width with respect to longitudinal center line of the data line 171. Of the third light blocking member 220C and the first light blocking member 220A that form the first width W1, the third light blocking member 220C has the width B3 substantially equal to the width B1 of the first light blocking member 220A. The width B3 of the third light blocking member 220C and the width B1 of the first light blocking member 220A are smaller than the width B2 of the second light blocking member 220B. Alternatively, the third light blocking member 220C and the first light blocking member 220A positioned between the third pixel PXC and the adjacent first pixel PXA may have a relatively large width. Accordingly, the third color displayed by the third pixel PXC and the first color displayed by the first pixel PXA are not mixed. Therefore, a deterioration of the display quality due to the color mixture between the first pixel PXA and the third pixel PXC that are adjacent to each other and that displaying the first color and the third color, respectively, may be prevented.

[0105] As such, in the pixel where the color mixture may be recognized between the colors displayed by the pixels adjacent to each other, the width of the light blocking member is increased, and thus, the color mixture may be prevented between the pixels adjacent to each other, thus preventing the display quality from being deteriorated due to the color mixture.

[0106] The width of the plurality of branch electrodes 192 of the pixel electrode 191 in the first pixel PXA may be substantially the same as the width of the plurality of branch electrodes 192 of the pixel electrode in the second pixel PXB or the third pixel PXC, and the interval between the plurality of branch electrodes 192 of the pixel electrode 191 in the first pixel PXA may be substantially equal to the interval of the plurality of branch electrodes 192 of the pixel electrode 191 in the second pixel PXB or the third pixel PXC. The number of the plurality of branch electrodes 192 of the pixel electrode in the second pixel PXB may be larger than the number of the plurality of branch electrodes 192 of the pixel electrode 191 in the first pixel PXA or the third pixel PXC. By reducing the number of the plurality of branch electrodes 192 in the first pixel PXA or the third pixel PXC where the color mixture may be recognized, luminance may be relatively decreased, thereby preventing the color mixture of the second pixel PXB.

[0107] FIG. 13 is a layout view of three pixels of a liquid crystal display according to an exemplary embodiment of the present invention, and FIG. 14 is a layout view of a portion of the liquid crystal display of FIG. 13. FIG. 15 to FIG. 17 are cross-sectional views of a portion of the liquid crystal display of FIG. 13.

[0108] Referring to FIG. 13, the liquid crystal display according to an exemplary embodiment includes the first pixel PXA, the second pixel PXB, and the third pixel PXC that are adjacent to each other and that display the different colors from each other. The first pixel PXA, the second pixel PXB, and the third pixel PXC are sequentially arranged and are repeatedly positioned. For example, the third pixel PXC and the second pixel PXB may be positioned at both sides of the first pixel PXA, the first pixel PXA and the third pixel PXC may be positioned at both sides of the second pixel PXB, and the second pixel PXB and the first pixel PXA may be positioned at both sides of the third pixel PXC.

[0109] The first pixel PXA, the second pixel PXB, and the third pixel PXC each include the pixel electrode 191 including a plurality of branch electrodes 192.

[0110] In the first pixel PXA, an interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 is the first interval G1. In the second pixel PXB, an interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 is the second interval G2. Similarly, in the third pixel PXC, an interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 is the third interval G3.

[0111] The first interval G1, the second interval G2, and the third interval G3 are substantially equal to each other.

[0112] Referring to FIG. 14, the first pixel PXA, the second pixel PXB, and the third pixel PXC are respectively enclosed by the first light blocking member 220A, the second light blocking member 220B, and the third light blocking member 220C.

[0113] The first light blocking member 220A, the second light blocking member 220B, and the third light blocking member 220C are divided based on longitudinal center lines of the data lines 171 as indicated by dotted lines in FIG. 14. For example, the first light blocking member 220A is positioned close to the first pixel PXA between the longitudinal center lines of two data lines 171 positioned at both sides of the first pixel PXA, the second light blocking member 220B is positioned close to the second pixel PXB between the longitudinal center lines of two data lines 171 positioned at both sides of the second pixel PXB, and the third light blocking member 220C is positioned close to the third pixel PXC between the longitudinal center lines of two data lines 171 positioned at both sides of the third pixel PXC.

[0114] A width of the first light blocking member 220A and the third light blocking member 220C positioned between the first pixel PXA and the third pixel PXC adjacent to each other is referred to as the first width W1, a width of the first light blocking member 220A and the second light blocking member 220B positioned between the first pixel PXA and the second pixel PXB adjacent to each other is referred to as the second width W2, and a width of the second light blocking member 220B and the third light blocking member 220C positioned between the second pixel PXB and the third pixel PXC adjacent to each other is referred to as the third width W3. The first width W1 and the third width W3 may be substantially equal to each other, and the second width W2 may be larger than the first width W1 and the third width W3.

[0115] Referring to FIG. 15 to FIG. 17, the first color filter 230A displaying the first color is positioned in the first pixel PXA, the second color filter 230B displaying the second color is positioned in the second pixel PXB, and the third color filter

230C displaying the third color is positioned in the third pixel PXC. When the first color and the third color are mixed with the second color, a mixed color may be recognized. For example, the first color may be red, the second color may be green, and the third color may be blue. When blue is mixed with green, when red with mixed to green, or when blue and red are mixed with each other, the mixed color may be recognized. However, the first color, the second color, and the third color are not limited thereto, and may be changed according to color characteristics of the backlight of the liquid crystal display.

[0116] With respect to the data line **171** positioned between the first pixel PXA and the second pixel PXB, the first interval G1 between the pixel electrode **191** of the first pixel PXA and the data line **171** is substantially equal to the second interval G2 between the pixel electrode **191** of the second pixel PXB and the data line **171**. With respect to the data line **171** positioned between the second pixel PXB and the third pixel PXC, the third interval G3 between the pixel electrode **191** of the third pixel PXC and the data line **171** is substantially equal to the second interval G2 between the pixel electrode **191** of the second pixel PXB and the data line **171**. Similarly, with respect to the data line **171** positioned between the third pixel PXC and the first pixel PXA, the third interval G3 between the pixel electrode **191** of the third pixel PXC and the data line **171** is substantially equal to the first interval G1 between the pixel electrode **191** of the first pixel PXA and the data line **171**.

[0117] The width B2 of the second light blocking member **220B** is larger than the width B1 of the first light blocking member **220A** and the width B3 of the third light blocking member **220C**.

[0118] Referring to FIG. 14 and FIG. 15, the first light blocking member **220A** and the second light blocking member **220B** positioned between the first pixel PXA and the second pixel PXB adjacent to each other have different widths from each other with respect to the longitudinal center line of the data line **171**. Of the first light blocking member **220A** and the second light blocking member **220B** that form the second width W2, the second light blocking member **220B** has the width B2 which is smaller than the width B1 of the first light blocking member **220A**. Accordingly, when observing the second pixel PXB, the third light L3 displaying the first color through the pixel electrode **191** of the first pixel PXA is covered by the second light blocking member **220B** having the relatively large width B2, and accordingly, the third light L3 is not mixed with light displaying the second color through the pixel electrode **191** of the second pixel PXB.

[0119] Referring to FIG. 14 and FIG. 16, the second light blocking member **220B** and the third light blocking member **220C** positioned between the second pixel PXB and the third pixel PXC adjacent to each other have different widths from each other with respect to the longitudinal center line of the data line **171**. Of the second light blocking member **220B** and the third light blocking member **220C** that form the third width W3, the second light blocking member **220B** has the width B2 which is larger than the width B3 of the third light blocking member **220C**. Accordingly, when observing the second pixel PXB, the fourth light L4 displaying the third color through the pixel electrode **191** of the third pixel PXC is covered by the second light blocking member **220B** having the relatively large width B2, and accordingly, the fourth light L4 is not mixed with light displaying the second color through the pixel electrode **191** of the second pixel PXB.

[0120] Referring to FIG. 14 and FIG. 17, the third light blocking member **220C** and the first light blocking member **220A** positioned between the third pixel PXC and the first pixel PXA adjacent to each other have substantially the same width with respect to the longitudinal center line of the data line **171**. Of the third light blocking member **220C** and the first light blocking member **220A** that form the first width W1, the third light blocking member **220C** has the width B3 substantially equal to the width B1 of the first light blocking member **220A**. The width B3 of the third light blocking member **220C** and the width B1 of the first light blocking member **220A** are smaller than the width of the second light blocking member **220B**. Alternatively, the third light blocking member **220C** and the first light blocking member **220A** positioned between the third pixel PXC and the first pixel PXA adjacent to each other may have a relatively large width. Accordingly, the third color displayed by the third pixel PXC and the first color displayed by the first pixel PXA are not mixed. Therefore, a deterioration of the display quality due to the color mixture between the first pixel PXA and the third pixel PXC that are adjacent to each other and that display the first color and the third color, respectively, may be prevented.

[0121] As such, in the pixel where the color mixture may be recognized between the colors displayed by the pixels adjacent to each other, the width of the light blocking member is increased, and thus, the color mixture may be prevented between the pixels adjacent to each other, thus preventing the display quality from being deteriorated due to the color mixture.

[0122] The width of the plurality of branch electrodes **192** of the pixel electrode **191** in the first pixel PXA may be substantially the same as the width of the plurality of branch electrodes **192** of the pixel electrode **191** in the second pixel PXB and third pixel PXC, and the interval between the plurality of branch electrodes **192** of the pixel electrode **191** in the first pixel PXA may be substantially equal to the interval between the plurality of branch electrodes **192** of the pixel electrode **191** in the second pixel PXB or the third pixel PXC. The number of the plurality of branch electrodes **192** of the pixel electrode **191** in the second pixel PXB may be larger than the number of the plurality of branch electrodes **192** of the pixel electrode **191** in the first pixel PXA or the third pixel PXC. As such, by reducing the number of the plurality of branch electrodes **192** in the first pixel PXA or the third pixel PXC where the color mixture is recognized, luminance may be relatively decreased, thereby preventing the color mixture in the second pixel PXB.

[0123] FIG. 18 is a layout view of three pixels of a liquid crystal display according to an exemplary embodiment of the present invention, and FIG. 19 is a layout view of a portion of the liquid crystal display of FIG. 18. FIG. 20 to FIG. 22 are cross-sectional views of a portion of the liquid crystal display of FIG. 18.

[0124] Referring to FIG. 18, the liquid crystal display according to an exemplary embodiment includes the first pixel PXA, the second pixel PXB, and the third pixel PXC that are adjacent to each other and that displaying different colors from each other. The first pixel PXA, the second pixel PXB, and the third pixel PXC are sequentially arranged in a repeating pattern. For example, the third pixel PXC and the second pixel PXB may be positioned at both sides of the first pixel PXA, the first pixel PXA and the third pixel PXC may be positioned at both sides of the second pixel PXB, and the

second pixel PXB and the first pixel PXA may be positioned at both sides of the third pixel PXC.

[0125] The first pixel PXA, the second pixel PXB, and the third pixel PXC each include the pixel electrode 191 including the plurality of branch electrodes 192.

[0126] In the first pixel PXA, an interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 is the first interval G1. In the second pixel PXB, an interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 is the second interval G2. Similarly, in the third pixel PXC, an interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 is the third interval G3. The first interval G1 and the third interval G3 are substantially equal to each other, and the second interval G2 is smaller than the first interval G1 and the third interval G3.

[0127] For example, among the first pixel PXA, the second pixel PXB, and the third pixel PXC that display different colors from each other, at least one pixel, for example, the second pixel PXB, has the interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 further reduced as compared with the other pixels (e.g., pixels PXA and PXC).

[0128] Referring to FIG. 19, the first pixel PXA, the second pixel PXB, and the third pixel PXC are respectively enclosed by the first light blocking member 220A, the second light blocking member 220B, and the third light blocking member 220C.

[0129] The first light blocking member 220A, the second light blocking member 220B, and the third light blocking member 220C are divided based on longitudinal center lines of the data lines 171 as indicated by dotted lines in FIG. 19. For example, the first light blocking member 220A is positioned close to the first pixel PXA between the longitudinal center lines of two data lines 171 positioned at both sides of the first pixel PXA, the second light blocking member 220B is positioned close to the second pixel PXB between the longitudinal center lines of two data lines 171 positioned at both sides of the second pixel PXB, and the third light blocking member 220C is positioned close to the third pixel PXC between the longitudinal center lines of two data lines 171 positioned at both sides of the third pixel PXC.

[0130] Referring to FIG. 19, a width of the first light blocking member 220A and the third light blocking member 220C positioned between the first pixel PXA and the third pixel PXC adjacent to each other is referred to as the first width W1, a width of the first light blocking member 220A and the second light blocking member 220B positioned between the first pixel PXA and the second pixel PXB adjacent to each other is referred to as the second width W2, and a width of the second light blocking member 220B and the third light blocking member 220C positioned between the second pixel PXB and the third pixel PXC adjacent to each other is referred to as the third width W3. The first width W1, the second width W2, and the third width W3 may be substantially equal to each other.

[0131] Referring to FIG. 20 to FIG. 22, the first color filter 230A displaying the first color is positioned in the first pixel PXA, the second color filter 230B displaying the second color is positioned in the second pixel PXB, and the third color filter 230C displaying the third color is positioned in the third pixel

PXC. When the first color and the third color are mixed with the second color, a mixed color may be recognized. For example, the first color may be red, the second color may be green, and the third color may be blue. When blue is mixed with green, when red is mixed with green, or when blue and red are mixed with each other, the mixed color may be recognized. However, the first color, the second color, and the third color are not limited thereto and may be changed according to color characteristics of the backlight of the liquid crystal display.

[0132] Referring to FIG. 20, with respect to the data line 171 positioned between the first pixel PXA and the second pixel PXB, the first interval G1 between the pixel electrode 191 of the first pixel PXA and the data line 171 is larger than the second interval G2 between the pixel electrode 191 of the second pixel PXB and the data line 171. Accordingly, when observing the second pixel PXB, light displaying the first color through the first color filter 230A and the pixel electrode 191 of the first pixel PXA is covered by the light blocking member 220B and is not recognized. Therefore, the light displaying the second color through the pixel electrode 191 of the second pixel PXB and the light displaying the first color are not mixed.

[0133] As such, in the pixel where the color mixture may be recognized between the colors displayed by the pixels adjacent to each other, the interval between the data line 171 and the pixel electrode 191 is increased, and thus, the color mixture may be prevented between the adjacent pixels of the liquid crystal display, thus preventing the display quality from being deteriorated due to the color mixture.

[0134] Similarly, as shown in FIG. 21, with respect to the data line 171 positioned between the second pixel PXB and the third pixel PXC, the third interval G3 between the pixel electrode 191 of the third pixel PXC and the data line 171 is larger than the second interval G2 between the pixel electrode 191 of the second pixel PXB and the data line 171.

[0135] Accordingly, when observing the second pixel PXB, the light displaying the third color through the third color filter 230C and the pixel electrode 191 of the third pixel PXC is covered by the second light blocking member 220B and is not recognized. Therefore, the light displaying the second color through the pixel electrode 191 of the second pixel PXB and the light displaying the third color through the third color filter 230C are not mixed.

[0136] Referring to FIG. 22, with respect to the data line 171 positioned between the third pixel PXC and the first pixel PXA, the third interval G3 between the pixel electrode 191 of the third pixel PXC and the data line 171 is substantially equal to the first interval G1 between the pixel electrode 191 of the first pixel PXA and the data line 171, and the third interval G2 and the first interval G1 are larger than the second interval G2. Accordingly, the third color displayed by the third pixel PXC and the first color displayed by the first pixel PXA are not mixed. Therefore, a deterioration of the display quality due to the color mixture between the first pixel PXA and the third pixel PXC that are adjacent to each other and that display the first color and the third color, respectively, may be prevented.

[0137] Referring back to FIG. 19 to FIG. 22, the width of the second light blocking member 220B is larger than the width of the first light blocking member 220A and the width of the third light blocking member 220C.

[0138] Referring to FIG. 19 and FIG. 20, the first light blocking member 220A and the second light blocking member 220B positioned between the first pixel PXA and the

second pixel PXB adjacent to each other have different widths from each other with respect to the longitudinal center line of the data line 171. Of the first light blocking member 220A and the second light blocking member 220B that form the second width W2, the first light blocking member 220A has the width B1 which is smaller than the width B2 of the second light blocking member 220B. Accordingly, when observing the second pixel PXB, the light displaying the first color through the pixel electrode 191 of the first pixel PXA is covered by the second light blocking member 220B having the relatively large width B2, and accordingly, the third light L3 is not mixed with light displaying the second color through the pixel electrode 191 of the second pixel PXB.

[0139] Referring to FIG. 19 and FIG. 21, the second light blocking member 220B and the third light blocking member 220C positioned between the second pixel PXB and the third pixel PXC adjacent to each other have different widths from each other with respect to the longitudinal center line of the data line 171. Of the second light blocking member 220B and the third light blocking member 220C that form the third width W3, the second light blocking member 220B has the width B2 which is larger than the width B3 of the third light blocking member 220C. Accordingly, when observing the second pixel PXB, the light displaying the third color through the pixel electrode 191 of the third pixel PXC is covered by the second light blocking member 220B having the relatively large width B2, and accordingly, the light is not mixed with light displaying the second color through the pixel electrode 191 of the second pixel PXB.

[0140] Referring to FIG. 19 and FIG. 22, the third light blocking member 220C and the first light blocking member 220A positioned between the third pixel PXC and the first pixel PXA adjacent to each other have substantially the same width with respect to the longitudinal center line of the data line 171. Of the third light blocking member 220C and the first light blocking member 220A that form the first width W1, the third light blocking member 220C has the width B3 substantially equal to the width B1 of the first light blocking member 220A. The width B3 of the third light blocking member 220C and the width B1 of the first light blocking member 220A are smaller than the width B2 of the second light blocking member 220B. Alternatively, the third light blocking member 220C and the first light blocking member 220A positioned between the third pixel PXC and the first pixel PXA adjacent to each other may have a relatively large width. Accordingly, the third color displayed by the third pixel PXC and the first color displayed by the first pixel PXA are not mixed. Therefore, a deterioration of the display quality due to the color mixture between the first pixel PXA and the third pixel PXC that are adjacent to each other and that display the first color and the third color, respectively, may be prevented.

[0141] As such, in the pixel where the color mixture may be recognized between the colors displayed by the pixels adjacent to each other, the width of the light blocking member is increased, and thus, the color mixture may be prevented between the pixels adjacent to each other, thus preventing the display quality from being deteriorated due to the color mixture.

[0142] The width of the plurality of branch electrodes 192 of the pixel electrode 191 in the first pixel PXA may be substantially the same as the width of the plurality of branch electrodes 192 of the pixel electrode 191 in the second pixel PXB or third pixel PXC, and the interval between the plurality

of branch electrodes 192 of the pixel electrode 191 in the first pixel PXA may be substantially equal to the interval between the plurality of branch electrodes 192 of the pixel electrode 191 in the second pixel PXB or the third pixel PXC. The number of the plurality of branch electrodes 192 of the pixel electrode 191 in the second pixel PXB may be larger than the number of the plurality of branch electrodes 192 of the pixel electrode 191 in the first pixel PXA or the third pixel PXC. As such, by reducing the number of the plurality of branch electrodes 192 of the first pixel PXA or the third pixel PXC where the color mixture is recognized, luminance may be relatively decreased, thereby preventing the color mixture in the second pixel PXB.

[0143] FIG. 23 is a layout view of three pixels of a liquid crystal display according to another exemplary embodiment of the present invention, and FIG. 24 is a layout view of a portion of the liquid crystal display of FIG. 23.

[0144] Referring to FIG. 23, the liquid crystal display according to an exemplary embodiment includes the first pixel PXA, the second pixel PXB, and the third pixel PXC that are adjacent to each other and that display different colors from each other. The first pixel PXA, the second pixel PXB, and the third pixel PXC are sequentially arranged in a repeating pattern. For example, the third pixel PXC and the second pixel PXB may be positioned at both sides of the first pixel PXA, the first pixel PXA and the third pixel PXC may be positioned at both sides of the second pixel PXB, and the second pixel PXB and the first pixel PXA may be positioned at both sides of the third pixel PXC.

[0145] The first pixel PXA, the second pixel PXB, and the third pixel PXC each include the pixel electrode 191 including the plurality of branch electrodes 192.

[0146] In the first pixel PXA, an interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 is the first interval G1. In the second pixel PXB, an interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 is the second interval G2. Similarly, in the third pixel PXC, an interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 is the third interval G3. The first interval G1 and the third interval G3 are substantially equal to each other, and the second interval G2 is smaller than the first interval G1 and the third interval G3.

[0147] For example, among the first pixel PXA, the second pixel PXB, and the third pixel PXC that display different colors from each other, at least one pixel, that is, e.g., the second pixel PXB, may have the interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171 and the adjacent data line 171 further reduced as compared with the other pixels (e.g., pixels PXA and PXC).

[0148] Referring to FIG. 24, the first pixel PXA, the second pixel PXB, and the third pixel PXC are respectively enclosed by the first light blocking member 220A, the second light blocking member 220B, and the third light blocking member 220C.

[0149] The first light blocking member 220A, the second light blocking member 220B, and the third light blocking member 220C are divided based on longitudinal center lines of the data lines 171 as indicated by dotted lines in FIG. 24. For example, the first light blocking member 220A is positioned close to the first pixel PXA between the longitudinal

center lines of two data lines **171** positioned at both sides of the first pixel PXA, the second light blocking member **220B** is positioned close to the second pixel PXB between the longitudinal center lines of two data lines **171** positioned at both sides of the second pixel PXB, and the third light blocking member **220C** is positioned close to the third pixel PXC between the longitudinal center lines of two data lines **171** positioned at both sides of the third pixel PXC.

[0150] Referring to FIG. **24**, a width of the first light blocking member **220A** and the third light blocking member **220C** positioned between the first pixel PXA and the third pixel PXC adjacent to each other is referred to as the first width W1, a width of the first light blocking member **220A** and the second light blocking member **220B** positioned between the first pixel PXA and the second pixel PXB adjacent to each other is referred to as the second width W2, and a width of the second light blocking member **220B** and the third light blocking member **220C** positioned between the second pixel PXB and the third pixel PXC adjacent to each other is referred to as the third width W3. The first width W1, the second width W2, and the third width W3 may be substantially equal to each other.

[0151] Referring to FIG. **24**, with respect to the data line **171** positioned between the first pixel PXA and the second pixel PXB, the first interval G1 between the pixel electrode **191** of the first pixel PXA and the data line **171** is larger than the second interval G2 between the pixel electrode **191** of the second pixel PXB and the data line **171**. Accordingly, when observing the second pixel PXB, light displaying the first color through the first color filter **230A** and the pixel electrode **191** of the first pixel PXA is covered by the second light blocking member **220B** and is not recognized. Therefore, light displaying the second color through the pixel electrode **191** of the second pixel PXB and the light displaying the first color are not mixed.

[0152] With respect to the data line **171** positioned between the second pixel PXB and the third pixel PXC, the third interval G3 between the pixel electrode **191** of the third pixel PXC and the data line **171** is larger than the second interval G2 between the pixel electrode **191** of the second pixel PXB and the data line **171**.

[0153] Accordingly, when observing the second pixel PXB, light displaying the third color through the third color filter **230C** and the pixel electrode **191** of the third pixel PXC is covered by the light blocking member **220** and is not recognized. Therefore, light passing the pixel electrode **191** of the second pixel PXB and the light displaying the third color through the third color filter **230C** are not mixed.

[0154] With respect to the data line **171** positioned between the third pixel PXC and the first pixel PXA, the third interval G3 between the pixel electrode **191** of the third pixel PXC and the data line **171** is substantially equal to the first interval G1 between the pixel electrode **191** of the first pixel PXA and the data line **171**, and the third interval G2 and the first interval G1 are larger than the second interval G2. Accordingly, the third color displayed by the third pixel PXC and the first color displayed by the first pixel PXA are not mixed. Therefore, a deterioration of the display quality due to the color mixture between the first pixel PXA and the third pixel PXC that are adjacent to each other and that display the first color and the third color, respectively, may be prevented.

[0155] Referring back to FIG. **24** and FIG. **25**, the first light blocking member **220A** and the third light blocking member **220C** positioned between the first pixel PXA and the third

pixel PXC adjacent to each other have substantially the same width with respect to the longitudinal center line of the data line **171**. For example, the first light blocking member **220A** and the third light blocking member **220C** forming the first width W1 have substantially the same width. The first light blocking member **220A** and the second light blocking member **220B** positioned between the first pixel PXA and the second pixel PXB adjacent to each other have substantially the same width with respect to the longitudinal center line of the data line **171**. For example, the first light blocking member **220A** and the second light blocking member **220B** forming the second width W2 have substantially the same width. The second light blocking member **220B** and the third light blocking member **220C** positioned between the second pixel PXB and the third pixel PXC adjacent to each other have substantially the same width with respect to the longitudinal center line of the data line **171**. For example, the second light blocking member **220B** and the third light blocking member **220C** forming the third width W3 have substantially the same width. The first width W1, the second width W2, and the third width W3 may be substantially equal to each other.

[0156] According to an exemplary embodiment of the present invention, the width of the plurality of branch electrodes **192** of the pixel electrode **191** in the second pixel PXB is larger than the width of the plurality of branch electrodes **192** of the pixel electrode **191** in the first pixel PXA or third pixel PXC, and the interval between the plurality of branch electrodes **192** of the pixel electrode **191** in the second pixel PXB may be larger than the interval between the plurality of branch electrodes **192** of the pixel electrode **191** in the first pixel PXA or the third pixel PXC. The number of the plurality of branch electrodes **192** of the pixel electrode **191** in the pixel electrode of the second pixel PXB may be substantially equal to the number of the plurality of branch electrodes **192** of the pixel electrode **191** of the first pixel PXA or the third pixel PXC. By reducing the interval between the plurality of branch electrodes **192** of the pixel electrode **191** in the first pixel PXA or the third pixel PXC having a relatively large distance from the data line **171** and by making the number of the plurality of branch electrodes **192** of the pixel electrode **191** in the first pixel PXA or third pixel PXC as the number of the plurality of branch electrodes **192** of the pixel electrode **191** in the second pixel PXB, the transmittance of the first pixel PXA or the third pixel PXC may be prevented from being decreased.

[0157] As such, in the pixel where the color mixture may be recognized between the colors displayed by the pixels adjacent to each other, the interval between the data line **171** and the pixel electrode **191** is increased, and thus, the color mixture may be prevented between the adjacent pixels of the liquid crystal display, thus preventing the display quality from being deteriorated due to the color mixture.

[0158] FIG. **25** is a layout view of three pixels of a liquid crystal display according to another exemplary embodiment of the present invention, and FIG. **26** is a layout view of a portion of the liquid crystal display of FIG. **25**.

[0159] Referring to FIG. **25**, the liquid crystal display according to an exemplary embodiment includes the first pixel PXA, the second pixel PXB, and the third pixel PXC that are adjacent to each other and that display different colors from each other. The first pixel PXA, the second pixel PXB, and the third pixel PXC are sequentially arranged and are repeatedly positioned. For example, the third pixel PXC and the second pixel PXB may be positioned at both sides of the first pixel PXA, the first pixel PXA and the third pixel PXC

may be positioned at both sides of the second pixel PXB, and the second pixel PXB and the first pixel PXA may be positioned at both sides of the third pixel PXC.

[0160] The first pixel PXA, the second pixel PXB, and the third pixel PXC each include the pixel electrode 191 including the plurality of branch electrodes 192.

[0161] In the first pixel PXA, an interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 is the first interval G1. In the second pixel PXB, an interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 is the second interval G2. In the third pixel PXC, an interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 is the third interval G3. The first interval G1 and the third interval G3 are substantially equal to each other, and the second interval G2 is smaller than the first interval G1 and the third interval G3.

[0162] For example, among the first pixel PXA, the second pixel PXB, and the third pixel PXC that display different colors from each other, at least one pixel, for example, the second pixel PXB, may have the interval between an edge of the pixel electrode 191, which is substantially parallel to an adjacent data line 171, and the adjacent data line 171 further reduced as compared with the other pixels (e.g., pixels PXA and PXC).

[0163] Referring to FIG. 26, the first pixel PXA, the second pixel PXB, and the third pixel PXC are respectively enclosed by the first light blocking member 220A, the second light blocking member 220B, and the third light blocking member 220C.

[0164] The first light blocking member 220A, the second light blocking member 220B, and the third light blocking member 220C are divided based on longitudinal center lines of the data lines 171 as indicated by dotted lines in FIG. 26. For example, the first light blocking member 220A is positioned close to the first pixel PXA between the longitudinal center line of two data lines 171 positioned at both sides of the first pixel PXA, the second light blocking member 220B is positioned close to the second pixel PXB between the longitudinal center lines of two data lines 171 positioned at both sides of the second pixel PXB, and the third light blocking member 220C is positioned close to the third pixel PXC between the longitudinal center lines of two data lines 171 positioned at both sides of the third pixel PXC.

[0165] Referring to FIG. 26, a width of the first light blocking member 220A and the third light blocking member 220C positioned between the first pixel PXA and the third pixel PXC adjacent to each other is referred to as the first width W1, a width of the first light blocking member 220A and the second light blocking member 220B positioned between the first pixel PXA and the second pixel PXB adjacent to each other is referred to as the second width W2, and a width of the second light blocking member 220B and the third light blocking member 220C positioned between the second pixel PXB and the third pixel PXC adjacent to each other is referred to as the third width W3. The first width W1, the second width W2, and the third width W3 may be substantially equal to each other.

[0166] Referring to FIG. 26, with respect to the data line 171 positioned between the first pixel PXA and the second pixel PXB, the first interval G1 between the pixel electrode 191 of the first pixel PXA and the data line 171 is larger than

the second interval G2 between the pixel electrode 191 of the second pixel PXB and the data line 171. Accordingly, when observing the second pixel PXB, light displaying the first color through the first color filter 230A and the pixel electrode 191 of the first pixel PXA is covered by the light blocking member 220B and is not recognized. Therefore, light displaying the second color through the pixel electrode 191 of the second pixel PXB and the light displaying the first color are not mixed.

[0167] With respect to the data line 171 positioned between the second pixel PXB and the third pixel PXC, the third interval G3 between the pixel electrode 191 of the third pixel PXC and the data line 171 is larger than the second interval G2 between the pixel electrode 191 of the second pixel PXB and the data line 171.

[0168] Accordingly, when observing the second pixel PXB, light displaying the third color through the third color filter 230C and the pixel electrode 191 of the third pixel PXC is covered by the second light blocking member 220B and is not recognized. Therefore, light displaying the second color through the pixel electrode 191 of the second pixel PXB and the light displaying the third color through the third color filter 230C are not mixed.

[0169] With respect to the data line 171 positioned between the third pixel PXC and the first pixel PXA, the third interval G3 between the pixel electrode 191 of the third pixel PXC and the data line 171 is substantially equal to the first interval G1 between the pixel electrode 191 of the first pixel PXA and the data line 171, and the third interval G2 and the first interval G1 are larger than the second interval G2. Accordingly, the third color displayed by the third pixel PXC and the first color displayed by the first pixel PXA are not mixed. Therefore, a deterioration of the display quality due to the color mixture between the first pixel PXA and the third pixel PXC that are adjacent to each other and that display the first color and the third color, respectively, may be prevented.

[0170] Referring to FIG. 26, according to an exemplary embodiment of the present invention, the width of the second light blocking member 220B is larger than the width of the first light blocking member 220A and the width of the third light blocking member 220C.

[0171] The first light blocking member 220A and the second light blocking member 220B positioned between the first pixel PXA and the second pixel PXB adjacent to each other have different widths from each other with respect to the longitudinal center line of the data line 171. Of the first light blocking member 220A and the second light blocking member 220B that form the second width W2, the first light blocking member 220A has the width B1 which is smaller than the width B2 of the second light blocking member 220B. Accordingly, when observing the second pixel PXB, light displaying the first color through the pixel electrode 191 of the first pixel PXA is covered by the second light blocking member 220B having a relatively large width, and accordingly, the light is not mixed with light displaying the second color through the pixel electrode 191 of the second pixel PXB.

[0172] The second light blocking member 220B and the third light blocking member 220C positioned between the second pixel PXB and the third pixel PXC adjacent to each other have different widths from each other with respect to the longitudinal center line of the data line 171. Of the second light blocking member 220B and the third light blocking member 220C that form the third width W3, the second light blocking member 220B has the width B2 which is larger than

the width B3 of the third light blocking member 220B. Accordingly, when observing the second pixel PXB, light displaying the third color through the pixel electrode 191 of the third pixel PXC is covered by the second light blocking member 220B having a relatively large width, and accordingly, the light is not mixed with light displaying the second color through the pixel electrode 191 of the second pixel PXB.

[0173] As such, by increasing the interval between the data line 171 and the pixel electrode 191 and by increasing the width of the light blocking member, the color mixture may be prevented between the adjacent pixels of the liquid crystal display, thus preventing the display quality from being deteriorated due to the color mixture may be prevented.

[0174] As such, in the pixel where the color mixture may be recognized between the colors displayed by the pixels adjacent to each other, the width of the light blocking member may be increased, and thus, the color mixture may be prevented from occurring between the pixels adjacent to each other, thus preventing the display quality from being deteriorated due to the color mixture.

[0175] According to an exemplary embodiment of the present invention, the width of the plurality of branch electrodes 192 of the pixel electrode 191 in the second pixel PXB is larger than the width of the plurality of branch electrodes 192 of the pixel electrode 191 in first pixel PXA or third pixel PXC, and the interval between the plurality of branch electrodes 192 of the pixel electrode 191 in the second pixel PXB may be larger than the interval between the plurality of branch electrodes 192 of the pixel electrode 191 in the first pixel PXA or the third pixel PXC. The number of the plurality of branch electrodes 192 of the pixel electrode 191 in the second pixel PXB may be substantially equal to the number of the plurality of branch electrodes 192 of the pixel electrode 191 in the first pixel PXA or the third pixel PXC. By reducing the interval between the plurality of branch electrodes 192 of the pixel electrode 191 in the first pixel PXA or the third pixel PXC having a relatively larger distance from the data line 171 and by making the number of the plurality of branch electrodes 192 of the pixel electrode 191 in the first pixel PXA or third pixel PXC substantially the same as the number of the plurality of branch electrodes 192 of the pixel electrode 191 in the second pixel PXB, the transmittance of the first pixel PXA or the third pixel PXC may be prevented from being deteriorated.

[0176] Colors displayed by pixels adjacent to each other may be mixed. In the liquid crystal display according to an exemplary embodiment of the present invention, the interval between the pixel electrode and the data line between pixels adjacent to each other may be increased or the width of the light blocking member of the pixel where the color mixture may be recognized may be increased. Therefore, color mixture may be prevented from occurring between the pixels displaying different colors from each other.

[0177] While exemplary embodiments of the present invention have been described with reference to the figures, it is to be understood by one of ordinary skill that the invention is not limited to the disclosed embodiments and various modifications may be made thereto.

What is claimed is:

1. A liquid crystal display comprising:

a first pixel, a second pixel, and a third pixel, each displaying a different color;

a first data line positioned between the first pixel and the second pixel and a second data line positioned between the second pixel and the third pixel; and

a first pixel electrode, a second pixel electrode, and a third pixel electrode, respectively positioned in the first, second, and third pixels,

wherein an interval between the first pixel electrode and the first data line is larger than an interval between the second pixel electrode and the first data line, and

an interval between the second pixel electrode and the second data line is smaller than an interval between the third pixel electrode and the second data line.

2. The liquid crystal display of claim 1, wherein each of the first, second, and third pixel electrodes includes a plurality of branch electrodes,

wherein a width of the plurality of branch electrodes in the first pixel electrode is substantially equal to a width of the plurality of branch electrodes in the second pixel electrode, and an interval between the plurality of branch electrodes in the first pixel electrode is substantially equal to an interval between the plurality of branch electrodes in the second pixel electrode, and

wherein the width of the plurality of branch electrodes in the second pixel electrode is substantially equal to a width of the plurality of branch electrodes in the third pixel electrode, and the interval between the plurality of branch electrodes in the second pixel electrode is substantially equal to an interval between the plurality of branch electrodes in the third pixel electrode.

3. The liquid crystal display of claim 2, further comprising: a first light blocking member overlapping the first data line and disposed close to the first pixel with respect to a longitudinal center line of the first data line;

a second light blocking member overlapping the first data line and the second data line and disposed close to the second pixel with respect to the longitudinal center line of the first data line and a longitudinal center line of the second data line; and

a third light blocking member overlapping the second data line and disposed close to the third pixel with respect to the longitudinal center line of the second data line, wherein

a width of the first light blocking member overlapping the first data line is smaller than a width of the second light blocking member overlapping the first data line, and

the width of the second light blocking member overlapping the second data line is larger than a width of the third light blocking member overlapping the second data line.

4. The liquid crystal display of claim 3, further comprising: a third data line positioned between the third pixel and a fourth pixel close to the third pixel;

a third light blocking member overlapping the third data line and disposed close to the third pixel with respect to a longitudinal center line of the third data line; and

a fourth light blocking member overlapping the third data line and disposed close to the fourth pixel with respect to the longitudinal center line of the third data line,

wherein the width of the third light blocking member overlapping the third data line is substantially equal to a width of the fourth light blocking member overlapping the third data line.

5. The liquid crystal display of claim 4, wherein the width of the first light blocking member and the second light blocking member overlapping the first data line is

substantially equal to the width of the second light blocking member and the third light blocking member overlapping the second data line, and the width of the first light blocking member and the second light blocking member overlapping the first data line is substantially equal to the width of the third light blocking member and the fourth light blocking member overlapping the third data line.

6. The liquid crystal display of claim 4, wherein the width of the first light blocking member and the second light blocking member overlapping the first data line is substantially equal to the width of the second light blocking member and the third light blocking member overlapping the second data line, and the width of the first light blocking member and the second light blocking member overlapping the first data line is larger than the width of the third light blocking member and the fourth light blocking member overlapping the third data line.

7. The liquid crystal display of claim 1, wherein each of the first, second, and third pixel electrodes includes a plurality of branch electrodes, wherein a width of the plurality of branch electrodes in the first pixel electrode is smaller than a width of the plurality of branch electrodes in the second pixel electrode, and an interval between the plurality of branch electrodes in the first pixel electrode is smaller than an interval between the plurality of branch electrodes in the second pixel electrode, and a width of the plurality of branch electrodes in the second pixel electrode is larger than a width of the plurality of branch electrodes in the third pixel electrode, and an interval between the plurality of branch electrodes in the second pixel electrode is larger than an interval between the plurality of branch electrodes in the third pixel electrode.

8. The liquid crystal display of claim 7, further comprising: a first light blocking member overlapping the first data line and disposed close to the first pixel with respect to a longitudinal center line of the first data line; a second light blocking member overlapping the first data line and the second data line and disposed close to the second pixel with respect to the longitudinal center line of the first data line and a longitudinal center line of the second data line; and a third light blocking member overlapping the second data line and disposed close to the third pixel with respect to the longitudinal center line of the second data line, wherein

a width of the first light blocking member overlapping the first data line is smaller than a width of the second light blocking member overlapping the first data line, and the width of the second light blocking member overlapping the second data line is larger than a width of the third light blocking member overlapping the second data line.

9. The liquid crystal display of claim 8, further comprising: a third data line positioned between the third pixel and a fourth pixel close to the third pixel; a third light blocking member overlapping the third data line and disposed close to the third pixel with respect to a longitudinal center line of the third data line; and a fourth light blocking member overlapping the third data line and disposed close to the fourth pixel with respect to the longitudinal center line of the third data line, and

wherein the width of the third light blocking member overlapping the third data line is substantially equal to a width of the fourth light blocking member overlapping the third data line.

10. The liquid crystal display of claim 9, wherein the width of the first light blocking member and the second light blocking member overlapping the first data line is substantially equal to the width of the second light blocking member and the third light blocking member overlapping the second data line, and

the width of the first light blocking member and the second light blocking member overlapping the first data line is substantially equal to the width of the third light blocking member and the fourth light blocking member overlapping the third data line.

11. The liquid crystal display of claim 9, wherein the width of the first light blocking member and the second light blocking member overlapping the first data line is substantially equal to the width of the second light blocking member and the third light blocking member overlapping the second data line, and the width of the first light blocking member and the second light blocking member overlapping the first data line is larger than the width of the third light blocking member and the fourth light blocking member overlapping the third data line.

12. The liquid crystal display of claim 1, further comprising:

a first light blocking member overlapping the first data line and disposed close to the first pixel with respect to a longitudinal center line of the first data line;

a second light blocking member overlapping the first data line and the second data line and disposed close to the second pixel with respect to the longitudinal center line of the first data line and a longitudinal center line of the second data line; and

a third light blocking member overlapping the second data line and disposed close to the third pixel with respect to the longitudinal center line of the second data line, wherein

a width of the first light blocking member overlapping the first data line is smaller than a width of the second light blocking member overlapping the first data line, and the width of the second light blocking member overlapping the second data line is larger than a width of the third light blocking member overlapping the second data line.

13. The liquid crystal display of claim 12, further comprising:

a third data line positioned between the third pixel and a fourth pixel close to the third pixel;

a third light blocking member overlapping the third data line and disposed close to the third pixel with respect to a longitudinal center line of the third data line; and

a fourth light blocking member overlapping the third data line and disposed close to the fourth pixel with respect to the longitudinal center line of the third data line,

wherein the width of the third light blocking member overlapping the third data line is substantially equal to a width of the fourth light blocking member overlapping the third data line.

14. The liquid crystal display of claim 13, wherein the width of the first light blocking member and the second light blocking member overlapping the first data line is substantially equal to the width of the second light

blocking member and the third light blocking member overlapping the second data line, and the width of the first light blocking member and the second light blocking member overlapping the first data line is substantially equal to the width of the third light blocking member and the fourth light blocking member overlapping the third data line.

15. The liquid crystal display of claim **13**, wherein the width of the first light blocking member and the second light blocking member overlapping the first data line is substantially equal to the width of the second light blocking member and the third light blocking member overlapping the second data line, and the width of the first light blocking member and the second light blocking member overlapping the first data line is larger than the width of the third light blocking member and the fourth light blocking member overlapping the third data line.

16. A liquid crystal display comprising:

- a first pixel, a second pixel, and a third pixel, each displaying a different color;
 - a first data line positioned between the first pixel and the second pixel and a second data line positioned between the second pixel and the third pixel;
 - a first pixel electrode, a second pixel electrode, and a third pixel electrode respectively positioned in the first, second, and third pixels;
 - a first light blocking member overlapping the first data line and disposed close to the first pixel with respect to a longitudinal center line of the first data line;
 - a second light blocking member overlapping the first data line and the second data line and disposed close to the second pixel with respect to the longitudinal center line of the first data line and a longitudinal center line of the second data line; and
 - a third light blocking member overlapping the second data line and disposed close to the third pixel with respect to the longitudinal center line of the second data line,
- wherein a width of the first light blocking member overlapping the first data line is smaller than a width of the second light blocking member overlapping the first data line, and the width of the second light blocking member overlapping the second data line is larger than a width of the third light blocking member overlapping the second data line.

17. The liquid crystal display of claim **16**, wherein each of the first, second, and third pixel electrodes includes a plurality of branch electrodes, wherein a width of the plurality of branch electrodes in the first pixel electrode is substantially equal to a width of the plurality of branch electrodes in the second pixel electrode, and an interval between the plurality of branch electrodes in the first pixel electrode is substantially equal to an interval between the plurality of branch electrodes in the second pixel electrode, and a width of the plurality of branch electrodes in the second pixel electrode is substantially equal to a width of the plurality of branch electrodes in the third pixel electrode, and an interval between the plurality of branch electrodes in the second pixel electrode is substantially equal to an interval between the plurality of branch electrodes in the third pixel electrode.

18. The liquid crystal display of claim **3**, further comprising:

a third data line positioned between the third pixel and a fourth pixel close to the third pixel;

a third light blocking member overlapping the third data line and disposed close to the third pixel with respect to a longitudinal center line of the third data line; and

a fourth light blocking member overlapping the third data line and disposed close to the fourth pixel with respect to the longitudinal center line of the third data line,

wherein the width of the third light blocking member overlapping the third data line is substantially equal to a width of the fourth light blocking member overlapping the third data line.

19. The liquid crystal display of claim **18**, wherein

the width of the first light blocking member and the second light blocking member overlapping the first data line is substantially equal to the width of the second light blocking member and the third light blocking member overlapping the second data line, and

the width of the first light blocking member and the second light blocking member overlapping the first data line is substantially equal to the width of the third light blocking member and the fourth light blocking member overlapping the third data line.

20. The liquid crystal display of claim **18**, wherein

the width of the first light blocking member and the second light blocking member overlapping the first data line is substantially equal to the width of the second light blocking member and the third light blocking member overlapping the second data line, and

the width of the first light blocking member and the second light blocking member overlapping the first data line is larger than the width of the third light blocking member and the fourth light blocking member overlapping the third data line.

21. The liquid crystal display of claim **16**, wherein

each of the first, second, and third pixel electrodes includes a plurality of branch electrodes,

wherein a width of the plurality of branch electrodes in the first pixel electrode is smaller than a width of the plurality of branch electrodes in the second pixel electrode, and an interval between the plurality of branch electrodes in the first pixel electrode is smaller than an interval between the plurality of branch electrodes in the second pixel electrode, and

a width of the plurality of branch electrodes in the second pixel electrode is larger than a width of the plurality of branch electrodes in the third pixel electrode, and an interval between the plurality of branch electrodes in the second pixel electrode is larger than an interval between the plurality of branch electrodes in the third pixel electrode.

22. The liquid crystal display of claim **21**, further comprising:

a third data line positioned between the third pixel and a fourth pixel close to the third pixel;

a third light blocking member overlapping the third data line and disposed close to the third pixel with respect to the longitudinal center line of the third data line; and

a fourth light blocking member overlapping the third data line and disposed close to the fourth pixel with respect to the longitudinal center line of the third data line,

wherein the width of the third light blocking member overlapping the third data line is substantially equal to a width of the fourth light blocking member overlapping the third data line.

23. The liquid crystal display of claim **22**, wherein the width of the first light blocking member and the second light blocking member overlapping the first data line is substantially equal to the width of the second light blocking member and the third light blocking member overlapping the second data line, and

the width of the first light blocking member and the second light blocking member overlapping the first data line is substantially equal to the width of the third light blocking member and the fourth light blocking member overlapping the third data line.

24. The liquid crystal display of claim **22**, wherein

the width of the first light blocking member and the second light blocking member overlapping the first data line is substantially equal to the width of the second light blocking member and the third light blocking member overlapping the second data line, and

the width of the first light blocking member and the second light blocking member overlapping the first data line is larger than the width of the third light blocking member and the fourth light blocking member overlapping the third data line.

25. A liquid crystal display comprising:

a first pixel, a second pixel, and a third pixel, wherein the second pixel is positioned between the first and third pixels;

a first data line positioned between the first pixel and the second pixel and a second data line positioned between the second pixel and the third pixel;

a first pixel electrode, a second pixel electrode, and a third pixel electrode respectively positioned in the first, second, and third pixels;

a first light blocking member overlapping the first data line and disposed close to the first pixel with respect to a longitudinal center line of the first data line;

a second light blocking member overlapping the first data line and the second data line and disposed close to the second pixel with respect to the longitudinal center line of the first data line and a longitudinal center line of the second data line; and

a third light blocking member overlapping the second data line and disposed close to the third pixel with respect to the longitudinal center line of the second data line,

wherein an interval between the first pixel electrode and the first data line is different from an interval between the second pixel electrode and the first data line or a width of the first light blocking member overlapping the first data line is different from a width of the second light blocking member overlapping the first data line.

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专利名称(译)	液晶显示器，混色减少		
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[标]申请(专利权)人(译)	三星显示有限公司		
申请(专利权)人(译)	三星DISPLAY CO. , LTD.		
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摘要(译)

液晶显示器包括第一像素，第二像素和第三像素，每个显示不同的颜色。第一数据线位于第一像素和第二像素之间，第二数据线位于第二像素和第三像素之间。第一像素电极，第二像素电极和第三像素电极分别位于第一，第二和第三像素中。第一像素电极和第一数据线之间的间隔大于第二像素电极和第一数据线之间的间隔。第二像素电极和第二数据线之间的间隔小于第三像素电极和第二数据线之间的间隔。

