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(57) **ABSTRACT**

Disclosed are a liquid crystal display device and a testing method thereof. The liquid crystal display device includes a plurality of scan lines, and a plurality of data line sets crossing the scan lines, where each data line set includes a first data line, a second data line, and a third data line. The device also includes a shorting bar for the data lines, a first switch control line for the data lines and a second switch control line for the data lines. Through holes are not used to connect the data lines and the shorting bar, thus disadvantages of display devices caused by defects in through holes is eliminated. Additionally, the area for testing structures is decreased, so that a small-sized liquid crystal display devices can be produced.

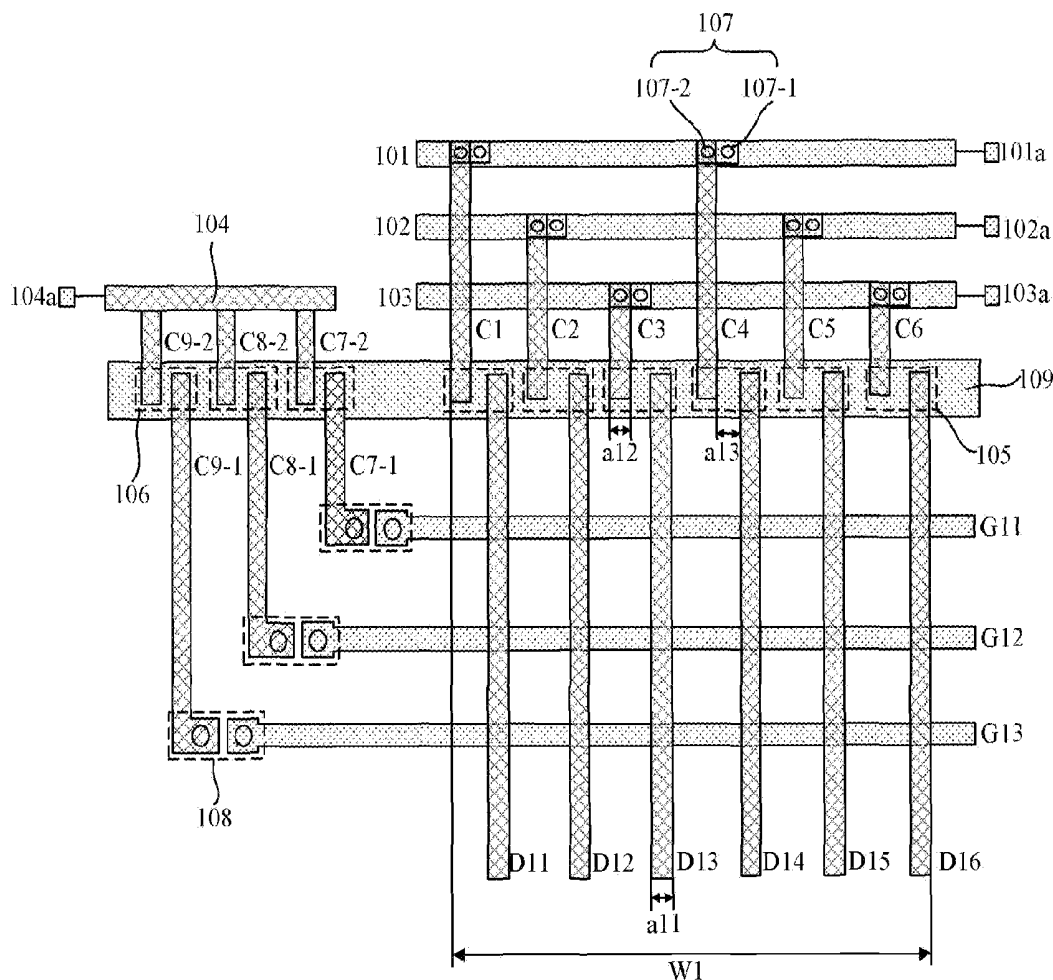
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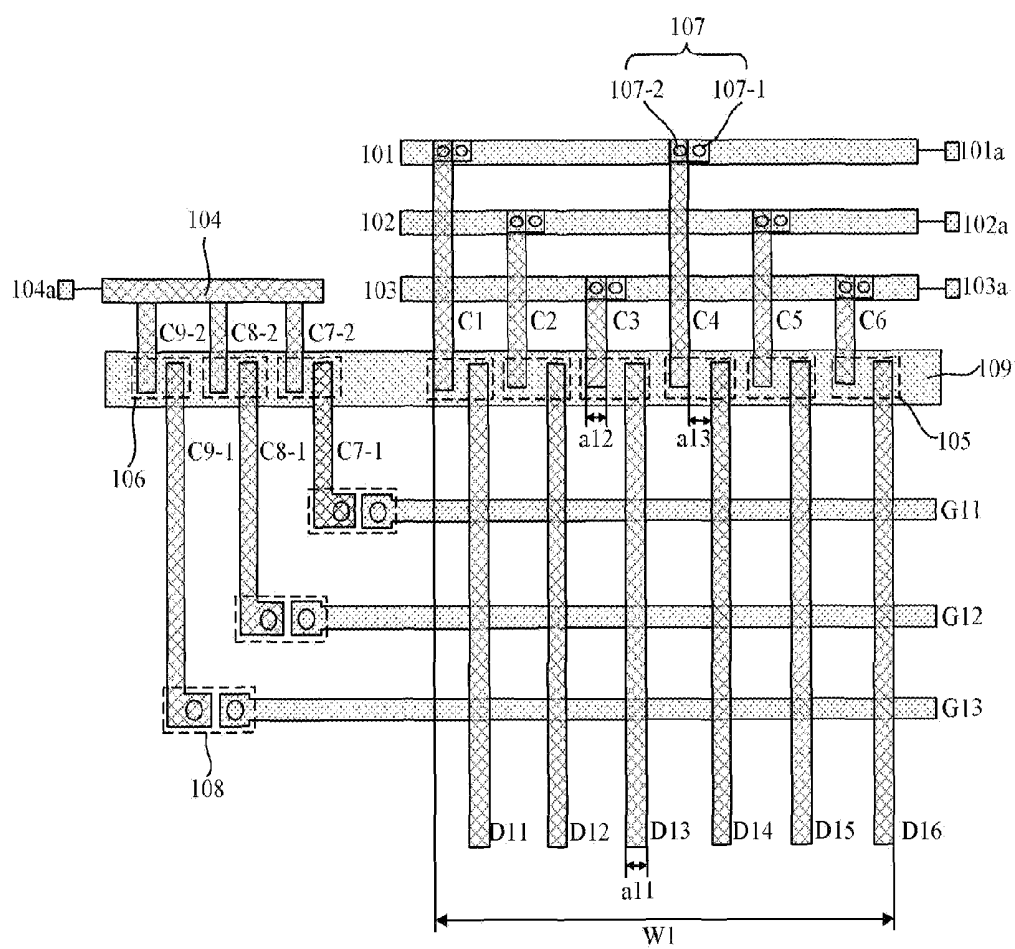


Fig.1

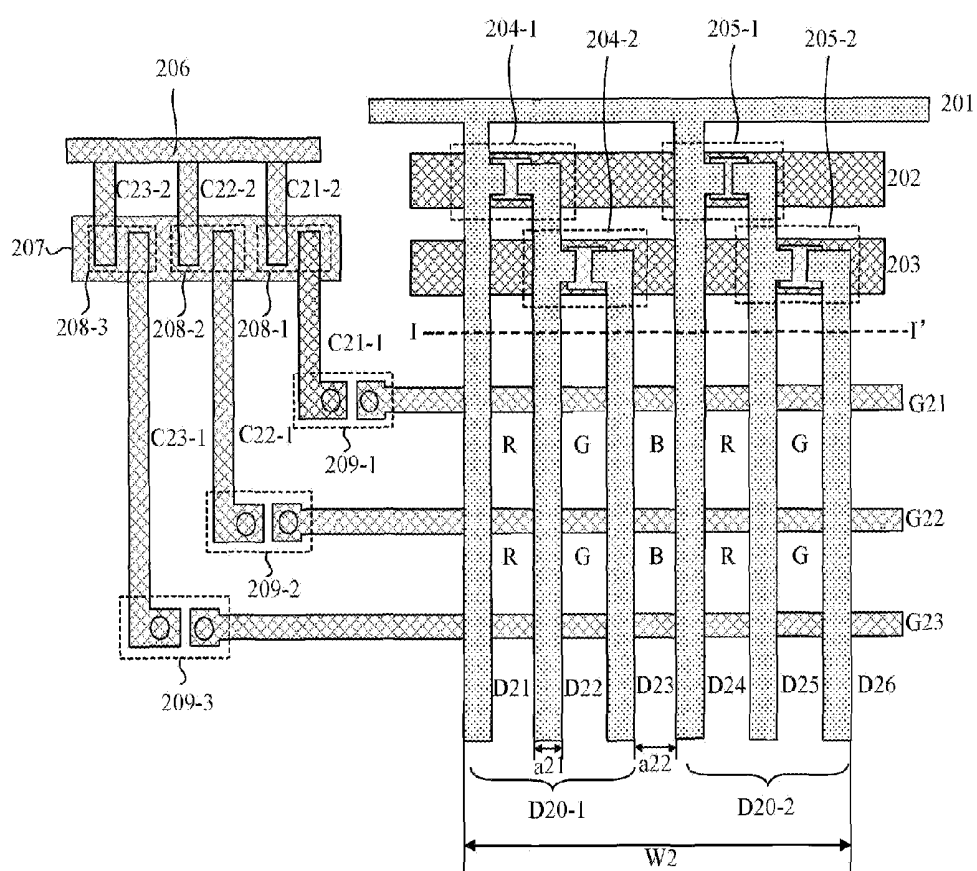


Fig.2

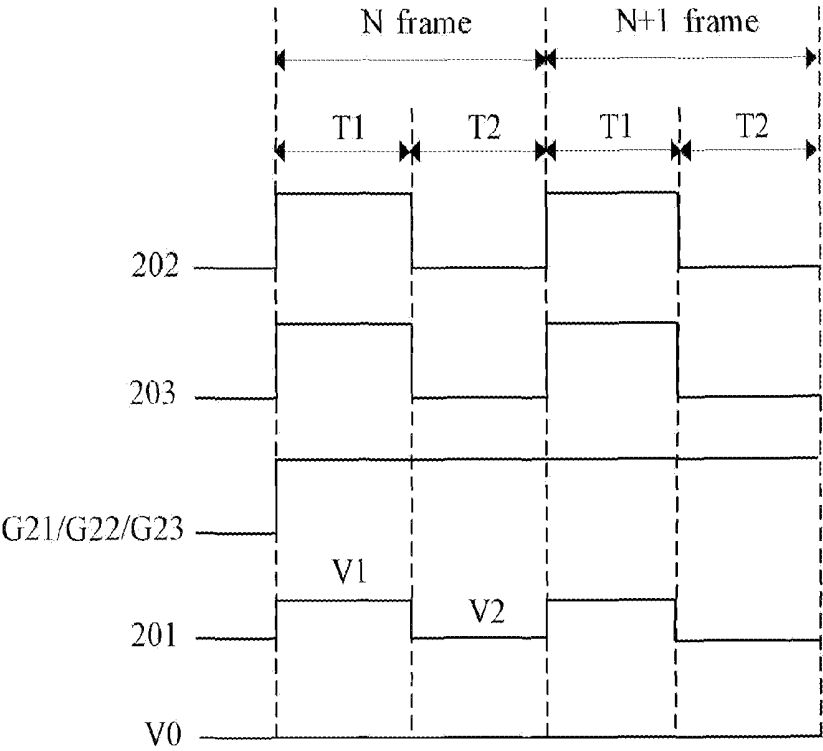


Fig.3

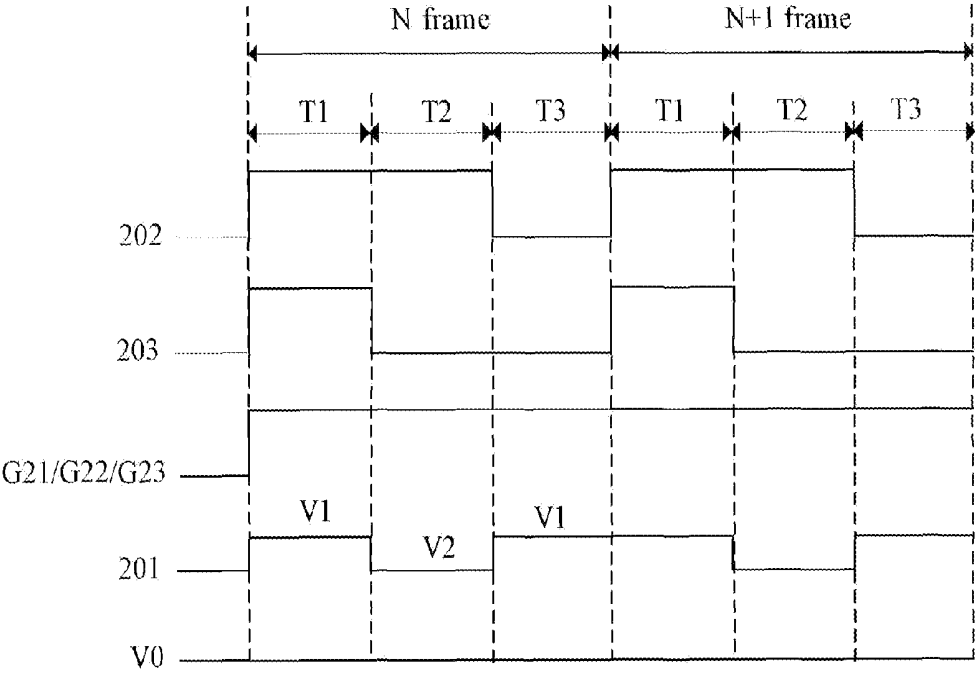


Fig.4

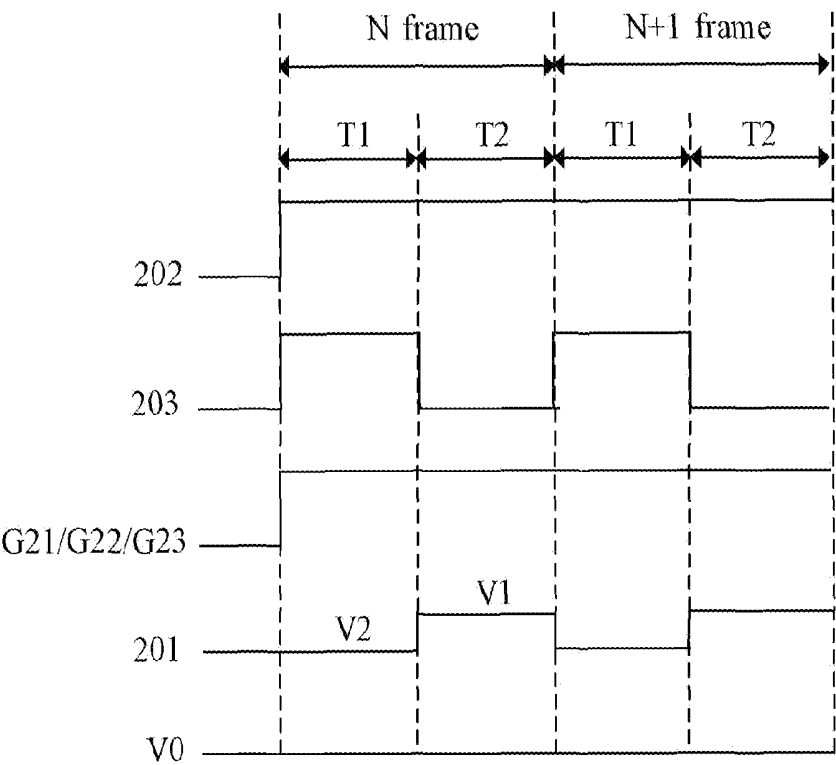


Fig.5

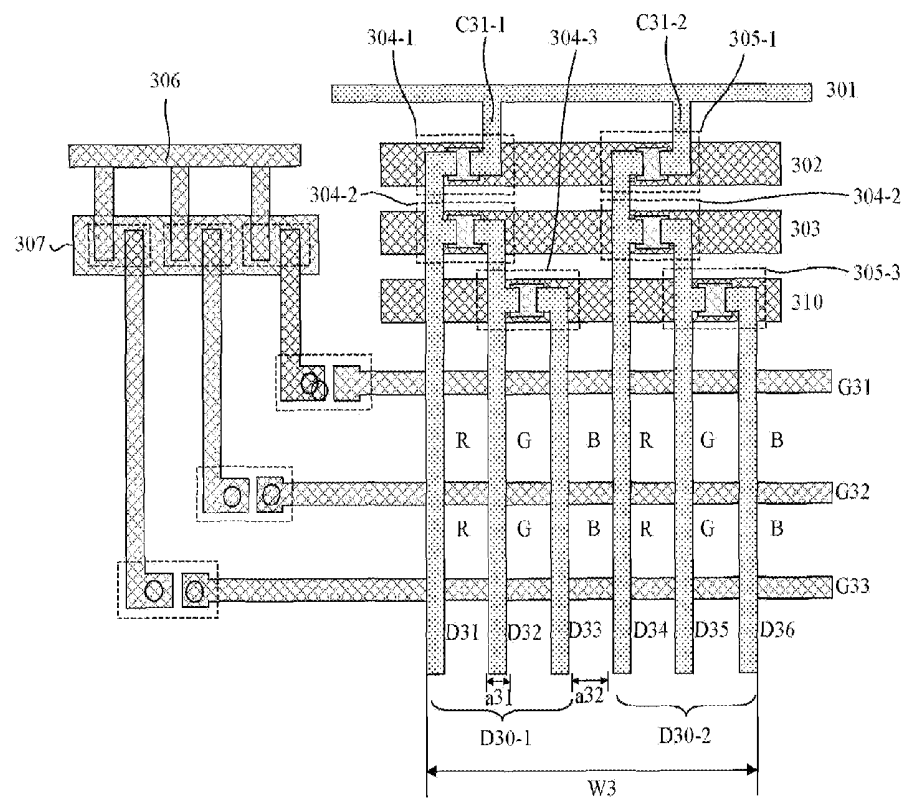


Fig.6

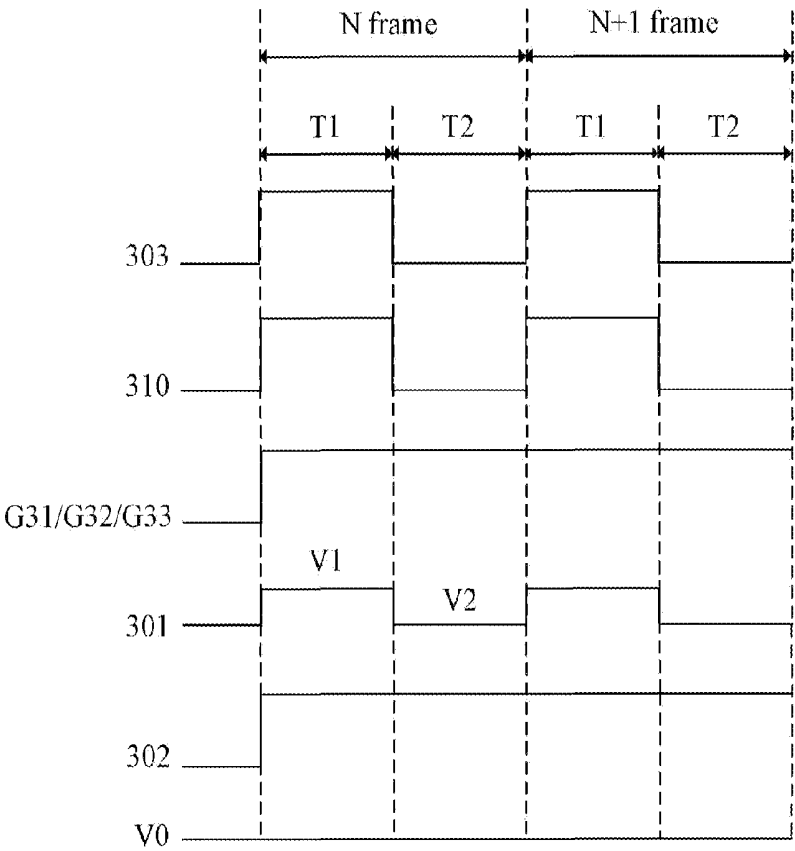


Fig.7

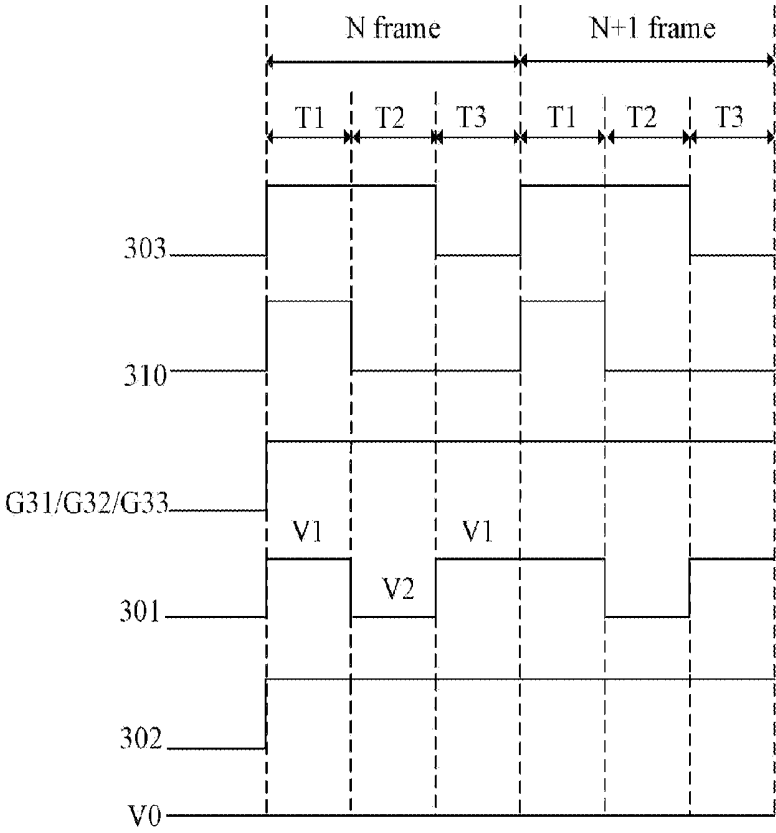


Fig.8

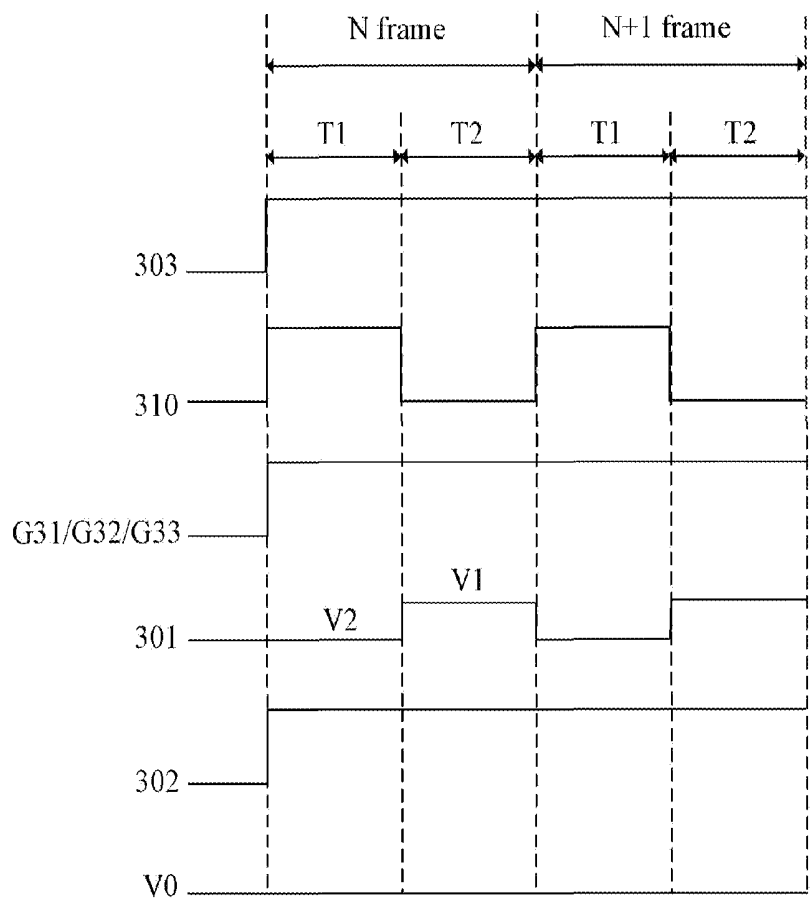


Fig.9

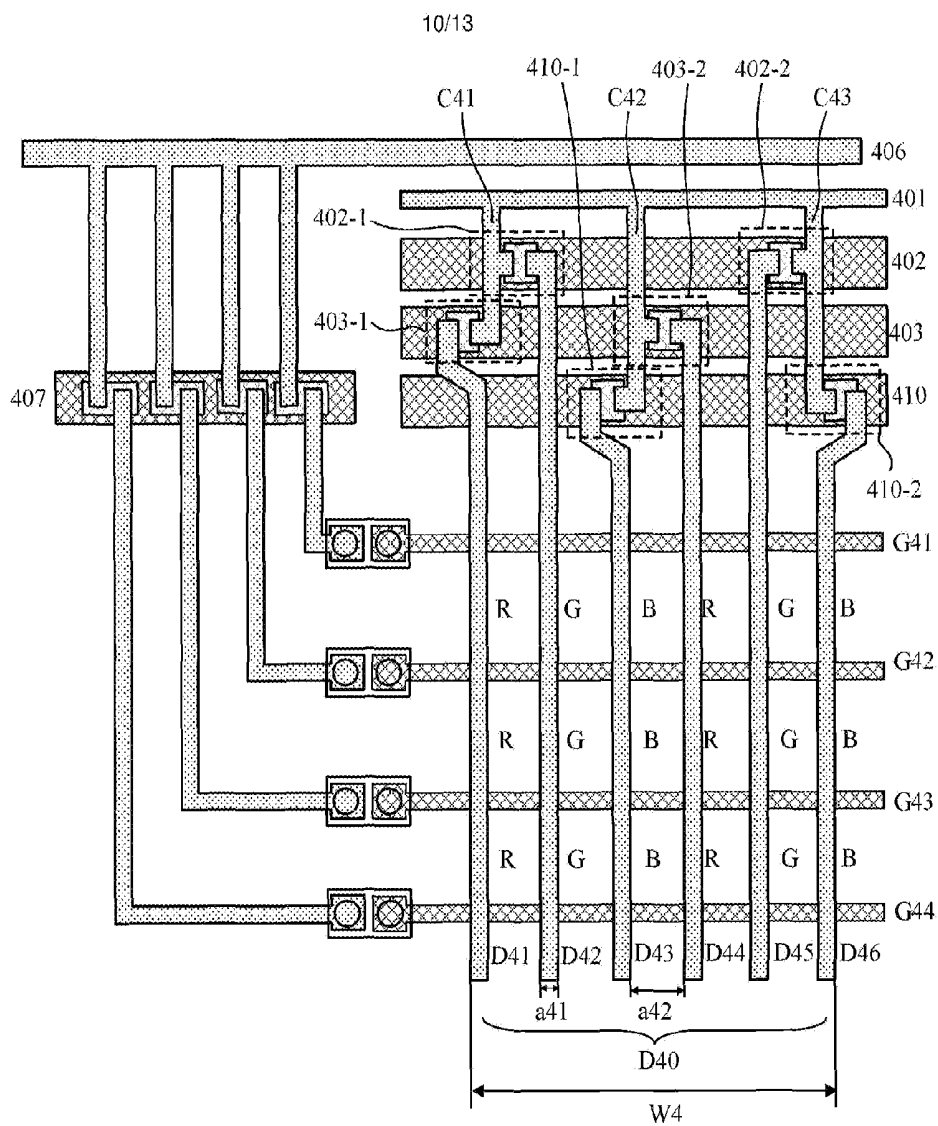


Fig.10

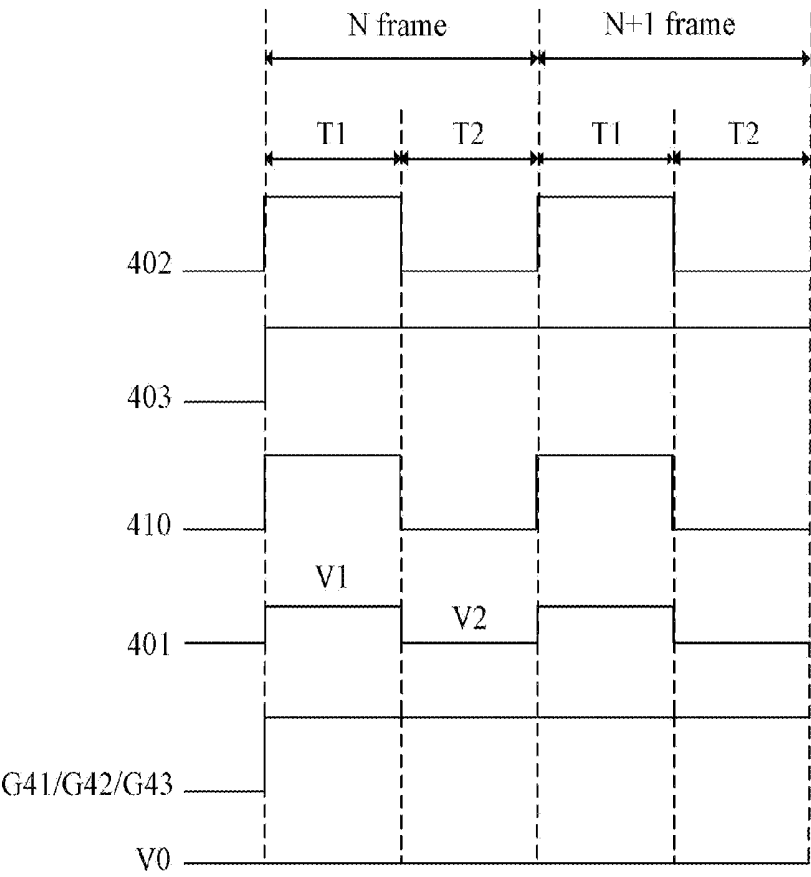


Fig.11

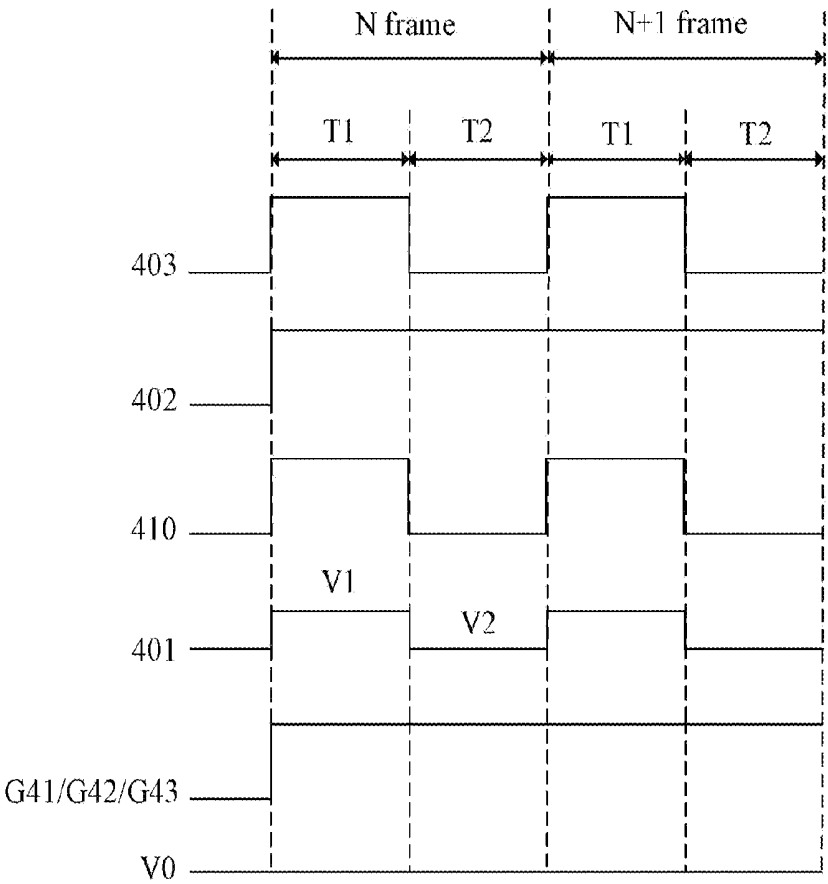


Fig.12

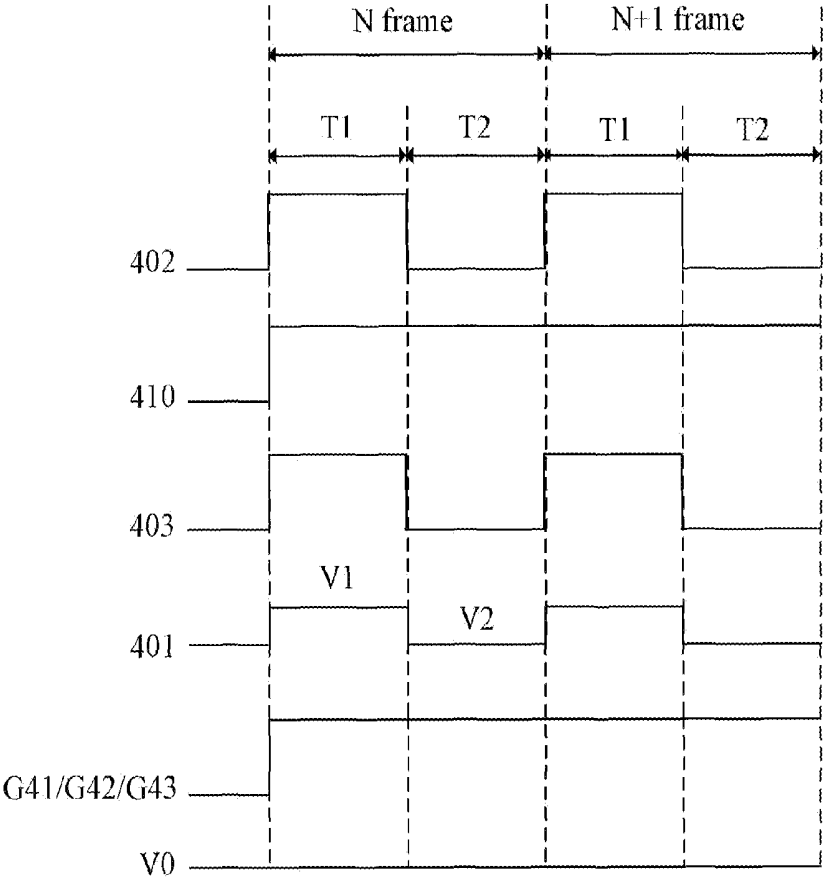


Fig.13

LIQUID CRYSTAL DISPLAY DEVICE

CROSS-REFERENCES TO RELATED APPLICATIONS

[0001] This application claims priority to and is a continuation of International Application No. PCT/CN2012/080784, filed on Aug. 30, 2012 and entitled “LIQUID CRYSTAL DISPLAY DEVICE”, which application claims the benefit of Chinese Patent Application No. 201110254100.3, filed with the Chinese Patent Office on Aug. 30, 2011 and entitled “LIQUID CRYSTAL DISPLAY DEVICE AND TESTING METHOD THEREOF”, the contents of which are incorporated herein by reference in their entirety.

FIELD OF THE INVENTION

[0002] The technology relates to display devices, and in particular to a liquid crystal display device.

BACKGROUND OF THE INVENTION

[0003] A liquid crystal display device is provided with an effective display area formed by display pixels in a matrix shape. The effective display area is provided with a plurality of scan lines extending along the row direction of the display pixels and a plurality of data lines extending along the column direction of the display pixels. Switch elements and pixel electrodes connected with the switch elements are arranged at intersections of the scan lines and the data lines. The switch elements are, for example, thin film transistors (TFTs), and the switch elements can respond to signals supplied to each scan line and send the signals from the data lines to each corresponding pixel electrode. The scan lines and the data lines extend to the peripheral part of the effective display area (for example, the scan lines and data lines are connected with a drive circuit), and the quality of pictures displayed in the effective display area is tested by inputting test signals at the peripheral part. The scan lines, the data lines and other circuit connecting lines are usually collectively referred to as signal lines.

[0004] With the increase of display pixel density, various signal lines such as the scan lines and the data lines in the effective display area and the peripheral part thereof are thinner and thinner in line width, and intervals are smaller and smaller, so various poor wiring conditions such as line breakage and short circuits are more likely. Therefore, for timely discovery of the poor wiring conditions, preventing large-scale poor conditions and preventing rejects from entering the next procedure to cause the waste of materials, the scan lines, the data lines and other signal lines are required to be checked after wiring so as to determine whether the poor wiring conditions exist or not.

[0005] At present, a common detecting method for the liquid crystal display device is that respective signal lines are connected together by using a plurality of shorting bars, test signals are input to the shorting bars, the test signals are transmitted to the scan lines and the data lines of the effective display area through corresponding elements, and the quality of the pictures displayed in the effective display area is detected.

[0006] An example is shown in FIG. 1, which is a structural schematic diagram of a liquid crystal display device. The liquid crystal display device comprises four shorting bars 101, 102, 103 and 104, test terminals 101a, 102a, 103a and 104a connected with the four shorting bars 101, 102, 103 and

104 respectively, six data lines D11, D12, D13, D14, D15 and D16, twelve connecting lines C1, C2, C3, C4, C5, C6, C7-1, C7-2, C8-1, C8-2, C9-1 and C9-2, three scan lines G11, G12 and G13, thin film transistors 105 and 106, through holes 107 and 108, and a switch control line 109.

[0007] In this case, the data lines D11 and D14 are red signal data lines, the data lines D12 and D15 are green signal data lines, and the data lines D13 and D16 are blue signal data lines. The data lines D11, D12, D13, D14, D15 and D16 are electrically connected with the connecting lines C1, C2, C3, C4, C5 and C6 through the thin film transistor 105 respectively. The connecting lines C1, C4 and the shorting bar 101, the connecting lines C2, C5 and the shorting bar 102, and the connecting lines C3, C6 and the shorting bar 103 are electrically connected through the through holes 107 and connecting metals (not shown in the figure) respectively. The scan lines G11, G12 and G13 are electrically connected with the connecting lines C7-1, C8-1 and C9-1 through the through holes 108 and connecting metals (not shown in the figure) respectively; the connecting lines C7-1, C8-1 and C9-1 and the connecting lines C7-2, C8-2 and C9-2 all intersect with the switch control line 109; and switch elements 106 are respectively formed at an intersection points of the connecting lines C7-1 and C7-2 and the switch control line 109, an intersection points of the connecting lines C8-1 and C8-2 and the switch control line 109 and an intersection points of the connecting lines C9-1 and C9-2 and the switch control line 109, so that the scan lines G11, G12 and G13 are electrically connected with the shorting bar 104 through the through holes 108, the connecting metals, the connecting lines C7-1, C8-1 and C9-1, the connecting lines C7-2, C8-2 and C9-2 and the switch elements 106.

[0008] The shorting bars 101, 102 and 103 electrically connected with the data lines are called data line shorting bars. The shorting bar 104 connected with the scan lines is called a scan line shorting bar.

[0009] Generally speaking, the shorting bars 101, 102 and 103 and the connecting lines C1, C2, C3, C4, C5 and C6 are in different circuit layers (not formed in a same step), so the shorting bars 101, 102 and 103 are electrically connected with the corresponding connecting lines through the through holes 107. In detail, each through hole 107 comprises through holes 107-1 and 107-2, for example, the through hole 107-1 of the shorting bar 101 is electrically connected with the through hole 107-2 of the connecting line C4 through connecting metals (not shown in the figure), so that the shorting bar 101 is connected with the data line D14, and connecting modes of the other shorting bars and connecting lines are similar thereto. The connecting metals are generally indium tin oxide (ITO) or indium zinc oxide (IZO). Electric test can be carried out through inputting test signals to the test terminals 101a, 102a, 103a and 104a.

[0010] The shorting bars 101, 102 and 103 and the connecting lines C1, C2, C3, C4, C5 and C6 are positioned in different circuit layers, because overline connections are required between the shorting bar 101 and the connecting lines C1, C4 and between the shorting bar 102 and the connecting lines C2, C5. As shown in FIG. 1, the connecting line C1 is required to span the shorting bars 102 and 103 to be connected with the shorting bar 101. If the connecting lines and the shorting bars are formed in the same circuit layer, the connecting line C1 and the shorting bars 102 and 103 will be shorted.

[0011] Therefore, the liquid crystal display device has the following defects:

[0012] firstly, in the manufacturing process of the liquid crystal display device, for example, in the procedures of picking and placing glass in array process, coating an alignment film and rubbing the alignment film in box process and the like, the ITO or the IZO at the through holes **107** is often burnt due to high static electricity; due to the through hole defect, the antistatic function of the shorting bars is disabled and also an array substrate experiences a phenomenon of poor display when testing electrical property, so that the product is scrapped; and

[0013] because each data line corresponds to a connecting line, the data lines **D11**, **D12**, **D13**, **D14**, **D15** and **D16** and the connecting lines **C1**, **C2**, **C3**, **C4**, **C5** and **C6** occupy a relatively larger area, which is adverse to reduce the size occupied by the test signal control part; and furthermore, because more data lines are superposed with the switch control line **109**, switch signals of the switch elements are delayed.

BRIEF SUMMARY OF THE INVENTION

[0014] One inventive aspect is a liquid crystal display device. The display device includes a plurality of scan lines, and a plurality of data line sets intersecting the scan lines, where each data line set includes a first data line, a second data line, a third data line. The display device also includes a plurality of pixel units formed near intersections of the scan lines and the data lines, where the first data line in each data line set controls pixel units have a first color, the second data line in each data line set controls pixel units have a second color, and the third data line in each data line set controls pixel units have a third color. The display device also includes a data line shorting bar, a switch control line for the first data lines of the data line sets, and a switch control line for the second data lines of the data line sets, where the first data line in each data line set is connected with the data line shorting bar, where the switch control line for the first data lines intersects the second data line in each data line set. The display device also includes a first switch element formed at each intersection of the second data lines and the switch control line for the first data lines, where a gate electrode of the first switch element is arranged on the switch control line for the first data line, where a source electrode of the first switch element is connected with the data line shorting bar, and a drain electrode of the first switch element is connected with the second data line, and where the switch control line for the second data lines intersects the third data line in each data line set. The display device also includes a second switch element formed at each intersection of the third data lines and the switch control line for the second data lines, where a gate electrode of the second switch element is arranged on the switch control line for the second data line, a source electrode of the second switch element is connected with the data line shorting bar through the first switch element, and a drain electrode of the second switch element is connected with the third data line.

[0015] Another inventive aspect is a liquid crystal display device. The display device includes a plurality of scan lines, and a plurality of data line sets intersecting the scan lines, where each data line set includes a first data line. The display device also includes a second data line, a third data line, and a plurality of pixel units formed in areas near intersections of the scan lines and the data lines, where the first data line in each data line set controls pixel units have a first color, the

second data line in each data line set controls pixel units have a second color, and the third data line in each data line set controls pixel units have a third color. The display device also includes a data line shorting bar, a switch control line for the first data line of the data line sets, a switch control line for the second data line of the data line sets, a switch control line for the third data line of the data line sets, where the switch control line for the first data line intersects the first data line in each data line set. The display device also includes a first switch element formed at each intersection of the first data line and the switch control line for the first data, where a gate electrode of the first switch element is arranged on the switch control line for the first data line, where a source electrode of the first switch element is connected with the data line shorting bar, and where a drain electrode of the first switch element is connected with the first data line, where the switch control line for the second data line intersects the second data line in each data line set. The display device also includes a second switch element formed at each intersection of the second data line and the switch control line for the second data line, where a gate electrode of the second switch element is arranged on the switch control line for the second data line, where a source electrode of the second switch element is connected with the data line shorting bar through the first switch element, and where a drain electrode of the second switch element is connected with the second data line. The switch control line for the third data line intersects the third data line in each data line set. The display device also includes a third switch element formed at each intersection of the third data line and the switch control line for the data line, where a gate electrode of the third switch element is arranged on the switch control line for the third data line, where a source electrode of the third switch element is connected with the data line shorting bar through the first switch element and the second switch element, and where a drain electrode of the third switch element is connected with the third data line.

[0016] Another inventive aspect is a liquid crystal display device. The display device includes a plurality of scan lines, and a plurality of data line sets intersected the scan lines, where each data line set includes a first data line, a second data line, a third data line, a fourth data line, a fifth data line and a sixth data line. The display device also includes a plurality of pixel units formed near intersections of the scan lines and the data lines, where the first data line and the fourth data line in each data line set control pixel units have a first color, the second data line and the fifth data line control pixel units have a second color, and the third data line and the sixth data line control pixel units have a third color. The display device also includes a data line shorting bar, a switch control line for the first data line of the data line sets, a switch control line for the second data line of the data line sets, and a switch control line for the third data line of the data line set, where the switch control line for the first data lines intersects the second data line and the fifth data line in each data line set. The display device also includes a first switch element formed at each intersection of the second data line and the switch control line for the first data lines, and a second switch element is formed at each intersection of the fifth data line and the switch control line for the first data lines. The switch control line for the second data lines intersects the first data line and the fourth data line in each data line set. The display device also includes a third switch element formed at each intersection of the first data line and the switch control line for the second data lines, a fourth switch element formed at each intersection of the

fourth data line and the switch control line for the second data lines, where the switch control line for the third data lines intersects the third data line and the sixth data line in each data line set. The display device also includes a fifth switch element formed at each intersection of the third data line and the switch control line for the third data lines, and a sixth switch element formed at each intersection point of the sixth data line and the switch control line for the third data lines, where gate electrodes of the first switch element and the second switch element are arranged on the switch control line for the first data line. Gate electrodes of the third switch element and the fourth switch element are arranged on the switch control line for the second data line, and gate electrodes of the fifth switch element and the sixth switch element are arranged on the switch control line for the third data line. Source electrodes of the first, second, third, fourth, fifth, and sixth switch elements are connected with the data line shorting bar, and a drain electrode of the first switch element is connected with the second data line, a drain electrode of the second switch element is connected with the fifth data line, a drain electrode of the third switch element is connected with the first data line, a drain electrode of the fourth switch element is connected with the fourth data line, a drain electrode of the fifth switch element is connected with the third data line, and a drain electrode of the sixth switch element is connected with the sixth data line.

BRIEF DESCRIPTION OF THE DRAWINGS

[0017] FIG. 1 is a schematic diagram of a liquid crystal display device;

[0018] FIG. 2 is a structural schematic diagram of a liquid crystal display device in a first embodiment;

[0019] FIG. 3 is a flowchart diagram showing when the liquid crystal display device displays red pictures in the first embodiment;

[0020] FIG. 4 is a flowchart diagram showing when the liquid crystal display device displays green pictures in the first embodiment;

[0021] FIG. 5 is a flowchart diagram showing when the liquid crystal display device displays blue pictures in the first embodiment;

[0022] FIG. 6 is a structural schematic diagram of a liquid crystal display device in a second embodiment;

[0023] FIG. 7 is a flowchart diagram showing when the liquid crystal display device displays red pictures in the second embodiment;

[0024] FIG. 8 is a flowchart diagram showing when the liquid crystal display device displays green pictures in the second embodiment;

[0025] FIG. 9 is a flowchart diagram showing when the liquid crystal display device displays blue pictures in the second embodiment;

[0026] FIG. 10 is a structural schematic diagram of a liquid crystal display device in a third embodiment;

[0027] FIG. 11 is a flowchart diagram showing when the liquid crystal display device displays red pictures in the third embodiment;

[0028] FIG. 12 is a flowchart diagram showing when the liquid crystal display device displays green pictures in the third embodiment; and

[0029] FIG. 13 is a flowchart diagram showing when the liquid crystal display device displays blue pictures in the third embodiment.

DETAILED DESCRIPTION OF THE INVENTION

[0030] As mentioned in the background, because the data line shorting bar and the connecting lines are generally positioned in different circuit layers, the data line shorting bar is electrically connected with the connecting lines by means of through holes, but in the manufacturing process of the liquid crystal display device, connecting conductive layers at the through holes are frequently burnt due to over-high static electricity. Due to the through hole defect, not only is the antistatic function of the data line shorting bar disabled, but also the electric test cannot be carried out. Therefore, the application provides a liquid crystal display device and a testing method thereof, where the liquid crystal display device eliminates the through holes and thus eliminates the adverse influences of the through hole defects on the liquid crystal display device, ensures smooth operation of the electric test, and can reduce the area occupied by the data lines and can also reduce the switch signal delay of the switch elements.

First Embodiment

[0031] For convenience, three scan lines and two data line sets are taken as examples for detailed description below. However, it should be realized that the number of the scan lines and the number of the data lines are not limited thereto.

[0032] Please refer to FIG. 2, which is a structural schematic diagram of a liquid crystal display device in the first embodiment of the application. As shown in FIG. 2, the liquid crystal display device comprises an effective display area and a test area, where the test area comprises a data line test area and a scan line test area.

[0033] The effective display area comprises three scan lines G21, G22 and G23, two data line sets D20-1 and D20-2, and a plurality of pixel units formed in areas near intersections of the three scan lines G21, G22 and G23 with data lines in the two data line sets D20-1 and D20-2. The three scan lines G21, G22 and G23 are parallel to each other and are separated from each other by a fixed interval, and extend along the row direction of the array matrix of the liquid crystal display device. The data line set D20-1 comprises three adjacent data lines D21, D22 and D23; the data line set D20-2 comprises three adjacent data lines D24, D25 and D26. The six data lines D21, D22, D23, D24, D25 and D26 are parallel to each other and are separated from each other by a fixed interval, and extend along the direction vertical to the three scan lines G21, G22 and G23. The six data lines can be made of the same material, and are formed by synchronous film forming, photo-etching and etching in the manufacturing process of the liquid crystal display device.

[0034] In this case, D21 and D24 are first data lines, D22 and D25 are second data lines, and D23 and D26 are third data lines.

[0035] The pixel units comprise red pixel units, green pixel units and blue pixel units. The pixel units also include a pixel electrode arranged in each pixel unit, where each pixel electrode is connected with a TFT. The TFT selectively conducts under the control of scan line signals and transmit signals from the corresponding data line to the corresponding pixel electrode to thereby control the state of liquid crystal molecules, so that one color or a combination of colors, of three primary colors, namely red, green and blue, is displayed in the display area. The data lines D21 and D24 are red signal data lines and are connected with pixel electrodes R of red pixel

units, the data lines D22 and D25 are green signal data lines and are connected with pixel electrodes G of green pixel units, and the data lines D23 and D26 are blue signal data lines and are connected with pixel electrodes B of blue pixel units.

[0036] It should be pointed out that in a data line set, the line sequence of respective color signal data lines is not limited thereto, and can be another line sequence. For example, the data lines D21 and D24 are green signal data lines, the data lines D22 and D25 are red signal data line, and the data lines D23 and D26 are blue signal data lines and so on.

[0037] The data line test area comprises a data line shorting bar 201, two switch control lines 202 and 203 for the data lines, a first switch element 204-1 and a second switch element 204-2 formed at intersection points of the data line set D20-1 with the switch control lines 202 and 203 for the data lines, and a first switch element 205-1 and a second switch element 205-2 formed at intersection points of the data line set D20-2 with the switch control lines 202 and 203 for the data lines.

[0038] In this case, the switch control line 202 for the data line is a switch control line for the first data line, and the switch control line 203 for the data line is a switch control line for the second data line.

[0039] Gate electrodes of the two first switch elements 204-1 and 205-1 are both arranged on the switch control line 202 for the data line, and gate electrodes of the two second switch elements 204-2 and 205-2 are both arranged on the switch control line 203 for the data line. Source electrodes of the first switch elements 204-1 and 205-1 are directly connected with the data line shorting bar 201, and source electrodes of the second switch elements 204-2 and 205-2 are connected with the data line shorting bar 201 through the first switch elements 204-1 and 205-1. Drain electrodes of the first switch elements 204-1 and 205-1 are arranged on the switch control line 202 for the data line and connected with the data lines D22 and D25, and drain electrodes of the second switch elements 204-2 and 205-2 are arranged on the switch control line 203 for the data line and connected with the data lines D23 and D26. The first switch elements 204-1 and 205-1 and the second switch elements 204-2 and 205-2 in the embodiment are all thin film transistor switch elements, the switch control line 202 for the data line is used for controlling connection and disconnection of the two first switch elements 204-1 and 205-1, and similarly, the switch control line 203 for the data line is used for controlling connection and disconnection of the two second switch elements 204-2 and 205-2.

[0040] In this case, the gate electrodes of the first switch elements 204-1 and 205-1 are arranged on the switch control line 202 for the data line and connected with the switch control line 202 for the data line, and specifically, the switch control line 202 for the data line can be used as the gate electrodes of the first switch elements 204-1 and 205-1.

[0041] The gate electrodes of the second switch elements 204-2 and 205-2 are arranged on the switch control line 203 for the data line and connected with the switch control line 203 for the data line, and specifically, the switch control line 203 for the data line can be used as the gate electrodes of the second switch elements 204-2 and 205-2.

[0042] The source electrodes of the second switch elements 204-2 and 205-2 are connected with the data line shorting bar 201 through the first switch elements 204-1 and 205-1, and specifically, the source electrode of the second switch element 204-2 is connected with the drain electrode of the first switch element 204-1, and thus connected with the data line

shorting bar 201 through the first switch element 204-1; and the source electrode of the second switch element 205-2 is connected with the drain electrode of the first switch element 205-1, and thus connected with the data line shorting bar 201 through the first switch element 205-1.

[0043] The drain electrodes of the first switch elements 204-1 and 205-1 are arranged on the switch control line 202 for the data line and insulated with the switch control line 202 for the data line, and the drain electrodes of the second switch elements 204-2 and 205-2 are arranged on the switch control line 203 for the data line and insulated with the switch control line 203 for the data line.

[0044] The scan line test area comprises a scan line shorting bar 206, a switch control line 207 for the scan lines, three switch elements 208-1, 208-2 and 208-3 for the scan lines, three first connecting lines C21-1, C22-1 and C23-1 for the scan lines and three second connecting lines C21-2, C22-2 and C23-2 for the scan lines. Gate electrodes of the three switch elements 208-1, 208-2 and 208-3 for the scan lines are all arranged on the switch control line 207 for the scan lines. Source electrodes of the three switch elements 208-1, 208-2 and 208-3 for the scan lines are respectively connected with the scan line shorting bar 206 through the second connecting lines C21-2, C22-2 and C23-2. Drain electrodes of the three switch elements 208-1, 208-2 and 208-3 for the scan lines are arranged on the switch control line 207 for the scan lines and are respectively connected with three scan lines G21, G22 and G23 through the three first connecting lines C21-1, C22-1 and C23-1.

[0045] In this case, the gate electrodes of the three switch elements 208-1, 208-2 and 208-3 for the scan lines are all arranged on the switch control line 207 for the scan lines, and specifically, the switch control line 207 for the scan lines is used as the gate electrode of respective switch elements for the scan lines.

[0046] The drain electrodes of the three switch elements 208-1, 208-2 and 208-3 for the scan lines are arranged on the switch control line 207 for the scan lines and insulated with the switch control line 207 for the scan lines.

[0047] In the embodiment, because the three scan lines G21, G22 and G23 and the three first connecting lines C21-1, C22-1 and C23-1 for the scan lines are formed in different conductive layers, the first connecting lines C21-1, C22-1 and C23-1 for the scan lines are connected with the three scan lines G21, G22 and G23 through three through holes 209-1, 209-2 and 209-3 respectively. Generally, after an array substrate is aligned with a color film substrate, the through holes 209-1, 209-2 and 209-3 of the scan line test area are generally packaged in frame sealing glue of the array substrate and the color film substrate, so static electricity is difficultly produced at these through holes. Connecting metals (such as ITO or IZO) at these through holes are difficultly to be burnt, and thus the through holes in the scan line test area are not damaged in the embodiment. It can be understood that the through holes and the connecting lines for the scan lines can be replaced by switch elements in the scan line test area, to thereby save the area occupied by the scan line test area. Variations made by those skilled in the art according to the description of the data line test area without inventive work should belong to the protective scope of the application.

[0048] It should be pointed out that the switch elements 208-1, 208-2 and 208-3 for the scan lines can also be eliminated, so that the scan lines G21, G22 and G23 are directly electrically connected with the first connecting lines C21-1,

C22-1 and C23-1, the second connecting lines C21-2, C22-2 and C23-2, and the scan line shorting bar 206 through the through holes 209-1, 209-2 and 209-3. Although in the connecting mode, the connections of the scan line shorting bar 206 with respective scan lines G21, G22 and G23 are to be cut off by using laser after the test is completed to facilitate drive circuit module assembly of the next step, the procedure of making the switch elements for the scan lines is reduced in this mode.

[0049] In this case, the switch elements 208-1, 208-2 and 208-3 for the scan lines are eliminated, so that the scan lines G21, G22 and G23 can be directly electrically connected with the first connecting lines C21-1, C22-1 and C23-1, the second connecting lines C21-2, C22-2 and C23-2, and the scan line shorting bar 206 through the through holes 209-1, 209-2 and 209-3. This is because the switch elements 208-1, 208-2 and 208-3 for the scan lines are eliminated, so that the second connecting lines C21-2, C22-2 and C23-2 for the scan lines are directly connected with the scan line shorting bar 206.

[0050] In the embodiment, none of the data line shorting bar 201, the scan line shorting bar 206, the switch control lines 202 and 203, and the switch control line 207 for the scan lines are connected with test terminals, so test signals can be directly applied to the data line shorting bar 201, the scan line shorting bar 206, the switch control lines 202 and 203 for the data lines and the switch control line 207 for the scan lines. However, it should be realized that the data line shorting bar 201, the scan line shorting bar 206, the switch control lines 202 and 203, and the switch control line 207 for the scan lines can also be connected with the test terminals, and the test terminals can be made of the same materials as or different materials from the data line shorting bar 201, the scan line shorting bar 206, the switch control lines 202 and 203, and the switch control line 207 for the scan lines. In addition, a plurality of test terminals can be made of the same or different materials, for example, one part of the test terminals can be made of molybdenum, while the other part of the test terminals can be made of aluminum or aluminum alloy.

[0051] According to above description, the data lines D21 and D24 are directly connected with the data line shorting bar 201, the data lines D22 and D24 are electrically connected with the data line shorting bar 201 through the first switch elements 204-1 and 205-1 respectively, and the data lines D23 and D26 are electrically connected with the data line shorting bar 201 through the first switch elements 204-1 and 205-1 and the second switch element 204-2 and 205-2 respectively. In addition, two switch control lines 202 and 203 for the data lines are provided, where each switch control line for the data line is connected with two switch elements, the data lines corresponding to the two switch elements on the same switch control line for the data line correspond to pixel electrodes with the same color, and the liquid crystal display device can display pictures with different colors by applying corresponding control signals to the two switch control lines 202 and 203 for the data lines without the need of through holes. As a result, the adverse influences of the through hole defect on the liquid crystal display device are eliminated, and smooth operation of the electric test is ensured.

[0052] Moreover, as in shown FIG. 1, in liquid crystal display devices of the prior art, if line widths of the data lines D11, D12, D13, D14, D15 and D16 are all a_{11} , line widths of the connecting lines C1, C2, C3, C4, C5 and C6 are all a_{12} and distances between the connecting lines and adjacent data lines are a_{13} , and the distance W_1 between the connecting

line C1 and the data line D16 is $6 \cdot a_{11} + 6 \cdot a_{12} + 11 \cdot a_{13}$; and if $a_{11} = a_{12} = a_{13} = a$, then $W_1 = 23a$. In the embodiment, if line widths of the data lines D21, D22, D23, D24, D25 and D26 are all a_{21} and distances between adjacent data lines are a_{22} , then the distance W_2 between the data line D21 and the data line D26 is $6 \cdot a_{21} + 5 \cdot a_{22}$, and if $a_{21} = a_{22} = a$, then $W_2 = 11a$. Compared with the prior art, in the embodiment, the width of the data line test area, which can be reduced, is $W_1 - W_2 = 12a$. It can be seen that in comparison with the prior art, in the embodiment, the area occupied by the data line test area is reduced, thereby facilitating the production of small-sized liquid crystal display devices. Moreover, compared with the prior art, in the embodiment, the number of the data lines superposed with each switch control line for the data line is reduced, thereby reducing the switch signal delay of the switch elements.

[0053] The application further provides a testing method for the liquid crystal display device. The testing method comprises:

[0054] S1: providing a start voltage signal to the switch control line for the scan lines, the scan line shorting bar and the switch control lines for the data lines, and providing a data voltage signal to the data line shorting bar, so that the liquid crystal display device displays black pictures or white pictures.

[0055] In the condition that the liquid crystal display device displays the black pictures or the white pictures, the testing method includes executing step S2: providing a start voltage signal to the switch control line for the scan lines and the scan line shorting bar, and regulating data voltage signals on the plurality of switch control lines for the data lines and the data line shorting bar, so that the liquid crystal display device displays pictures with different colors.

[0056] The liquid crystal display device in a normally white mode is taken as an example below to describe the testing method for the liquid crystal display device according to the embodiment in detail.

[0057] 1) Detecting Red Pictures

[0058] Please refer to FIG. 2 and FIG. 3, where FIG. 3 is a timing diagram showing when the liquid crystal display device displays red pictures in the first embodiment of the application. As shown in FIG. 3, in a time period of one frame (comprising a time period T1 and a time period T2), the method comprises the following testing steps:

[0059] During time period T1, providing a start voltage signal (for example, 15V) to the switch control line 207, the scan line shorting bar 206, and the switch control lines 202 and 203 to turn on the first switch elements 204-1 and 205-1 and the second switch elements 204-2 and 205-2 and also to turn on the thin film transistors in all the pixel units in the effective display area. The method also includes, during time period T1, providing a first voltage V1 (for example, 5V) greater than a common electrode voltage V0 (for example, 0V) to the data line shorting bar 201, so that the liquid crystal display device displays black pictures.

[0060] The method also includes, during time period T2, providing a start voltage signal to the switch control line 207 and the scan line shorting bar 206, to make signals on the three scan lines G21, G22 and G23 be the start voltage signal and thus turn on the thin film transistors in all the pixel units in the effective display area. The method also includes, during time period T2, providing a cut-off voltage to the switch control lines 202 and 203, so that the two first switch elements 204-1 and 205-1 and the two second switch elements 204-2 and

205-2 are all in a cut-off state, and providing a second voltage **V2** (for example, 0.01V) close to the common electrode voltage **V0** to the data line shorting bar **201**, and applying signals of the second voltage **V2** to the corresponding red pixel electrodes **R** through the red signal data lines **D21** and **D24**, so that light can penetrate the red pixel units, and red pictures are displayed in the display area of the liquid crystal display device. Therefore, the red pictures can be detected.

[0061] 2) Detecting Green Pictures

[0062] Please refer to FIG. 4, which is a timing diagram showing when the liquid crystal display device displays green pictures in the first embodiment of the application. As shown in FIG. 4, in a time period of one frame (comprising a time period **T1**, a time period **T2** and a time period **T3**), the method comprises the following testing steps:

[0063] During time period **T1**, providing a start voltage signal (for example, 15V) to the switch control line **207**, the scan line shorting bar **206** and the switch control lines **202** and **203**, to turn on all of the two first switch elements **204-1** and **205-1** and the two second switch elements **204-2** and **205-2** and also to turn on the thin film transistors in all the pixel units in the effective display area. The method also includes, during time period **T1**, providing a first voltage **V1** (for example, 5V) greater than a common electrode voltage **V0** (for example, 0V) to the data line shorting bar **201**, so that the liquid crystal display device displays black pictures.

[0064] The method also includes, during time period **T2**, providing a start voltage signal to the switch control line **207** and the scan line shorting bar **206**, to make signals on the three scan lines **G21**, **G22** and **G23** be the start voltage signal and thus turn on the thin film transistors in all the pixel units in the effective display area. The method also includes, during time period **T2**, providing a cut-off voltage signal to the switch control line **203**, so that the two second switch elements **204-2** and **205-2** are both in a cut-off state, simultaneously providing a start voltage signal to the switch control line **202** for the data line, so that the two first switch elements **204-1** and **205-1** are in a turn-on state, and providing a second voltage **V2** (for example, 0.01V) close to the common electrode voltage **V0** to the data line shorting bar **201**, so that light can penetrate the red pixel units and the green pixel units.

[0065] The method also includes, during time period **T3**, providing a start voltage signal to the switch control line **207** and the scan line shorting bar **206**, to turn on the thin film transistors in all the pixel units in the effective display area. The method also includes, during time period **T3**, providing a cut-off voltage to the switch control lines **202** and **203**, so that the two first switch elements **204-1** and **205-1** and the two second switch elements **204-2** and **205-2** are all in a cut-off state, and providing the first voltage **V1** greater than the common electrode voltage **V0** to the data line shorting bar **201**, so that light cannot penetrate the red pixel units, and finally green pictures are displayed in the display area of the liquid crystal display device. Therefore, the green pictures can be detected.

[0066] 3) Detecting Blue Pictures

[0067] Please refer to FIG. 5, which is a diagram showing when the liquid crystal display device displays the blue pictures in the first embodiment of the application. As shown in FIG. 5, in a time period of one frame (comprising a time period **T1** and a time period **T2**), the method comprises the following testing steps:

[0068] During time period **T1**, providing a start voltage signal (for example, 15V) to the switch control line **207**, the

scan line shorting bar **206** and the switch control lines **202** and **203**, to turn on the two first switch elements **204-1** and **205-1** and the two second switch elements **204-2** and **205-2** and also to turn on thin film transistors in all the pixel units in the effective display area. method also includes, during time period **T1**, providing the second voltage **V2** close to the common electrode voltage **V0** to the data line shorting bar **201**, so that the liquid crystal display device displays white pictures.

[0069] The method also includes, during time period **T2**, providing a start voltage signal to the switch control line **207** and the scan line shorting bar **206**, to make signals on the three scan lines **G21**, **G22** and **G23** be the start voltage signal and thus to turn on the thin film transistors in all the pixel units in the effective display area. The method also includes, during time period **T2**, providing a cut-off voltage to the switch control line **203**, so that the two second switch elements **204-2** and **205-2** are both in a cut-off state, and providing a start voltage to the switch control line **202**, so that the two first switch elements **204-1** and **205-1** are both in a turn-on state. The method also includes during time period **T2**, providing the first voltage **V1** greater than the common electrode voltage **V0** to the data line shorting bar **201**, so that light cannot penetrate the red pixel units and the green pixel units. As a result, blue pictures are displayed in the display area of the liquid crystal display device. Therefore, the blue pictures can be detected.

[0070] 4) Detecting Black Pictures

[0071] Black pictures may be produced using a method including providing a start voltage signal (for example, 15V) to the switch control line **207**, the scan line shorting bar **206** and the switch control lines **202** and **203**, to turn on the two first switch elements **204-1** and **205-1** and the two second switch elements **204-2** and **205-2**, and also to turn on thin film transistors in all the pixel units in the effective display area. The method also includes providing a first voltage **V1** (for example, 5V) greater than a common electrode voltage **V0** (for example, 0V) to the data line shorting bar **201**, so that the liquid crystal display device displays black pictures. As a result, line defects can be detected.

[0072] 5) Detecting White Pictures

[0073] White pictures may be produced using a method including providing a start voltage signal (for example, 15V) to the switch control line **207**, the scan line shorting bar **206** and the switch control lines **202** and **203**, to turn on the two first switch elements **204-1** and **205-1** and the two second switch elements **204-2** and **205-2**, and also to turn on thin film transistors in all the pixel units in the effective display area. The method also includes providing the second voltage **V2** close to the common electrode voltage **V0** to the data line shorting bar **201**, so that the liquid crystal display device displays white pictures. As a result, line defects can be detected.

[0074] With respect to the above several conditions, when the red, green and blue pictures are to be switched, for example, when the red pictures are required to be switched into the green pictures, firstly a start voltage control signal is provided to the switch control lines **202** and **203**, and a signal of the first voltage **V1** greater than the common electrode voltage **V0** is provided to the data line shorting bar **201**, so that the black pictures are displayed in the effective display area. Then a start voltage signal is provided to the switch control line **202**, and the second voltage **V2** close to the common electrode voltage **V0** is provided to the data line shorting bar **201**. In addition, a cut-off voltage is provided to the switch

control lines **202** and **203**, and the first voltage **V1** greater than the common electrode voltage **V0** is provided to the data line shorting bar **201**, so that the green pictures are displayed in the effective display area of the liquid crystal display device. Thus, the switching from the red pictures to the green pictures is realized. The switching modes between every two of the red, green and blue pictures are similar. When the black and grey pictures are required to be switched, only the voltage signal of the data line shorting bar need be regulated.

[0075] In conclusion, in the testing method for the liquid crystal display device provided in the embodiment, the liquid crystal display device can display pictures with various colors such as red, green, blue, black, white and grey by regulating the voltage signals on the plurality of switch control lines for the data lines, the switch control line for the scan lines, the data line shorting bar and the scan line shorting bar. Moreover, through holes are eliminated in the data line test area of the liquid crystal display device, so line defects of the scan lines or the data lines due to the through holes are eliminated in the test process, thereby increasing the testing efficiency, reducing the area occupied by the data line test area, and also reducing the switch signal delay of the switch elements.

[0076] In the above first embodiment, each data line set comprises three data lines for controlling the pixel units of three colors respectively. Further, each data line set can also comprise four data lines for controlling the pixel units of red, green, blue and white respectively. Correspondingly, the liquid crystal display device comprises three switch control lines for the data lines.

[0077] Such embodiments include a switch control line for the third data line, and each data line set further comprises a fourth data line. The switch control line for the third data line intersects with the fourth data line in each data line set, and a third switch element is formed at each intersection point. A gate electrode of the third switch element is arranged on the switch control line for the third data line, a source electrode of the third switch element is connected with the data line shorting bar through the second switch element, and a drain electrode of the third switch element is connected with the fourth data line at the intersection point.

[0078] In this case, the gate electrode of the third switch element is arranged on the switch control line for the third data line and is connected with the switch control line for the third data line. In addition, the source electrode of the third switch element is connected with the data line shorting bar through the second switch element, that is, the source electrode of the third switch element is connected with the drain electrode of the second switch element, and the source electrode of the second switch element is connected with the drain electrode of the first switch element, so that the third switch element is connected with the data line shorting bar.

Second Embodiment

[0079] In the first embodiment, after the test is completed, the connections of the data line shorting bar **201** with the data lines **D21**, **D22**, **D23**, **D24**, **D25** and **D26** are cut off by using laser along the line I-I' direction as shown in FIG. 2 to facilitate drive circuit module assembly in the next step. Such laser cutting is not necessary in the second embodiment. The second embodiment provides a liquid crystal display device, where the liquid crystal display device comprises three switch control lines for the data lines, where each data line set comprises three data lines. The second embodiment includes switch elements (a first switch element, a second switch ele-

ment and a third switch element) which are formed at intersections of each data line set and the three switch control lines for the data lines. All the data lines in the embodiment are connected with the data line shorting bar through the switch elements and the connecting lines, and none are directly connected with the data line shorting bar, so laser cutting is not required after the test is completed.

[0080] For convenience, three scan lines and two data line sets are taken as examples for detailed description below, but it should be realized that the number of the scan lines and the number of the data lines are not limited thereto.

[0081] Specifically refer to FIG. 6, which is a schematic diagram of a liquid crystal display device of the second embodiment of the application. An effective display area of the liquid crystal display device comprises three scan lines **G31**, **G32** and **G33**, two data line sets **D30-1** and **D30-2**, and a plurality of pixel units formed in areas encircled by intersecting the three scan lines **G31**, **G32** and **G33** with respective data lines in the two data line sets **D30-1** and **D30-2**, wherein the data line set **D30-1** comprises three adjacent data lines **D31**, **D32** and **D33**; and the data line set **D30-2** comprises three adjacent data lines **D34**, **D35** and **D36**, wherein the data lines **D31** and **D34** are red signal data lines and are connected with pixel electrodes R of the red pixel units; the data lines **D32** and **D35** are green signal data lines and are connected with pixel electrodes G of the green pixel units; and the data lines **D33** and **D36** are blue signal data lines and are connected with pixel electrodes B of the blue pixel units.

[0082] In this case, **D31** and **D34** are first data lines, **D32** and **D35** are second data lines, and **D33** and **D36** are third data lines.

[0083] It should be pointed out that in one data line set, the line sequence of respective color signal data lines is not limited thereto, and can be other sequence. For example, the data lines **D31** and **D34** are green signal data lines, the data lines **D32** and **D35** are red signal data lines, the data lines **D33** and **D36** are blue signal data lines and so on.

[0084] The data line test area of the liquid crystal display device comprises a data line shorting bar **301**, three switch control lines **302**, **303** and **310** for the data lines, a first switch element **304-1**, a second switch element **304-2** and a third switch element **304-3** formed at intersection points of the data line set **D30-1** and the switch control lines **302**, **303** and **310** for the data lines, and a first switch element **305-1**, a second switch element **305-2** and a third switch element **305-3** formed at intersection points of the data line set **D30-2** and the switch control lines **302**, **303** and **310** for the data lines.

[0085] In this case, the switch control line **302** for the data line is a switch control line for the first data line, the switch control line **303** for the data line is a switch control line for the second data line, and the switch control line **310** for the data line is a switch control line for the third data line.

[0086] Gate electrodes of the two first switch elements **304-1** and **305-1** are arranged on the switch control line **302** for the data line, gate electrodes of the two second switch elements **304-2** and **305-2** are arranged on the switch control line **303** for the data line, and gate electrodes of the two third switch elements **304-3** and **305-3** are arranged on the switch control line **310** for the data line.

[0087] Source electrodes of the two first switch elements **304-1** and **305-1** are connected with the data line shorting bar **301** through connecting lines **C31-1** and **C31-2**;

[0088] Source electrodes of the two second switch elements **304-2** and **305-2** are connected with the data line short-

ing bar **301** through the two first switch elements **304-1** and **305-1** and the connecting lines **C31-1** and **C31-2**, and the source electrode of the second switch element **304-2** is connected with the drain electrode of the first switch element **304-1**, so as to be connected with the data line shorting bar **301** through the first switch element **304-1** and the connecting line **C31-1**. In addition, the source electrode of the second switch element **305-2** is connected with the drain electrode of the first switch element **305-1**, so as to be connected with the data line shorting bar **301** through the first switch element **305-1** and the connecting line **C31-2**.

[0089] Source electrodes of the two third switch elements **304-3** and **305-3** are connected with the data line shorting bar **301** through the two first switch elements **304-1** and **305-1**, the two second switch elements **304-2** and **305-2** and the connecting lines **C31-1** and **C31-2**, and the source electrode of the third switch element **304-3** is connected with the drain electrode of the second switch element **304-2**, so as to be connected with the data line shorting bar **301** through the first switch element **304-1**, the second switch element **304-2**, and the connecting line **C31-1**. The source electrode of the third switch element **305-3** is connected with the drain electrode of the second switch element **305-2**, so as to be connected with the data line shorting bar **301** through the first switch element **305-1**, the second switch element **305-2**, and the connecting line **C31-2**.

[0090] Drain electrodes of the first switch elements **304-1** and **305-1** are arranged on the switch control line **302** for the data line and insulated with the switch control line **302** for the data line. Drain electrodes of the second switch elements **304-2** and **305-2** are arranged on the switch control line **303** for the data line and insulated with the switch control line **303** for the data line, and drain electrodes of the third switch elements **304-3** and **305-3** are arranged on the switch control line **310** for the data line and insulated with the switch control line **310** for the data line.

[0091] The drain electrodes of the two first switch elements **304-1** and **305-1** are connected with the data lines **D31** and **D34** respectively. The drain electrodes of the two second switch elements **304-2** and **305-2** are connected with the data lines **D32** and **D35** respectively, and the drain electrodes of the two third switch elements **304-3** and **305-3** are connected with the data lines **D33** and **D36** respectively.

[0092] The switch control line **302** for the data line is used for controlling connection and disconnection of the first switch elements **304-1** and **305-1**. In addition, the switch control line **303** for the data line is used for controlling connection and disconnection of the second switch elements **304-2** and **305-2**, and the switch control line **310** for the data line is used for controlling connection and disconnection of the third switch elements **304-3** and **305-3**. The scan line test area of the embodiment may be the same as that of the first embodiment.

[0093] In the embodiment, none of the data line shorting bar, the scan line shorting bar, the switch control lines for the data lines and the switch control line for the scan lines are connected with test terminals, so test signals can be directly applied to the data line shorting bar, the scan line shorting bar, the switch control lines for the data lines and the switch control line for the scan lines. However, it should be realized that the data line shorting bar, the scan line shorting bar, the switch control lines for the data lines and the switch control line for the scan lines can also be connected with the test terminals, and the test terminals can be made of the same

materials as or different materials from the data line shorting bar, the scan line shorting bar, the switch control lines for the data lines and the switch control line for the scan lines. In addition, a plurality of test terminals can be made of the same or different materials, for example, one part of the test terminals can be made of molybdenum, while the other part of the test terminals can be made of aluminum or aluminum alloy.

[0094] All the data lines in the embodiment are connected with the data line shorting bar through the switch elements and the connecting lines and none are directly connected with the data line shorting bar, so laser cutting is not required after the test is completed. Moreover, through holes are not required by the liquid crystal display device, so that the adverse influences of the through hole defect on the liquid crystal display device are eliminated, and smooth operation of the electric test can be ensured.

[0095] Moreover, as shown in FIG. 6, in the embodiment, if line widths of the data lines **D31**, **D32**, **D33**, **D34**, **D35** and **D36** are a_1 and distances between adjacent data lines are a_2 , then the distance W_3 between the data line **D31** and the data line **D36** is $6 \cdot a_1 + 5 \cdot a_2$; and if $a_1 = a_2 = a$, then $W_3 = 11a$. Compared with the prior art, in the embodiment, the width of the data line test area, which can be reduced, is $W_1 - W_3 = 12a$. It can be seen that in the embodiment, the area occupied by the data line test area is reduced, thereby facilitating the production of small-sized liquid crystal display devices. Moreover, compared with the prior art, in the embodiment, the number of the data lines superposed with each switch control line for the data line is reduced, thereby reducing the switch signal delay of the switch elements.

[0096] The liquid crystal display device in a normally white mode is taken as an example below to describe the testing method for the liquid crystal display device according to the embodiment in detail.

[0097] 1) Detecting Red Pictures

[0098] Please refer to FIG. 7, which is a timing diagram showing when the liquid crystal display device displays red pictures in the second embodiment of the application. As shown in FIG. 7, in a time period of one frame (comprising a time period **T1** and a time period **T2**), the method comprises the following testing steps:

[0099] During the time period **T1**, providing a start voltage signal (for example, 15V) to the switch control line **307**, the scan line shorting bar **306** and the switch control lines **302**, **303** and **310**, to turn on all of the two first switch elements **304-1** and **305-1**, the two second switch elements **304-2** and **305-2**, and the two third switch elements **304-3** and **305-3**, and also to turn on thin film transistors in all the pixel units in the effective display area. The method also includes, during time period **T1**, providing a first voltage **V1** (for example, 5V) greater than a common electrode voltage **V0** (for example, 0V) to the data line shorting bar **301**, so that the liquid crystal display device displays black pictures.

[0100] The method also includes, during time period **T2**, providing a start voltage signal to the switch control line **307** for the scan lines and the scan line shorting bar **306**, to make signals on the three scan lines **G31**, **G32** and **G33** be all high voltage signals and thus to turn on the thin film transistors in all the pixel units in the effective display area. The method also includes, during time period **T2**, providing a cut-off voltage to the switch control lines **303** and **310**, so that the second switch elements **304-2** and **305-2** and the third switch elements **304-3** and **305-3** are all in a cut-off state, and providing a start voltage to the switch control lines **302**, so that

the first switch elements **304-1** and **305-1** are in a turn-on state. The method also includes, during time period T2, providing a second voltage V2 (for example, 0.01V) close to the common electrode voltage V0 to the data line shorting bar **301**, and applying signals of the second voltage V2 to the red pixel electrodes through the red signal data lines D31 and D34, so that light can penetrate the red pixel units, and the red pictures are displayed in the display area of the liquid crystal display device. Therefore, the red pictures can be detected.

[0101] 2) Detecting Green Pictures

[0102] Please refer to FIG. 8, which is a timing diagram showing when the liquid crystal display device displays green pictures in the second embodiment of the application. As shown in FIG. 8, in a time period of one frame (comprising a time period T1, a time period T2 and a time period T3), the method comprises the following testing steps:

[0103] During time period T1, providing a start voltage signal (for example, 15V) to the switch control line **307**, the scan line shorting bar **306** and the switch control lines **302**, **303** and **310**, to turn on all of the two first switch elements **304-1** and **305-1**, the two second switch elements **304-2** and **305-2**, and the two third switch elements **304-3** and **305-3**, and also to turn on thin film transistors in all the pixel units in the effective display area. The method also includes, during time period T1 providing a first voltage V1 (for example, 5V) greater than a common electrode voltage V0 (for example, 0V) to the data line shorting bar **301**, so that the liquid crystal display device displays black pictures;

[0104] The method also includes, during time period T2, providing a start voltage signal to the switch control line **307** and the scan line shorting bar **306**, to make signals on the three scan lines G31, G32 and G33 be the start voltage signal and thus to turn on the thin film transistors in all the pixel units in the effective display area. The method also includes, during time period T2, providing a cut-off voltage signal to the switch control line **310**, so that the two third switch elements **304-3** and **305-3** are both in a cut-off state, and providing a start voltage signal to the switch control lines **302** and **303**, so that the two first switch elements **304-1** and **305-1** and the two second switch elements **304-2** and **305-2** are all in a turn-on state. The method also includes, during time period T2, providing a second voltage V2 (for example, 0.01V) close to the common electrode voltage V0 to the data line shorting bar **301**, so that light can penetrate the red pixel units and the green pixel units.

[0105] The method also includes, during time period T3, providing a start voltage signal to the switch control line **307** and the scan line shorting bar **306**, to turn on the thin film transistors in all the pixel units in the effective display area. The method also includes, during time period T3, providing a cut-off voltage to the switch control line **303** for the data line, so that the two second switch elements **304-2** and **305-2** are both in a cut-off state, and providing a start voltage to the switch control line **302** for the data line, so that the first switch elements **304-1** and **305-1** are in a turn-on state. The method also includes, during time period T3, providing the first voltage V1 greater than the common electrode voltage V0 to the data line shorting bar **301**, so that light cannot penetrate the red pixel units, so that green pictures are displayed in the display area of the liquid crystal display device. Therefore, the green pictures can be detected.

[0106] 3) Detecting Blue Pictures

[0107] Please refer to FIG. 9, which is a timing diagram showing when the liquid crystal display device displays the

blue pictures in the second embodiment of the application. As shown in FIG. 9, in a time period of one frame (comprising a time period T1 and a time period T2), the method comprises the following testing steps:

[0108] During time period T1, providing a start voltage signal (for example, 15V) to the switch control line **307**, the scan line shorting bar **306** and the switch control lines **302**, **303** and **310**, to turn on all of the two first switch elements **304-1** and **305-1**, the two second switch elements **304-2** and **305-2**, and the two third switch elements **304-3** and **305-3**, and also turn on thin film transistors in all the pixel units in the effective display area. The method also includes, during time period T1, providing the second voltage V2 close to the common electrode voltage V0 to the data line shorting bar **301**, so that the liquid crystal display device displays white pictures.

[0109] The method also includes, during time period T2, providing a start voltage signal to the switch control line **307** and the scan line shorting bar **306**, to make signals on the three scan lines G31, G32 and G33 be the start voltage signal and thus to turn on the thin film transistors in all the pixel units in the effective display area. The method also includes, during time period T2, providing a start voltage to the switch control lines **302** and **303** for the data lines, so that the two first switch elements **304-1** and **305-1** and the two second switch elements **304-2** and **305-2** are all in a turn-on state, and providing a cut-off voltage to the switch control line **310** for the data line, so that the two third switch elements **304-3** and **305-3** are both in a cut-off state. The method also includes, during time period T2, providing the first voltage V1 greater than the common electrode voltage V0 to the data line shorting bar **301**, so that light cannot penetrate the red pixel units and the green pixel units, and the blue pictures are displayed in the display area of the liquid crystal display device. Therefore, the blue pictures can be detected.

[0110] 4) Detecting Black Pictures

[0111] Black pictures may be produced using a method including providing a start voltage signal (for example, 15V) to the switch control line **307**, the scan line shorting bar **306** and the switch control lines **302**, **303** and **310**, to turn on all of the two first switch elements **304-1** and **305-1**, the two second switch elements **304-2** and **305-2**, and the two third switch elements **304-3** and **305-3**, and also turn on thin film transistors in all the pixel units in the effective display area. The method also includes providing a first voltage V1 (for example, 5V) greater than a common electrode voltage V0 (for example, 0V) to the data line shorting bar **301**, so that the liquid crystal display device displays black pictures. As a result, line defects may be detected.

[0112] 5) Detecting White Pictures

[0113] White pictures may be produced using a method including providing a start voltage signal (for example, 15V) to the switch control line **307**, the scan line shorting bar **306** and the switch control lines **302**, **303** and **310**, to turn on all of the two first switch elements **304-1** and **305-1**, the two second switch elements **304-2** and **305-2** and the two third switch elements **304-3** and **305-3** and also turn on thin film transistors in all the pixel units in the effective display area. The method also includes providing the second voltage V2 close to the common electrode voltage V0 to the data line shorting bar **301**, so that the liquid crystal display device displays white pictures. As a result, line defects may be detected.

[0114] In the testing method for the liquid crystal display device provided in the embodiment, the liquid crystal display device can display pictures with various colors such as red,

green, blue, black, white and grey by regulating the voltage signals on the plurality of switch control lines for the data lines, the switch control line for the scan lines, the data line shorting bar and the scan line shorting bar. All the data lines in the embodiment are connected with the data line shorting bar through the switch elements and are not directly connected with the data line shorting bar, so laser cutting is not used after the test is completed. In addition, through holes are not required by the liquid crystal display device, thereby eliminating the adverse influences of the through hole defect on the liquid crystal display device and ensuring smooth operation of the electric test. Moreover, compared with the prior art, in the embodiment, the area occupied by the data line test area is reduced to thereby facilitate the production of small-sized liquid crystal display devices. In addition, the number of the data lines superposed with the switch control lines for the data lines is reduced, and the switch signal delay of the switch elements is reduced.

[0115] In the second embodiment, each data line set comprises three data lines for controlling the pixel units of three colors respectively. Further, each data line set can also comprise four data lines for controlling the pixel units of red, green, blue and white respectively. Correspondingly, the liquid crystal display device comprises three switch control lines for the data lines. Specifically:

[0116] Such embodiments include a switch control line for the fourth data line, where each data line set further comprises a fourth data line, and the fourth data line in each data line set controls pixel units with a same color. The switch control line for the fourth data line intersects the fourth data line in each data line set, and a fourth switch element is formed at each intersection point. A gate electrode of the fourth switch element is arranged on the switch control line for the fourth data line, a source electrode of the fourth switch element is connected with the drain electrode of the third switch element, and a drain electrode of the fourth switch element is connected with the fourth data line at the intersection point.

[0117] In this case, the gate electrode of the fourth switch element is arranged on the switch control line for the fourth data line and connected with the switch control line for the fourth data line. The source electrode of the fourth switch element is connected with the drain electrode of the third switch element, so as to be connected with the data line shorting bar through the third switch element, the second switch element, the first switch element and the connecting line.

Third Embodiment

[0118] In the second embodiment, the source electrode of the first switch element is connected with the data line shorting bar through the connecting line, the source electrode of the second switch element is connected with the data line shorting bar through the first switch element and the connecting line, and the source electrode of the third switch element is connected with the data line shorting bar through the first switch element, the second switch element and the connecting line. Accordingly, a test signal of the data line shorting bar of the liquid crystal display device in the second embodiment is transmitted to the red signal data lines through one switch element, transmitted to the green signal data lines through two switch elements and transmitted to the blue signal data lines through three switch elements, where the signal is delayed by each of the switch elements. In addition, three time periods T1, T2 and T3 are required when a green picture

is displayed. The third embodiment provides a liquid crystal display device which has three switch control lines for data lines, each data line set comprises six data lines, and six switch elements are formed at intersection points of each data line set with the three switch control lines for the data lines. The test signal of the data line shorting bar of the liquid crystal display device can be transmitted to the corresponding data lines through only one switch element, so that the signal delay is reduced.

[0119] For convenience, four scan lines and a data line set are taken as examples for detailed description below. However, it should be realized that the number of the scan lines and the number of the data lines are not limited thereto.

[0120] Specifically refer to FIG. 10, which is a schematic diagram of a liquid crystal display device in the third embodiment of the application. The effective display area of the liquid crystal display device comprises four scan lines G41, G42, G43 and G44, a data line set D40, and a plurality of pixel units formed in areas encircled by intersecting the four scan lines G41, G42, G43 and G44 with data lines in the data line set D40.

[0121] The data line set D40 comprises six adjacent data lines D41, D42, D43, D44, D45 and D46, wherein the data lines D41 and D44 are red signal data lines and are connected with pixel electrodes R of the red pixel units; the data lines D42 and D45 are green signal data lines and are connected with pixel electrodes G of the green pixel units; and the data lines D43 and D46 are blue signal data lines and are connected with pixel electrodes B of the blue pixel units.

[0122] In this case, D41 is a first data line, D42 is a second data line, D43 is a third data line, D44 is a fourth data line, D45 is a fifth data line, and D46 is a sixth data line.

[0123] It should be pointed out that in one data line set, the line sequence of respective color signal data lines is not limited thereto, and can be another sequence. For example, the data lines D41 and D44 are green signal data lines, the data lines D42 and D45 are blue signal data lines, the data lines D43 and D46 are red signal data lines and so on.

[0124] A first switch element 402-1, a second switch element 402-2, a third switch element 403-1, a fourth switch element 403-2, a fifth switch element 410-1 and a sixth switch element 410-2 are formed at intersection points of the data line set D40 with the three switch control lines 402, 403 and 410 for the data lines.

[0125] In this case, the switch control line 402 for the data line is a switch control line for the first data line, the switch control line 403 for the data line is a switch control line for the second data line, and the switch control line 410 for the data line is a switch control line for the third data line.

[0126] Gate electrodes of the first switch element 402-1 and the second switch element 402-2 are arranged on the same switch control line 402 for the data line, gate electrodes of the third switch element 403-1 and the fourth switch element 403-2 are arranged on the same switch control line 403 for the data line, and gate electrodes of the fifth switch element 410-1 and the sixth switch element 410-2 are arranged on the same switch control line 410 for the data line.

[0127] Source electrodes of the first switch element 402-1 and the third switch element 403-1 are connected with the data line shorting bar 401 through the first connecting line C41, source electrodes of the second switch element 402-2 and the sixth switch element 410-2 are connected with the data line shorting bar through the third connecting line C43, and source electrodes of the fourth switch element 403-2 and

the fifth switch element **410-1** are connected with the data line shorting bar through the second connecting line **C42**.

[0128] Drain electrodes of the first switch element **402-1** and the second switch element **402-2** are arranged on the same switch control line **402** for the data line and connected with the data lines for controlling the green pixel units. In addition, drain electrodes of the third switch element **403-1** and the fourth switch element **403-2** are arranged on the same switch control line **403** for the data line and connected with the data lines for controlling the red pixel units, and drain electrodes of the fifth switch element **410-1** and the sixth switch element **410-2** are arranged on the same switch control line **410** for the data line and connected with the data lines for controlling the blue pixel units.

[0129] The scan line test area of the embodiment is as same as that of the first embodiment.

[0130] In the embodiment, none of the data line shorting bar, the scan line shorting bar, the switch control lines for the data lines and the switch control line for the scan lines are connected with test terminals, so test signals can be applied to the data line shorting bar, the scan line shorting bar, the switch control lines for the data lines and the switch control line for the scan lines. However, it should be realized that the data line shorting bar, the scan line shorting bar, the switch control lines for the data lines and the switch control line for the scan lines can also be connected with the test terminals, and the test terminals can be made of the same materials as or different materials from the data line shorting bar, the scan line shorting bar, the switch control lines for the data lines and the switch control line for the scan lines. In addition, a plurality of test terminals can be made of the same or different materials, for example, one part of the test terminals can be made of molybdenum, while the other part of the test terminals can be made of aluminum or aluminum alloy.

[0131] It can be seen from the above that the test signals of the data line shorting bar of the liquid crystal display device can be transmitted to the corresponding data lines only through one switch element, so that the signal delay is reduced.

[0132] Moreover, as shown in FIG. 10, in the embodiment, if line widths of the data lines **D41**, **D42**, **D43**, **D44**, **D45** and **D46** are **a41** and distances between adjacent data lines are **a42**, then the distance **W4** between the data line **D41** and the data line **D46** is $6 \cdot a_{41} + 5 \cdot a_{42}$; and if $a_{41} = a_{42} = a$, then $W4 = 11a$. Compared with the prior art, in the embodiment, the width of the data line test area, which can be reduced, is $W1 - W4 = 12a$, thereby reducing the area occupied by the data line test area and facilitating the production of small-sized liquid crystal display devices.

[0133] The liquid crystal display device in a normally white mode is taken as an example below to describe the testing method for the liquid crystal display device according to the embodiment in details.

[0134] 1) Detecting Red Pictures

[0135] Please refer to FIG. 11, which is a timing diagram showing when the liquid crystal display device displays the red pictures in the third embodiment of the application. As shown in FIG. 11, in a time period of one frame (comprising a time period **T1** and a time period **T2**), the method comprises the following testing steps:

[0136] During time period **T1**, providing a start voltage signal (for example, 15V) to the switch control line **407**, the scan line shorting bar **406** and the switch control lines **402**, **403** and **410**, to turn on all of the first switch element **402-1**,

the second switch element **402-2**, the third switch element **403-1**, the fourth switch element **403-2**, the fifth switch element **410-1**, and the sixth switch element **410-2**, and also turn on thin film transistors in all the pixel units in the effective display area. The method also includes, during time period **T1**, providing a first voltage **V1** (for example, 5V) greater than a common electrode voltage **V0** (for example, 0V) to the data line shorting bar **401**, so that the liquid crystal display device displays black pictures.

[0137] The method also includes, during time period **T2**, providing a start voltage signal to the switch control line **407** and the scan line shorting bar **406**, to make signals on the four scan lines **G41**, **G42**, **G43** and **G44** be the start voltage signals and thus turn on the thin film transistors in all the pixel units of the effective display area. The method also includes, during time period **T2**, providing a start voltage signal to the switch control line **403**, so that the third switch element **403-1** and the fourth switch element **403-2** are in a turn-on state, and providing a cut-off voltage signal to the switch control lines **402** and **410**, so that the first switch element **402-1**, the second switch element **402-2**, the fifth switch element **410-1** and the sixth switch element **410-2** are all in a cut-off state. The method also includes, during time period **T2**, providing a second voltage **V2** (for example, 0.01V) close to the common electrode voltage **V0** to the data line shorting bar **401**, and applying signals of the second voltage **V2** to the red pixel electrodes through the red signal data lines **D41** and **D44**, so that light can penetrate the red pixel units, and finally the red pictures are displayed in the display area of the liquid crystal display device. Therefore, the red pictures can be detected.

[0138] 2) Detecting Green Pictures

[0139] Please refer to FIG. 12, which is a timing diagram showing when the liquid crystal display device displays the green pictures in the third embodiment of the application. As shown in FIG. 12, in a time period of one frame (comprising a time period **T1** and a time period **T2**), the method comprises the following testing steps:

[0140] During time period **T1**, providing a start voltage signal (for example, 15V) to the switch control line **407**, the scan line shorting bar **406** and the switch control lines **402**, **403** and **410**, to turn on all of the first switch element **402-1**, the second switch element **402-2**, the third switch element **403-1**, the fourth switch element **403-2**, the fifth switch element **410-1**, and the sixth switch element **410-2**, and also to turn on thin film transistors in all the pixel units in the effective display area. The method also includes, during time period **T1**, providing a first voltage **V1** (for example, 5V) greater than a common electrode voltage **V0** (for example, 0V) to the data line shorting bar **401**, so that the liquid crystal display device displays black pictures.

[0141] The method also includes, during time period **T2**, providing a start voltage signal to the switch control line **407** and the scan line shorting bar **406**, to make signals on the four scan lines **G41**, **G42**, **G43** and **G44** be the start voltage signals and thus turn on the thin film transistors in all the pixel units in the effective display area. The method also includes, during time period **T2**, providing a cut-off voltage signal to the switch control lines **403** and **410**, so that the third switch element **403-1**, the fourth switch element **403-2**, the fifth switch element **410-1** and the sixth switch element **410-2** are all in a cut-off state, and providing a start voltage signal to the switch control line **402** for the data line, so that the first switch element **402-1** and the second switch element **402-2** are in a turn-on state. The method also includes, during **T2**, providing

a second voltage V2 (for example, 0.01V) close to the common electrode voltage V0 to the data line shorting bar 401, and applying signals of the second voltage V2 to the green pixel electrodes through the green signal data lines D42 and D45, so that light can penetrate the green pixel units. As a result, green pictures are displayed in the display area of the liquid crystal display device. Therefore, the green pictures can be detected.

[0142] 3) Detecting Blue Pictures

[0143] Please refer to FIG. 13, which is a timing diagram showing when the liquid crystal display device displays the blue pictures in the third embodiment of the application. As shown in FIG. 13, in a time period of one frame (comprising a time period T1 and a time period T2), the method comprises the following testing steps:

[0144] During time period T1, providing a start voltage signal (for example, 15V) to the switch control line 407, the scan line shorting bar 406 and the switch control lines 402, 403 and 410, to turn on all of the first switch element 402-1, the second switch element 402-2, the third switch element 403-1, the fourth switch element 403-2, the fifth switch element 410-1 and the sixth switch element 410-2 and also turn on thin film transistors in all the pixel units in the effective display area. The method also includes, during time period T1, providing a first voltage V1 (for example, 5V) greater than a common electrode voltage V0 (for example, 0V) to the data line shorting bar 401, so that the liquid crystal display device displays black pictures.

[0145] The method also includes, during time period T2, providing a start voltage signal to the switch control line 407 and the scan line shorting bar 406, to make signals on the four scan lines G41, G42, G43 and G44 be the start voltage signals and thus turn on the thin film transistors in all the pixel units in the effective display area. The method also includes, during time period T2, providing a cut-off voltage signal to the switch control lines 402 and 403, so that the first switch element 402-1, the second switch element 402-2, the third switch element 403-1 and the fourth switch element 403-2 are all in a cut-off state, and providing a start voltage signal to the data line switch control line 410, so that the fifth switch element 410-1 and the sixth switch element 410-2 are in a turn-on state. The method also includes, during time period T2, providing the second voltage V2 (for example, 0.01V) close to the common electrode voltage V0 to the data line shorting bar 401, and applying signals of the second voltage V2 to the blue pixel electrodes through the green signal data lines D43 and D46, so that light can penetrate the blue pixel units. As a result, blue pictures are displayed in the display area of the liquid crystal display device. Therefore, the blue pictures can be detected.

[0146] 4) Detecting Black Pictures

[0147] Black pictures may be produced using a method including providing a start voltage signal (for example, 15V) to the switch control line 407, the scan line shorting bar 406 and the switch control lines 402, 403 and 410, to turn on all of the first switch element 402-1, the second switch element 402-2, the third switch element 403-1, the fourth switch element 403-2, the fifth switch element 410-1, and the sixth switch element 410-2, and also to turn on thin film transistors in all the pixel units in the effective display area. The method also includes providing a first voltage V1 (for example, 5V) greater than a common electrode voltage V0 (for example,

0V) to the data line shorting bar 401, so that the liquid crystal display device displays black pictures. As a result, line defects may be detected.

[0148] 5) Detecting White Pictures

[0149] White pictures may be produced using a method including providing a start voltage signal (for example, 15V) to the switch control line 407, the scan line shorting bar 406 and the switch control lines 402, 403 and 410, to turn on all of the first switch element 402-1, the second switch element 402-2, the third switch element 403-1, the fourth switch element 403-2, the fifth switch element 410-1, and the sixth switch element 410-2, and also to turn on thin film transistors in all the pixel units in the effective display area. The method also includes providing the second voltage V2 (for example, 0.01V) close to the common electrode voltage V0 (for example, 0V) to the data line shorting bar 401, so that the liquid crystal display device displays the white pictures. As a result, line defects may be detected.

[0150] In the third embodiment, a data line set comprises six data lines for controlling the pixel units of three colors respectively. Further, a data line set can also comprise eight data lines for controlling the pixel units of red, green, blue and white respectively. Correspondingly, the liquid crystal display device comprises four switch control lines for the data lines.

[0151] In conclusion, in the testing method for the liquid crystal display device provided in the embodiment, the liquid crystal display device can display pictures with various colors such as red, green, blue, black, white and grey by regulating the voltage signals on the plurality of switch control lines for the data lines, the switch control line for the scan lines, the data line shorting bar and the scan line shorting bar. The test signals of the data line shorting bar of the liquid crystal display device can be transmitted to the corresponding data lines through only one switch element, and compared with the second embodiment, the signal delay is reduced. In addition, through holes are not required by the liquid crystal display device, thereby eliminating the adverse influences of the through hole defect on the liquid crystal display device and ensuring smooth operation of the electric test. Moreover, compared with the prior art, in the embodiment, the area occupied by the data line test area is reduced to thereby facilitate the production of small-sized liquid crystal display devices.

[0152] It should be pointed out that the respective embodiments have been described progressively in the specification, where the description of each of the embodiments has emphasized its difference(s) from the other embodiments, and mutual reference can be made to the description of the related part(s). Moreover, very simple forms and non-precise ratios are adopted in the drawings only for the purpose of conveniently and clearly illustrating the respective embodiments of the application in an auxiliary mode.

[0153] Those skilled in the art shall appreciate that the embodiments of the invention can be embodied as a method, a system or a computer program product. Therefore, embodiments may be all-hardware, all-software or software and hardware in combination. Furthermore, embodiments may include a computer program product as one or more computer useable storage mediums (including but not limited to a disk memory, a CD-ROM, an optical memory, etc.) in which computer useable program instructions are contained.

[0154] The invention has been described with reference to flow charts and/or block diagrams of the method, the device

(system) and the computer program product according to the embodiments of the invention. It shall be appreciated that respective flows and/or blocks in the flow charts and/or the block diagrams and combinations of the flows and/or the blocks in the flow charts and/or the block diagrams can be embodied in computer program instructions. These computer program instructions can be loaded onto a general-purpose computer, a specific-purpose computer, an embedded processor or a processor of another programmable data processing device to produce a machine so that the instructions executed on the computer or the processor of the other programmable data processing device create means for performing the functions specified in the flow(s) of the flow charts and/or the block(s) of the block diagrams.

[0155] These computer program instructions can also be stored into a computer readable memory capable of directing the computer or the other programmable data processing device to operate in a specific manner so that the instructions stored in the computer readable memory create manufactures including instruction means which perform the functions specified in the flow(s) of the flow charts and/or the block(s) of the block diagrams.

[0156] These computer program instructions can also be loaded onto the computer or the other programmable data processing device so that a series of operational steps are performed on the computer or the other programmable data processing device to create a computer implemented process so that the instructions executed on the computer or the other programmable device provide steps for performing the functions specified in the flow(s) of the flow charts and/or the block(s) of the block diagrams.

[0157] The foregoing description of the disclosed embodiments enables those skilled in the art to practice or use the invention. Numerous modifications to these embodiments will be apparent to those skilled in the art, and the general principle defined herein can be embodied in other embodiments without departing from the spirit or scope of the invention. Accordingly the invention will not be limited to these embodiments illustrated herein but shall conform to the broadest scope consistent with the principle and novel characteristics disclosed herein.

What is claimed is:

1. A liquid crystal display device, comprising:

a plurality of scan lines;

a plurality of data line sets intersecting the scan lines, wherein each data line set comprises a first data line, a second data line, a third data line;

a plurality of pixel units formed near intersections of the scan lines and the data lines,

wherein the first data line in each data line set controls pixel units having a first color, the second data line in each data line set controls pixel units having a second color, and the third data line in each data line set controls pixel units having a third color;

a data line shorting bar;

a switch control line for the first data lines of the data line sets;

a switch control line for the second data lines of the data line sets,

wherein the first data line in each data line set is connected with the data line shorting bar,

wherein the switch control line for the first data lines intersects the second data line in each data line set;

a first switch element formed at each intersection of the second data lines and the switch control line for the first data lines, wherein a gate electrode of the first switch element is arranged on the switch control line for the first data line, a source electrode of the first switch element is connected with the data line shorting bar, and a drain electrode of the first switch element is connected with the second data line, and

wherein the switch control line for the second data lines intersects the third data line in each data line set; and

a second switch element formed at each intersection of the third data lines and the switch control line for the second data lines, wherein a gate electrode of the second switch element is arranged on the switch control line for the second data line, a source electrode of the second switch element is connected with the data line shorting bar through the first switch element, and a drain electrode of the second switch element is connected with the third data line.

2. The liquid crystal display device of claim 1, further comprising:

a scan line shorting bar;

a switch control line for the scan lines; and

a plurality of switch elements for the scan lines,

wherein gate electrodes of the switch elements for the scan lines are arranged on the switch control line for the scan lines, wherein source electrodes of the switch elements are connected with the scan line shorting bar, and wherein drain electrodes of the switch elements are connected with the plurality of scan lines.

3. The liquid crystal display device of claim 2, wherein the drain electrodes of the switch elements are connected with the scan lines via through holes.

4. The liquid crystal display device of claim 1, wherein the source electrode of the second switch elements are connected with the drain electrodes of the first switch elements, and are connected with the data line shorting bar through the first switch elements.

5. The liquid crystal display device of claim 1, further comprising a switch control line for the third data line, wherein each data line set further comprises a fourth data line, and the fourth data line in each data line set controls pixel units with a fourth color,

wherein the switch control line for the third data line intersects the fourth data line in each data line set, and a third switch element is formed at each intersection of the fourth data line and the switch control line for the third data line, and

wherein a gate electrode of the third switch element is arranged on the switch control line for the third data line, a source electrode of the third switch element is connected with the drain electrode of the second switch element, and a drain electrode of the third switching element is connected with the fourth data line.

6. The liquid crystal display device of claim 1, further comprising at least one of the following test terminals:

a test terminal connected with the data line shorting bar; and

test terminals connected with the switch control lines for the data lines.

7. The liquid crystal display device of claim 2, further comprising at least one of the following test terminals:

a test terminal connected with the scan line shorting bar; and

a test terminal connected with the switch control line for the scan lines.

8. A liquid crystal display device, comprising:

a plurality of scan lines;

a plurality of data line sets intersecting the scan lines, wherein each data line set comprises a first data line, a second data line, a third data line;

a plurality of pixel units formed in areas near intersections of the scan lines and the data lines,

wherein the first data line in each data line set controls pixel units have a first color, the second data line in each data line set controls pixel units have a second color, and the third data line in each data line set controls pixel units have a third color;

a data line shorting bar;

a switch control line for the first data line of the data line sets;

a switch control line for the second data line of the data line sets;

a switch control line for the third data line of the data line sets,

wherein the switch control line for the first data line intersects the first data line in each data line set;

a first switch element formed at each intersection of the first data line and the switch control line for the first data, wherein a gate electrode of the first switch element is arranged on the switch control line for the first data line, wherein a source electrode of the first switch element is connected with the data line shorting bar, and wherein a drain electrode of the first switch element is connected with the first data line,

wherein the switch control line for the second data line intersects the second data line in each data line set;

a second switch element formed at each intersection of the second data line and the switch control line for the second data line, wherein a gate electrode of the second switch element is arranged on the switch control line for the second data line, wherein a source electrode of the second switch element is connected with the data line shorting bar through the first switch element, and wherein a drain electrode of the second switch element is connected with the second data line,

wherein the switch control line for the third data line intersects the third data line in each data line set; and

a third switch element formed at each intersection of the third data line and the switch control line for the data line, wherein a gate electrode of the third switch element is arranged on the switch control line for the third data line, wherein a source electrode of the third switch element is connected with the data line shorting bar through the first switch element and the second switch element, and wherein a drain electrode of the third switch element is connected with the third data line.

9. The liquid crystal display device of claim **8**, further comprising:

a switch control line for a fourth data line, wherein each data line set further comprises a fourth data line, and the fourth data line in each data line set controls pixel units having a fourth color, wherein the switch control line for the fourth data line intersects the fourth data line in each data line set, and a fourth switch element is formed at each intersection of the fourth data line and the switch control line for the fourth data line, wherein a gate electrode of the fourth switch element is arranged on the

switch control line for the fourth data line, wherein a source electrode of the fourth switch element is connected with the data line shorting bar through the third switch element, wherein the second switch element and the first switch element, and wherein a drain electrode of the fourth switch element is connected with the fourth data line.

10. The liquid crystal display device of claim **8**, further comprising:

a scan line shorting bar;

a switch control line for the scan lines;

a switch element for each of the scan lines;

a first connecting line for each of the scan lines, and

a second connecting line for each of the scan lines,

wherein gate electrodes of the switch elements for the scan lines are arranged on the switch control line for the scan lines, wherein source electrodes of the switch elements for the scan lines are connected with the scan line shorting bar, and wherein drain electrodes of the switch elements for the scan lines are connected with the plurality of scan lines. R

11. The liquid crystal display device of claim **9**, wherein the drain electrodes of the switch elements for the scan lines are connected with the scan lines via through holes.

12. The liquid crystal display device of claim **8**, wherein the source electrode of the second switch element is connected with the drain electrode of the first switch element, and is connected with the data line shorting bar through the first switch element, and wherein the source electrode of the third switch element is connected with the drain electrode of the second switch element, and is connected with the data line shorting bar through the second switch element and the first switch element.

13. The liquid crystal display device of claim **8**, further comprising at least one of the following test terminals:

a test terminal connected with the data line shorting bar; and

test terminals connected with the switch control lines for the data lines.

14. The liquid crystal display device of claim **9**, further comprising at least one of the following test terminals:

a test terminal connected with the scan line shorting bar; and

a test terminal connected with the switch control line for the scan lines.

15. A liquid crystal display device, comprising:

a plurality of scan lines;

a plurality of data line sets intersected the scan lines, wherein each data line set comprises a first data line, a second data line, a third data line, a fourth data line, a fifth data line and a sixth data line;

a plurality of pixel units formed near intersections of the scan lines and the data lines,

wherein the first data line and the fourth data line in each data line set control pixel units have a first color, the second data line and the fifth data line control pixel units have a second color, and the third data line and the sixth data line control pixel units have a third color;

a data line shorting bar;

a switch control line for the first data line of the data line sets;

a switch control line for the second data line of the data line sets;

a switch control line for the third data line of the data line sets,
 wherein the switch control line for the first data lines intersects the second data line and the fifth data line in each data line set;
 a first switch element formed at each intersection of the second data line and the switch control line for the first data lines, and wherein a second switch element is formed at each intersection of the fifth data line and the switch control line for the first data lines,
 wherein the switch control line for the second data lines intersects the first data line and the fourth data line in each data line set;
 a third switch element formed at each intersection of the first data line and the switch control line for the second data lines;
 a fourth switch element formed at each intersection of the fourth data line and the switch control line for the second data lines,
 wherein the switch control line for the third data lines intersects the third data line and the sixth data line in each data line set;
 a fifth switch element formed at each intersection of the third data line and the switch control line for the third data lines; and
 a sixth switch element formed at each intersection point of the sixth data line and the switch control line for the third data lines,
 wherein gate electrodes of the first switch element and the second switch element are arranged on the switch control line for the first data line, wherein gate electrodes of the third switch element and the fourth switch element are arranged on the switch control line for the second data line, and wherein gate electrodes of the fifth switch element and the sixth switch element are arranged on the switch control line for the third data line,
 wherein source electrodes of the first, second, third, fourth, fifth, and sixth switch elements are connected with the data line shorting bar,

wherein a drain electrode of the first switch element is connected with the second data line, wherein a drain electrode of the second switch element is connected with the fifth data line, wherein a drain electrode of the third switch element is connected with the first data line, wherein a drain electrode of the fourth switch element is connected with the fourth data line, wherein a drain electrode of the fifth switch element is connected with the third data line, and wherein a drain electrode of the sixth switch element is connected with the sixth data line.

16. The liquid crystal display device of claim **15**, further comprising:

a scan line shorting bar;
 a switch control line for the scan lines; and
 a plurality of switch elements for the scan lines,
 wherein gate electrodes of the switch elements for the scan lines are arranged on the switch control line for the scan lines, wherein source electrodes of the switch elements are connected with the scan line shorting bar, and wherein drain electrodes of the switch elements are connected with the plurality of scan lines.

17. The liquid crystal display device of claim **16**, wherein the drain electrodes of the switch elements are connected with the scan lines via through holes.

18. The liquid crystal display device of claim **15**, further comprising at least one of the following test terminals:

a test terminal connected with the data line shorting bar;
 and
 test terminals connected with the switch control lines for the data lines.

19. The liquid crystal display device of claim **16**, further comprising at least one of the following test terminals:

a test terminal connected with the scan line shorting bar;
 and
 a test terminal connected with the switch control line for the scan lines.

* * * * *

专利名称(译)	液晶显示装置		
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申请号	US13/962939	申请日	2012-08-30
[标]申请(专利权)人(译)	上海天马微电子有限公司		
申请(专利权)人(译)	上海天马微电子有限公司.		
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[标]发明人	HUANG XIANJUN		
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摘要(译)

公开了一种液晶显示装置及其测试方法。液晶显示装置包括多条扫描线和与扫描线交叉的多条数据线组，其中每条数据线组包括第一数据线，第二数据线和第三数据线。该器件还包括用于数据线的短路棒，用于数据线的第二开关控制线。通孔不用于连接数据线和短路棒，因此消除了由通孔缺陷引起的显示装置的缺点。另外，减小了用于测试结构的区域，从而可以生产小尺寸的液晶显示装置。

