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Liu et al.(10) **Pub. No.: US 2019/0129258 A1**(43) **Pub. Date: May 2, 2019**(54) **ARRAY SUBSTRATE, LIQUID CRYSTAL
DISPLAY PANEL AND DISPLAY APPARATUS****G06F 3/041** (2006.01)**H01L 27/12** (2006.01)(71) Applicants: **BOE Technology Group Co., Ltd.**,
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Technology Co., Ltd.**, Beijing (CN)(52) **U.S. Cl.**CPC **G02F 1/134309** (2013.01); **G02F 1/13338**
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2001/134354 (2013.01); **G06F 3/0412**
(2013.01); **H01L 27/12** (2013.01); **G02F**
2201/123 (2013.01); **G02F 1/1368** (2013.01)(72) Inventors: **Xiaona Liu**, Beijing (CN); **Feng Li**,
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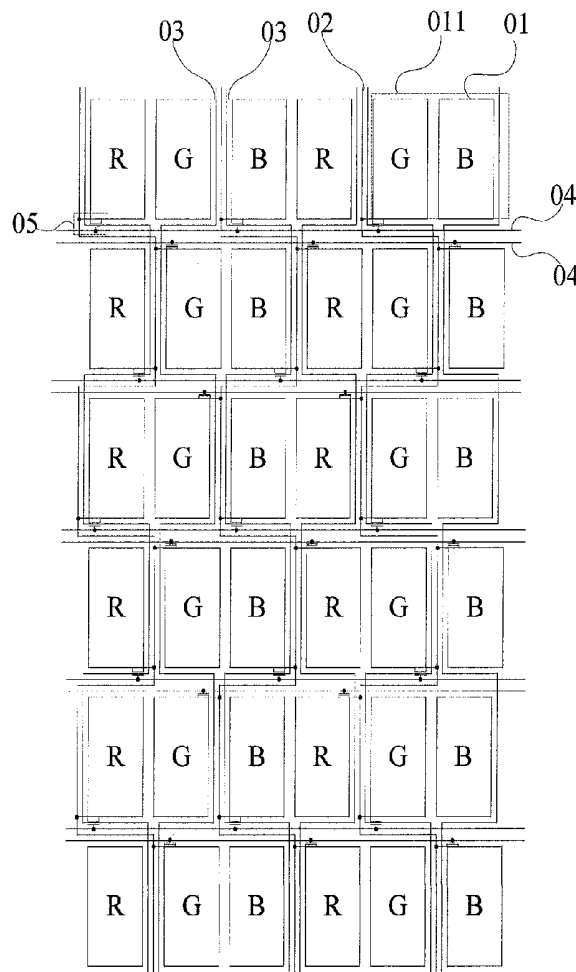
(57)

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The present disclosure relates to an array substrate, a liquid crystal display panel, and a display apparatus. In an embodiment, an array substrate includes: a plurality of pixel electrodes arranged in an array, data signal lines each connected to a corresponding one of the pixel electrodes, and touch control signal lines extending in a same direction as the data signal lines. Every two ones of the pixel electrodes adjacent to each other in a row direction constitute an electrode set. Each of the data signal lines is located in a gap between two adjacent electrode sets in a column direction, and the touch control signal lines are each disposed between one of the data signal lines and either of two adjacent electrode sets located adjacent to the one data signal line in the row direction.



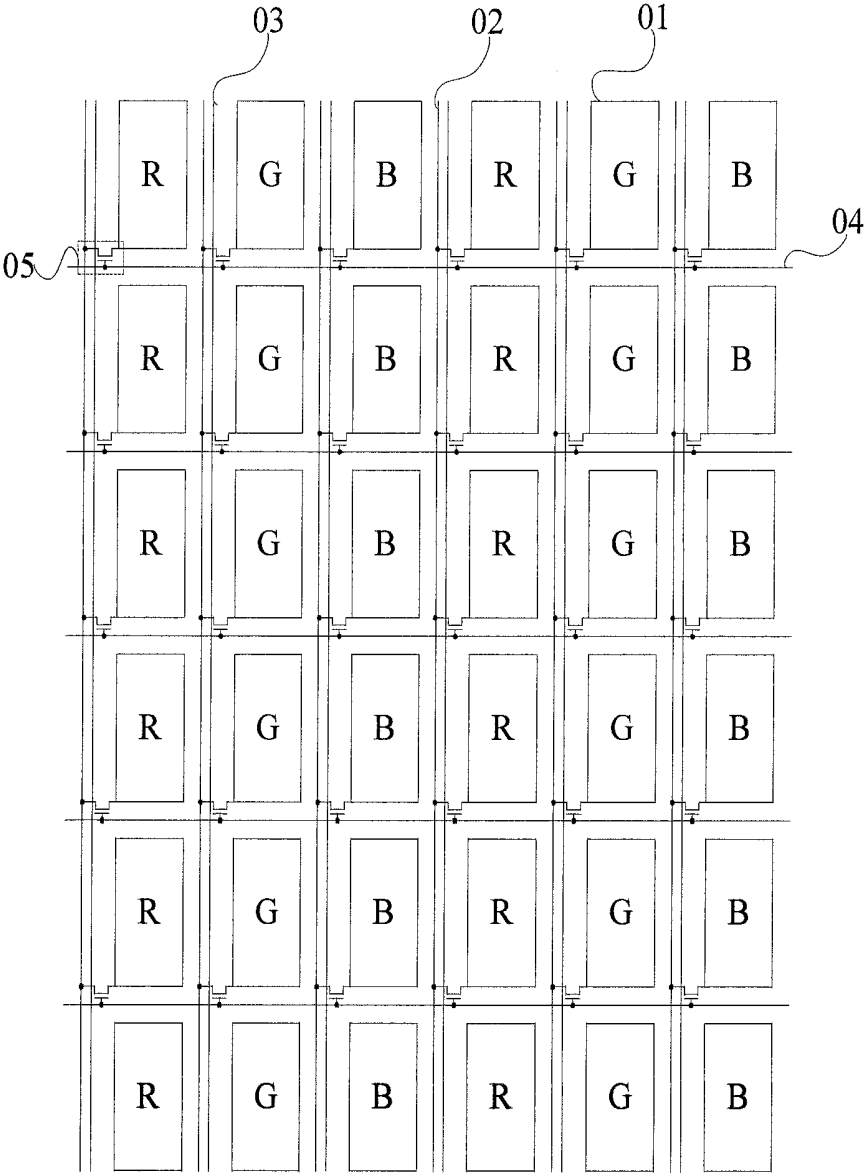


Fig. 1

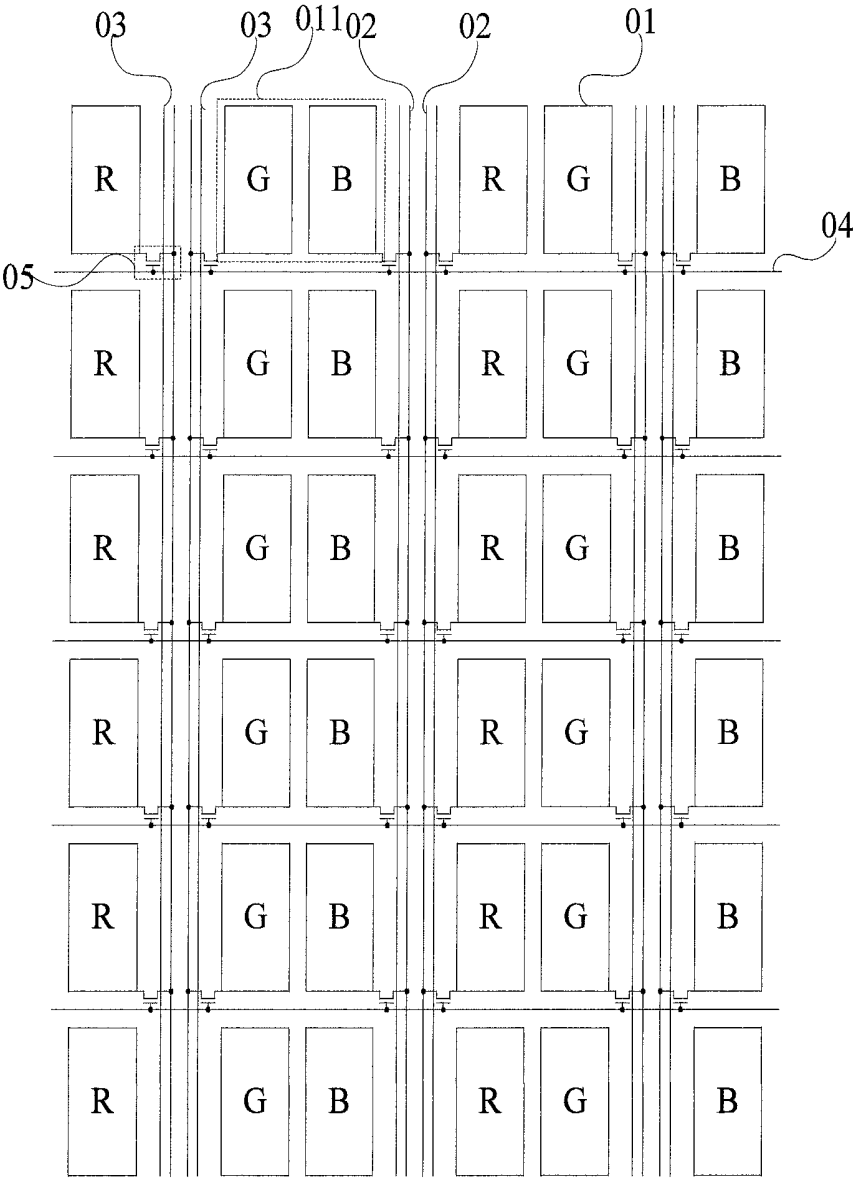


Fig. 2

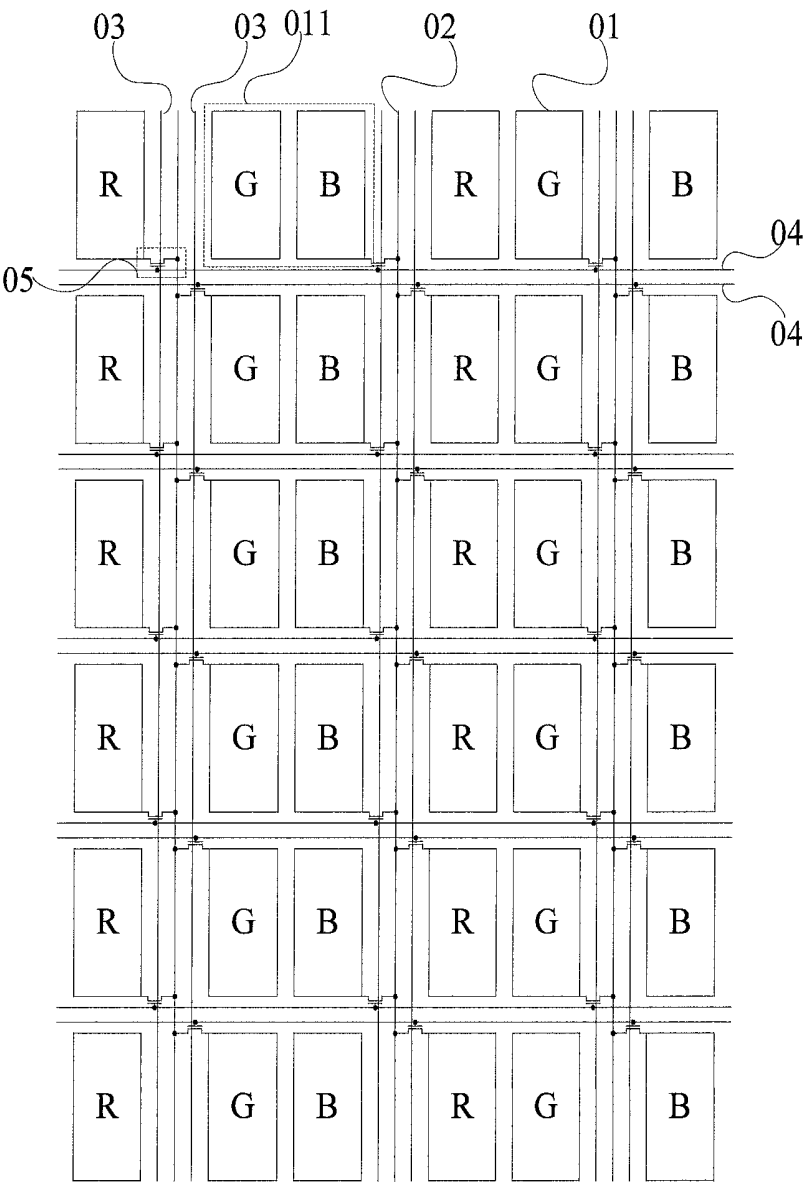


Fig. 3

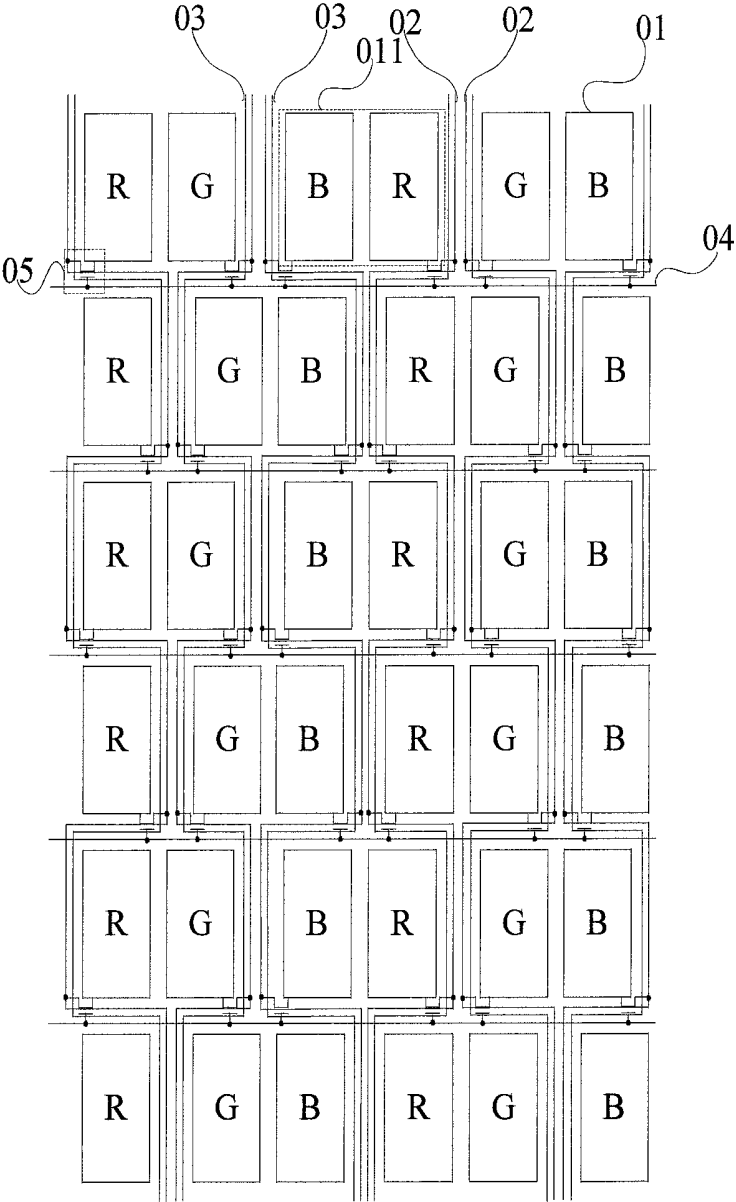


Fig. 4

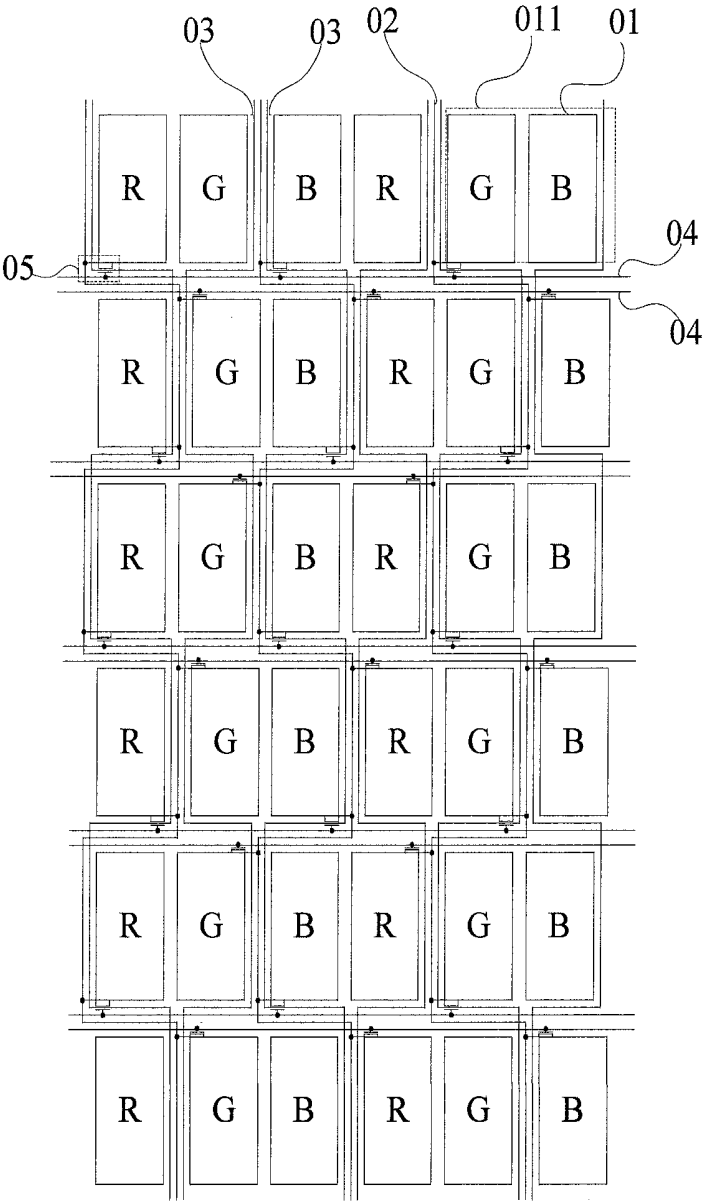


Fig. 5

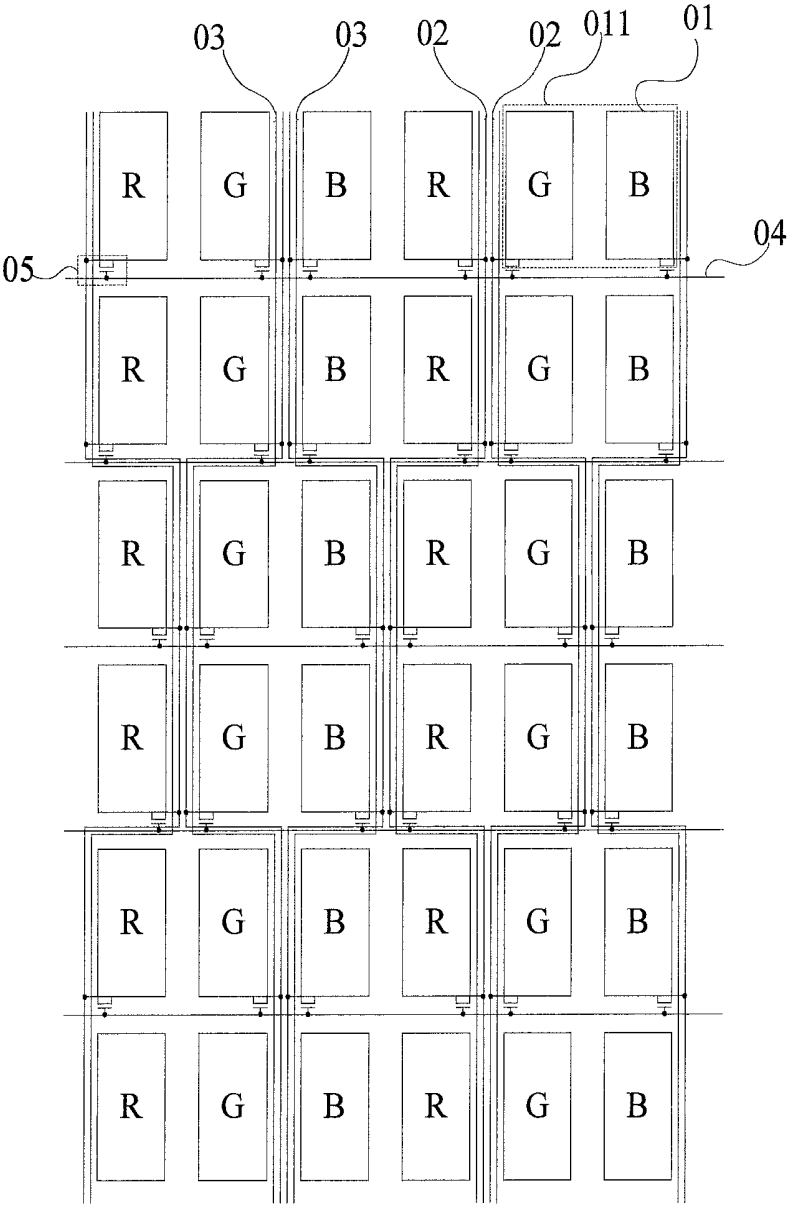


Fig. 6

ARRAY SUBSTRATE, LIQUID CRYSTAL DISPLAY PANEL AND DISPLAY APPARATUS

CROSS-REFERENCE TO RELATED APPLICATION

[0001] This application claims priority to Chinese Patent Application No. 201711022303.3 filed on Oct. 27, 2017 in the State Intellectual Property Office of China, the disclosure of which is hereby incorporated by reference in its entirety.

BACKGROUND

1. Technical Field

[0002] The present disclosure relates to the field of display technology, and particularly to an array substrate, a liquid crystal display panel, and a display apparatus.

2. Description of the Related Art

[0003] At present, touch control technology is more and more widely used in display products, especially in products of medium and small sizes. Addition of touch control components inside a display product can save cost and increase production capacity, and thus has great application space.

SUMMARY

[0004] According to an aspect of the present disclosure, there is provided an array substrate, comprising: a plurality of pixel electrodes arranged in an array, data signal lines each connected to a corresponding one of the pixel electrodes, and touch control signal lines extending in a same direction as the data signal lines; wherein:

[0005] every two ones of the pixel electrodes adjacent to each other in a row direction constitute an electrode set, each of the data signal lines is located in a gap between two adjacent electrode sets in a column direction, and the touch control signal lines are each disposed between one of the data signal lines and either of two electrode sets located adjacent to the one data signal line in the row direction.

[0006] In some embodiments, distances between the data signal lines and the pixel electrodes respectively connected to the data signal lines are equal to one another.

[0007] In some embodiments, one, where the data signal line is disposed, of the gaps between the adjacent pixel electrodes in the column direction has a greater width than that of other one, where no data signal line is disposed, of the gaps between the adjacent pixel electrodes in the column direction.

[0008] In some embodiments, the adjacent pixel electrodes in the column direction are staggered one another by a distance that is equal to a factor "X" multiplied by a width of the pixel electrode and the factor "X" is larger than zero and less than one.

[0009] In some embodiments, the data signal lines and the touch control signal lines extend in a straight line manner along the gaps between the electrode sets.

[0010] In some embodiments, the data signal lines and the touch control signal lines extend in a zigzag manner along the gaps between the electrode sets.

[0011] In some embodiments, the data signal line and the touch control signal line are located in different layers of films, respectively.

[0012] In some embodiments, each of the data signal lines is connected to the pixel electrodes located in a same column.

[0013] In some embodiments, the array substrate has a single-gate structure, and the data signal lines are in a one-to-one correspondence with the touch control signal lines.

[0014] In some other embodiments, the array substrate has a dual-gate structure, and each of the data signal lines is connected to two columns of pixel electrodes adjacent to the each data signal line, respectively.

[0015] In some embodiments, each of the data signal lines corresponds to two of the touch control signal lines, respectively.

[0016] According to another aspect of the present disclosure, there is provided a liquid crystal display panel comprising the array substrate of any one of the abovementioned embodiments.

[0017] In some embodiments, in the array substrate, distances between the data signal lines and the pixel electrodes respectively connected to the data signal lines are equal to one another.

[0018] In some embodiments, in the array substrate, one, where the data signal line is disposed, of the gaps between the adjacent pixel electrodes in the column direction has a greater width than that of other one, where no data signal line is disposed, of the gaps between the adjacent pixel electrodes in the column direction.

[0019] In some embodiments, in the array substrate, the adjacent pixel electrodes in the column direction are staggered one another by a distance that is equal to a factor "X" multiplied by a width of the pixel electrode and the factor "X" is larger than zero and less than one.

[0020] In some embodiments, in the array substrate, the data signal lines and the touch control signal lines extend in a straight line manner along the gaps between the electrode sets.

[0021] In some embodiments, in the array substrate, the data signal lines and the touch control signal lines extend in a zigzag manner along the gaps between the electrode sets.

[0022] In some embodiments, each of data signal lines is connected to the pixel electrodes in a same column; and the array substrate has a single-gate structure, and the data signal lines are in a one-to-one correspondence with the touch control signal lines.

[0023] In some other embodiments, the array substrate has a dual-gate structure, each of the data signal lines is connected to two columns of pixel electrodes adjacent to the each data signal line, respectively, and each of the data signal lines corresponds to two of the touch control signal lines, respectively.

[0024] According to yet another aspect of the present disclosure, there is provided a display apparatus comprising the liquid crystal display panel of any one of the abovementioned embodiments.

BRIEF DESCRIPTION OF THE DRAWINGS

[0025] FIG. 1 is a schematic diagram showing a structure of an array substrate;

[0026] FIG. 2 is a schematic diagram showing a structure of an array substrate according to an embodiment of the present disclosure;

[0027] FIG. 3 is a schematic diagram showing a structure of an array substrate according to another embodiment of the present disclosure;

[0028] FIG. 4 is a schematic diagram showing a structure of an array substrate according to still another embodiment of the present disclosure;

[0029] FIG. 5 is a schematic diagram showing a structure of an array substrate according to yet another embodiment of the present disclosure; and

[0030] FIG. 6 is a schematic diagram showing a structure of an array substrate according to a further embodiment of the present disclosure.

DETAILED DESCRIPTION OF THE EMBODIMENTS

[0031] Solutions of an array substrate, a liquid crystal display panel and a display apparatus according to embodiments of the present disclosure will be described hereinafter in detail in conjunction with the embodiments and with reference to the attached drawings.

[0032] Shapes and sizes of these components/elements shown in the attached drawings do not reflect real proportion of the array substrate, but are intended only to illustrate the contents of this disclosure.

[0033] As shown in FIG. 1, generally, pixel regions are bounded by data signal lines 02 and gate lines 04 crossing the data signal lines in an array substrate of a touch control display product. Pixel electrodes 01 are disposed in the pixel regions. The data signal lines 02 load data signals to the pixel electrodes 01 through switching transistors 05. Generally, touch control signal lines 03 are additionally disposed alongside the data signal lines 02, and the data signal lines 02 and the touch control signal lines 03 are fabricated from the same film. As shown in FIG. 1, it can be seen that the data signal line 02 connected to one of adjacent pixel electrodes 01 is close to the other of the adjacent pixel electrodes 01, so that a coupling capacitance C_{pd} between the other of the adjacent pixel electrodes 01 and the data signal line 02 is large, easily resulting in a poor picture display and thus adversely affecting picture display quality.

[0034] Accordingly, embodiments of the present disclosure provide an array substrate. As shown in FIG. 2 to FIG. 6, the array substrate comprises: a plurality of pixel electrodes 01 arranged in an array, data signal lines 02 each connected to a corresponding one of the pixel electrodes 01, and touch control signal lines 03 extending in the same direction as the data signal lines 02.

[0035] Every two ones of the pixel electrodes 01 adjacent to each other in a row direction constitute an electrode set 011. The data signal lines 02 are located in gaps between the adjacent electrode sets 011 in a column direction, and the touch control signal line 03 is disposed between each of two adjacent electrode sets 011 and the data signal line 02 located between the two adjacent electrode sets 011 in the row direction.

[0036] Specifically, in the array substrate according to the embodiments of the present disclosure, the touch control signal line 03 is disposed between the data signal line 02 and the pixel electrode 01 located on either side of the data signal line 02. Accordingly, it is ensured that each data signal line 02 is always located on a side of the touch control signal line 03 away from the pixel electrodes 01 connected to the each data signal line 02, and thus a distance between the data signal line 02 and the adjacent pixel electrode 01 is

increased, thereby greatly decreasing a coupling capacitance C_{pd} between the data signal line 02 and the adjacent pixel electrode 01, reducing probability of occurrence of a poor picture display and thus improving picture display quality.

[0037] In the array substrate according to the embodiments of the present disclosure, each data signal line 02 is set to be always located on a side of the touch control signal line 03 away from the pixel electrodes 01 connected to the each data signal line 02. Thereby, the distance between the each data signal line 02 and the adjacent pixel electrode 01 can be in a range that is greater than 0 and less than or equal to 15 μm .

[0038] In the existing array substrate, as shown in FIG. 1, one data signal line 02 is spaced from the pixel electrodes 01 on both sides of the one data signal line 02 by different distances. Specifically, one data signal line 02 is closer to adjacent one (on a left side of the data signal line 02 in FIG. 1) of the pixel electrodes 01, and is farther from the other pixel electrode 01 (on a right side of the data signal line 02 in FIG. 1) connected to the one data signal line 02, thereby resulting in great difference in coupling capacitance C_{pd} , and thus also causing a poor picture display and adversely affecting picture display quality.

[0039] In the array substrate according to the embodiments of the present disclosure, as shown in FIG. 2 to FIG. 6, distances between the data signal lines 02 and the pixel electrodes 01 respectively connected to the data signal lines 02 are equal to one another.

[0040] Specifically, the distances between the data signal lines 02 and the pixel electrodes 01 respectively connected to the data signal lines 02 are set to be the same. Accordingly, it can be ensured that there is the same coupling capacitance C_{pd} between each of the data signal lines 02 and a proximate column of pixel electrodes 01 (generally which are the pixel electrodes 01 connected to the each data signal line 02), alleviating a poor picture display caused by a difference of the coupling capacitances C_{pd} between the different data signal lines 02 and the pixel electrodes 01, and thus improving picture display quality.

[0041] In the array substrate according to the embodiments of the present disclosure, as shown in FIG. 2 to FIG. 6, every two adjacent pixel electrodes 01 in the row direction serve as the electrode set 011. The data signal line 02 is located in a gap between the adjacent electrode sets 011 in a column direction. It can be appreciated that the data signal lines 02 are disposed only in the gaps between alternate ones of the pixel electrodes 01 in each row.

[0042] In some embodiments, as shown in FIG. 2 and FIG. 3, the data signal lines 02 may be disposed only in odd-numbered ones of gaps between the pixel electrodes in each row in the column direction, and no data signal lines 02 are disposed in even-numbered ones of gaps between the pixel electrodes in the column direction. In this case, the electrode sets 011 may be regarded as being aligned with one another in the column direction. In other embodiments, as shown in FIG. 4 to FIG. 5, the data signal lines 02 may be disposed only in even-numbered ones of gaps between the pixel electrodes in each odd-numbered row in the column direction, and no data signal lines 02 are disposed in odd-numbered ones of gaps between the pixel electrodes in each odd-numbered row in the column direction; and the data signal lines 02 may be disposed only in odd-numbered ones of gaps between the pixel electrodes in each even-numbered row in the column direction, and no data signal lines 02 are

disposed in even-numbered ones of gaps between the pixel electrodes in each even-numbered row in the column direction. In this case, the rows of electrode sets **011** may be regarded as being staggered from one another in the column direction. In some further embodiments, as shown in FIG. 6, the data signal lines **02** may be disposed only in even-numbered gaps of gaps between the pixel electrodes in first, second, fifth and sixth rows in the column direction, and no data signal lines **02** are disposed in odd-numbered gaps of gaps between the pixel electrodes in the first, second, fifth and sixth rows in the column direction; and the data signal lines **02** may be disposed only in odd-numbered ones of gaps between the pixel electrodes in third and fourth rows in the column direction, and no data signal lines **02** are disposed in even-numbered gaps of gaps between the pixel electrodes in the third and fourth rows in the column direction. In this case, every two rows of electrode sets **011** may be regarded as being staggered from another two rows of electrode sets **011** in the column direction. In addition, in ones, where no data signal lines **02** are disposed, of the gaps between the pixel electrodes in the column direction, other signal lines, for example the touch control signal lines **03**, detection signal lines, or the like, may also be disposed according to actual requirements. There is no limitation on the other signal lines herein.

[0043] In the array substrate according to the embodiments of the present disclosure, one, where the data signal line is disposed, of the gaps between the adjacent pixel electrodes in the column direction has a greater width than that of other one, where no data signal line is disposed, of the gaps between the adjacent pixel electrodes in the column direction. As shown in FIG. 2 to FIG. 5, one (for example a gap between the adjacent electrode sets **011**), where the data signal line **02** is disposed, of the gaps between the adjacent pixel electrodes in the column direction has a greater width than that of other one (for example a gap between the adjacent pixel electrodes in one electrode set **011**), where no data signal line **02** is disposed, of the gaps between the adjacent pixel electrodes in the column direction.

[0044] Specifically, the width of one, where no data signal line **02** is disposed, of the gaps between the adjacent pixel electrodes in the column direction is reduced as far as possible, so that a usable area of the pixel electrode **01** can be increased, thereby raising an aperture ratio of a pixel. Of course, in specific application, as shown in FIG. 6, the widths of the gaps between the pixel electrodes in the column direction may also be set to be the same. There is no limitation on the widths of the gaps herein.

[0045] In the array substrate according to the embodiments of the present disclosure, as shown in FIG. 4 and FIG. 5, in the case where one, where the data signal line **02** is disposed, of the gaps in the column direction has a greater width than that of other one, where no data signal line is disposed, of the gaps in the column direction, it may be designed that the adjacent pixel electrodes **01** are staggered one another in the column direction by a distance that is equal to a factor "X" multiplied by a width of the pixel electrode and the factor "X" is larger than zero and less than one. In other words, the pixel electrodes **01** are not aligned with one another in the column direction. In this way, visual effect caused by a difference in the widths of the gaps between the pixel electrodes in the column direction can be alleviated.

[0046] In the array substrate according to the embodiments of the present disclosure, as shown in FIG. 2 and FIG. 3, the data signal lines **02** and the touch control signal lines **03** may extend in a straight line manner along the gaps between the electrode sets.

[0047] Specifically, when the data signal lines **02** and the touch control signal lines **03** extend in a straight line manner along the gaps between the electrode sets, it can be ensured that the data signal lines **02** and the touch control signal lines **03** have minimal wiring lengths, facilitating wiring design. However, no data signal lines **02** are disposed in the gaps between the adjacent pixel electrodes **01** in the electrode sets. Generally, since one, where the data signal line **02** is disposed, of the gaps between the adjacent pixel electrodes in the column direction has a greater width than that of other one, where no data signal line **02** is disposed, of the gaps between the adjacent pixel electrodes in the column direction, visual effect of picture display is adversely affected by nonuniformity of adjacent gaps along whole columns on the entire array substrate.

[0048] In the array substrate according to the embodiments of the present disclosure, as shown in FIG. 4 and FIG. 5, the data signal lines **02** and the touch control signal lines **03** may extend in a zigzag manner along the gaps between the electrode sets.

[0049] Specifically, when the data signal lines **02** and the touch control signal lines **03** extend in a zigzag manner along the gaps between the electrode sets, as shown in the figure, the data signal lines **02** and the touch control signal lines **03** may be regarded as having a layout of "zigzag" shape. In this way, arrangement of pixels can be optimized and visual effect caused by a difference in the widths of the gaps between the pixel electrodes in the column direction can be alleviated.

[0050] In the array substrate according to the embodiments of the present disclosure, as shown in FIG. 4 and FIG. 5, in every two adjacent rows of pixel electrodes, ones, where the data signal lines **02** are disposed, of the gaps between the pixel electrodes in the column direction have different positions. For example, the data signal lines **02** are disposed in even-numbered ones of gaps between the pixel electrodes in each odd-numbered row in the column direction, and no data signal lines **02** are disposed in odd-numbered ones of gaps between the pixel electrodes in each odd-numbered row in the column direction; and the data signal lines **02** are disposed in odd-numbered ones of gaps between the pixel electrodes in each even-numbered row in the column direction, and no data signal lines **02** are disposed in even-numbered ones of gaps between the pixel electrodes in each even-numbered row in the column direction. In other words, each data signal line **02** extending in the column direction is zigzagged between adjacent rows of pixel electrodes **01**. Rows of electrode sets **011** may be regarded as being arranged to be staggered one another in the column direction by a distance that is less than a width of one pixel electrode.

[0051] Of course, each data signal line **02** extending in the column direction may also be bent at any row of pixel electrodes **01**. For example, as shown in FIG. 6, each data signal line **02** may be zigzagged at every two rows of pixel electrodes **01**. There is no limitation on where the data signal lines are zigzagged herein.

[0052] In the array substrate according to the embodiments of the present disclosure, the data signal line **02** and

the touch control signal line **03** are respectively located in different layers of films to ensure that no short circuit will occur at crossing positions between the data signal lines **02** and the touch control signal lines **03**.

[0053] In the array substrate according to the embodiments of the present disclosure, as shown in FIG. 2 and FIG. 4, each data signal line **02** is connected to the same column of pixel electrodes **01**. In other words, the data signal lines **02** are in a one-to-one correspondence with these columns of the pixel electrodes **01**. In this case, generally, the array substrate has a single-gate structure. In other words, one row of pixel electrodes **01** are connected to one gate line **04** through corresponding switching transistors **05**.

[0054] Specifically, when the data signal lines **02** and the touch control signal lines **03** extend in a straight line manner along the gaps between the electrode sets, as shown in FIG. 2, each data signal line **02** is located on one side of the column of pixel electrodes **01** connected to it. When the data signal lines **02** and the touch control signal lines **03** extend in a zigzag manner along the gaps between the electrode sets, as shown in FIG. 4, the data signal lines **02** are located on both sides of the column of pixel electrodes **01** connected to them, respectively.

[0055] In the array substrate according to the embodiments of the present disclosure, when the array substrate has a single-gate structure, the data signal lines **02** may be in a one-to-one correspondence with the touch control signal lines **03**.

[0056] Specifically, as shown in FIG. 2 and FIG. 4, the touch control signal line **03** is disposed only between each data signal line **02** and the pixel electrodes **01** connected to the each data signal line **02**, and no touch control signal line **03** is disposed in one, where no data signal line **02** is disposed, of the gaps between the pixel electrodes in the column direction. Accordingly, it is ensured that one, where no data signal line **02** is disposed, of the gaps between the pixel electrodes in the column direction can have a minimal width. In this way, an aperture ratio of a pixel can be increased as far as possible.

[0057] In the array substrate according to the embodiments of the present disclosure, as shown in FIG. 3 and FIG. 5, the array substrate may also have a dual-gate structure. In other words, one row of pixel electrodes **01** are connected to two gate lines **04** through corresponding switching transistors **05**, respectively. In this way, half of the data signal lines **02** can be saved, thereby facilitating raising of an aperture ratio. In this case, each data signal line **02** is connected to two columns of pixel electrodes **01** adjacent to it, respectively.

[0058] In the array substrate according to the embodiments of the present disclosure, as shown in FIG. 3 and FIG. 5, one data signal line **02** corresponds to two touch control signal lines **03**.

[0059] Specifically, as shown in FIG. 3 and FIG. 5, the touch control signal line **03** is disposed only between each data signal line **02** and the pixel electrodes **01** connected to the each data signal line **02**, and no touch control signal line **03** is disposed in one, where no data signal line **02** is disposed, of the gaps between the pixel electrodes in the column direction. Accordingly, it is ensured that one, where no data signal line **02** is disposed, of the gaps between the pixel electrodes in the column direction can have a minimal width. In this way, an aperture ratio of a pixel can be increased as far as possible.

[0060] Based on the same inventive concept, embodiments of the present disclosure also provide a liquid crystal display panel comprising the array substrate according to the embodiments of the present disclosure. Since principles of the liquid crystal display panel for solving a problem are similar to those of the array substrate, implementations of the array substrate may be referred to for implementations of the liquid crystal display panel, and repeated contents are no longer described for the sake of brevity.

[0061] Based on the same inventive concept, embodiments of the present disclosure also provide a display apparatus comprising the liquid crystal display panel according to the embodiments of the present disclosure. The display apparatus may comprise any products or parts having a displaying function, such as a mobile phone, a tablet computer, a TV, a display, a notebook computer, a digital frame, and a navigator, and the likes. The embodiments of the liquid crystal display panel may be referred to for implementations of the display apparatus and repeated contents are no longer described for the sake of brevity.

[0062] The embodiments of the present disclosure provide the array substrate, the liquid crystal display panel, and the display apparatus. The array substrate comprises: a plurality of pixel electrodes arranged in an array, data signal lines each connected to a corresponding one of the pixel electrodes, and touch control signal lines extending in the same direction as the data signal lines. Every two ones of the pixel electrodes adjacent to each other in a row direction constitute an electrode set. Each of the data signal lines is located in a gap between two adjacent electrode sets in a column direction, and the touch control signal lines are each disposed between one of the data signal lines and either of two electrode sets located adjacent to the one data signal line in the row direction. The touch control signal line is disposed between the data signal line and each of the pixel electrodes located on both sides of the data signal line. Accordingly, it is ensured that the data signal line is always located on a side of the touch control signal line away from the pixel electrodes, and thus a distance between the data signal line and the adjacent pixel electrode is increased, thereby greatly decreasing a coupling capacitance between the data signal line and the pixel electrode, reducing probability of occurrence of a poor picture display and improving picture display quality.

[0063] Apparently, it will be understood by those skilled in the art that various changes, modifications, substitutions and alterations may be made without departing from the spirit and scope of the disclosure. Therefore, if the changes, modifications, substitutions and alterations of the present disclosure belong to the scope defined in the appended claims of the present disclosure and their equivalents, the present disclosure is intended to include the changes, modifications, substitutions and alterations.

What is claimed is:

1. An array substrate, comprising: a plurality of pixel electrodes arranged in an array, data signal lines each connected to a corresponding one of the pixel electrodes, and touch control signal lines extending in a same direction as the data signal lines; wherein:

every two ones of the pixel electrodes adjacent to each other in a row direction constitute an electrode set, each of the data signal lines is located in a gap between two adjacent electrode sets in a column direction, and the touch control signal lines are each disposed between

one of the data signal lines and either of two electrode sets located adjacent to the one data signal line in the row direction.

2. The array substrate of claim 1, wherein distances between the data signal lines and the pixel electrodes respectively connected to the data signal lines are equal to one another.

3. The array substrate of claim 1, wherein one, where the data signal line is disposed, of the gaps between the adjacent pixel electrodes in the column direction has a greater width than that of other one, where no data signal line is disposed, of the gaps between the adjacent pixel electrodes in the column direction.

4. The array substrate of claim 3, wherein the adjacent pixel electrodes in the column direction are staggered one another by a distance that is equal to a factor "X" multiplied by a width of the pixel electrode and the factor "X" is larger than zero and less than one.

5. The array substrate of claim 3, wherein the data signal lines and the touch control signal lines extend in a straight line manner along the gaps between the electrode sets.

6. The array substrate of claim 4, wherein the data signal lines and the touch control signal lines extend in a zigzag manner along the gaps between the electrode sets.

7. The array substrate of claim 1, wherein the data signal line and the touch control signal line are located in different layers of films, respectively.

8. The array substrate of claim 1, wherein each of the data signal lines is connected to the pixel electrodes located in a same column.

9. The array substrate of claim 8, wherein the array substrate has a single-gate structure, and the data signal lines are in a one-to-one correspondence with the touch control signal lines.

10. The array substrate of claim 1, wherein the array substrate has a dual-gate structure, and each of the data signal lines is connected to two columns of pixel electrodes adjacent to the each data signal line, respectively.

11. The array substrate of claim 10, wherein each of the data signal lines corresponds to two of the touch control signal lines, respectively.

12. A liquid crystal display panel comprising the array substrate of claim 1.

13. The liquid crystal display panel of claim 12, wherein in the array substrate, distances between the data signal lines and the pixel electrodes respectively connected to the data signal lines are equal to one another.

14. The liquid crystal display panel of claim 12, wherein in the array substrate, one, where the data signal line is disposed, of the gaps between the adjacent pixel electrodes in the column direction has a greater width than that of other one, where no data signal line is disposed, of the gaps between the adjacent pixel electrodes in the column direction.

15. The liquid crystal display panel of claim 14, wherein in the array substrate, the adjacent pixel electrodes in the column direction are staggered one another by a distance that is equal to a factor "X" multiplied by a width of the pixel electrode and the factor "X" is larger than zero and less than one.

16. The liquid crystal display panel of claim 14, wherein in the array substrate, the data signal lines and the touch control signal lines extend in a straight line manner along the gaps between the electrode sets.

17. The liquid crystal display panel of claim 15, wherein in the array substrate, the data signal lines and the touch control signal lines extend in a zigzag manner along the gaps between the electrode sets.

18. The liquid crystal display panel of claim 12, wherein each of data signal lines is connected to the pixel electrodes in a same column; and the array substrate has a single-gate structure, and the data signal lines are in a one-to-one correspondence with the touch control signal lines.

19. The liquid crystal display panel of claim 12, wherein the array substrate has a dual-gate structure, each of the data signal lines is connected to two columns of pixel electrodes adjacent to the each data signal line, respectively, and each of the data signal lines corresponds to two of the touch control signal lines, respectively.

20. A display apparatus comprising the liquid crystal display panel of claim 12.

* * * * *

专利名称(译)	阵列基板，液晶显示面板和显示装置		
公开(公告)号	US20190129258A1	公开(公告)日	2019-05-02
申请号	US15/919363	申请日	2018-03-13
[标]申请(专利权)人(译)	京东方科技集团股份有限公司 北京京东方显示技术有限公司		
申请(专利权)人(译)	京东方科技集团股份有限公司. 北京京东方显示技术有限公司.		
当前申请(专利权)人(译)	京东方科技集团股份有限公司. 北京京东方显示技术有限公司.		
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摘要(译)

阵列基板，液晶显示面板和显示装置技术领域在一个实施例中，阵列基板包括：排列成阵列的多个像素电极，每个连接到对应的一个像素电极的数据信号线，以及沿与数据信号线相同的方向延伸的触摸控制信号线。在行方向上彼此相邻的每两个像素电极构成电极组。每条数据信号线在列方向上位于两个相邻电极组之间的间隙中，并且触摸控制信号线各自设置在一条数据信号线和与一条数据相邻的两个相邻电极组中的任一条之间行方向的信号线。

