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Lee et al.(10) **Pub. No.: US 2006/0125751 A1**(43) **Pub. Date: Jun. 15, 2006**(54) **APPARATUS DRIVING A LIQUID CRYSTAL
DISPLAY****Publication Classification**(76) Inventors: **Chang-Hun Lee**, Kyungki-do (KR);
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G09G 3/36 (2006.01)
(52) **U.S. Cl.** **345/89**Correspondence Address:
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McLean, VA 22102-4215 (US)(57) **ABSTRACT**

The present invention is directed to an apparatus of driving a liquid crystal display device including a plurality of pixels. The driving apparatus includes a gray voltage generator for generating a plurality of gray voltages, an image signal driver for selecting gray voltages corresponding to color signals and applying the selected gray voltages as image signal voltages to the pixels, and a signal controller for controlling the image signal driver based on the color signals and input control signals for controlling the color signals. At least two successive first gray voltages among the plurality of gray voltages are variable and at least two successive second gray voltages among the plurality of gray voltages are fixed.

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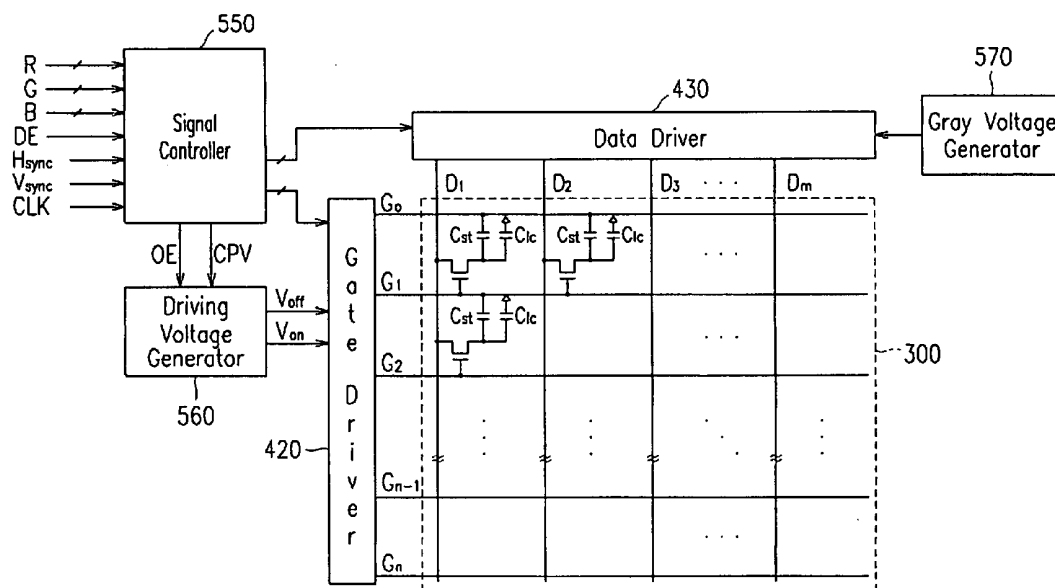


FIG. 1

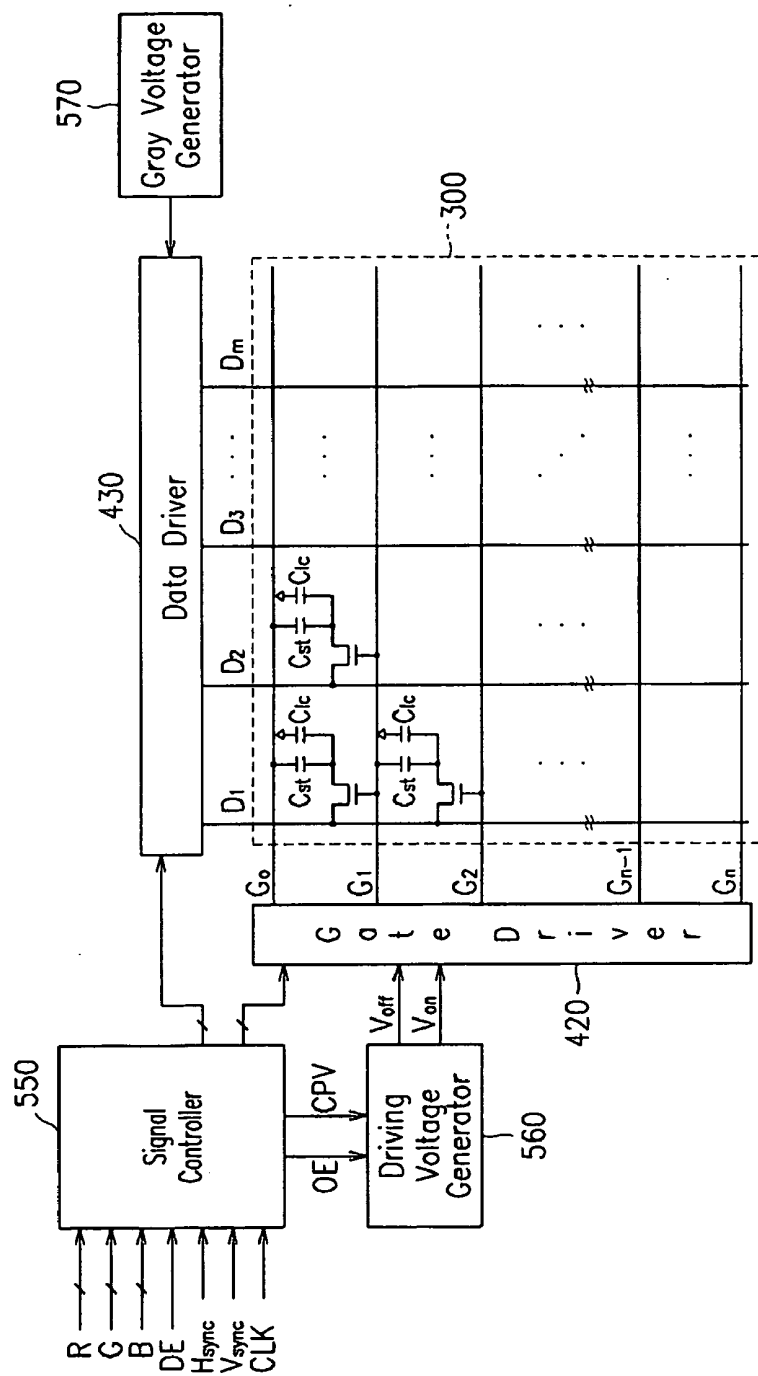


FIG. 2

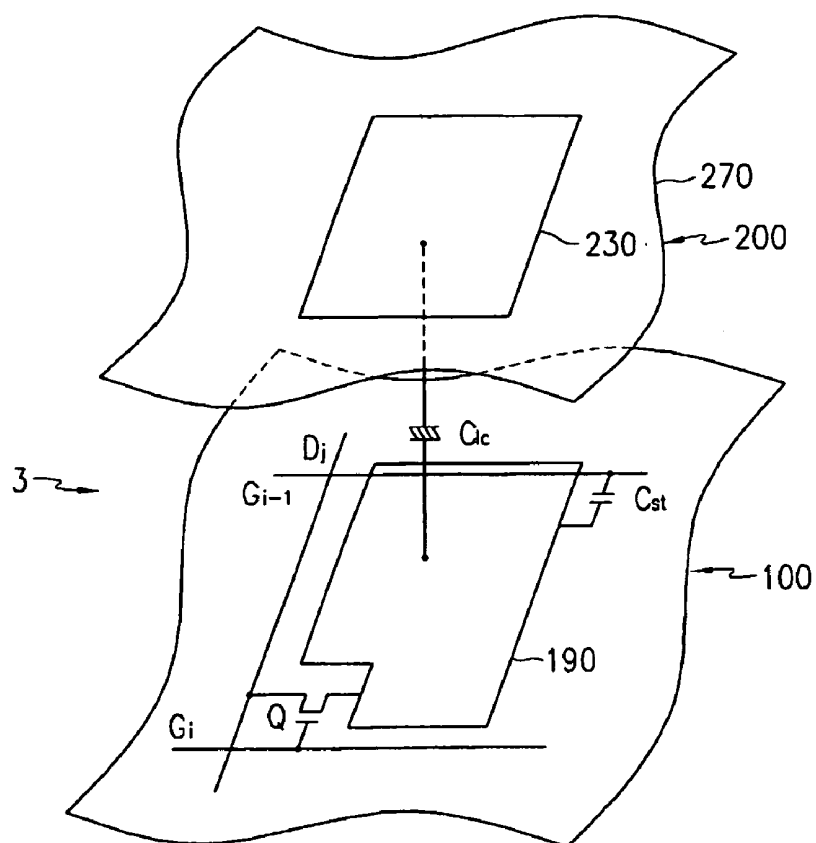


FIG.3

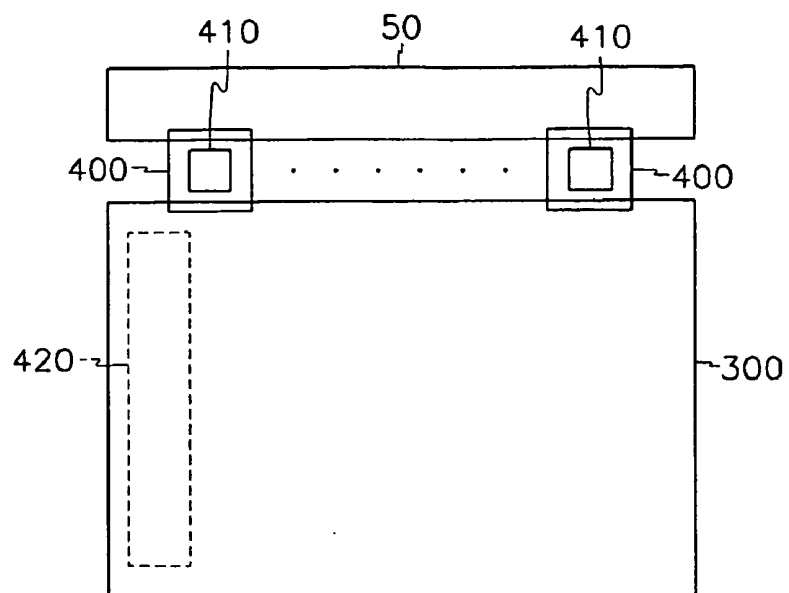
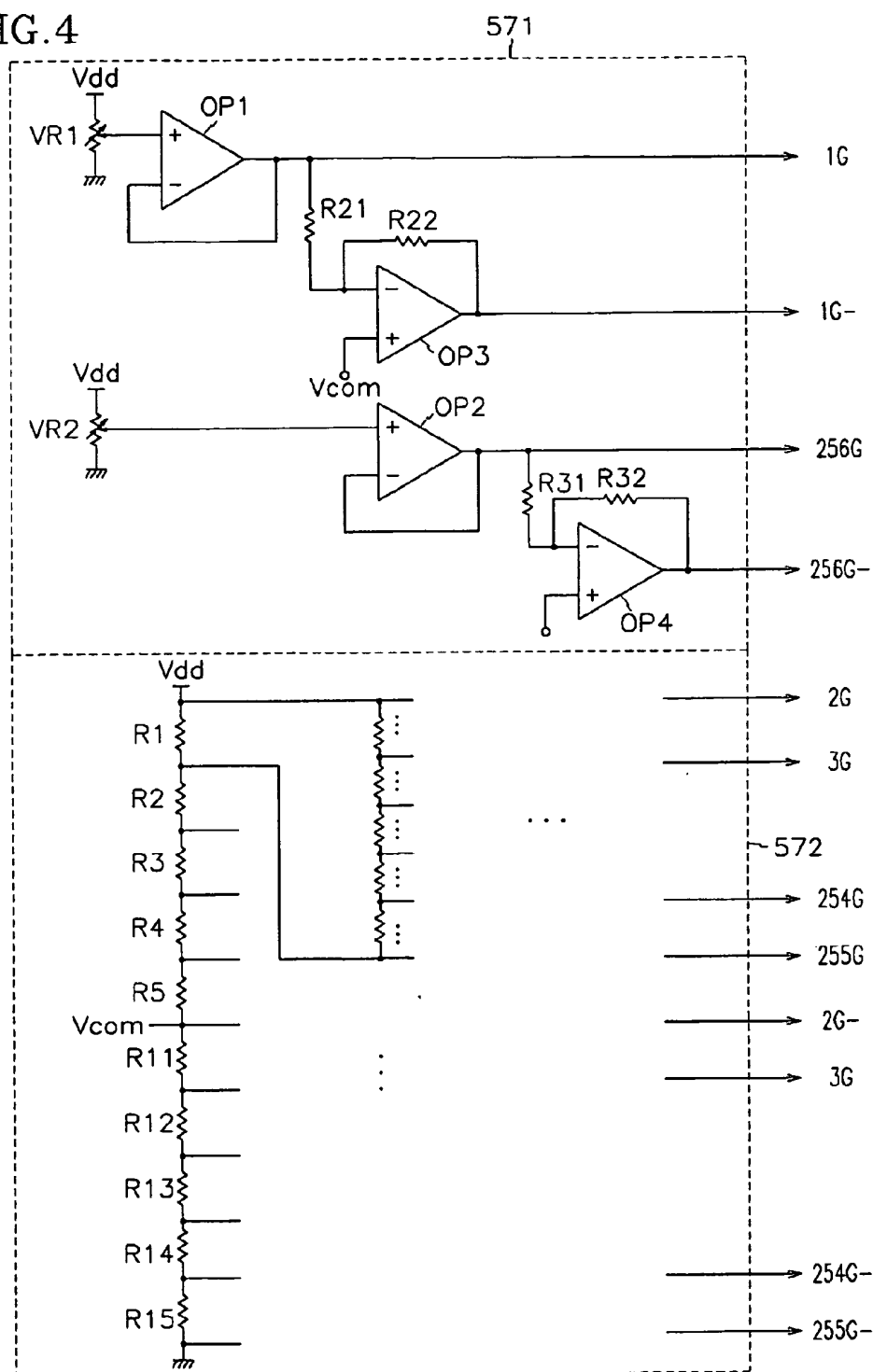
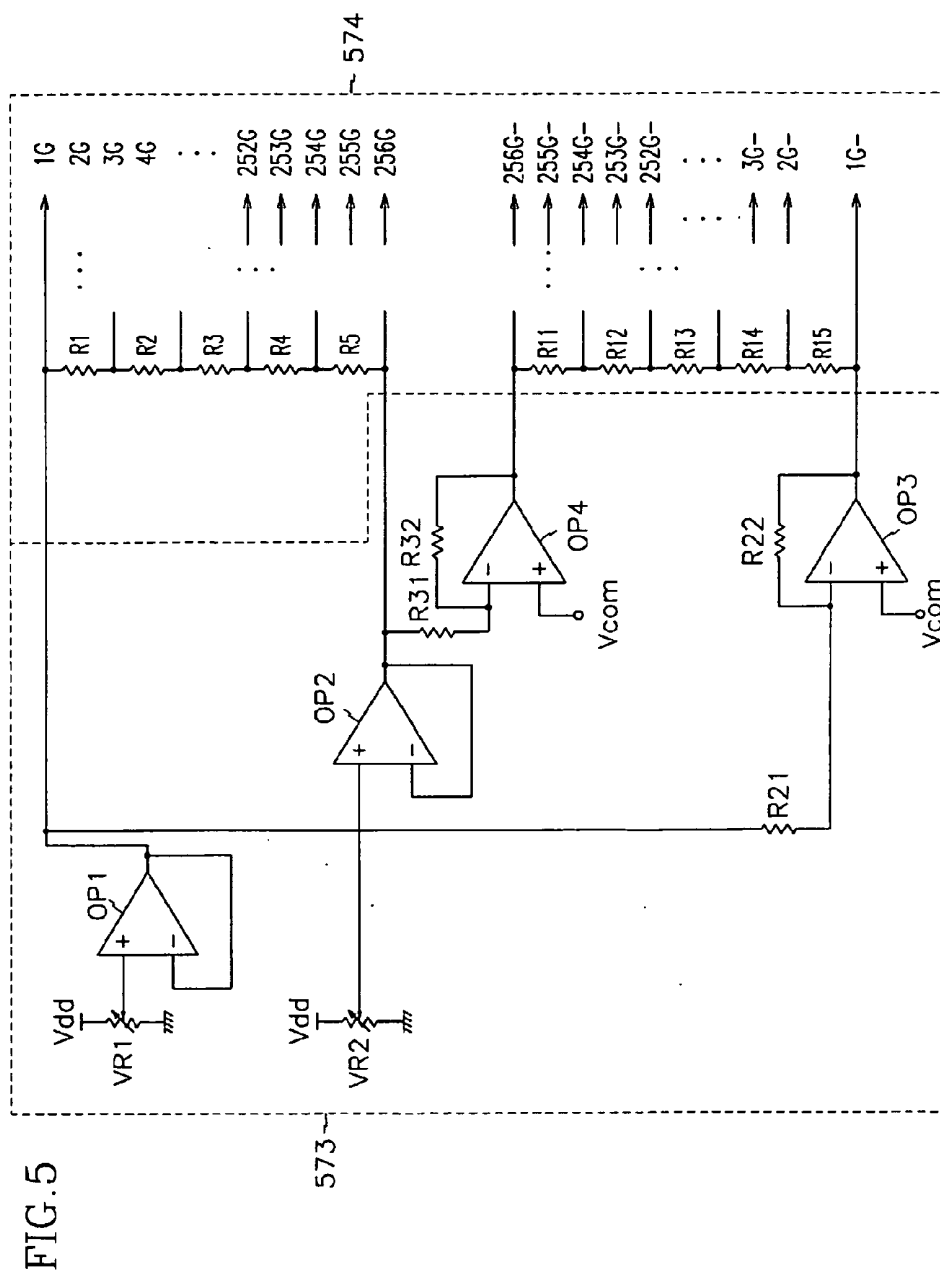


FIG. 4





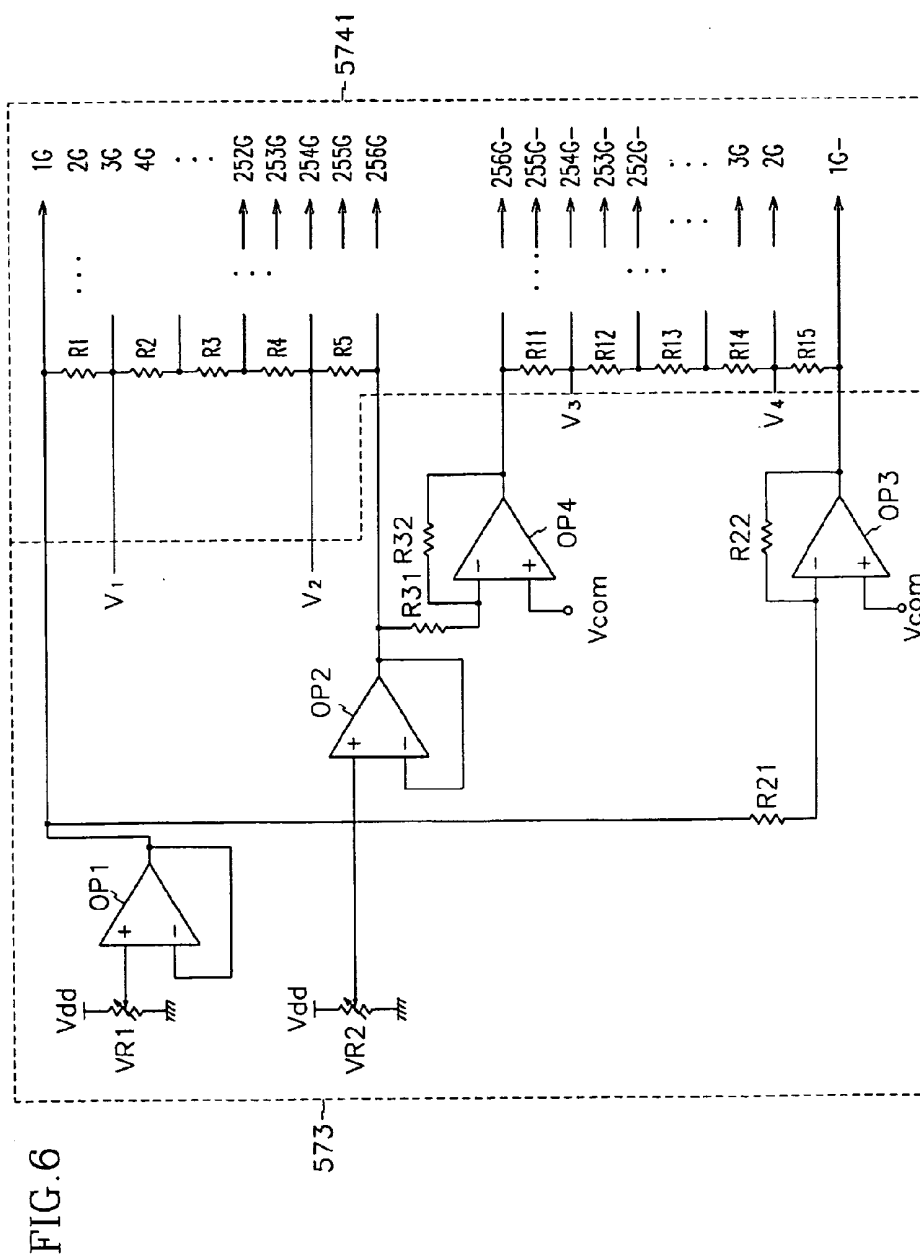
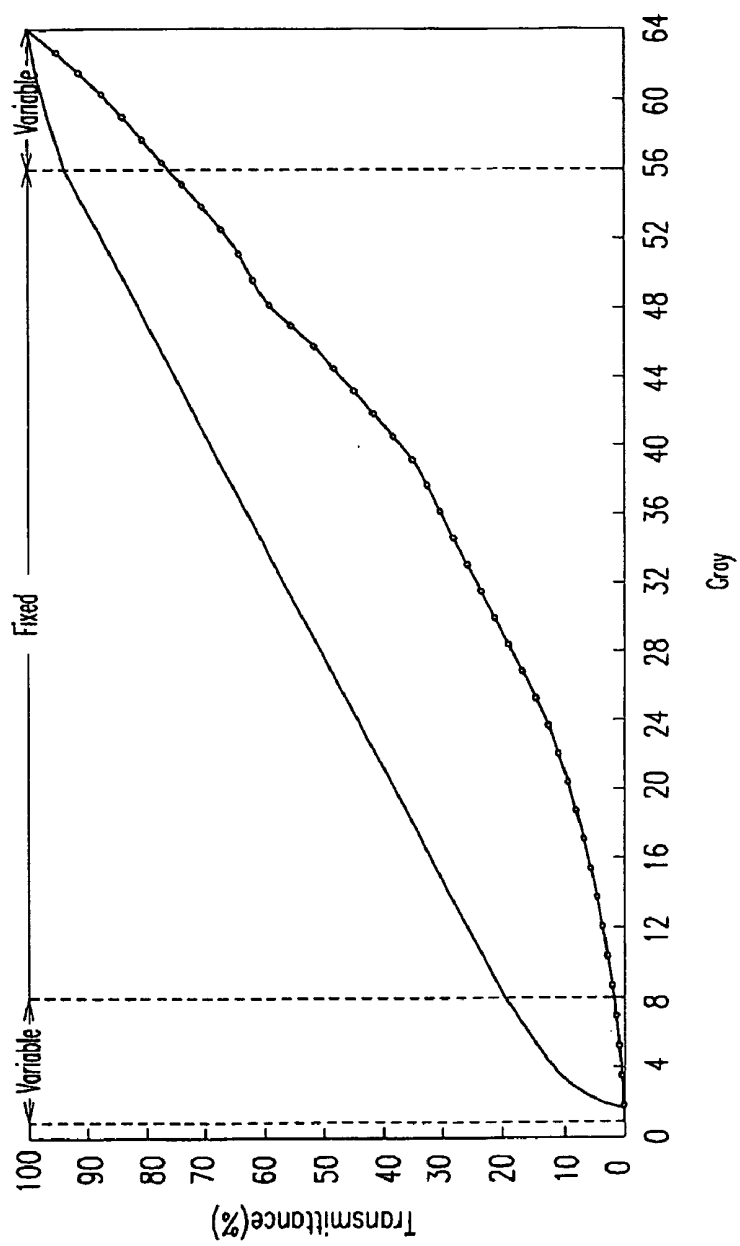


FIG. 7



APPARATUS DRIVING A LIQUID CRYSTAL DISPLAY

BACKGROUND OF THE INVENTION

[0001] (a) Field of the Invention

[0002] The present invention relates generally to an apparatus of driving a liquid crystal display, and more particularly to a driving apparatus for a liquid crystal display for optionally adjusting white gray voltages and black gray voltages supplied to the liquid crystal display.

[0003] (b) Description of the Related Art

[0004] A liquid crystal display (LCD) is a display apparatus for obtaining desired images by applying electric field to liquid crystal material having dielectric anisotropy and injected between two panels, and adjusting the intensity of the electric field so that the amount of light passing through the panels is controlled. Such an LCD is representative of a flat panel display device handy to carry. As a kind of LCD, a TFT-LCD employing thin film transistors (TFTs) as switching devices is mainly being used.

[0005] Although most of the LCDs have used a twisted nematic (TN) mode, recently an LCD in an optically compensated bend (OCB) mode for improving a slow response time and a narrow viewing angle is briskly being studied.

[0006] The OCB mode LCD uses a bend alignment of liquid crystal molecules. However, since the bend alignment is broken under application of a voltage lower than a specific threshold voltage V_c , the OCB mode LCD can be driven by a voltage larger than the threshold voltage V_c .

[0007] However, since a voltage to transmittance (VT) curve for the OCB mode LCD is unstable, it is difficult to secure a proper driving because of serious variation of characteristics of products depending on process deviation, for example, thickness of a liquid crystal layer and alignment deviations.

[0008] In other words, in the case of a normally white mode OCB LCD, it is difficult to adjust the luminance since a significant luminance difference is generated even by a minute voltage variation due to a sharp gradient of the VT curve in a white region corresponding to low voltages. On the other hand, since the VT curve in a black region corresponding to high voltages has several inflection points, there occurs a gray inversion that the higher the voltage is, the higher the luminance is. Since such a phenomenon produces significant differences in gray characteristics even by minute process deviations of products, there occurs a problem that the luminance is different even for the same gray voltage.

SUMMARY OF THE INVENTION

[0009] In considerations of the above problems, it is a motivation of the present invention to easily adjust gray voltages according to gray characteristics.

[0010] To achieve the motivation, according to an aspect of the present invention, a apparatus of driving a liquid crystal display including a plurality of pixels includes: a gray voltage generator for generating a plurality of gray voltages; an image signal driver for selecting gray voltages corresponding to color signals and applying the selected

gray voltages as image signal voltages to the pixels, and a signal controller for controlling display of the image signal driver based on the color signals and input control signals for controlling the color signals, wherein at least two successive first gray voltages among the plurality of gray voltages are variable and at least two successive second gray voltages among the plurality of gray voltages are fixed.

[0011] The first gray voltages may be varied by using a variable resistor and includes a black voltage or a white voltage.

[0012] The driving apparatus can vary at least two successive third gray voltages among the plurality of gray voltages.

[0013] Preferably, the first gray voltages include a black voltage, the third gray voltages include a white voltage, and the first gray voltages and the third gray voltages may be varied independently.

[0014] The plurality of gray voltages preferably include a plurality of pairs of gray voltages, each pair of gray voltages having opposite polarity and equal magnitude.

[0015] At least one pair among the plurality of pairs of gray voltages are preferably varied in association with each other.

[0016] The variable resistor and the signal controller are preferably mounted on a printed circuit board external to the liquid crystal display.

[0017] According to another aspect of the present invention, an apparatus of driving a liquid crystal display including a plurality of pixels includes: a gray voltage generator for generating a plurality of gray voltages; a data driver for selecting gray voltages corresponding to color signals and applying the selected gray voltages as image signal voltages to the pixels; and a signal controller for controlling the image signal driver by means of the color signals and control input signals for controlling the color signals, wherein the gray voltage generator includes a first variable resistor unit connected to the data driver and providing a first variable gray voltage, a first array of resistors connected between the first variable resistor and a first voltage and generating a plurality of second gray voltages to be provided for the data driver, and a second array of resistors having a terminal connected to the first voltage and generating a plurality of third gray voltages.

[0018] Preferably, the gray voltage generator includes a polarity inverter connected to the data driver for inverting polarity of a signal from the first variable resistor unit and providing the polarity-inverted signal to the data driver.

[0019] Preferably, the first variable resistor unit includes a variable resistor and a buffer connected between a variable terminal of the variable resistor and an input terminal of the polarity inverter and outputting the first variable gray voltage.

[0020] Preferably, the polarity inverter includes an operational amplifier having an inverting terminal connected to the buffer, a non-inverting terminal supplied with an inverting reference signal, and an output terminal, a first resistor connected between the buffer and the inverting terminal, and the second resistor connected between the inverting terminal and the output terminal.

[0021] Preferably, the gray voltage generator further includes a second variable resistor unit connected to the data driver for providing a fourth variable gray voltage, the gray voltage generator further includes a third array of resistors connected between the second variable resistor unit and a second voltage and generating a plurality of fifth gray voltages to be provided to the data driver, and the second array of resistors is connected between the first voltage and the second voltage.

[0022] Preferably, the first gray voltage may include a black voltage and the fourth variable gray voltage includes a white voltage.

[0023] Preferably, the polarity inverter and the data driver are provided on the same chip as the data driver, and the variable resistor is provided on the same chip as the signal controller.

[0024] Preferably, the liquid crystal display has an OCB mode.

BRIEF DESCRIPTION OF THE DRAWINGS

[0025] FIG. 1 is a schematic view of an LCD according to an embodiment of the present invention;

[0026] FIG. 2 is an equivalent circuit diagram of one pixel;

[0027] FIG. 3 is a configuration view of an LCD according to the present invention;

[0028] FIG. 4 is a detailed circuit diagram of a gray voltage generator according to an embodiment of the present invention;

[0029] FIG. 5 is a detailed circuit diagram of a gray voltage generator according to another embodiment of the present invention;

[0030] FIG. 6 is a detailed circuit diagram of a gray voltage generator according to still another embodiment of the present invention;

[0031] FIG. 7 is a graph showing a relationship between the gray and the transmittance which is obtained according to an embodiment of the present invention.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENT

[0032] Preferred embodiment of the present invention will now be described in detail with reference to the accompanying drawings.

[0033] FIG. 1 is a schematic block diagram of an LCD according to an embodiment of the present invention, and FIG. 2 is an equivalent circuit diagram of one pixel.

[0034] Referring to FIG. 1, an LCD according to an embodiment of the present invention includes a liquid crystal panel assembly 300, a gate driver 420 and a data driver 430 connected to the panel assembly 300, a driving voltage generator 560 connected to the gate driver 420, a gray voltage generator 570 connected to the data driver 430, and a signal controller 550 for controlling these elements.

[0035] From the point of view of an equivalent circuit, the panel assembly 300 includes a plurality of signal lines G_0 - G_n and D_1 - D_m and a plurality of pixels connected thereto,

and each pixel includes a switching element Q connected to corresponding ones of the signal lines G_0 - G_n and D_1 - D_m , a liquid crystal capacitor C_{lc} and a storage capacitor C_{st} connected thereto. The signal lines G_0 - G_n and D_1 - D_m include a plurality of scanning signal lines or gate lines G_0 - G_n extending in a row direction for conveying scanning signals or gate signals and a plurality of image signal lines or data lines D_1 - D_m extending in a column direction for conveying image signals or data signals. The switching element Q has three terminals, a control terminal connected to the gate lines G_0 - G_n , another connected to the data lines D_1 - D_m , and the other connected to one terminal of the liquid crystal capacitor C_{lc} and one terminal of the storage capacitor C_{st} .

[0036] The other terminal of the liquid crystal capacitor C_{lc} is coupled to a common voltage V_{com} or a reference voltage. The other terminal of the storage capacitor C_{st} is connected to a gate line located just above (hereinafter referred to as "previous gate line") as seen from the figure. Such a connection is called as a previous gate scheme. However, the other terminal of the storage capacitor C_{st} can be connected to another voltage such as the common voltage, which case is called as an independent wiring scheme.

[0037] On the other hand, a structure of the panel assembly 300 is shown in FIG. 2. For the sake of convenience, only one pixel is shown in FIG. 2.

[0038] As shown in FIG. 2, the panel assembly 300 includes a lower panel 100 and an upper panel 200 which are opposite to each other, and a liquid crystal layer 3 interposed between the two panels. The lower panel 100 includes gate lines G_{i-1} , a data line D_i , a switching element Q, and a storage capacitor C_{st} . The liquid crystal capacitor C_{lc} has two terminals, one being a pixel electrode 190 of the lower panel 100 and the other being a common electrode 270 of the upper panel 200. The liquid crystal layer 3 functions as a dielectric between the two electrodes 190 and 270. The liquid crystal layer 3 of this embodiment can take the OCB mode, for example, in which the liquid crystal molecules are oriented in a bend alignment such that the molecular orientations are symmetrical with respect to a mid-plane between the lower panel 100 and the upper panel 200.

[0039] The pixel electrode 190 is connected to the switching element Q, and the common electrode 270 covers the entire surface of the upper panel 200 and is connected to the common voltage V_{com} .

[0040] The orientations of the liquid crystal molecules are changed depending on an electric field generated by the pixel electrode 190 and the common electrode 270 and thereby, polarization of light passing through the liquid crystal layer 3 is changed. Such a change of polarization leads to a change of transmittance of light by a polarizer (not shown) attached to the panels 100 and 200.

[0041] The pixel electrode 190 overlaps the previous gate line G_{i-1} with an insulator therebetween, and thereby, the pixel electrode 190 forms one terminal of the storage capacitor C_{st} while the previous gate line G_{i-1} forms the other terminal of the storage capacitor C_{st} . In case of independent wiring scheme, a separate wire supplied with a voltage such as the common voltage overlaps the pixel electrode 190 to form a storage capacitor C_{st} .

[0042] FIG. 2 shows a MOS transistor implemented by a thin film transistor having a channel layer formed by amor-

phous silicon or polycrystalline silicon in an actual process, as an example of the switching element Q.

[0043] Unlike FIG. 2, there is a case that the common electrode 270 is provided in the lower panel 100. In this case, both of the two electrodes 190 and 270 have shapes of stripes or bars.

[0044] On the other hand, in order to realize color display, each pixel represents a color, which can be accomplished by providing a red, green or blue color filter 230 in a region corresponding to the pixel electrode 190. Although the color filter 230 is typically formed in a corresponding area of the upper panel 200 as shown in FIG. 2, it also can be formed on or under the pixel electrode 190 of the lower panel 100.

[0045] Referring to FIG. 1 again, typically, the gate driver 420 and the data driver 430 also called a scan driver and a source driver, respectively, include a plurality of gate driving ICs and a plurality of data driving ICs, respectively. Each IC can be separately provided outside the panel assembly 300, mounted on the panel assembly 300, or formed on the panel assembly through the same process as the signal lines G_0 - G_n and D_1 - D_m and the thin film transistors Q.

[0046] FIG. 3 shows a structure in which the gate driver 420 is integrated into the panel assembly 300 and the data driver 430 including a plurality of chips 410 is mounted on flexible circuit films 400.

[0047] The gate driver 420 connected to the gate lines G_0 - G_n of the panel assembly 300 applies the gate signal, which is a combination of a gate on voltage V_{on} and a gate off voltage V_{off} from the driving voltage generator 560, to the gate lines G_0 - G_n .

[0048] The data driver 430 connected to the data lines D_1 - D_m of the panel assembly 300 selects correspondences among the gray voltages from the gray voltage generator 570 and applies the selected gray voltages, as the data signals, to the data lines D_1 - D_m .

[0049] As shown in FIG. 3, the signal controller 550 and the driving voltage generator 560 are provided on a printed circuit board (PCB) 500, which is mechanically and electrically connected to the panel assembly 300 through the flexible circuit films 400. The gray voltage generator 570 according to the embodiment of the present invention can be provided in part on both the PCB 500 and the chips 410 or provided only on the PCB 500.

[0050] The operation of the gate driver 420, the data driver 430, the driving voltage generator 560, etc. are controlled by the signal controller 550 connected to them, which will be in detail described.

[0051] The signal controller 550 is supplied with RGB color signals R, G and B and input control signals, for example, a vertical synchronization signal V_{sync} , a horizontal synchronization signal H_{sync} , a main clock CLK, a data enable signal DE, etc., for controlling display of the RGB color signals R, G and B from an external graphics controller (not shown). The signal controller 550 produces gate control signals and data control signals based on the input control signals, and then, sends the gate control signals to the gate driver 420 and the driving voltage generator 560 and the data control signals and properly processed color signals R, G and B to the data driver 430.

[0052] The gate control signals include a vertical synchronization start signal STV for notifying a start of output of a gate on pulse ("high level" interval of the gate signal), a gate clock signal CPV for controlling an output time of the gate on pulse, an output enable signal OE for defining the width of the gate on pulse, etc. Among these signals, the output enable signal OE and the gate clock signal CPV are provided to the driving voltage generator 560. The data control signals include a horizontal synchronization start signal STH for notifying a start of input of color signals, a load signal LOAD or TP for instructing to apply data voltages to the corresponding data lines, a data clock signal HCLK, etc.

[0053] At that time, the signal controller 550 outputs the received color signals R, G and B based on the data enable signal DE.

[0054] Upon receipt of the vertical synchronization start signal STV, the gate driver 420 applies the gate on voltage V_{on} from the driving voltage generator 560, as the gate on pulse, to the gate lines G_0 - G_n to turn on the switching elements Q connected to the gate lines G_0 - G_n , in synchronization with the gate clock signal CPV. At that time, the width of the gate on pulse is determined by the output enable signal OE. During remaining time when the gate on pulse is not applied, the gate off voltage V_{off} is applied to the gate lines G_0 - G_n to turn off the switching elements Q. In addition, the driving voltage generator 560 generates the common voltage V_{com} and provides it to the panel assembly 300.

[0055] The data driver 430 stores the received color signals R, G and B into a shift register (not shown) upon receiving the horizontal synchronization start signal STH, and then selects gray voltages corresponding to the color signals R, G and B among the gray voltages from the gray voltage generator 570 and applies the selected gray voltages to the corresponding data lines upon receiving a pulse of the load signal LOAD, in synchronization with the data clock signal HCLK. Then, the data signals are applied to respective pixels through the activated switching elements Q connected to the corresponding data lines.

[0056] The present invention proposes two embodiments which control the gray voltages generated by the gray voltage generator 570 such that the same gray gives the same luminance.

[0057] Referring to FIGS. 4 and 5, the gray voltage generator according to embodiments of the present invention will be described in detail.

[0058] FIG. 4 is a detailed circuit diagram of an exemplary gray voltage generator according to an embodiment of the present invention.

[0059] As shown in FIG. 4, a gray voltage generator 570 according to an embodiment of the present invention includes two independent voltage output units 571 and 572 for generating the gray voltages.

[0060] A first voltage output unit 571 of the two voltage output units 571 and 572 generates voltages corresponding to the lowest gray (i.e., the black gray) and the highest gray (i.e., the white gray), which are independent from each other. In case of a normally white mode, the voltage corresponding to the black gray is the highest while the voltage corresponding to the white gray is the lowest. In the embodiment of the present invention, each of the color signals R, G and

B includes 8 bits, and accordingly, the total number of possible grays is 256 ($=2^8$). The black voltage becomes a first gray voltage 1G and the white voltage becomes a 256-th gray voltage 256G, with a relationship of the first gray voltage > the 256-th gray voltage. On the other hand, since the property of the liquid crystal material may get deteriorated if an electric field is applied in only one direction to the liquid crystal material, it is necessary to apply a voltage with opposite polarity. Accordingly, the first voltage output unit 571 according to the embodiment of the present invention generates all of positive and negative black voltages 1G and 1G- and positive and negative white voltages 256G and 256G-. Here, the positive and negative polarities mean polarities with respect to the common voltage V_{com} . In addition, since a positive gray voltage gives the same luminance as a corresponding negative gray voltage, a difference between the positive gray voltage and the common voltage V_{com} is preferably equal to that between the negative gray voltage and the common voltage V_{com} .

[0061] Circuits for generating the black voltages 1G and 1G- and the white voltages 256G and 256G- have substantially the same configuration in the first voltage output unit 571. The circuit for generating the black voltages 1G and 1G- includes a variable resistor VR1 and two operational amplifiers OP1 and OP3 serially connected to the variable resistor VR1 with two resistors R21 and R22.

[0062] The operational amplifier OP1 outputting the positive black voltage 1G has a non-inverting input (+) connected to the variable resistor VR1 and an inverting input (-) and an output in negative feedback.

[0063] The operational amplifier OP3 inverting the positive black voltage 1G to be output as the negative black voltage 1G- includes an inverting input (-) connected to the output of the operational amplifier OP1 through the input resistor R21 and a non-inverting input (+) connected to the common voltage V_{com} . In addition, the operational amplifier OP3 is in negative feedback through the feedback resistor R22 connected between the inverting input (-) and the output of the operational amplifier OP3.

[0064] The circuit for generating the white voltages 256G and 256G- includes a variable resistor VR2, two operational amplifiers OP2 and OP4, and two resistors R31 and R32. The internal connections in the circuit are configured in the same manner as the circuit for generating the black voltages 1G and 1G-, for generating the positive and negative white voltages 256G and 256G-.

[0065] The second voltage output unit 572 generates the remaining gray voltages, i.e., second to 255-th gray voltages 2G~255G and 2G~-255G-, which are not generated in the first voltage output unit 571. The configuration of the second voltage output unit 572 is shown in FIG. 4.

[0066] As shown in FIG. 4, the second voltage output unit 572 includes an array of resistors connected between a supply voltage Vdd and the common voltage V_{com} . The array of resistors has a hierarchical configuration that the resistors are connected in stages. More particularly, as shown in FIG. 4, a predetermined number of resistors R1~R5 in a first stage are connected in series between the power source Vdd and the common voltage V_{com} , and subsequently, a predetermined number of resistors in a second stage are connected in series across each resistor in

the first stage, . . . , and so on. Such a connection is repeated until a proper number of gray voltages are obtained. The number of resistors in each stage can be varied as required. It is shown in FIG. 4 that the number of resistors in the first stage is five and the number of resistors in the second stage is five. The above-described array of resistors connected between the power source Vdd and the common voltage V_{com} generates positive gray voltages 2G~255G. In addition, the second voltage output unit 572 includes another array of resistors connected between the common voltage V_{com} and a ground for generating negative gray voltages 2G~-255G-. A connection configuration of these resistors is equal to that of the resistors for generating the positive gray voltages 2G~255G.

[0067] Among the circuit components of the first and second voltage output units 571 and 572 having the above-described configuration, the variable resistors VR1 and VR2 and a plurality of voltage dividing resistors R1~R5, R1~R15, . . . can be mounted on the PCB 500, and the operational amplifiers OP1~OP4 with the resistors R21, R31, R22 and R32 can be mounted on the chips 410.

[0068] Now, the operation of the gray voltage generator 570 according to the embodiment of the present invention as described above will be described.

[0069] After an LCD is manufactured, a gray characteristic of the LCD is checked. If luminance of black and white grays is deviated from a reference value, an input voltage applied to the non-inverting inputs of the operational amplifiers OP1 and OP2 is varied by adjusting resistances of the variable resistors VR1 and VR2 of the first voltage output unit 571 mounted on the PCB 500, such that the luminance has the reference value. At that time, it is preferable that the black voltage is not lower than the second gray voltage and the white voltage is not higher than the 255-th gray voltage. The operational amplifiers OP1 and OP2, which work as buffers, output the adjusted voltages via their outputs. The output voltages of the operational amplifiers OP1 and OP2 are supplied to the data driver 430 as the positive black voltage 1G and the positive white voltage 256G, respectively, and also supplied to their own inverting inputs and the inverting inputs of the operational amplifiers OP3 and OP4.

[0070] The operational amplifiers OP3 and OP4 generate the negative black voltage 1G- and the negative white voltage 256G-, respectively, by inverting and amplifying the voltages inputted into their inverting inputs with gains defined by the resistors R21 and R22; and R31 and R32, and provide the generated voltages for the data driver 430. Here, the negative voltage means the voltage which is equal in magnitude to but different in sign from the positive voltage in view of a difference between the common voltage V_{com} and each of these voltages.

[0071] On the other hand, the second gray voltage output unit 572 generates the positive and negative gray voltages 2G~255G and 2G~-255G- for indicating the remaining grays existing between the black gray and the white gray by means of a voltage dividing function of the resistors having predetermined resistances and connected in a hierarchical configuration, and provides the generated voltages to the data driver 430.

[0072] According to the embodiment as described above, the black voltages 1G and 1G- and the white voltages 256G

and 256G- are adjustable, but regardless of such an adjustment, the remaining gray voltages 2G~255G and 2G~255G- are outputted by the resistors R1 to R5 and R11 to R15 having resistances determined in accordance with a predetermined gamma curve.

[0073] Next, a configuration and operation of an exemplary gray voltage generator 570 according to another embodiment of the present invention will be described with reference to FIG. 5. Here, like reference numerals will be given to those elements for performing like function as in the embodiment shown in FIG. 4.

[0074] As shown in FIG. 5, a gray voltage generator 570 according to another embodiment of the present invention includes a voltage regulator 573 and a voltage output unit 574 connected thereto.

[0075] The voltage regulator 573 and the voltage output unit 574 of this embodiment have configurations similar to those of the first and second voltage output units 571 and 572 in the previous embodiment, except that the supply voltage V_{dd}, the common voltage V_{com}, and the ground in the second voltage output unit 572 of the previous embodiment are replaced with an output of an operational amplifier OP1, outputs of two operational amplifiers OP2 and OP4, and an output of an operational amplifier OP3, respectively, in the voltage regulator 573. Here, when the common voltage V_{com} in the second voltage output unit 572 is replaced, the output of the operational amplifier OP2 is connected to an array of resistors for generating the positive gray voltages and the output of the operational amplifier OP4 is connected to an array of resistors for generating the negative gray voltages. Due to such a connection relationship, the gray voltages outputted from nodes of the resistors depend on input voltages from the operational amplifiers OP1 to OP4.

[0076] In other words, the difference between FIG. 5 and FIG. 4 is that a plurality of resistors are connected in a series between the power source, the common voltage V_{com} and the ground in FIG. 4 while the plurality of resistors are connected in series between the outputs of the operational amplifiers OP1 to OP4 in FIG. 5. Therefore, further description about a connection relationship between the resistors is omitted.

[0077] The voltage regulator 573 and the voltage output unit 574 of the present embodiment can be mounted on the PCB 500.

[0078] The above-described gray voltage generator 570 of this embodiment inputs the outputs of the voltage regulator 573 to the voltage output unit 574.

[0079] The voltage output unit 574 provides the voltages from the operational amplifiers OP1 to OP4, as the positive black voltage, the positive white voltage, the negative black voltage and the negative white voltage, respectively, for the data driver 430. The remaining gray voltages are provided for the data driver 430 as proper values of voltages in accordance with a voltage dividing function of the array of resistors.

[0080] Next, a configuration and operation of an exemplary gray voltage generator 570 according to still another embodiment of the present invention will be described with reference to FIG. 6. Here, like reference numerals will be

given to those elements for performing like function as in the embodiment shown in FIG. 5.

[0081] As shown in FIG. 6, a gray voltage generator 570 of the present embodiment includes a voltage regulator 573 and a voltage output unit 5741 connected thereto, like in FIG. 5. The voltage regulator 573 in FIG. 6 has the same configuration as the voltage regulator shown in FIG. 5.

[0082] The voltage output unit 5741 in FIG. 6 is different from the voltage output unit 574 in FIG. 5 in that voltages V1, V2, V3 and V4 having predetermined values are applied to nodes between resistors R1 and R2, between resistors R4 and R5, between resistors R11 and R12, and between resistors R14 and R15, respectively.

[0083] When values of the variable resistors VR1 and VR2 of the voltage regulator 573 are determined, and accordingly, values of output voltages of operational amplifiers OP1 and OP2 are determined, positive gray voltages across the resistors R1 and R5 at which the voltages V1 and V2 are respectively applied are determined under the influence of the output voltages of the operational amplifiers OP1 and OP2. However, voltages outputted within a range of application of the voltages V1 and V2 are outputted as a plurality of positive gray voltages resulting from a division of the predetermined voltages V1 and V2 by the resistors R2 to R4, regardless of the output voltages of the operational amplifiers OP1 and OP2.

[0084] In addition, like the positive gray voltages, in case of the negative gray voltages, the gray voltages influenced by output voltages of operational amplifiers OP3 and OP4 are voltages divided by the resistors R11 and R15 at which the voltages V3 and V4 are applied. However, the voltages divided by the resistors R12 to R14 are outputted as the negative gray voltages depending on the voltages V3 and V4 having predetermined values, under no influence of the output voltages of the operational amplifiers V3 and V4.

[0085] As described above, the positive and negative gray voltages across the resistors R1, R5, R11 and R15 at which the voltages V1 to V4 are respectively applied are under the influence of the output voltages of the operational amplifiers OP1 to OP4 such that they are varied in connection with a variation of resistances of the variable resistors VR1 and VR2. On the contrary, the positive and negative gray voltages across the remaining resistors R2 to R3 and R12 to R14 are outputted as fixed values determined by the voltages V1 to V4 and resistances of the remaining resistors.

[0086] Although it is shown in FIG. 6 that the preset voltages V1 to V4 are applied to nodes between resistors R1 and R2, between resistors R4 and R5, between resistors R11 and R12, and between resistors R14 and R15, respectively, positions at which the voltages V1 to V4 are applied can be varied based on magnitudes of the output voltages of the operational amplifiers OP1 to OP4.

[0087] As described above, according to the embodiment of the present invention as shown in FIG. 7, the gray voltages can be changed up to arbitrary ranges near the black and white grays by adjusting the resistances of the variable resistors VR1 and VR2.

[0088] In addition, the voltage regulator 573 and the voltage output unit 5741 can be mounted on the PCB 500.

[0089] According to the embodiments of the present invention as described above, since the black and white voltages can be easily adjusted by adjusting the resistances of the variable resistors mounted on the liquid crystal panel assembly based on the gray characteristic of the LCD, the black and white characteristics for each LCD can be uniformly maintained.

[0090] Particularly, by separately adjusting voltages near the white voltages and the black voltages issued in the OCB mode and making no effect on other gray voltages, adjustment efficiency is improved.

1. An apparatus of driving a liquid crystal display including a plurality of pixels, the apparatus comprising:

a gray voltage generator for generating a plurality of gray voltages;

an image signal driver for selecting gray voltages corresponding to color signals and applying the selected gray voltages as image signal voltages to the pixels; and

a signal controller for controlling the image signal driver based on the color signals and input control signals for controlling the color signals,

wherein at least two successive first gray voltages among the plurality of gray voltages are variable and at least two successive second gray voltages among the plurality of gray voltages are fixed.

2. The driving apparatus of claim 1, wherein the first gray voltages are varied by using a variable resistor.

3. The driving apparatus of claim 1, wherein the first gray voltages include a black voltage or a white voltage.

4. The driving apparatus of claim 1, wherein at least two successive third gray voltages among the plurality of gray voltages are variable.

5. The driving apparatus of claim 4, wherein the first gray voltages include a black voltage, the third gray voltages include a white voltage, and the first gray voltages and the third gray voltages are varied independently.

6. The driving apparatus of claim 1, wherein the plurality of gray voltages include a plurality of pairs of gray voltages, each pair of gray voltages having opposite polarity and equal magnitude.

7. The driving apparatus of claim 6, wherein at least one pair of plurality a pairs of gray voltages are variable in association with each other.

8. The driving apparatus of claim 2, wherein the variable resistor and the signal controller are mounted on a printed circuit board external to the liquid crystal display.

9. An apparatus for a liquid crystal display including a plurality of pixels comprising:

a gray voltage generator for generating a plurality of gray voltages;

an data driver for selecting gray voltages corresponding to color signals and applying the selected gray voltages as image signal voltages to the pixels; and

a signal controller for controlling the data driver based on the color signals and input control signals for controlling the color signals,

wherein the gray voltage generator includes a first variable resistor unit connected to the data driver for providing a first variable gray voltage, a first array of resistors connected between the first variable resistor and a first voltage and generating a plurality of second gray voltages to be provided for the data driver, and a second array of resistors having a terminal connected to the first voltage and generating a plurality of third gray voltages.

10. The driving apparatus of claim 9, wherein the gray voltage generator includes a polarity inverter connected to the data driver for inverting polarity of a signal from the first variable resistor unit and providing the is polarity-inverted signal to the data driver.

11. The driving apparatus of claim 10, wherein the first variable resistor unit comprises a variable resistor and a buffer connected between a variable terminal of the variable resistor and an input terminal of the polarity inverter and outputting the first variable gray voltage.

12. The driving apparatus of claim 11, wherein the polarity inverter comprises an operational amplifier having an inverting terminal connected to the buffer, a non-inverting terminal supplied with an inverting reference voltage, and an output terminal, a first resistor connected between the buffer and the inverting terminal, and a second resistor connected between the inverting terminal and the output terminal.

13. The driving apparatus of any one of claims, wherein the gray voltage generator further comprises a second variable resistor unit connected to the data driver for providing a fourth variable gray voltage.

14. The driving apparatus of claim 13, wherein the gray voltage generator further comprises a third array of resistors connected between the second variable resistor unit and a second voltage and generating a plurality of fifth gray voltages to be provided to the data driver.

15. The driving apparatus of claim 14, wherein the second array of resistors is connected between the first voltage and the second voltage.

16. The driving apparatus of claim 14, wherein the first gray voltage includes a black voltage and the fourth variable gray voltage includes a white voltage.

17. The driving apparatus of claim 10, wherein the polarity inverter and the data driver are provided on the same chip.

18. The driving apparatus of claim 2, wherein the variable resistor is provided on the same place as the signal controller.

19. The driving apparatus of claim 1, wherein the liquid crystal display has an OCB mode.

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专利名称(译)	驱动液晶显示器的装置		
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摘要(译)

本发明涉及一种驱动包括多个像素的液晶显示装置的装置。该驱动装置包括：灰度电压发生器，用于产生多个灰度电压；图像信号驱动器，用于选择对应于颜色信号的灰度电压；以及将所选择的灰度电压作为图像信号电压施加到像素；以及信号控制器，用于控制图像信号驱动器基于颜色信号和输入控制信号，用于控制颜色信号。多个灰度电压中的至少两个连续的第一灰度电压是可变的，并且多个灰度电压中的至少两个连续的第二灰度电压是固定的。

