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(19) **United States**(12) **Patent Application Publication** (10) **Pub. No.: US 2006/0033875 A1**
Lin et al. (43) **Pub. Date: Feb. 16, 2006**(54) **LIQUID CRYSTAL PANEL STRUCTURE****Publication Classification**(75) Inventors: **Jin-Lan Lin**, Tainan County (TW);
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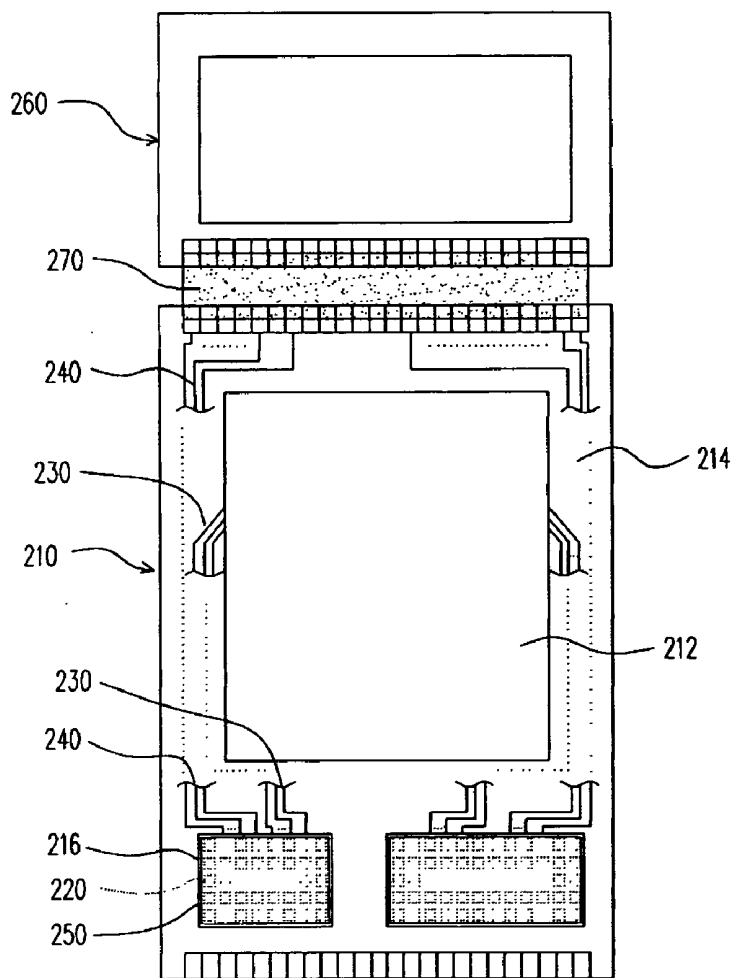
Jul. 7, 2004 (TW)..... 093120307

(51) **Int. Cl.****G02F 1/1345** (2006.01)(52) **U.S. Cl.** **349/152**

(57)

ABSTRACT

A liquid crystal panel structure includes a liquid crystal panel, a plurality of first and second contact terminals, and a plurality of first and second peripheral wirings. The liquid crystal display has a display area and a non-display area. The non-display area has at least one driving chip lamination area. The first and second contact terminals are allocated in the lamination area of the driving chip. The first peripheral wirings are allocated on the non-display area, and the first contact terminals are electrically connected to the pixels of the display area. The liquid crystal panel can selectively provides allocation of two types of driving chips, such that the liquid crystal panel can be applied to single or dual display module.

200

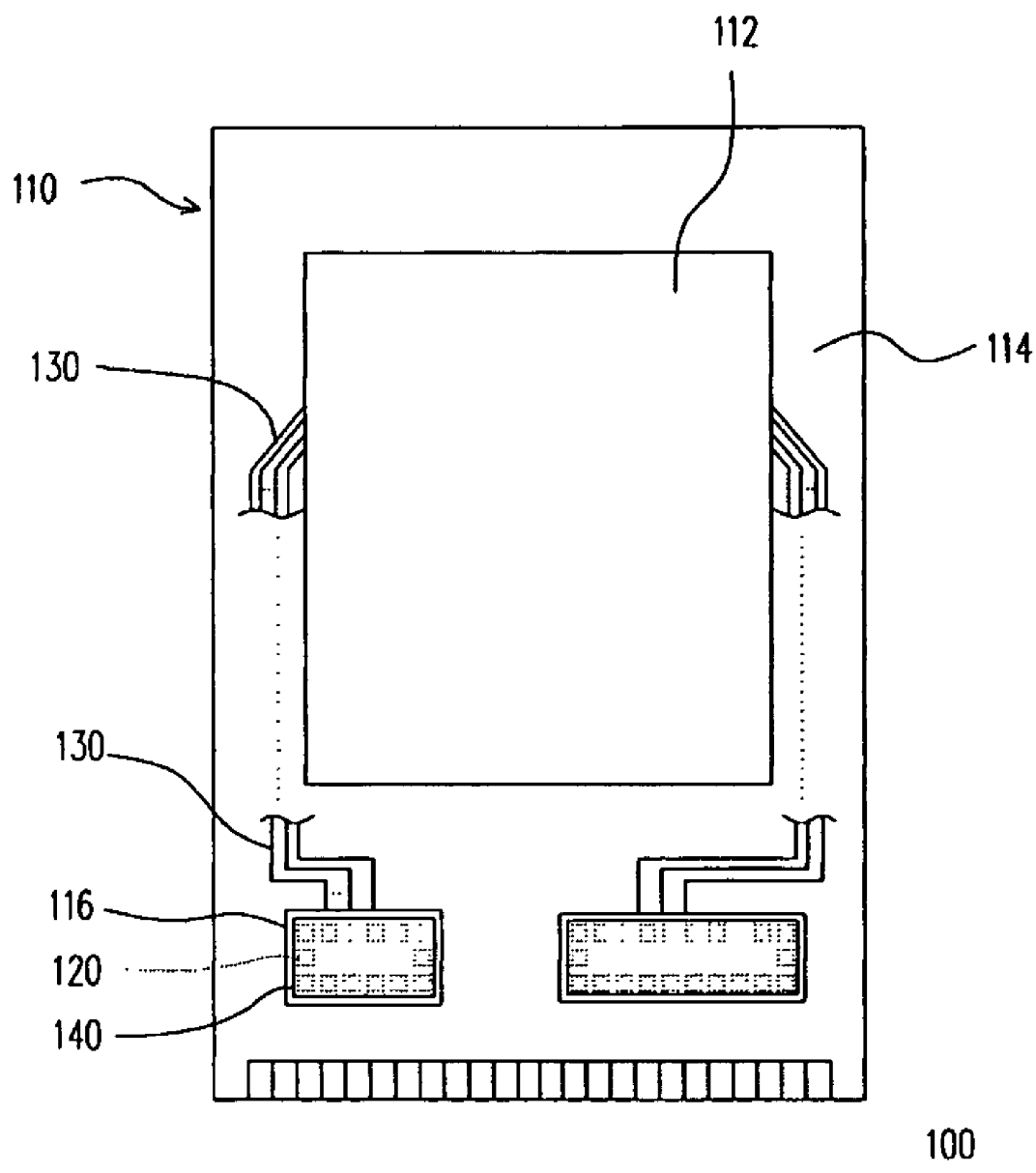


FIG. 1

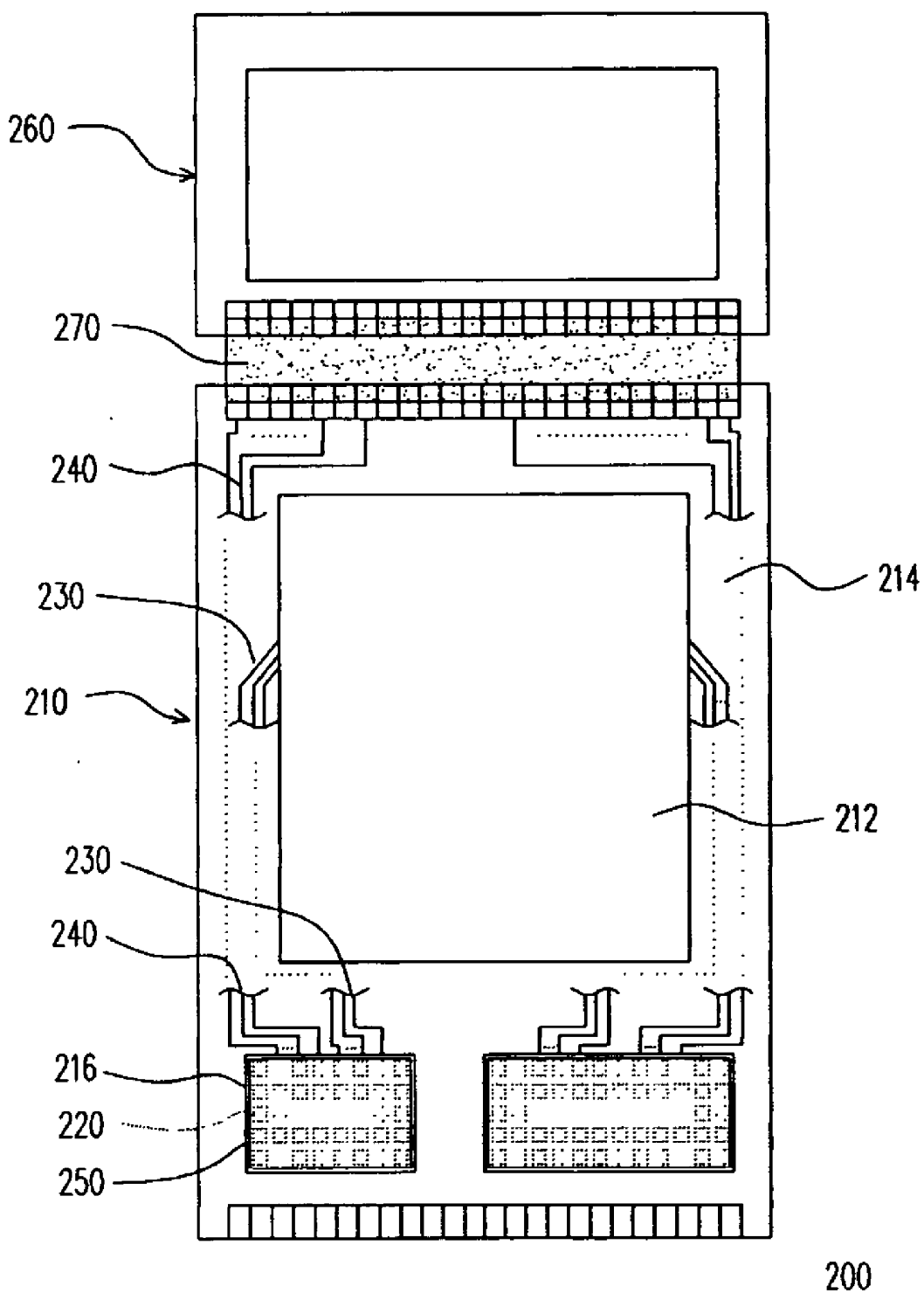


FIG. 2

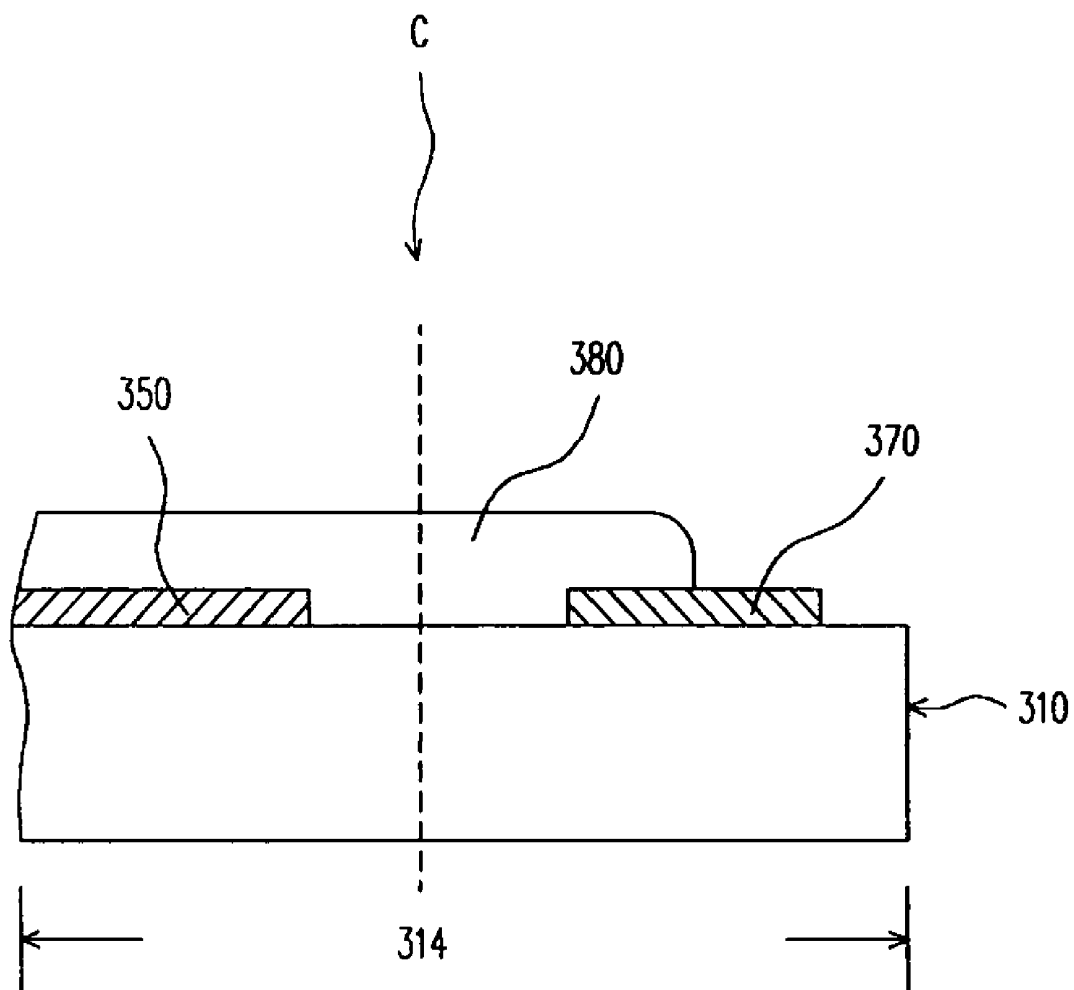


FIG. 4

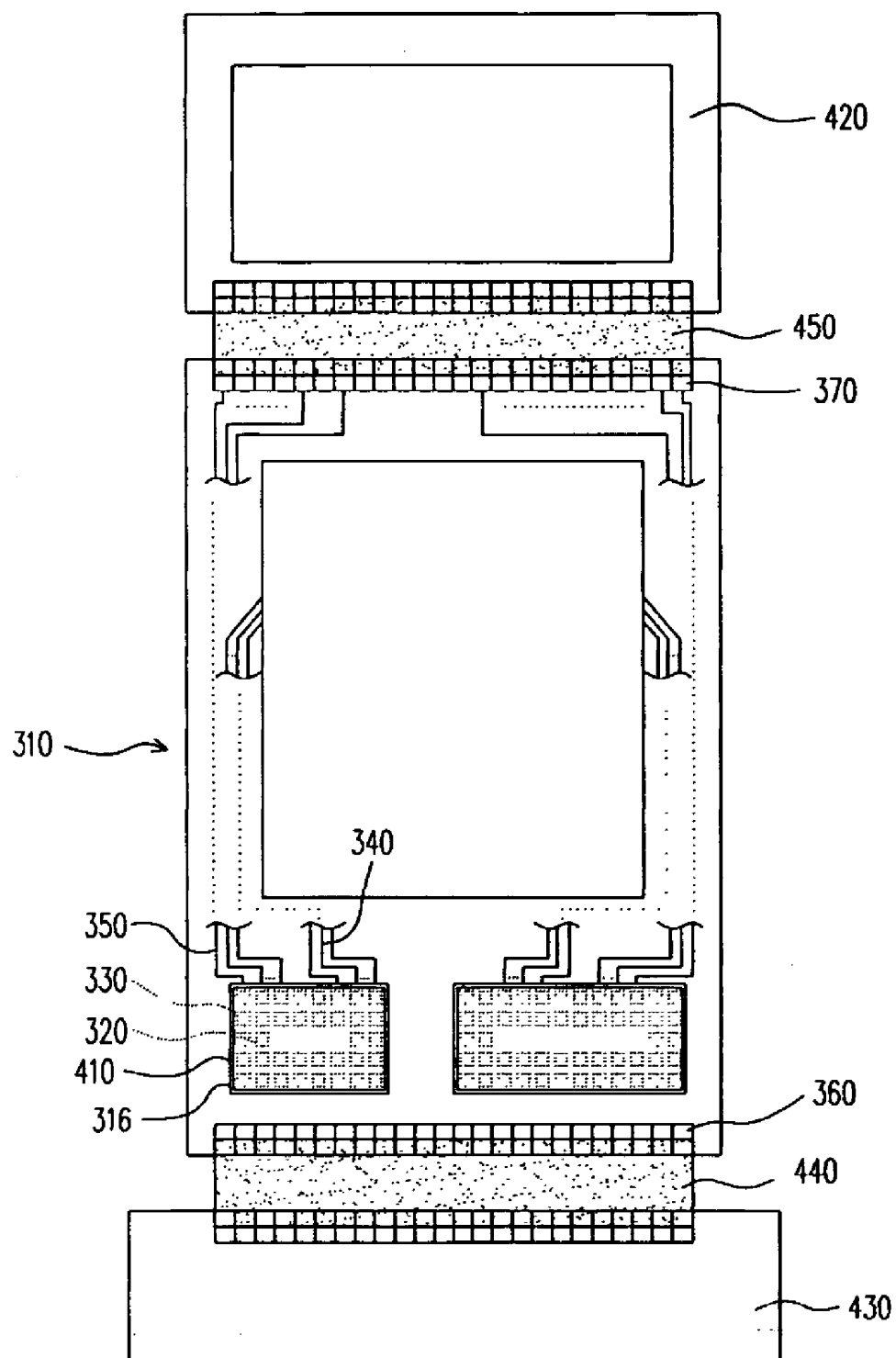


FIG. 5

400

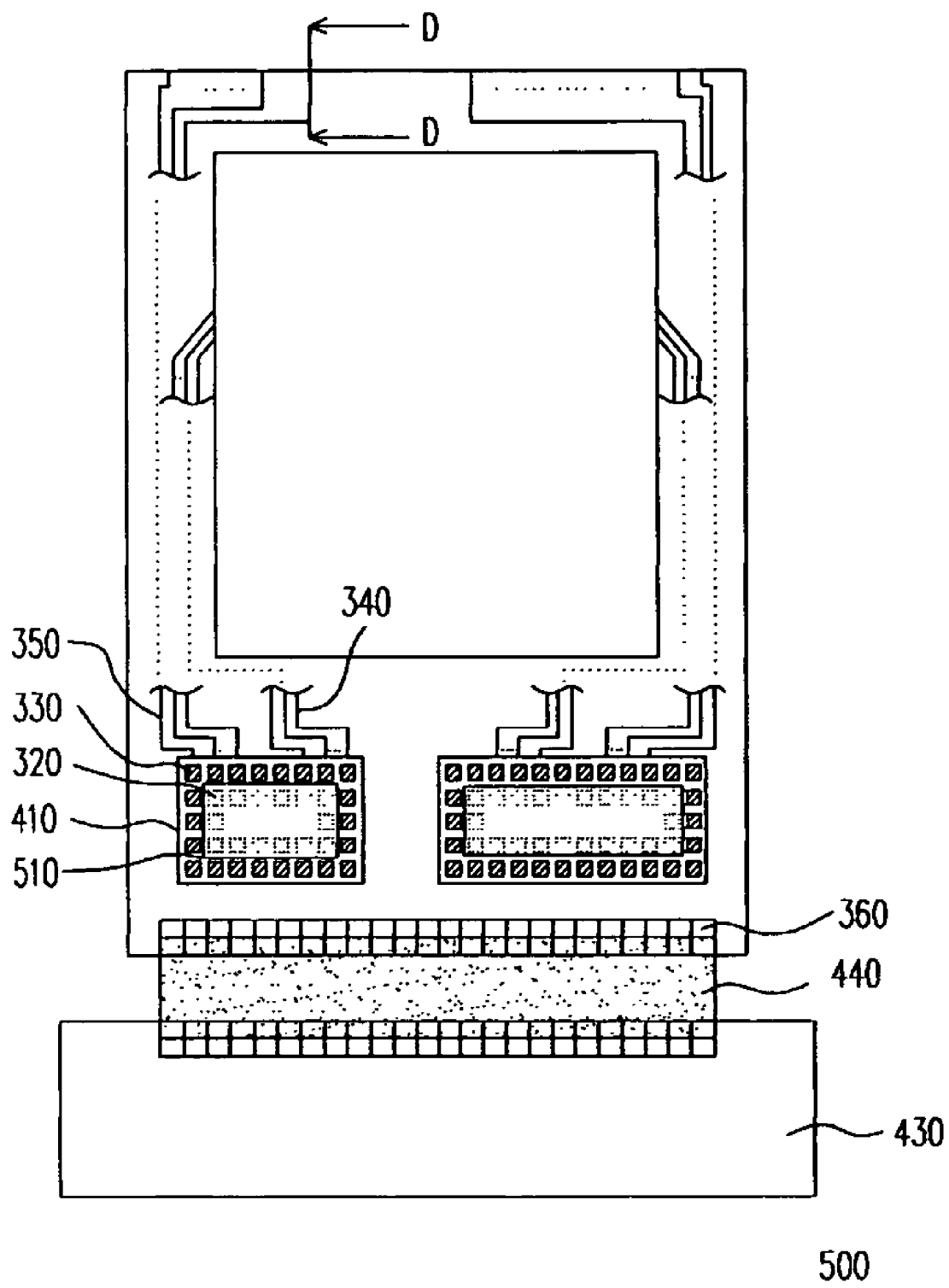


FIG. 6

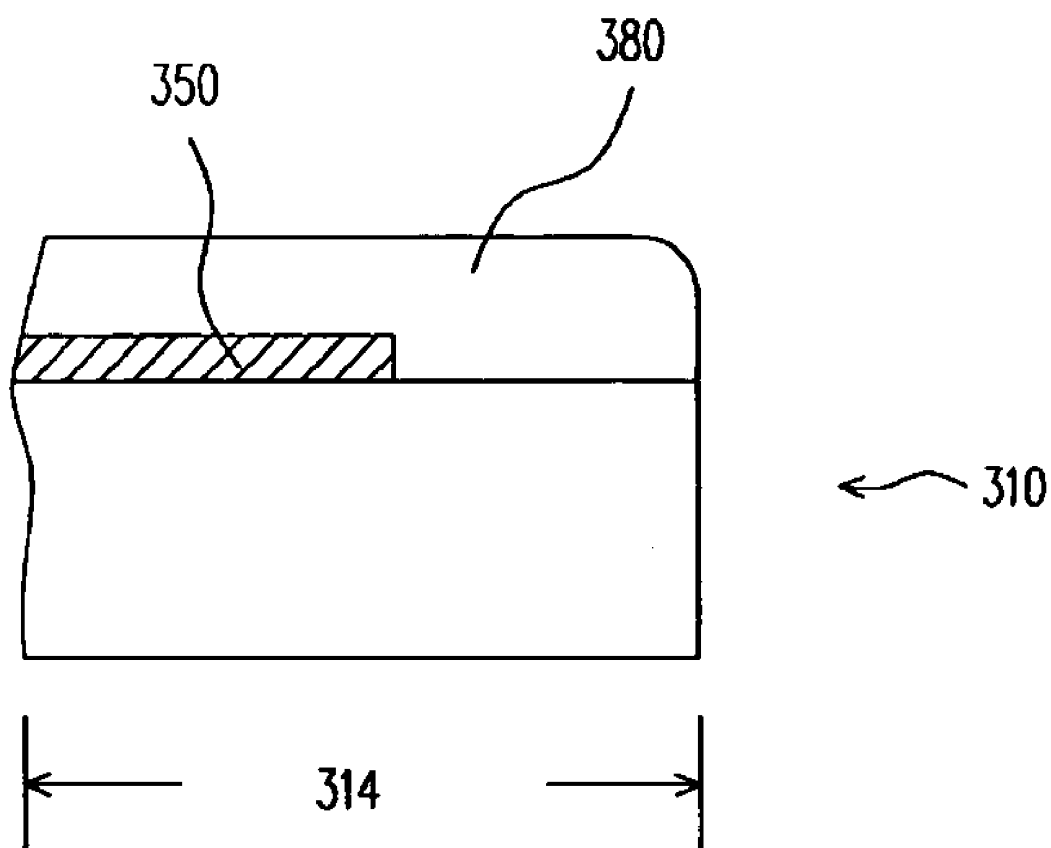


FIG. 7

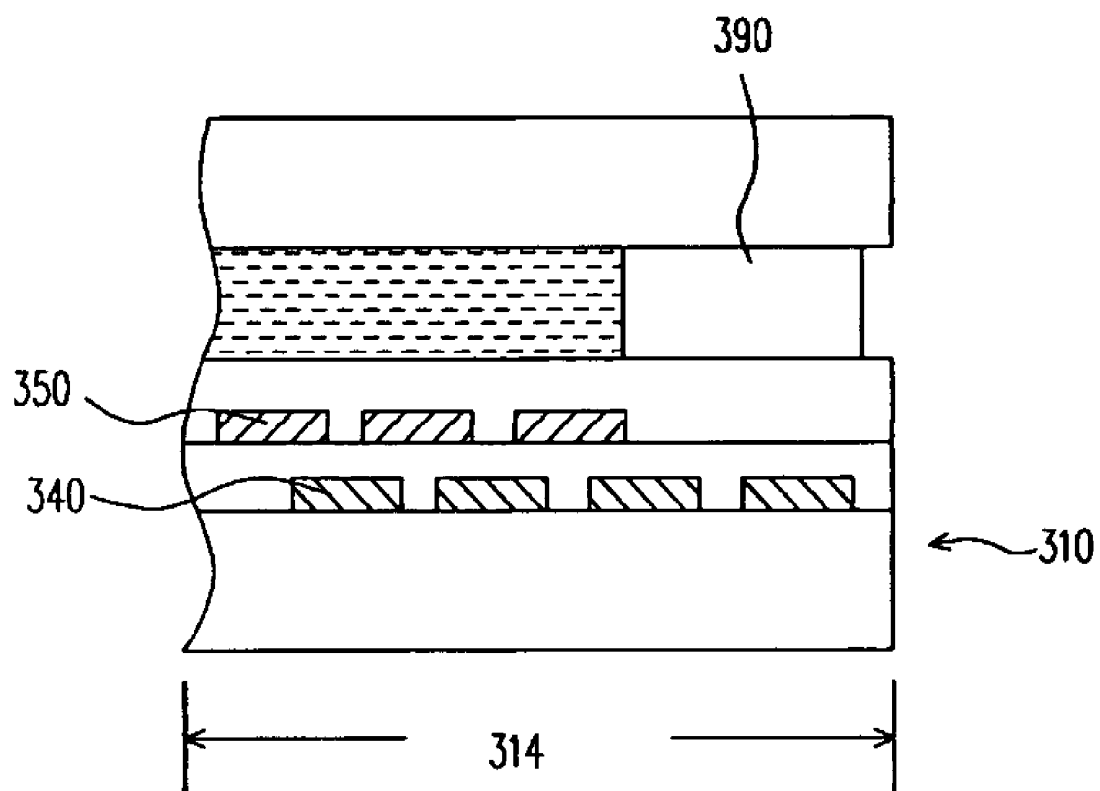


FIG. 8

LIQUID CRYSTAL PANEL STRUCTURE

BACKGROUND OF THE INVENTION

[0001] 1. Field of Invention

[0002] The present invention relates in general to a liquid crystal panel structure, and more particularly, to a structure of a liquid crystal display suitably used in a single display module or a dual display module.

[0003] 2. Related Art

[0004] The demand of being light, thin, short and small for modern electronic products has rendered various kinds of electronic components to be designed and fabricated with smaller volumes. On the other hand, the more and more powerful function of the electronic products allows the conventional electronic devices to be made with much improved portability. For example, various palm or hand-held devices such as the cell phones, personal digital assistants (PDAs) and game boys have become the mainstreams in the market.

[0005] Currently, the commonly seen cell phones do not only include the upright model of which an operation interface (key panel) and a display screen formed on the same panel, but also include the clamshell type of which the operation panel and the display screen can be folded face to face with each other. Compared to the upright model, the clamshell cell phone has advantages such as smaller volume and larger display screen. Recently, the clamshell cell phone may further comprise two screens, including one smaller screen formed on the exterior side of the cell phone which displays time or incoming call.

[0006] FIG. 1 shows a conventional single display module 100 comprising a liquid crystal panel 110, a plurality of contact terminals 120, a plurality of peripheral wirings 130 and a driving chip 140.

[0007] The liquid crystal panel 110 includes a display area 112 and a non-display area 114 which comprises a driving chip lamination area 116 (two in FIG. 1). The contact terminals 120 are allocated in the driving chip lamination area 116. The peripheral wirings 130 are allocated on the non-display area, so as to electrically connect the contact terminals 120 on the driving chip lamination area 116 and the pixels (not shown) on the display area 112. The driving chip 140 is electrically connected to the contact terminals 120 on the driving chip lamination area 116 to drive the liquid crystal panel 110.

[0008] As the conventional single liquid crystal module 100 does not have additional wiring on the liquid crystal panel 110, the liquid crystal panel 110 cannot be combined with another liquid crystal panel. That is, the single display module 100 cannot be expanded into a dual display module.

[0009] FIG. 2 illustrates a conventional dual display module 200 that comprises a first liquid crystal display 210 (the main screen), a plurality of contact terminals 220, a plurality of first and second peripheral wirings 230 and 240, a driving chip 250 and a second liquid crystal panel 260 (the auxiliary screen).

[0010] The first liquid crystal panel 210 includes a display area 212 and a non-display area 214. The non-display area 214 includes a driving chip lamination area 216 and a

plurality of contact terminals 220 allocated in the driving chip lamination area 216. The first and second peripheral wirings 230 and 240 are allocated in the non-display area. The first peripheral wirings 230 are electrically connected to a part of the contact terminals 220 in the driving chip lamination area 216 and a plurality of pixels in the display area 212. The second peripheral wirings 240 have one end connected to the remaining contact terminals 220 in the driving chip lamination area 216 and a second end electrically connected to the second liquid crystal panel 216 via a soft circuit film 270. The driving chip 250 is allocated in the driving chip lamination area 216 to electrically connect the contact terminals 220 in the driving chip lamination area 216, so as to drive the first and second liquid crystal panels 210 and 260.

[0011] In the conventional dual module 220, the first liquid crystal panel 210 can only be used simultaneously with the second crystal panel 260. That is, the first liquid crystal panel 210 cannot be independently activated. In other words, if the second liquid crystal panel 260 is removed from the conventional dual display module 200, the first liquid crystal panel 210 cannot operate to display due to the requirement and restriction of simultaneous activation of both the first and second liquid crystal panels 210 and 260.

SUMMARY OF THE INVENTION

[0012] The present invention is to provide a liquid crystal panel structure allowing allocation of driving chips with two different driving modes, such that a screen display module selectively operates as a single and a dual display module can be obtained.

[0013] In one aspect, a liquid crystal panel structure of the present invention includes a liquid crystal panel, a plurality of first contact terminals, a plurality of second contact terminals, a plurality of first peripheral wirings and a plurality of second peripheral wirings. The liquid crystal panel has a display area and a non-display area. The display area has a plurality of pixels and the non-display area has at least one driving chip lamination area. The first contact terminals are allocated in the driving chip lamination area. The second contact terminals are allocated in the driving chip lamination area. The first peripheral wirings are allocated in the non-display area to electrically connect to the pixels of the display area. The second peripheral wirings are allocated in the non-display area to electrically connect to the second contact terminals.

[0014] Moreover, the liquid crystal panel structure includes a plurality of first connect pads, a plurality of second connect pads and a plurality of anti-corrosion metal lines. The first contact pads may be allocated on the non-display area to electrically connect to the first and the second contact terminals. The second contact pads may be allocated on the non-display area to electrically connect the second peripheral wirings. Each anti-corrosion metal lines may be allocated between one of the second peripheral wirings and one of the second contact pads, so as to electrically connect the second peripheral wiring and the second contact pad.

[0015] In another aspect, a dual display module of the present invention includes a first liquid crystal panel, a plurality of first contact terminals, a plurality of second contact terminals, a plurality of first peripheral wirings, a plurality of second peripheral wirings, a second liquid

crystal panel and at least one driving chip. The first liquid crystal panel has a display area and a non-display area. The display area has a plurality of pixels and the non-display area has at least one driving chip lamination area. The first contact terminals are allocated in the driving chip lamination area. The second contact terminals are allocated in the driving chip lamination area. The first peripheral wirings are allocated in the non-display area to electrically connect to the pixels of the display area. The second peripheral wirings are allocated in the non-display area to electrically connect to the second contact terminals. The second liquid crystal panel is electrically connected to the second peripheral wirings. The driving chip is allocated on the driving chip lamination area and electrically connected to the first and the second contact terminals to drive the first and the second liquid crystal panel.

[0016] Moreover, the dual display module includes a plurality of first connect pads, a plurality of second connect pads and a plurality of anti-corrosion metal lines. The first contact pads may be allocated on the non-display area to electrically connect to the first and the second contact terminals. The second contact pads may be allocated on the non-display area to electrically connect the second peripheral wirings. Each anti-corrosion metal lines may be allocated between one of the second peripheral wirings and one of the second contact pads, so as to electrically connect the second peripheral wiring and the second contact pad.

[0017] Still in another aspect, a single display module of the present invention includes a liquid crystal panel, a plurality of first contact terminals, a plurality of second contact terminals, a plurality of first peripheral wirings, a plurality of second peripheral wirings and at least one driving chip. The liquid crystal panel has a display area and a non-display area. The display area has a plurality of pixels and the non-display area has at least one driving chip lamination area. The first contact terminals are allocated in the driving chip lamination area. The second contact terminals are allocated in the driving chip lamination area. The first peripheral wirings are allocated in the non-display area to electrically connect to the pixels of the display area. The second peripheral wirings are allocated in the non-display area to electrically connect to the second contact terminals. The driving chip is allocated on the driving chip lamination area and electrically connected to the first and the second contact terminals to drive the liquid crystal panel.

[0018] Furthermore, the above-mentioned liquid crystal panel structure, dual display module and single module may include a plurality of third contact terminals. The third contact terminals may be allocated on the driving chip lamination area and electrically connected to the first peripheral wirings, the first contact terminals and the pixels of the display area.

[0019] The liquid crystal panel includes a driving chip lamination area allocated with two set of contact terminals allowing the driving chips with two different driving modes to be allocated. Thereby, the liquid crystal panel can be independently used or connected to another liquid crystal panel, so as to achieve the switchable feature between the single and the dual display modules.

BRIEF DESCRIPTION OF THE DRAWINGS

[0020] The present invention will become more fully understood from the detailed description given hereinafter, and thus are not limitative of the present invention, and wherein:

[0021] FIG. 1 shows a conventional single display module;

[0022] FIG. 2 shows a conventional dual display module;

[0023] FIG. 3 is a schematic drawing showing an exemplary structure of a liquid crystal panel;

[0024] FIG. 4 shows a cross sectional view along line A-A of FIG. 3 of which the anti-corrosion wirings are formed;

[0025] FIG. 5 shows an embodiment of a dual display module;

[0026] FIG. 6 shows an embodiment of a single display module;

[0027] FIG. 7 shows a cross sectional view along line D-D of FIG. 6 after anti-corrosion wirings; and

[0028] FIG. 8 shows a cross sectional view along line B-B of FIG. 3.

DETAILED DESCRIPTION OF THE INVENTION

[0029] FIG. 3 shows an embodiment of the structure of a liquid crystal panel. As shown, the structure liquid crystal panel 300 includes a first liquid crystal panel (main screen) 310, a plurality of first contact terminals 320, a plurality of second contact terminals 330, a plurality of first peripheral wirings 340 and a plurality of second peripheral wirings 350.

[0030] The first liquid crystal panel 310 includes a display area 312 and a non-display area 314 in which at least one driving chip lamination area 316 (two in this embodiment, including a gate driving chip lamination area and source driving chip lamination area). The first contact terminals 320 and the second contact terminals 330 are allocated in each of the driving chip lamination area 316. For example, the second contact terminals 330 are formed around the first contact terminals 320 in this embodiment.

[0031] The first peripheral wirings 340 are allocated in the non-display area 314 to electrically connect the first contact terminals 320 in the driving chip lamination area 316 and a plurality of pixels (not shown) in the display area 312. The second peripheral wirings 350 are also allocated in the non-display area 314 to electrically connect the second terminals 330 in the driving chip lamination area 316.

[0032] In addition, a plurality of first contact pads 360 is allocated in the non-display area 314 to connect the first and second contact terminals 320 and 330 in the driving chip lamination area 316 via third peripheral wirings (not shown). The first contact pads 360 are preferably used as contact interface for electrically connecting the first liquid crystal panel 310 and a control circuit board (not shown).

[0033] The non-display area 314 may further comprise a plurality of second contact pads 370 electrically connected to one end of the second peripheral wirings 350. The second

contact pads **370** are used as electrical interface to electrically connect the first liquid crystal **310** with another liquid crystal (not shown).

[0034] FIG. 4 illustrates the cross sectional view along a line A-A as shown in FIG. 3 after anti-corrosion metal lines are formed. Referring to FIGS. 3 and 4, an anti-corrosion metal line **380** is allocated between each of the second peripheral wirings **350** and the corresponding second contact pad **370**. The second peripheral wirings **350** and the second contact pads **370** are thus electrically connected to each other via the anti-corrosion metal lines **380**. The anti-corrosion metal lines **380** may be fabricated by indium tin oxide (ITO) or other anti-corrosion material. When the liquid crystal panel structure **300** is applied to the single display module, the anti-corrosion metal lines **380** serve for wiring cutting.

[0035] FIG. 5 shows an embodiment of a dual display module. The dual display module **400** includes the above liquid crystal panel **300**, at least one driving chip **410** (two as shown in this embodiment, including a gate driving chip and a source driving chip), a second liquid crystal panel **420** (auxiliary or secondary screen) **420** and a control circuit board **430**.

[0036] The driving chip **410** is allocated on the driving chip lamination area **316** to electrically connect the first contact terminals **320** and the second contact terminals **330**. The first contact pads **360** of the first liquid crystal panel **310** are electrically connected to the control circuit board **430** via a first soft circuit film **440**. The second contact pads **370** of the first liquid crystal panel **310** are electrically connected to the second liquid crystal panel **420** via a second soft circuit film **450**. As the driving chip **410** is electrically connected to the first and second contact terminals **320** and **330**, the driving chip **310** can simultaneously drive the first and second liquid crystal panels **310** and **420** to achieve the dual display function.

[0037] FIG. 6 shows a cross sectional view of a single display module, and FIG. 7 shows a cross sectional view along line B-B of FIG. 6. In this embodiment, the single display module **500** includes the liquid crystal panel structure **300**, at least one driving chip **510** (two in this embodiment, includes a gate driving chip and a source/drain driving chip) and a control circuit board **430**.

[0038] The driving chip **510** is allocated on the driving chip lamination area **316** to electrically connect to the first contact terminals **320**, and electrically insulated from the second contact terminals **330**. The first contact pads **360** of the first liquid crystal panel **310** is electrically connected to the control circuit board **430** via a soft circuit film **440**. As the driving chip **510** is electrically connected to the first contact terminals **320** only, the liquid crystal panel **310** structure is suitable for use in the single display module.

[0039] FIG. 7 shows the cross sectional view along line D-D of FIG. 6 after anti-corrosion metal lines are formed. Referring 3, 4, 6 and 7, the second contact pads **370** of the first liquid crystal panel **310** are easily corroded for being exposed. Therefore, before or after the driving chip **510** is allocated in the driving chip lamination area **316**, the anti-corrosion metal lines **380** are cut along the dash line C as shown in FIG. 4. As the anti-corrosion metal lines **380** are formed from anti-corrosion material such as indium tin

oxide, the second peripheral wirings **350** can be protected by the remaining anti-corrosion lines **380** to prevent corrosion.

[0040] In addition, as the liquid crystal panel structure **300** requires the first and second peripheral wirings **340** and **350** to be allocated in the non-display area **314**, a larger area of the non-display area **314** is required when the first and second peripheral wirings **340** and **350** are formed on the second wiring layer. Such design has a poorer space utilization. Therefore, at least a part of the first peripheral wiring **340** and at least a part of the second peripheral wiring **350** can be formed on different wiring layer to reduce the area required for the non-display area **314**. Referring to FIG. 8, the first peripheral wirings **340** are allocated on the non-display area **314** of the first liquid crystal panel **310** and the second peripheral wirings **350** are allocated above the first peripheral wirings **340**. Thereby, the area of the non-display area **314** is reduced to improve the space utilization. It will be appreciated that the positions of the first and second peripheral wirings can be exchanged or arranged in other manners.

[0041] In addition, the non-display area **314** further comprises a resin frame **390** under which a single type of wiring is allocated. In this embodiment, a part of the first peripheral wirings **340** is formed under the resin frame **390** to prevent short circuit between the first and second peripheral wirings **340** and **350** during the lamination process of the resin frame **390**.

[0042] According to the above, the current application includes at least the following advantages.

[0043] 1. Two set of contact terminals are formed within the driving chip lamination area to selectively provide allocation of driving chips in two different driving modes. Thereby, the liquid crystal panel structure can be used for both dual and single display modules.

[0044] 2. When the liquid crystal panel structure is applied to a single display module, the anti-corrosion metal lines serve to be cut provide anti-corrosion protection for the second peripheral wirings.

[0045] 3. The first and second peripheral wirings can be formed at different layers to improve the space utilization of the non-display area.

[0046] The invention being thus described, it will be obvious that the same may be varied in many ways. Such variations are not to be regarded as a departure from the spirit and scope of the invention, and all such modifications as would be obvious to one skilled in the art are intended to be included within the scope of the following claims.

What is claimed is:

1. A liquid crystal panel structure, comprising:

- a liquid crystal panel, including a display area and a non-display area, the display area comprising a plurality of pixels and the non-display area comprising at least one driving chip lamination area;
- a plurality of first contact terminals allocated in the driving chip lamination area;
- a plurality of second contact terminals allocated in the driving chip lamination area;

a plurality of first peripheral wirings allocated in the non-display area to electrically connect to the pixels of the display area; and

a plurality of second peripheral wirings allocated in the non-display area to electrically connect to the second contact terminals.

2. The structure of claim 1, further comprising a plurality of first contact pads allocated on the non-display area to electrically connect to the first and the second contact terminals.

3. The structure of claim 1, further comprising a plurality of second contact pads allocated on the non-display area to electrically connect the second peripheral wirings.

4. The structure of claim 3, further comprising a plurality of anti-corrosion metal lines each being allocated between one of the second peripheral wirings and one of the second contact pads, so as to electrically connect the second peripheral wiring and the second contact pad.

5. The structure of claim 4, wherein the anti-corrosion metal lines are fabricated from indium tin oxide.

6. The structure of claim 4, wherein the anti-corrosion metal lines serve as wiring to be cut.

7. The structure of claim 1, wherein a part of the first peripheral wirings and a part of the second peripheral wirings are formed in different layers.

8. The structure of claim 7, further comprising a resin frame allocated in the non-display area, and either the part of the first peripheral wirings or the part of the second peripheral wirings is formed under the resin frame.

9. A dual display module, comprising:

a first liquid crystal panel, including a display area and a non-display area, the display area comprising a plurality of pixels and the non-display area comprising at least one driving chip lamination area;

a plurality of first contact terminals allocated in the driving chip lamination area;

a plurality of second contact terminals allocated in the driving chip lamination area;

a plurality of first peripheral wirings allocated in the non-display area to electrically connected to the pixels of the display area;

a plurality of second peripheral wirings allocated in the non-display area to electrically connect to the second contact terminals;

a second liquid crystal panel, electrically connected to the second peripheral wirings; and

at least one driving chip allocated on the driving chip lamination area and electrically connected to the first and the second contact terminals to drive the first and the second liquid crystal panel.

10. The module of claim 9, further comprising a plurality of first contact pads allocated on the non-display area to electrically connect to the first and the second contact terminals, wherein the first contact pads are electrically connected to a control circuit board via a first soft circuit film.

11. The module of claim 9, further comprising a plurality of second contact pads allocated on the non-display area to electrically connect the second peripheral wirings, wherein the second contact pads are electrically connected to the second liquid crystal panel via a second soft circuit film.

12. The module of claim 11, further comprising a plurality of anti-corrosion metal lines each being allocated between one of the second peripheral wirings and one of the second contact pads, so as to electrically connect the second peripheral wiring and the second contact pad.

13. The module of claim 12, wherein the anti-corrosion metal lines are fabricated from indium tin oxide.

14. The module of claim 12, wherein the anti-corrosion metal lines serve as wiring to be cut.

15. The module of claim 9, wherein a part of the first peripheral wirings and a part of the second peripheral wirings are formed in different layers.

16. The module of claim 15, further comprising a resin frame allocated in the non-display area, and either the part of the first peripheral wirings or the part of the second peripheral wirings is formed under the resin frame.

17. A dual display module, comprising:

a liquid crystal panel, including a display area and a non-display area, the display area comprising a plurality of pixels and the non-display area comprising at least one driving chip lamination area;

a plurality of first contact terminals allocated in the driving chip lamination area;

a plurality of second contact terminals allocated in the driving chip lamination area;

a plurality of first peripheral wirings allocated in the non-display area to electrically connected to the pixels of the display area;

a plurality of second peripheral wirings allocated in the non-display area to electrically connect to the second contact terminals; and

at least one driving chip allocated on the driving chip lamination area and electrically connected to the first and the second contact terminals to drive the liquid crystal panel.

18. The module of claim 17, further comprising a plurality of first contact pads allocated on the non-display area to electrically connect to the first and the second contact terminals, wherein the first contact pads are electrically connected to a control circuit board via a first soft circuit film.

19. The module of claim 17, wherein a part of the first peripheral wirings and a part of the second peripheral wirings are formed in different layers.

20. The module of claim 19, further comprising a resin frame allocated in the non-display area, and either the part of the first peripheral wirings or the part of the second peripheral wirings is formed under the resin frame.

* * * * *

专利名称(译)	液晶面板结构		
公开(公告)号	US20060033875A1	公开(公告)日	2006-02-16
申请号	US11/175201	申请日	2005-07-07
[标]申请(专利权)人(译)	群创光电股份有限公司		
申请(专利权)人(译)	奇美电子股份有限公司.		
当前申请(专利权)人(译)	群创光电		
[标]发明人	LIN JIN LAN LIU KUAN YU		
发明人	LIN, JIN-LAN LIU, KUAN-YU		
IPC分类号	G02F1/1345		
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优先权	093120307 2004-07-07 TW		
外部链接	Espacenet USPTO		

摘要(译)

一种液晶面板结构，包括液晶面板，多个第一和第二接触端子，以及多个第一和第二外围配线。液晶显示器具有显示区域和非显示区域。非显示区域具有至少一个驱动芯片层叠区域。第一和第二接触端子分配在驱动芯片的层叠区域中。第一外围配线分配在非显示区域上，并且第一接触端子电连接到显示区域的像素。液晶面板可以选择性地提供两种类型的驱动芯片的分配，使得液晶面板可以应用于单显示模块或双显示模块。

